msleep (version 2025-05-10)

USAGE

sys msleep,amount

DESCRIPTION

msleep allows **sub-second** sleep times. 'amount' specifies the number of delay cycles.

amount should be between 1 and 65535.

depending on the hardware setup for the interval timer on the CPU09MON board, the delay can be in cycles of:

10mS (standard, no patch applied)		UniFLEX reports K10/ at boot time KBOLT=10
5mS	hardware patch applied,	UniFLEX _should_ be build to report K5/ KBOLT=20
2.5mS	hardware patch applied,	UniFLEX _should_ be build to report K2.5/ KBOLT=40
1.25mS	hardware patch applied,	UniFLEX _should_ be build to report K1.25/ KBOLT=80

It is important that the kernel settings (KBOLT) match the hardware patch (modification) applied on the CPU09MON board.

On mismatch, the realtime clock in the kernel will be off very quickly!

At the fastest rate a timer interval interrupt appears each 1.25 mS, but due to the efficient context switching, this is a light load for the CPU.

DIAGNOSTICS

Due to the hardware characteristics of the interval timer, the first cycle time may be anywhere between 0 and the KBOLT value. This is because the systemcall is a-synchronous with respect to the interval timer cycle. Subsequent cycles will be exactly at KBOLT interval.

(The KBOLT setting is in .../include/params.h)