



in be mapped

face.....

IO Decoding (on MON card)		
F000-F003	console ACIA	
F004-F007	timer/trap/LIGHTS PIA	
F008-F00F	Video Board	DIV3
F010-F07F	serial IO SR4 cards	DIV3
F080-F0FF	unassigned	DIV4
F100-F11F	IDE controller	DIV5
F120-F17F	unassigned	DIV5
F180-F1FF	unassigned	DIV6
F200-F2FF	IOP board	DIV7
F300-F3FF	unassigned	DIV7

If an IO board can do local sub address select it can be used in any area