## **CPU09RAM - GAL Selection Table.**

Type	I/O	G1		Programmer
GAL16V8A-15LP	2	RAM_U		1, 2
GAL16V8B-15LP	1	RAM_U		1, 2
GAL16V8D-15LP	1	RAM_U		1, 2
GAL16V8D-25LP	1	RAM_U		1, 2
GAL22V10B-15LP	3	X		1, 2
GAL22V10D-15LP	3	X		1, 2
ATF16V8B-15PC	1	ATF-RAM_U		2, 3, 1
ATF16V8B-25PC	1	ATF-RAM_U		2, 3, 1
ATF22V10B-15PC	1	X		2, 3, 1
ATF22V10C-15PC	5	X		2, 3, 1
PALCE16V8-15PC	1	ATF-RAM_U	< needs Atmel code	2
PALCE16V8-25PC	1	ATF-RAM_U	< needs Atmel code	2
PALCE16V8H-15PC/4	4	ATF-RAM_U	< needs Atmel code	2, 3
PALCE16V8H-25PC	4	ATF-RAM_U	< needs Atmel code	2, 3
PALCE22V10H-15PC/4	4	X		2
PALCE22V10H-25PC/4	4	X		2
PALCE22V10Q-25PC	4	X		 2

Tested

Any open field means not used/tested

First nr prefered

< RAM\_U code no UNIBUG, vector tests P3a an P3b failed!

I/O: 1 = I/O 50K pull up	Programmer:
2 = I/O No pull up	1 = Xgpro TL866II Plus, Sw 12.90 - Fw 04.2.132
3 = I/O Active pull up	2 = GALEP-4, Galep32 Version 1.20.4
4 = 100K 50K No pull up	3 = TOP2013, Sw Topall 8.92 - Fw 6.30
5 = I/O Pin keeper	·

Remarks: I/O type 4 depending on device suffix /4 = 100K, /5 = 50K, otherwise NO pull-up I/O type 2,4 and 5 may need a pull-up on floating input pins.

Some ATF16V8B had errors on the TL866II Plus but not on the GALEP-4.

Do select the full name in the programmer A, B, C, D, H or Q version!

Otherwise you will get programming errors!

TOP2013 also select the correct speed -10, -15 or -25!