**CPU09IOP - GAL Selection Table.** 

Туре	I/O	G1	G2	Programmer
0.41 (0)(0.4 (5) 5		105 /		
GAL16V8A-15LP	2	IOP_1	Х	1, 2
GAL16V8B-15LP	1	IOP_1	x	1, 2
GAL16V8D-15LP	1	IOP_1	х	1, 2
GAL16V8D-25LP	1	IOP_1	х	1, 2
GAL22V10B-15LP	3	Х	IOP_2	1, 2
GAL22V10D-15LP	3	Х	IOP_2	1, 2
ATF16V8B-15PC	1	ATF-IOP_1	х	2, 3, 1
ATF16V8B-25PC	1	ATF-IOP_1	х	2, 3, 1
ATF22V10B-15PC	1	Х	ATF-IOP_2	2, 3, 1
ATF22V10C-15PC	5	X	ATF-IOP_2	2, 3, 1
PALCE16V8-15PC	1	IOP_1	х	2
PALCE16V8-25PC	1	IOP_1	х	2
PALCE16V8H-15PC/4	4	IOP_1	х	2, 3
PALCE16V8H-25PC	4	IOP_1	х	2, 3
PALCE22V10H-15PC	4	Х		2
PALCE22V10H-25PC	4	Х		2
PALCE22V10Q-25PC	4	X	IOP_2	2

Tested Any open field means not used/tested First nr prefered Also tested a mix ATF / GAL / PALCE and -15 / -25

I/O: 1 = I/O 50K pull up	Programmer:
2 = I/O No pull up	1 = Xgpro TL866II Plus, software version 12.67
3 = I/O Active pull up	2 = GALEP-4, software Galep32 Version 1.20.4
4 = 100K 50K No pull up	•
5 = I/O Pin keeper	0 - 101 2010, 001111210 1011 0102

Remarks: I/O type 4 depending on device suffix /4 = 100K, /5 = 50K, otherwise NO pull-up
I/O type 2,4 and 5 may need a pull-up on floating input pins.
Some ATF16V8B had errors on the TL866II Plus but not on the GALEP-4.
Do select the full name in the programmer A, B, C, D, H or Q version!
Otherwise you will get programming errors!
TOP2013 also select the correct speed -10, -15 or -25!