

Job Posting: 176521 - Position: S26 DCS Validation Engineer Co-op 176521

Co-op Work Term Posted:	2026 - Summer
App Deadline	01/14/2026 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	01/06/2026 03:52 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Microchip Technology Inc.
Address Line 1	8555 Baxter Pl
Address Line 2	105
City	Burnaby
Postal Code / Zip Code	V5A 4V7
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Summer
 Job Title 	S26 DCS Validation Engineer Co-op 176521
Position Type	Co-op Position
Job Location	Burnaby, BC
Country	Canada
Duration	4 or 8 months
Work Mode	In-Person
Salary Currency	CAD
Salary	22.75 per hour for 40 Major List
Salary Range \$	22.75-34.5
Job Description	

DCS Validation Engineer Co-op

Division: DCS Arch & Validation

The System Engineering group in collaboration with Product Development, is responsible for ensuring that the end products operate fully in their intended applications. This involves the planning, design, integration, test and documentation of a complete system level platform used to test the prototype SoC's and associated Firmware/Software. The System Validation Design Engineer works in parallel with the IC and FW/SW designers to design an entire validation test systems, which could involve custom firmware development, digital/analog board designs, test script coding, and System-on-Chip integration.

Using these systems, together with the latest communication analyzers and test equipment, the System Validation Design Engineer will work as part of a team to develop and execute a Feature Test Plan that fully validates all features of the prototype IC and associated FW/SW. Working at the system level, the System Validation Design Engineer will gain knowledge in relevant industry standards could include PCIe, NAND, DDR and other standards commonly used in computer & data center solutions.

Responsibilities

As a VALIDATION ENGINEER CO-OP, you will work as part of a team to develop, execute and document a series of Feature Tests that will fully validate the operation of the prototype SOC as part of the overall system. These feature tests will exercise the various functional blocks of a prototype IC, associated FW/SW and tests the system level circuitry to performance criteria and to industry standards. Work will involve designing, building and debugging system level HW, SW and FW to test leading edge prototype SOC's in their intended applications. Specific tasks could include:

- Executing & managing system test suites to validate firmware feature enhancements & fixes.
- Building test setups & fixtures required to support validation testing
- Application level scripting for automated control of the test systems using Python or Tcl/Tk.
- Troubleshoot and resolve problems that may arise in complex PCIe system topologies.
- C/C++ test FW development
- Presenting technical information to small teams of engineers.

Job Requirements

Qualifications

- Electrical or Computer Engineering co-op student
- Experience/Knowledge of basic software engineering & programming concepts
- Basic knowledge of microprocessor architectures & computer systems
- Basic knowledge of Computer related protocols such as USB, PCIe, UART, etc
- Experience in a lab or testing environment, especially using test & measurement equipment

- Ability to read & understand basic electronic schematics involving digital and analog circuitry
- Experience/knowledge of Linux & Windows is an asset

Citizenship Requirement N/A
Position Start Date May 04, 2026 12:00 AM
Position End Date August 28, 2026 12:00 AM

APPLICATION INFORMATION

Application Procedure Through UBC Science Co-op
Cover Letter Required? Yes
Address Cover Letter to Isaac Leung