

Job Posting:174750 - Position: W26 FPGA/DSP Programmer - Co-Op/Intern 174750

Co-op Work Term Posted: 2026 - Winter
App Deadline 10/31/2025 09:00 AM
Application Method: Through Employer Website
Posting Goes Live: 10/23/2025 02:33 PM
Job Posting Status: Approved

ORGANIZATION INFORMATION

Organization Ciena Canada
Country Canada

JOB POSTING INFORMATION

Placement Term 2026 - Winter
** Job Title ** W26 FPGA/DSP Programmer - Co-Op/Intern 174750
Position Type Co-op Position
Job Location Ottawa, ON
Country Canada
Duration 4 months
Salary Currency CAD
Salary 25.0 per hour for 0 Major List
Salary Range \$ \$25.00 - \$33.00
Job Description

Job Title: FPGA/DSP Programmer - Co-Op/Intern Winter 2026

Job ID: R029311

As the global leader in high-speed connectivity, Ciena is committed to a people-first approach. Our teams enjoy a culture focused on prioritizing a flexible work environment that empowers individual growth, well-being, and belonging. We're a technology company that leads with our humanity-driving our business priorities alongside meaningful social, community, and societal impact.

The Opportunity:

Join Ciena, a global leader in optical networking systems, services, and software, as part of our WaveLogic Science Team. This team is dedicated to research and development, with a history of discovering and integrating cutting-edge technologies into Ciena's products. In this paid Co-Op position, you will contribute to innovative optical systems engineering projects while gaining hands-on experience in FPGA programming and digital signal processing (DSP).

How You Will Contribute:

As part of a multidisciplinary team, you will:

- FPGA Programming: Develop and program FPGA systems to capture, process, and export data in real-time measurement environments.
- Networking Protocols: Implement networking protocols such as Ethernet and UDP within FPGA systems.
- DSP Algorithms: Design efficient hardware implementations for DSP algorithms, including filtering and resampling.

Pay Range

The hourly pay range for this position is \$25.00 - \$33.00

Pay ranges at Ciena are designed to accommodate variations in knowledge, skills, experience, market conditions, and locations, reflecting our diverse products, industries, and lines of business. Please note that the pay range information provided in this posting pertains specifically to the primary location, which is the top location listed in case multiple locations are available.

In addition to competitive compensation, Ciena offers students access to the Employee Assistance Program (EAP), company-paid

holidays, paid sick leave, and vacation pay as required by applicable laws.

Not ready to apply? Join our Talent Community to get relevant job alerts straight to your inbox.

At Ciena, we are committed to building and fostering an environment in which our employees feel respected, valued, and heard.

Ciena values the diversity of its workforce and respects its employees as individuals. We do not tolerate any form of discrimination.

Ciena is an Equal Opportunity Employer, including disability and protected veteran status.

If contacted in relation to a job opportunity, please advise Ciena of any accommodation measures you may require.

Job Requirements

The Must Haves:

To be successful in this role, you should possess:

- Enrollment in an Undergraduate- or Graduate-level program in Electrical Engineering, Computer Engineering, or Computer Science.
- Demonstrated proficiency in FPGA programming, particularly using VHDL.
- Familiarity with basic computer networking, including Ethernet and UDP protocols.

Assets:

While not required, the following skills and experiences will set you apart:

- Understanding of real-time processing, lasers, and optics.
- Mechanical design experience related to enclosures for hardware systems.
- Hands-on experience integrating peripherals into hobby development boards (e.g., Arduino, Raspberry Pi).

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link:

https://ciena.wd5.myworkdayjobs.com/en-US/Careers/job/Ottawa/FPGA-DSP-Programmer---Co-Op-Intern-Winter-2026_R029311?source=LinkedIn+Job+Advertisement

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website. Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received. Students should submit their applications as soon as they are ready.