

Job Posting:173226 - Position: W26 Software (Test) Development Engineer Co-op 173226

Co-op Work Term Posted:	2026 - Winter
App Deadline	09/19/2025 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	09/12/2025 03:54 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Microchip Technology Inc.
Address Line 1	8555 Baxter Pl
Address Line 2	105
City	Burnaby
Postal Code / Zip Code	V5A 4V7
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Winter
 Job Title 	W26 Software (Test) Development Engineer Co-op 173226
Position Type	Co-op Position
Job Location	Burnaby, BC
Country	Canada
Duration	4 or 8 months
Work Mode	In-Person
Salary Currency	CAD
Salary	Salary Not Available, 40 Major List
Salary Range \$	22.75-34.5
Job Description	

About Microchip Technology

At Microchip, we work every day to innovate and develop products that solve our customers' technology challenges. Our team of more than 20,000 people worldwide is dedicated to delivering on the promise of working together to improve lives.

Learn about our guiding values that are the building blocks and foundation of our culture and discover how we make a positive impact in the communities in which we operate. We believe our culture fosters trust, collaboration and belonging.

Our people serve the industrial, computing, automotive, communications, aerospace and defense, and consumer market segments. As part of our global team, you can build technology solutions in our six growth areas - 5G, data centers, autonomous driving, the Internet of Things, electric vehicles, and artificial intelligence and machine learning.

About the Job:

We are looking for an energetic and self-motivated student to work with our firmware verification team in the Communication Business Unit at Microchip. We are building firmware for the high performance System on Chip (SoC) devices developed by our hardware teams. These SoC products contain clusters of CPUs, data switching subsystems and a variety of high speed and low speed interfaces. They support the aerospace and communications markets as well as a growing number of applications in next generation embedded systems.

Our focus is on technical excellence, teamwork, collaboration, and continuous improvements in an open and encouraging environment. If you have an interest in embedded software/firmware verification, embedded systems, and communications protocols (Ethernet, Optical Networks), then this is the position for you! Our team uses modern software development practices such as Agile/Scrum and Continuous Integration / Delivery.

As a Firmware Verification Engineering student, you will be part of a team that designs and implements tests for embedded software and drivers for modern System-on-Chip (SoC) devices which runs Linux/RTOSs and contains multi-CPU clusters, ethernet switches, high speed interfaces (PCIe/CXL) and memory & peripheral interfaces. You will assist the firmware verification team in several areas such as:

- Writing/debugging system level tests in Python
- Developing/supporting the Python test framework
- Supporting the hardware evaluation boards in the lab
- Maintaining/enhancing the automated regression test environment (Jenkins)

This position is a good stepping-stone to gain experience in working with enterprise-level firmware using modern software development tools and practices.

Responsibilities

- Design, develop, and debug Python tests and C based tests to verify firmware and maintain these tests in regression
- Debugging software/hardware to root cause test failures
- Maintaining/enhancing our automated regression test environment (Jenkins)
- Developing / supporting our test framework for both pre-silicon and post-silicon testing
- Setup and configuration of 3rd party test equipment and evaluations boards for SW verification
- Writing and reviewing comprehensive engineering documentation
- Entry and tracking of issues in corporate tracking and resolution tools
- Work with the product development, application, and validation engineering teams as part of test related activities

Job Requirements

Qualifications

- Enrolled in a Bachelor's Degree in Electrical Engineering, Computer Engineering, or equivalent.
- Strong knowledge of C/C++ and Python programming
- Must be comfortable in a lab environment engaging in hardware/software/firmware debug
- Knowledge of Real-Time operating constructs is an asset
- Experience with FPGA/ASICs is an asset
- Excellent analytical, problem solving, communication, and documentation skills
- Demonstrated a systematic approach to problem-solving within a team and on your own
- Strong interpersonal skills, experience as part of a collaborative development team

Citizenship Requirement N/A

Position Start Date January 05, 2026 12:00 AM

Position End Date April 24, 2026 12:00 AM

APPLICATION INFORMATION

Application Procedure Through UBC Science Co-op

Cover Letter Required? Optional

Address Cover Letter to Brian Vandegriend

Special Application Instructions

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.

Students should submit their applications as soon as they are ready.