Job Posting:175475 - Position: W26 CPU Core Performance Verification Intern - CPU/AI Hardware 175475B

Co-op Work Term Posted:	2026 - Winter
App Deadline	11/24/2025 09:00 AM
Application Method:	Through Employer Website
Posting Goes Live:	11/17/2025 02:50 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Tenstorrent Inc.
City	Santa Clara
Province / State	CA

JOB POSTING INFORMATION

Placement Term	2026 - Winter
<b> Job Title <b>	W26 CPU Core Performance Verification Intern - CPU/AI Hardware 175475B
Position Type	Co-op Position
Job Location	Austin, TX
Country	USA
Duration	4 months
Work Mode	In-Person
Salary Currency	US
Salary	0.0 per hour for 0 Major List
Salary Range $	$50/hr - $70/hr

Job Description

Tenstorrent is leading the industry on cutting-edge AI technology, revolutionizing performance expectations, ease of use, and cost efficiency. With AI redefining the computing paradigm, solutions must evolve to unify innovations in software models, compilers, platforms, networking, and semiconductors. Our diverse team of technologists have developed a high performance RISC-V CPU from scratch, and share a passion for AI and a deep desire to build the best AI platform possible. We value collaboration, curiosity, and a commitment to solving hard problems. We are growing our team and looking for contributors of all seniorities.
Join the team building high-performance RISC-V CPU cores at Tenstorrent. As a Performance Verification Intern, you will work alongside CPU architects, micro-architects, and performance engineers to ensure our designs meet real-world performance demands. This is a hands-on role that spans simulation, emulation, debug, and analysis.
This role is on-site, full-time (40 hours/week) in Santa Clara, CA or Austin, TX.

What You Will Learn

• How CPU performance is measured, validated, and optimized before silicon.
•How performance models, RTL implementations, and debug tools come together in a real chip development workflow.
•Techniques for balancing tradeoffs in microarchitecture to hit performance targets.
•What it takes to collaborate across architecture, design, and post-silicon teams in a high-performance CPU project.

Job Requirements

Who You Are

• Pursuing a BS, MS, or PhD in EE, ECE, CE, or CS with strong academic performance.
•Experienced with assembly, C/C++, and scripting in Python or Perl.
•Familiar with CPU architecture and performance concepts, including pipelines, memory systems, and branch prediction.
•Comfortable working through complex debug problems in a methodical, detail-oriented way.

What We Need

• Interns who can create performance test plans and write stimulus to stress real CPU scenarios.
•Engineers who can run simulations and emulations to validate performance metrics.
•Contributors who can bring up and debug workloads ranging from open-source benchmarks to industry-standard suites.
•Teammates who can investigate mismatches between RTL and performance models and help root-cause bottlenecks.

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Special Application Instructions

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.

Application Link: https://job-boards.greenhouse.io/tenstorrentuniversity/jobs/4501134007

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.

Students should submit their applications as soon as they are ready.