

Job Posting:173230 - Position: W26 AI-Automation Developer Co-op 173230

Co-op Work Term Posted:	2026 - Winter
App Deadline	09/19/2025 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	09/12/2025 04:24 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Microchip Technology Inc.
Address Line 1	8555 Baxter Pl
Address Line 2	105
City	Burnaby
Postal Code / Zip Code	V5A 4V7
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Winter
 Job Title 	W26 AI-Automation Developer Co-op 173230
Position Type	Co-op Position
Job Location	Burnaby, BC
Country	Canada
Duration	4 or 8 months
Work Mode	In-Person
Salary Currency	CAD
Salary	Salary Not Available, 40 Major List
Salary Range \$	22.75-34.5
Job Description	

Microchip's Communications Business Unit (CBU) is a leading provider of high-speed optical networking and connectivity solutions, delivering products that support 100Gbps+ technologies for telecom and hyperscale data center networks. CBU's product lines enable a wide range of cloud-based applications, including streaming services, social media platforms, and video conferencing and 64-bit mission-critical edge computing (including space applications). Our advanced network devices are designed to meet the demands of modern, bandwidth-intensive environments, ensuring reliable and efficient data transmission for global communications infrastructure.

CBU is seeking an Engineering Co-Op student to support the development, deployment, and ongoing enhancement of an AI-driven automation environment. This in-house tool is designed to advance automation within Microchip engineering teams.

Job Requirements

Key Responsibilities:

- Learn and become proficient with the existing AI automation infrastructure.
- Assist in the roll-out and support of the tool within CBU and across other Microchip business units.
- Gather and document user feedback to inform future improvements.
- Contribute to the enhancement of the tool under the guidance of CBU's AI experts.
- Support the integration of AI-driven automation into the design flow for an upcoming semiconductor project.

This position offers a unique opportunity to gain hands-on experience with both advanced semiconductor design flows and real-world AI applications, making it an excellent learning experience for students interested in the intersection of engineering and artificial intelligence.

Qualifications:

- Currently enrolled in a Bachelor's or Master's program in Electrical Engineering, Computer Engineering, Computer Science, or a related field.
- Strong analytical and problem-solving skills.
- Eagerness to learn new technologies and adapt to evolving methodologies.
- Experience in Python and good programming practices.
- Good communication and documentation skills.
- Experience with silicon design flows (an asset).
- Previous experience with AI concepts, tools, or projects (an asset).

Citizenship Requirement	N/A
Position Start Date	January 05, 2026 12:00 AM
Position End Date	April 24, 2026 12:00 AM

APPLICATION INFORMATION

Application Procedure	Through UBC Science Co-op
Cover Letter Required?	Optional
Address Cover Letter to	Mark Hainsworth