

Job Posting:173713 - Position: S26 FY26 Intern - Compute DSP/AI Processor Engineering Internship - Canada (16 months) 173713B

Co-op Work Term Posted:	2026 - Summer
App Deadline	09/29/2025 09:00 AM
Application Method:	Through Employer Website
Posting Goes Live:	09/22/2025 03:05 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Qualcomm MEMS Technologies Inc.
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Summer
 Job Title 	S26 FY26 Intern - Compute DSP/AI Processor Engineering Internship - Canada (16 months) 173713B
Position Type	Co-op Position
Job Location	Markham, ON
Country	Canada
Duration	16 months
Work Mode	To be confirmed
Salary Currency	CAD
Salary	Salary Not Available, 0 Major List
Job Description	

Qualcomm Overview:

Qualcomm is a company of inventors that unlocked 5G ushering in an age of rapid acceleration in connectivity and new possibilities that will transform industries, create jobs, and enrich lives. But this is just the beginning. It takes inventive minds with diverse skills, backgrounds, and cultures to transform 5G's potential into world-changing technologies and products. This is the Invention Age - and this is where you come in.

General Summary:

Qualcomm is a world-class organization and leading developer of next-generation wireless and multimedia technology.?

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Qualcomm enables a world where everyone and everything can be intelligently connected. As the world's leading wireless tech innovator, we push the boundaries of what's possible to enable next-gen experiences and drive digital transformation to help create a smarter, connected future for all. Our roadmap of breakthrough technologies expands our mobile innovations and solutions to support virtually every connected device. With our leadership in wireless connectivity, high-performance, low-power computing, and on-device AI, we're powering the connected intelligent edge.??

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We are looking for bright, energetic, motivated students to join Qualcomm's Canada 2026 intern class!?

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At Qualcomm Canada, our **Compute DSP/AI Processor team** is looking for ASIC design and verification engineering interns interested in developing world-class machine learning technologies to transform industries and enrich lives.?Under the supervision of an experienced engineer, the intern will contribute to design, verification, and modelling of ASIC HW IP (blocks) and sub-

systems that may be used in machine learning end user applications. **No background of any application space, such as AI or ML, is required. Through hands-on experience, you will learn a wide range of hardware design and verification techniques that transcend end user application.**

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By applying here, you are expressing interest in one of our many 2026 Qualcomm AI & Machine Learning Hardware Engineering?internships. It is important to note that this is not a job posting for a specific role. We will review resumes on an ongoing basis, and a recruiter may reach out to you.?

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Responsibilities may include:?

- Implement modules in hardware design languages such as SystemVerilog and HLS C/C++/ SystemC
 - Implement tests at both the module and sub-system levels using UVM and SystemVerilog
 - Implement reference and bus-functional models using C++, SystemC, or Python
 - Implement design/verification methodology and automation using Python, Perl, or TCL
 - Implement and debug constraints to achieve netlist synthesis and static timing closure
 - Make significant contributions to silicon debug and analysis
 - Perform power analysis to identify/fix power bugs and contribute to power modelling/reporting
- Step into many phases of ASIC development from specification -> microarchitecture -> RTL coding -> verification -> synthesis -> physical design -> post-silicon debug/correlation

Job Requirements

Minimum Qualifications:??

- Digital logic?
- Digital design?
- Verilog/VHDL?

One or more of the following qualifications will be an asset:??

- Relevant academic or personal project experience
- Relevant industry experience (internship, summer work, or others)?
- Computer and multimedia architecture knowledge (CPU, Bus interconnect, Audio, Display, Video, Camera, Graphics)?
- Knowledge of ASIC design and simulation tool sets (synthesis, simulator)?
- Knowledge of hardware description languages?(Verilog/VHDL, HLS C/C++, SystemVerilog, SystemC)?
- Knowledge of verification methodology (UVM, assertions, coverage-driven constrained-random verification)?
- Knowledge of scripting languages (Python, Perl, TCL, Shell)?
- Strong problem-solving skills
- Excellent verbal, written, and presentation skills
- Ability to work effectively as part of a team in a fast-paced environment

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?Educational Requirements:

- In study towards a bachelor's degree in one of the following: Electrical Engineering, Computer Engineering, Engineering Science, Computer Science, or related field
- Eligible candidates must have a graduation date in December 2027 or later, including May or June 2028.
- For 16-month internships: must be available May 2026 - August 2027

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Special Application Instructions

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.

Application Link:

https://qualcomm.eightfold.ai/careers?query=intern&location=any&pid=446714394725&domain=qualcomm.com&sort_by=relevance&triggerGoButton=false

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are

received.

Students should submit their applications as soon as they are ready.