Job Posting: 177520 - Position: S26 FPGA Developer Co-op 177520

Co-op Work Term Posted:	2026 - Summer
App Deadline	02/12/2026 09:00 AM
Application Method:	Through Employer Website
Posting Goes Live:	01/21/2026 03:46 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Ericsson
Address Line 1	4333 Still Creek Drive
City	Burnaby
Postal Code / Zip Code	V5C 6S6
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Summer
<b> Job Title <b>	S26 FPGA Developer Co-op 177520
Position Type	Co-op Position
Job Location	Ottawa, ON
Country	Canada
Duration	16 months
Salary Currency	CAD
Salary	25.5 per hour for 40 Major List
Salary Range $	$25.50 - 34.50 CAD

Job Description

Job Title: FPGA Developer Co-op
Job ID: 778627
At this time, Ericsson Canada Inc. does not provide immigration assistance/sponsorship now or in the future for this position.
About this opportunity:
This team is responsible for all aspects of FPGA development for Ericsson Radio Products: systemization, design, coding, simulation, verification and lab testing, to create new capabilities and satisfy customer needs. Candidates can expect a hands-on technical role where everyone is empowered to influence the direction of the product development. If you are selected for this position, you will work as part of a cross-functional systems, hardware, and software team combining efforts to deliver world class cellular products.
What you will do:
• Develop FPGA code for Radio products
•Define verification requirements and develop test cases to secure new and legacy functionality
•Trouble shoot and contribute to resolution of issues uncovered during verification and integration
•Automate aspects of the development flow using scripting languages
•Work with the team, and on your own, to perform software/hardware integration verification in the lab
•Perform hands-on subsystem or system verification of the delivered functionality
•Review design documentation, RTL code, test documentation, and verification implementation

•Report on verification results

What happens once you apply?

Click Here to find all you need to know about what our typical hiring process looks like.

Ericsson uses a merit-based hiring approach that values people with different experiences, perspectives and skillsets. We truly believe this approach drives innovation, which is essential for our future growth. We encourage people from all backgrounds to apply and realize their full potential as part of our Ericsson team. Ericsson is proud to be an Equal Opportunity employer, learn more.

If you need assistance or to request an accommodation due to a disability, please contact Ericsson at hr.direct.americas@ericsson.com.

DISCLAIMER: The above statements are intended to describe the general nature and level of work being performed by employees in this position. They are not an exhaustive list of all responsibilities, duties and skills required for this position, and you may be required to perform additional job tasks as assigned.

Primary country and city: Canada (CA) || Ottawa

Job details: Developer

Compensation and Benefits at Ericsson

At Ericsson, we know that our people are the key to our success. We offer a competitive package to help with your individual needs and goals.

Your Pay

The salary offered is dependent on various factors including, but not limited to, location, and the candidate's combination of job-related knowledge, qualifications, skills, education, training, and experience.

The salary range for this position is

• Bacherlors $25.50 - $34.50 CAD

•Masters $37.00 CAD

Job Requirements

The skills you bring:

• 2.7+ GPA on a 4.0 scale or equivalent

•Actively enrolled in an accredited undergraduate, Master's, or PhD on a full-time basis at a 4-year college or university at the time of the internship

•Be available to work 40 hours per week during the summer or Co-Op termAt least 3rd year standing in a B.Sc. or M.Sc. program in Electrical Engineering, Computer Engineering, or related field

•Strong analytical, troubleshooting and problem solving skills

•Programming skills including FPGA design and verification coding in Verilog, SystemVerilog; scripting languages such as Python, Perl, TCL and Make; and Object Oriented software development

•Knowledge of design principles for FPGA timing, clocking, reset strategies, logic synthesis, and placement and routing

•Understanding of testing and verification

•High attention to detail

•To be proactive, motivated, organized and efficient

•Strong interpersonal and communication skills

•Flexibility and the ability to multi-task

You might also have:

• Basic knowledge of wireless communication standards would be beneficial

•Knowledge of digital signal processing theory and/or RTL realizations of DSP functions is an asset

•Some understanding and experience with hardware building blocks and circuit boards is an asset

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link:

https://jobs.ericsson.com/careers/job/563121773994041

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website. Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received. Students should submit their applications as soon as they are ready.