## Job Posting:175239 - Position: S26 FPGA Engineering Intern - Hardware and/or Software 175239

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| **Co-op Work Term Posted:** | 2026 - Summer |
| **App Deadline** | 11/21/2025 09:00 AM |
| **Application Method:** | Through UBC Science Co-op |
| **Posting Goes Live:** | 11/07/2025 02:46 PM |
| **Job Posting Status:** | Approved |

## ORGANIZATION INFORMATION

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| **Organization** | Altera Corporation |
| **Address Line 1** | 150 Bloor Street West |
| **Address Line 2** | Suite 400 |
| **City** | Toronto |
| **Postal Code / Zip Code** | M5S 2X9 |
| **Province / State** | Ontario |
| **Country** | Canada |

## JOB POSTING INFORMATION

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|---|---|
| **Placement Term** | 2026 - Summer |
| **<b> Job Title <b>** | S26 FPGA Engineering Intern - Hardware and/or Software 175239 |
| **Position Type** | Co-op Position |
| **Job Location** | Toronto, ON |
| **Country** | Canada |
| **Duration** | 12 or 16 months |
| **Salary Currency** | CAD |
| **Salary** | 40.0 per hour for 0 Major List |
| **Salary Range $** | $40-45/hour |

**Job Description**

Altera is a company of bold and curious inventors and problem solvers who create some of the most astounding technology advancements and experiences in the world.

Do you love to solve technical challenges that no one has solved yet? Do you enjoy working with cross functional teams to deliver IP solutions for products that impact customers lives? If so, come join us to do something wonderful.

As an undergraduate intern you will potentially have the opportunity to:

• Research, design, develop, and optimize software tools that enable the use of Field Programmable Gate Arrays (FPGA).

• Develop and optimize compilers, flows, assemblers, models, tools, runtimes, and/or firmware that are closely coupled to FPGA silicon, IP, and boards, while leveraging strong knowledge of FPGA hardware, logic design, board design, semiconductor devices, and chip layout.

• Design, develop, and optimize software abstractions and frameworks for acceleration with the FPGA for domains such as deep learning, DSP algorithms, or data analytics.

• Respond to customer/client/stakeholder requests or events as they occur.

**Job Requirements**

We are looking for world-class engineers to help us build the future of technology. We are looking for someone that brings:

• Excellent technical skills.

•Excellent communication skills.

•Works well independently and develops quick engineering solutions for complex problems.

•High problem-solving skills and good tolerance for ambiguity.

•Knows how to prioritize tasks independently.

•Focus on quality, discipline, and accurate results for engineering customers.

•Contributes and works well in a multi-site team setting.

Relevant experience can be obtained through schoolwork, classes and project work, internships, military training, and/or work experience.

Minimum Education:

• The candidate must be pursuing a bachelor's degree in computer engineering, electrical engineering, engineering science, computer science, mathematics, or similar.

•3+ months of experience or coursework in one, or more, of the following: C/C++, Python, digital logic, FPGAs, Verilog, or similar.

This position is for an in-person role at our Altera office in downtown Toronto, Ontario.

**Citizenship Requirement**        N/A


## APPLICATION INFORMATION

**Application Procedure**        Through UBC Science Co-op

**Cover Letter Required?**        Yes

**Address Cover Letter to**        Hiring Manager