Job Posting: 176539 - Position: S26 DCS Applications Engineer Co-op 176539

Co-op Work Term Posted:	2026 - Summer
App Deadline	01/14/2026 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	01/06/2026 03:44 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Microchip Technology Inc.
Address Line 1	8555 Baxter Pl
Address Line 2	105
City	Burnaby
Postal Code / Zip Code	V5A 4V7
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Summer
<b> Job Title <b>	S26 DCS Applications Engineer Co-op 176539
Position Type	Co-op Position
Job Location	Burnaby, BC
Country	Canada
Duration	4 months
Work Mode	In-Person
Salary Currency	CAD
Salary	Salary Not Available, 40 Major List
Salary Range $	22.75-34.5
Job Description	

Applications Engineer Co-op

Division: DCS Applications Marketing

If you'd like to be part of a friendly, collaborative, and hard-working team that creates cutting-edge and innovative technology for today and tomorrow's hyperscale, cloud storage, enterprise servers and AI/ML needs, we'd love to hear from you!

As an Applications Engineer Co-op, you will be:

• Assisting the Applications Engineering team in lab activities such as bringing up and testing hardware and systems for a variety of Microchip storage products.

•You will have many opportunities and options to participate in SAS4, PCIe4/5/6 lab investigations, high-speed simulation (IBIS AMI) for PCIe Gen5/6 SERDES designs or firmware and protocol level problem analysis.

Job Requirements

Required Qualifications:

• Currently enrolled in Electrical Engineering/Computer Engineering/Computer Science program

•Excellent debugging and documentation skills

•Good writing and communication skills

•Familiarity with computer architecture

•Work experience through co-op work terms

Desired Qualifications:

• Basic understanding of electronic components, electronic circuits, PCB assemblies, lab equipment, and simulation software

•Ability to interpret electronic schematics and component datasheets

•Experience with measurement equipment: oscilloscopes, multimeters, data loggers

•Proficient in using CAD tools and MS Office products

•Experience with board-level debug & bring-up and related tools

•Experience with driver development / embedded FW development

•Familiarity with PCB manufacturing processes and PCB assemblies

•Familiarity with storage protocols such as SCSI, PCIe, SAS & SATA is an asset

•Familiarity with high-speed design and signal integrity concepts is an asset

Citizenship Requirement	N/A
Position Start Date	May 04, 2026 12:00 AM
Position End Date	August 28, 2026 12:00 AM

APPLICATION INFORMATION

Application Procedure	Through UBC Science Co-op
Cover Letter Required?	Yes
Address Cover Letter to	Jackson Wong