

Job Posting:174848 - Position: W26 Analog Design Co-op 174848

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| Co-op Work Term Posted: | 2026 - Winter |
| App Deadline | 11/14/2025 09:00 AM |
| Application Method: | Through Employer Website |
| Posting Goes Live: | 10/28/2025 11:25 AM |
| Job Posting Status: | Approved |

ORGANIZATION INFORMATION

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| Organization | Ciena Canada |
| Country | Canada |

JOB POSTING INFORMATION

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| Placement Term | 2026 - Winter |
| Job Title | W26 Analog Design Co-op 174848 |
| Position Type | Co-op Position |
| Job Location | Ottawa, ON |
| Country | Canada |
| Duration | 4 months |
| Salary Currency | CAD |
| Salary | 25.0 per hour for 0 Major List |
| Salary Range \$ | \$25.00-45.00 |
| Job Description | |

Job Title: Analog Design Co-op

Job ID: R029226

As the global leader in high-speed connectivity, Ciena is committed to a people-first approach. Our teams enjoy a culture focused on prioritizing a flexible work environment that empowers individual growth, well-being, and belonging. We're a technology company that leads with our humanity driving our business priorities alongside meaningful social, community, and societal impact.

How You Will Contribute:

- You will be responsible for the design and/or verification of various analog blocks assigned to you under the guidance of a senior analog design engineer. You will interact with layout team members who may assist with some aspects of the overall implementation through to full GDSII delivery to Ciena's integration partner.
- Reporting on status updates on a regular basis, participation in team meetings and sharing of experience with the rest of the group.
- Characterization in Ciena's state-of-the art lab of the analog circuits from test-chips through to full product implementation working with members of our Analog Macro Integration team.

Pay Range:

The hourly pay range for this position is \$25.00 - \$43.00.

Pay ranges at Ciena are designed to accommodate variations in knowledge, skills, experience, market conditions, and locations, reflecting our diverse products, industries, and lines of business. Please note that the pay range information provided in this posting pertains specifically to the primary location, which is the top location listed in case multiple locations are available.

In addition to competitive compensation, Ciena offers students access to the Employee Assistance Program (EAP), company-paid holidays, paid sick leave, and vacation pay as required by applicable laws.

Not ready to apply? Join our Talent Community to get relevant job alerts straight to your inbox.

At Ciena, we are committed to building and fostering an environment in which our employees feel respected, valued, and heard. Ciena values the diversity of its workforce and respects its employees as individuals. We do not tolerate any form of discrimination.

Ciena is an Equal Opportunity Employer, including disability and protected veteran status.

If contacted in relation to a job opportunity, please advise Ciena of any accommodation measures you may require.

Job Requirements

The Must Haves:

- Electrical or computer engineering, computer science or other applicable scientific degree at the BEng/BSc, MEng/MSc, or Ph.D level.
- Experience leading designs in advanced BiCMOS and/or CMOS technology with a minimum of 2-years industrial experience is preferred.
- A highly motivated self-starter, able to work independently, while being a great teammate
- Ability to methodically address technical problems
- Excellent organization, written and oral (English) interpersonal skills
- Proficiency with use of applicable design tools from MathWorks, Cadence, Mentor and Synopsys for analog design (eg Matlab, Virtuoso, Calibre, STAR-RC, MMSIM).

Assets:

- Experience with 2.5D or 3D E-M tools such as HFSS or EMX
- Experience with mixed-signal design validation using state-of the art probing and test equipment

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link:

https://ciena.wd5.myworkdayjobs.com/en-US/Careers/job/Ottawa/Analog-Design-Co-op_R029226?source=LinkedIn+Job+Advertisement

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website. Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received. Students should submit their applications as soon as they are ready.