

Job Posting:175568 - Position: W26 Product Engineer Intern 175568B

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| Co-op Work Term Posted: | 2026 - Winter |
| App Deadline | 11/27/2025 09:00 AM |
| Application Method: | Through Employer Website |
| Posting Goes Live: | 11/20/2025 11:46 AM |
| Job Posting Status: | Approved |

ORGANIZATION INFORMATION

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| Organization | Tenstorrent Inc. |
| City | Santa Clara |
| Province / State | CA |

JOB POSTING INFORMATION

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| Placement Term | 2026 - Winter |
| Job Title | W26 Product Engineer Intern 175568B |
| Position Type | Co-op Position |
| Job Location | Toronto, ON |
| Country | Canada |
| Duration | 12 or 16 months |
| Work Mode | Hybrid |
| Salary Currency | CAD |
| Salary | Salary Not Available, 0 Major List |
| Job Description | |

Tenstorrent is leading the industry on cutting-edge AI technology, revolutionizing performance expectations, ease of use, and cost efficiency. With AI redefining the computing paradigm, solutions must evolve to unify innovations in software models, compilers, platforms, networking, and semiconductors. Our diverse team of technologists have developed a high performance RISC-V CPU from scratch, and share a passion for AI and a deep desire to build the best AI platform possible. We value collaboration, curiosity, and a commitment to solving hard problems. We are growing our team and looking for contributors of all seniorities.

As part of our engineering team, you'll gain hands-on experience across firmware, system software, hardware, and validation while working on real-world problems in next-generation computing. Interns are paired with mentors and encouraged to explore multiple disciplines as they grow their technical skill set.

This role is hybrid, based out of Toronto, ON

Who You Are

- Passionate about computer architecture, ASIC design, and system-level thinking.
- Comfortable coding in Python, C, or C++, with strong debugging and scripting skills.
- Interested in silicon and system validation of chiplet-based designs.
- A clear communicator with analytical thinking and a strong desire to learn quickly.

What You Will Learn

- Learn how low-level software interacts with advanced AI and CPU hardware.
- Gain experience in silicon bring-up, testing, and performance tuning.
- Develop automation techniques for post-silicon verification and debugging.
- Collaborate with cross-functional teams across firmware, hardware, and software.

Job Requirements

What We Need

- Support development and maintenance of firmware for next-gen AI hardware.
- Contribute to performance and power validation for Die-to-Die PHY used in chiplets and systems.
- Assist with pre- and post-silicon bring-up and system debug.
- Generate silicon validation and performance reports for chiplet-based products.

Citizenship Requirement

N/A

APPLICATION INFORMATION**Application Procedure** Through Employer Website**Special Application Instructions**

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.

Application Link: <https://job-boards.greenhouse.io/tenstorrentuniversity/jobs/4985811007>

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.

Students should submit their applications as soon as they are ready.