

Job Posting: 176535 - Position: S26 DCS Firmware Development Engineer Co-op 176535

Co-op Work Term Posted:	2026 - Summer
App Deadline	01/14/2026 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	01/06/2026 03:47 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Microchip Technology Inc.
Address Line 1	8555 Baxter Pl
Address Line 2	105
City	Burnaby
Postal Code / Zip Code	V5A 4V7
Province / State	BC
Country	Canada

JOB POSTING INFORMATION

Placement Term	2026 - Summer
 Job Title 	S26 DCS Firmware Development Engineer Co-op 176535
Position Type	Co-op Position
Job Location	Burnaby, BC
Country	Canada
Duration	4 months
Work Mode	In-Person
Salary Currency	CAD
Salary	Salary Not Available, 40 Major List
Salary Range \$	22.75-34.5
Job Description	

Company Description

Microchip Technology Inc. is a leading provider of smart, connected and secure embedded control solutions. Its easy-to-use development tools and comprehensive product portfolio enable customers to create optimal designs which reduce risk while lowering total system cost and time to market. The company's solutions serve more than 120,000 customers across the industrial, automotive, consumer, aerospace and defense, communications, and computing markets. Headquartered in Chandler, Arizona, Microchip offers outstanding technical support along with dependable delivery and quality. For more information, visit the Microchip website at www.microchip.com.

The candidate will join the rapidly growing Data Center Solutions (DCS) business unit at Microchip. DCS has a broad portfolio of products being broadly deployed today by the industry's cutting-edge server/storage OEMs and hyperscale datacenters.

Customers deploy DCS solutions into applications ranging from Big Data capacity storage to artificial intelligence and machine learning that are helping to shape the next digital age. Our product portfolio includes SAS/PCIe/NVMe/CXL products that connect, manage and secure the world's information, including Flash Controllers, High Performance Switches, RAID Controllers and Memory Controllers. Join a team where you can expand your skill set and drive key elements of the industry's technology leadership.

Job Description

We are looking for a self-motivated and energetic co-op student to join our firmware and software team. The successful candidate will work on the design, development, unit test and verification of our best-in-class Switchtec™ product - a System-on-Chip (SoC) PCI Express (PCIe) switching product. You will collaborate with other firmware and software team members in our agile workflow to deliver successful product developments. Our focus is on technical excellence, teamwork, collaboration, and continuous improvements in an open and encouraging

Responsibilities:

- Assist in designing, developing, and testing new and existing firmware
- Develop scripts and software tools to support product debugging, unit testing, diagnostics, verification, and firmware code validation
- Contribute to designing and executing feature unit tests; help investigate and fix issues identified during testing
- Draft and improve technical engineering documentation and customer user guides
- Use tools such as protocol analyzers, oscilloscopes, probes, firmware utilities, and logging systems to support troubleshooting activities

Job Requirements**Qualifications:**

- Currently pursuing an undergraduate or graduate degree in Computer Science, Software Engineering, or Computer Engineering
- Strong experience in C and Python programming
- Strong interest and experience in firmware/software debugging and problem solving
- Previous co-op, internship, part-time work, or volunteer technical experience is an asset
- Strong analytical and documentation skills, with a willingness to learn and apply hands-on skills
- Good communication, interpersonal, and teamwork abilities

Value-add Qualifications:

- Exposure to embedded firmware or Linux software/kernel development through courses or projects
- Familiarity with communication protocols such as USB, PCIe, Ethernet, or TCP/IP
- Experience using Git for version control

Citizenship Requirement N/A**Position Start Date** May 04, 2026 12:00 AM**Position End Date** August 28, 2026 12:00 AM**APPLICATION INFORMATION****Application Procedure** Through UBC Science Co-op**Cover Letter Required?** Yes**Address Cover Letter to** Henry Liu