Job Posting:175439 - Position: W26 Machine Learning for Physical Design Intern - CPU/AI Hardware 175439B

Co-op Work Term Posted:	2026 - Winter
App Deadline	11/24/2025 09:00 AM
Application Method:	Through Employer Website
Posting Goes Live:	11/17/2025 01:39 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Tenstorrent Inc.
City	Santa Clara
Province / State	CA

JOB POSTING INFORMATION

Placement Term	2026 - Winter
<b> Job Title <b>	W26 Machine Learning for Physical Design Intern - CPU/AI Hardware 175439B
Position Type	Co-op Position
Job Location	Santa Clara, CA
Country	USA
Duration	4 or 8 months
Work Mode	Hybrid
Salary Currency	CAD
Salary	Salary Not Available, 0 Major List

Job Description

Tenstorrent is leading the industry on cutting-edge AI technology, revolutionizing performance expectations, ease of use, and cost efficiency. With AI redefining the computing paradigm, solutions must evolve to unify innovations in software models, compilers, platforms, networking, and semiconductors. Our diverse team of technologists have developed a high performance RISC-V CPU from scratch, and share a passion for AI and a deep desire to build the best AI platform possible. We value collaboration, curiosity, and a commitment to solving hard problems. We are growing our team and looking for contributors of all seniorities.
As an intern in the Physical Design (PD) team, you will work on high-performance designs going into industry leading AI/ML architectures. The student coming into this role will develop ML-based tools and flows to improve the PPA (Performance Power Area) and turnaround time for all aspects of chip implementation from synthesis to tapeout for various IPs. The work is done collaboratively with a group of highly experienced engineers across various domains of the ASIC.
This role is on-site, hybrid, based out of Santa Clara, CA working 4 days in office, 1 day remote.

What You Will Learn

• How to apply ML systems and flows to high-performance designs for industry-leading AI/ML architectures.
•Practical experience in extending existing ML systems and implementing cutting-edge algorithms.
•Gain exposure to core physical design fundamentals, including synthesis, place and route, and timing analysis.
•Experience collaborating with a group of highly experienced engineers across various domains of the ASIC implementation.
Compensation for all interns at Tenstorrent ranges from $50/hr - $70/hr including base and variable compensation targets. Experience, skills, education, background and location all impact the actual offer made.
Tenstorrent offers a highly competitive compensation package and benefits, and we are an equal opportunity employer.

Due to U.S. Export Control laws and regulations, Tenstorrent is required to ensure compliance with licensing regulations when

transferring technology to nationals of certain countries that have been licensing conditions set by the U.S. government.

Our engineering positions and certain engineering support positions require access to information, systems, or technologies that are subject to?U.S. Export Control laws and regulations, please note that citizenship/permanent residency, asylee and refugee information?and/or documentation will be required and considered as Tenstorrent moves through the employment process.

If a U.S. export license is required, employment will not begin until a license with acceptable conditions is granted by the U.S. government. If a U.S. export license with acceptable conditions is not granted by the U.S. government, then the offer of employment will be rescinded.
This offer of employment is contingent upon the applicant being eligible to access U.S. export-controlled technology. Due to U.S. export laws, including those codified in the U.S. Export Administration Regulations (EAR), the Company is required to ensure compliance with these laws when transferring technology to nationals of certain countries (such as EAR Country Groups D:1, E1, and E2). These requirements apply to persons located in the U.S. and all countries outside the U.S. As the position offered will have direct and/or indirect access to information, systems, or technologies subject to these laws, the offer may be contingent upon your citizenship/permanent residency status or ability to obtain prior license approval from the U.S. Commerce Department or applicable federal agency. If employment is not possible due to U.S. export laws, any offer of employment will be rescinded.

Job Requirements

Who You Are

• Currently pursuing a BS/MS/PhD in EE/ECE/CE/CS.
•Possessing deep knowledge of math, probability, statistics, and algorithms.
•Experienced in solving problems with Machine Learning models.
•Skilled in algorithms, data structures, and software development using Python and C/C++.

What We Need

• Ability to develop ML-based tools to improve PPA and turnaround time for chip implementation.
•Work closely with other PD engineers to develop ML tools for areas such as synthesis, PnR, timing closure, and power grid analysis.
•Responsibility for selecting appropriate datasets, data representation methods, and implementing new algorithms.
•Capability to run machine learning tests, perform statistical analysis, and fine-tune models using test results.

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website
Cover Letter Required? Optional
Special Application Instructions

Application Link:

https://job-boards.greenhouse.io/tenstorrentuniversity/jobs/4968215007
Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.
Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.
Students should submit their applications as soon as they are ready.