

Job Posting:169968 - Position: F25 Embedded Software Engineer - Co-op (Fall 2025 - 4-16 Months)

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| Co-op Work Term Posted: | 2025 - Fall |
| App Deadline | 06/06/2025 09:00 AM |
| Application Method: | Through Employer Website |
| Posting Goes Live: | 05/23/2025 10:44 AM |
| Job Posting Status: | Approved |

ORGANIZATION INFORMATION

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|---------------------|--------------|
| Organization | Ciena Canada |
| Country | Canada |

JOB POSTING INFORMATION

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|---------------------------------------|--|
| Placement Term | 2025 - Fall |
| Job Title | F25 Embedded Software Engineer - Co-op (Fall 2025 - 4-16 Months) |
| Position Type | Co-op Position |
| Job Location | Ottawa, ON |
| Country | Canada |
| Duration | 4 or 8 months |
| Salary Currency | CAD |
| Salary | Salary Not Available, 0 Major List |
| Salary Range \$ | \$24.50 - \$33.00 |
| Job Description | |

Embedded Software Engineer - Co-op (Fall 2025 - 4-16 Months)

R028004

Ciena is committed to our people-first philosophy. Our teams enjoy a culture focused on prioritizing a personalized and flexible work environment that empowers an individual's passions, growth, wellbeing and belonging. We're a technology company that leads with our humanity-driving our business priorities alongside meaningful social, community, and societal impact.

The Opportunity:

Work term of 4, 8, 12 or 16 months starting in September 2025

How You Will Contribute:

We design, implement, and maintain real-time embedded platform and application software for several of Ciena's flagship products. Functional areas in our scope range from low level bit flipping drivers to high level software state machines for interfacing with external applications:

- Linux and VxWorks kernels and base software components
- Drivers for proprietary and 3rd party ASICs, FPGAs, and other hardware components.
- Hardware/software interface to provide bridging of various abstraction layers.
- Fault and alarms subsystems to detect, report and maintain defects and manage their hysteresis and co-relations.
- System applications such as traffic/equipment protection, performance monitoring, etc.
- SDKs that abstract the underlying low-level software and hardware to facilitate platform application developments and integration.

Pay Range:

The hourly pay range for this position is \$24.50 - \$33.00

Job Requirements

Must Haves:

- Enrolled in a bachelor's or master's degree in computer or electrical engineering, computer science, or similar disciplines
- Experience with C or C++

Assets:

- Developing software for embedded systems or telecom products
- Understanding of CPU hardware architectures, hardware functional specifications, hardware timing diagrams, device data sheets
- Understanding of real-time operating systems and embedded systems
- Software development tools such as git, BitBucket, Gerrit, Jira, etc.
- Unit test frameworks such as Google Test, JUnit, Robot, PyTest, and the like
- Debuggers and tools (e.g., gdb), especially for troubleshooting embedded systems in user space and kernel space
- High-level programming and scripting languages such as Java, Python, Perl, Tcl, bash
- YANG modeling language and toolchains
- Software development methodologies such as Agile or Waterfall
- Familiarity with datacom and telecom concepts such as DWDM, SONET/SDH, OTN, Ethernet, OSI Model, TCP/IP, MPLS

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link:

https://ciena.wd5.myworkdayjobs.com/en-US/Careers/job/Ottawa/Embedded-Software-Engineer---Co-op--Fall-2025---4-16-Months-_R028004?workerSubType=ac99acc6fb6b108fe66503995497ae7c

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.

Students should submit their applications as soon as they are ready.