Job Posting:168568 - Position: F25 Embedded Software Engineering Intern 168568

Co-op Work Term Posted:	2025 - Fall
App Deadline	05/30/2025 09:00 AM
Application Method:	Through UBC Science Co-op
Posting Goes Live:	04/11/2025 03:21 PM
Job Posting Status:	Approved

ORGANIZATION INFORMATION

Organization	Kepler Communications Inc.
Address Line 1	355 ADELAIDE ST W, SUITE 500
City	Toronto
Postal Code / Zip Code	M5V 1S2
Province / State	ON
Country	Canada

JOB POSTING INFORMATION

Placement Term	2025 - Fall
<b> Job Title <b>	F25 Embedded Software Engineering Intern 168568
Position Type	Co-op Position
Job Location	Toronto, ON
Country	Canada
Duration	4 or 8 months
Work Mode	Fully Remote
	In-Person
Salary Currency	CAD
Salary	Salary Not Available, 0 Major List

Job Description

Job Title: Embedded Software Engineering Intern (September 2025)

Kepler is on an audacious mission to deliver Internet connectivity to space, creating the infrastructure to support the rapidly increasing data needs of the space economy. With 23 satellites launched to date and our optical constellation on the horizon, our ambition is to improve access to space-generated data, whether in LEO, MEO, GEO, or beyond! Kepler is hard at work innovating and continuing to grow and expand our most important asset - the Team!

We invest heavily to deliver the best products to our customers, and so we're on the hunt for a top-tier **Embedded Software Engineering Intern** who will have the opportunity to work on a variety of tasks, including satellite on-board software, payload software, IoT modules and ground equipment.

Position Responsibilities:

• Design and develop reliable, high quality software related to: on board software for our next generation of satellites; Embedded Linux for both space and ground systems; Firmware and drivers; IoT modules

•Writing thorough tests and documentation

•Participating in software design review and code review processes

•Work with electrical, digital design, and operations teams to identify required features and solidify the system architecture

Kepler Communications is committed to fostering an inclusive, accessible environment, where all employees and customers feel

valued, respected and supported. We welcome applications from: Women, Aboriginal persons, persons with disabilities, ethnic minorities, visible minorities, people who identify as LGBTQ+ and others who may contribute to diversification in our workplace
As part of our commitment to accessibility for all persons with disabilities, Kepler will, upon the request of the applicant, provide accommodation during the recruitment process to ensure equal access to applicants with disabilities. Please contact our People & Culture team, through our Career Page to make your accommodation needs known and we will consult with you to ensure suitable accommodation is provided.

Job Requirements

Position Requirements:

• 2+ years of embedded systems experience
•Currently enrolled in a Computer Science or equivalent degree program
•Strong software design and development skills, including algorithms, testing and debugging
•Proficiency in C/C++, especially in embedded topics such as interrupt handlers, concurrency, memory mapped peripherals, and hardware interfaces (SPI, I2C, CAN)
•Experience with embedded operating systems and build systems, including OpenEmbedded/Yocto and FreeRTOS
•Embedded Linux experience including writing kernel modules and drivers
•Excellent communication skills
•Ability to effectively use git
•Currently enrolled in a post-secondary program and returning to studies after the internship
•Available for a full-time, 4, 8, 12, or 16 month internship beginning September 2025
•Opportunity to work out of our new Toronto office! This position can be based onsite at our Toronto office (24 Ward Street), hybrid, or remote, depending on the candidate's location.

Bonus Points:

• Relevant experience with hobbies or University design team
•Python development experience
•Strong knowledge of computer networking and modern networking protocols
•Experience with RTL (Verilog and/or VHDL) and embedded SoCs
•Hardware debugging experience (oscilloscopes, logic analyzers, etc.)

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through UBC Science Co-op
Cover Letter Required? Optional
Special Application Instructions

Application Link:

https://jobs.lever.co/kepler/9199ebb4-896d-4b88-95bd-2d1d1af40fee

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website. Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received. Students should submit their applications as soon as they are ready.