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The diagram illustrates the pin connections for the LT7688 Reset Graphics Engine. The chip is shown with pins 1 through 40. The connections are as follows:

- Pin 1:** XO
- Pin 2:** +3V3
- Pin 3:** VDD_CAP2
- Pin 4:** RESET
- Pin 5:** RST1#
- Pin 6:** PWM12
- Pin 7:** PWM13
- Pin 8:** 32K_XO
- Pin 9:** 32K_XI
- Pin 10:** VDD-1-3
- Pin 11:** VDD-1-2
- Pin 12:** AIN3/INT03
- Pin 13:** SCL
- Pin 14:** SDA
- Pin 15:** INT11
- Pin 16:** VDD-1-4
- Pin 17:** DM
- Pin 18:** DP
- Pin 19:** GND
- Pin 20:** GND
- Pin 21:** INT02
- Pin 22:** INT17
- Pin 23:** TXD
- Pin 24:** RXD
- Pin 25:** RST2#
- Pin 26:** PWM0
- Pin 27:** PWM0L
- Pin 28:** INT14
- Pin 29:** VDD-1-1
- Pin 30:** AN17/INT07
- Pin 31:** INT#
- Pin 32:** SFC50#
- Pin 33:** SFC51#
- Pin 34:** SCLK
- Pin 35:** SDO
- Pin 36:** SFDI
- Pin 37:** VDD-2-5
- Pin 38:** VDD-2-4
- Pin 39:** NC-3
- Pin 40:** VSS-4

The diagram also shows the connection of the LT7688 to the SPI Flash and the Reset Graphics Engine (RGE). The SPI Flash is connected to pins 31-34. The Reset Graphics Engine (RGE) is connected to pins 1-10, 11-20, 21-30, and 31-40.