

## Configure 'SCB\_P4'



Name: SPIS\_1

Configuration

SPI Basic

**SPI Advanced**

Built-in



## Buffer size

RX buffer size: 8

TX buffer size: 8

## Interrupt

- ☐ None  
☒ Internal  
☐ External

## Interrupt sources

- |   |   |
|---|---|
| <input type="checkbox"/> SPI done           | <input type="checkbox"/> SPI bus error                |
| <input type="checkbox"/> TX FIFO not full   | <input checked="" type="checkbox"/> RX FIFO not empty |
| <input type="checkbox"/> TX FIFO empty      | <input checked="" type="checkbox"/> RX FIFO full      |
| <input type="checkbox"/> TX FIFO overflow   | <input type="checkbox"/> RX FIFO overflow             |
| <input type="checkbox"/> TX FIFO underflow  | <input type="checkbox"/> RX FIFO underflow            |
| <input type="checkbox"/> TX FIFO trigger: 0 | <input type="checkbox"/> RX FIFO trigger: 7           |

Datasheet

OK

Apply

Cancel