

## Configure 'ADC\_SAR\_SEQ\_P4'



Name: ADC\_SAR\_Seq\_0

## General

## Channels

## Built-in



## Timing

☒ Sample rate (SPS): 166666☐ Clock frequency (kHz): 2999.988

Actual sample rate (SPS): 166666

## Clock source

☒ Internal☐ External

## Sample mode

☒ Free running☐ Hardware trigger

## Input range

Vref select: VDDA

Vref value (V): 3.300

Input buffer gain: Disable

Single ended negative input: Vss

Differential mode range: Vn +/- Vdda (3.3 V)

Single ended mode range: 0.0 to Vref (3.3 V)

## Result data format

Differential result format: Signed

Single ended result format: Signed

Data format justification: Right

Samples averaged: 2

Alternate resolution (bits): 8

Averaging mode: Fixed Resolution

## Interrupt limits

Datasheet

OK

Apply

Cancel