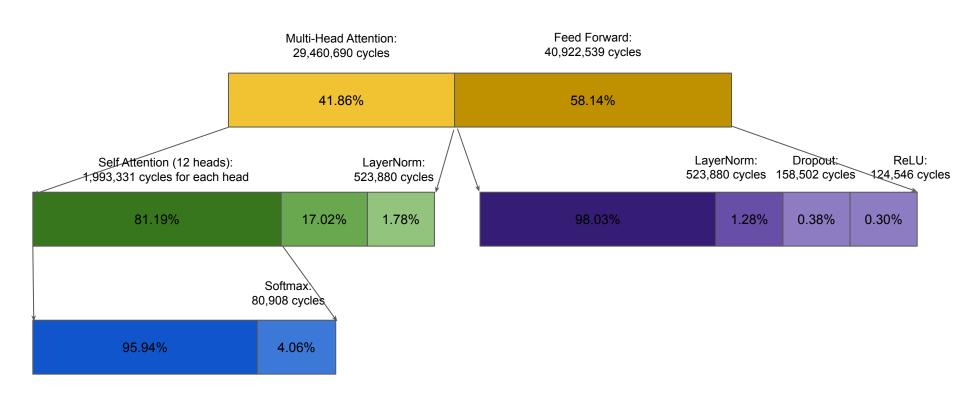
## Week 9

## **Transformer Performance**



## **SP-FLOP & Utilization**

kernel	execution cycles	utilization	performance (SP-FLOP/cy cle)
layernorm	523880	9.39%	0.844651
softmax	80908	44.30%	4.606912
self-attention	1993331	64.34%	10.1879
multihead-att ention	29460690	68.47%	10.8529
feed_forward	40922539	92.56%	14.78552
dropout	158502	15.51%	1.240413
relu	124546	19.73%	1.578597

## **New MatMul**

Example: A \* B + bias + C, A: D1xD2, B: D2xD3, bias:D3x1, C: D1xD3

 In the innermost loop, if it is the last calculation, the bias and C are loaded while the MAC is executed

kernel	execution cycles	utilization	performance (SP-FLOP/cy cle)
MatMul	598694	65.68%	10.508634
MatMul-biase d	608641	64.69%	10.343621
MatMul-biase d-MatAdd	610002	64.63%	10.327258

64x768x64, lower performance than 64x64x64 (~15 FLOP/Cycle, 80%)