

MASSACHUSETTS INSTITUTE OF TECHNOLOGY
Department of Electrical Engineering and Computer Science
6.301 Solid State Circuits

Fall 2013
Problem Set 2

Issued : Sept 10, 2013
Due : Sept 17, 2013

Problem 1: Saturation

Determine the DC Operating voltage V_1 at which Q_1 saturates and M_1 enters the triode regime. Your answer may be in terms of transistor parameters (I_S , $V_{CE,sat}$, β_0 , etc).

Problem 2: Small-signal Parameters

Calculate the small-signal transistor parameters g_m , r_π , and r_o in the following circuit. Assume $V_A = 60v$ and $\beta_0 = 200$.

- (a) $I_1 = .5\mu A$
- (b) $I_1 = 50\mu A$
- (c) Draw the small signal model
- (d) How does r_o affect the output impedance for each bias current?
- (e) hi

Problem 3: Extracting Parameters from Datasheets

Extract r_π , β_0 , r_μ , r_o , c_μ , and c_π from the 2n2222 datasheet posted on stellar.

Problem 4: Finding τ_F

In many cases, datasheets will not provide enough information to extract every transistor parameter. Using the 2n3866 datasheet posted on stellar, determine an upper bound on τ_F . Briefly explain all assumptions that you make and define τ_F and f_T .