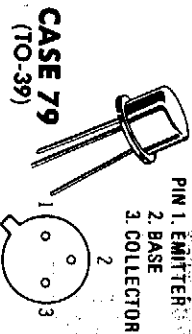


# 2N3866 (SILICON) 2N3866A

STYLE 1



**CASE 79**  
(TO-39)

Collector connected to case

NPN silicon transistor, designed for amplifier, frequency-multiplier, or oscillator applications in military and industrial equipment. Suitable for uses as output, driver, or pre-driver stages in VHF and UHF equipment.

## MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter	V <sub>CEO</sub>	30	Vdc
Collector-Base Voltage	V <sub>CB</sub>	55	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	3.5	Vdc
Collector Current	I <sub>C</sub>	0.4	Amp
Total Device Dissipation @ T <sub>C</sub> = 25°C	P <sub>D</sub>	5.0	Watts
Derate above 25°C		28.6	mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200	°C

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 5.0 mAdc, R <sub>BE</sub> = 10 ohms)	BV <sub>CEB</sub>	55	—	—	Vdc
Collector-Emitter Sustaining Voltage (I <sub>C</sub> = 5.0 mAdc, I <sub>B</sub> = 0)	BV <sub>CEO(sus)</sub>	30	—	—	Vdc
Collector-Base Breakdown Voltage (I <sub>E</sub> = 0, I <sub>C</sub> = 0.1 mAdc)	BV <sub>CBO</sub>	55	—	—	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 0.1 mAdc, I <sub>C</sub> = 0)	BV <sub>EBO</sub>	3.5	—	—	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 28 Vdc, I <sub>B</sub> = 0)	I <sub>CEO</sub>	—	—	20	μA
Collector Cutoff Current (V <sub>CE</sub> = 55 Vdc, V <sub>BE</sub> = 1.5 Vdc)	I <sub>CEx</sub>	—	—	100	μAdc

## ON CHARACTERISTICS

DC Current Gain (I <sub>C</sub> = 0.36 Adc, V <sub>CE</sub> = 5.0 Vdc) (I <sub>C</sub> = 0.05 Adc, V <sub>CE</sub> = 5.0 Vdc) 2N3866 (I <sub>C</sub> = 5.0 mAdc, V <sub>CE</sub> = 5.0 Vdc) 2N3866A	h <sub>FE</sub>	5.0 10 25	—	200 200	—
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 20 mAdc)	V <sub>CE(sat)</sub>	—	—	1.0	Vdc

## DYNAMIC CHARACTERISTICS

Current-Gain – Bandwidth Product (I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 15 Vdc, f = 200 MHz) 2N3866 2N3866A	f <sub>T</sub>	500 800	800	—	MHz
Output Capacitance (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	—	2.0	3.0	pF

## FUNCTIONAL TEST

Power Gain	Test Circuit-Figure 1	G <sub>pe</sub>	10	—	dB
Power Output	P <sub>in</sub> = 0.1 W, V <sub>CE</sub> = 28 Vdc f = 400 MHz, T <sub>C</sub> = 25°C	P <sub>out</sub>	1.0	—	Watts
Collector Efficiency		η	45	—	%

FIGURE 1 — 400 MHz RF AMPLIFIER CIRCUIT FOR POWER-OUTPUT TEST

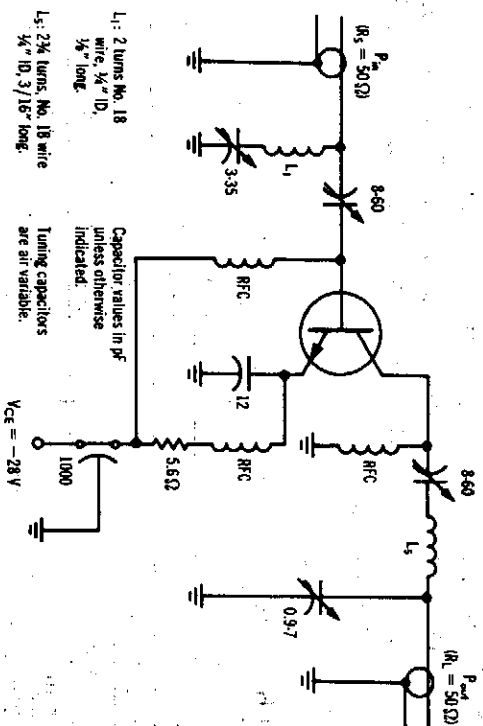


FIGURE 2 — POWER OUTPUT VERSUS FREQUENCY (Class C)

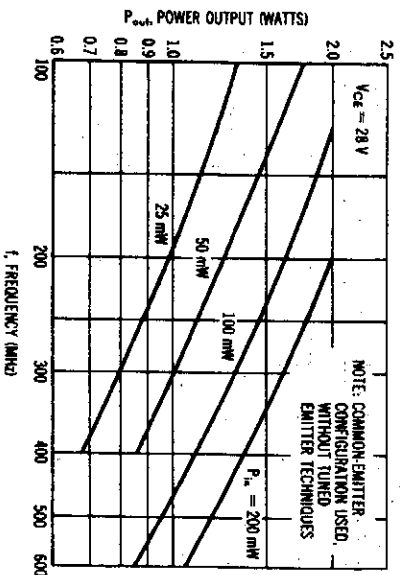


FIGURE 4 — PARALLEL INPUT RESISTANCE VERSUS FREQUENCY (Class C)

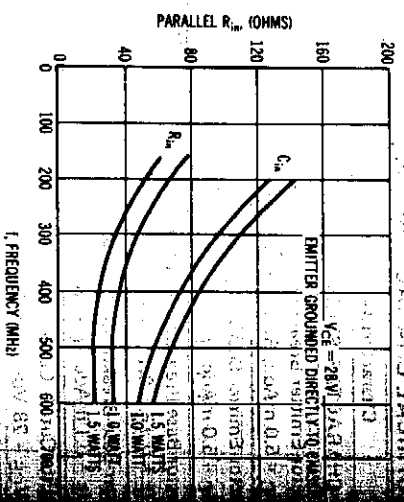


FIGURE 3 — POWER OUTPUT VERSUS POWER INPUT (Class C)

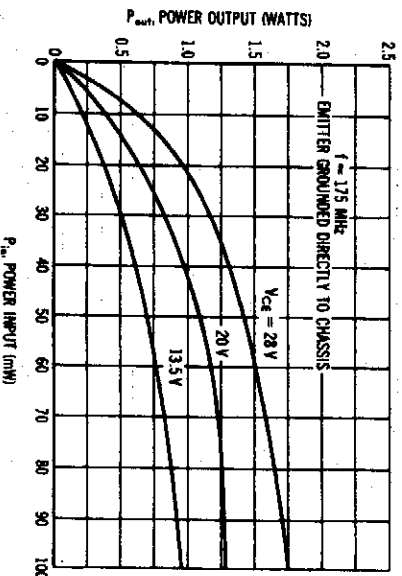


FIGURE 5 — PARALLEL OUTPUT CAPACITANCE VERSUS FREQUENCY (Class C)

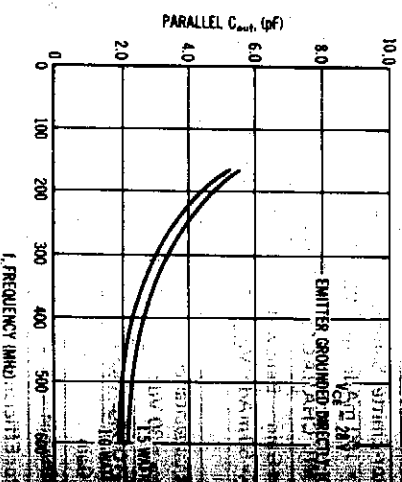


FIGURE 6 — SMALL-SIGNAL CURRENT GAIN  
VERSUS FREQUENCY

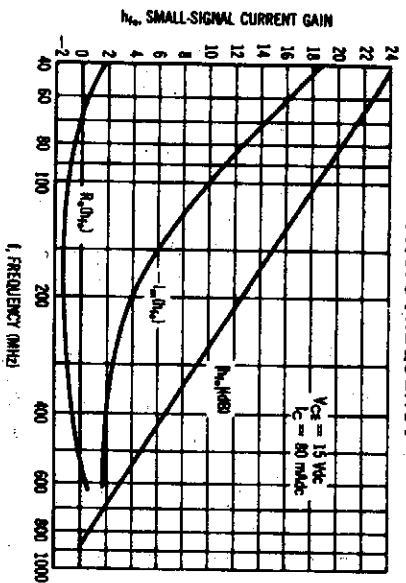


FIGURE 7 — OUTPUT CAPACITANCE  
VERSUS COLLECTOR VOLTAGE

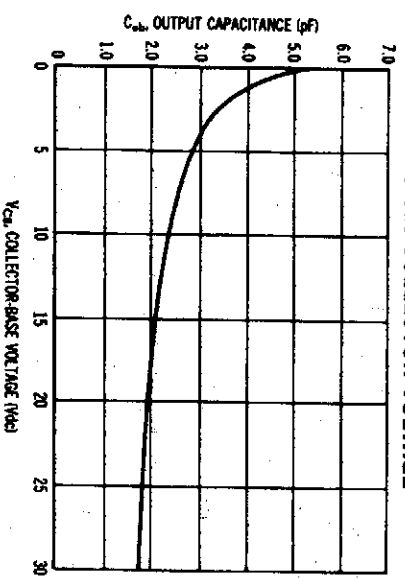


FIGURE 8 —  $f_T$  VERSUS COLLECTOR CURRENT

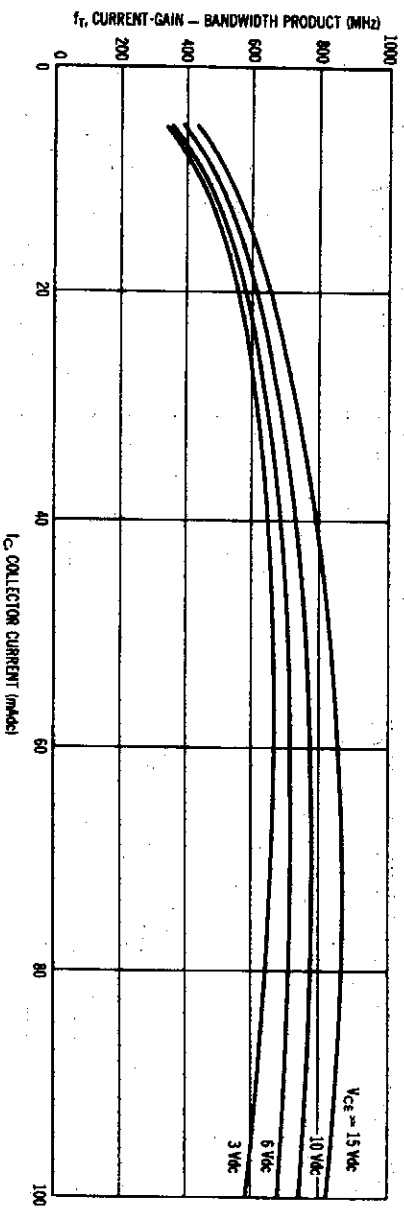


FIGURE 9 —  $r_b' C_c$  VERSUS COLLECTOR CURRENT

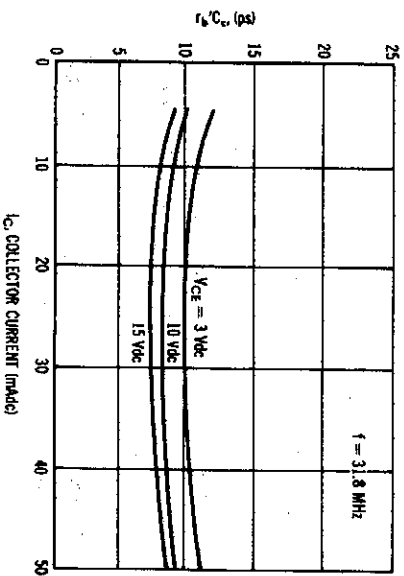


FIGURE 10 — DC CURRENT GAIN  
VERSUS COLLECTOR CURRENT

