Signal	Values
/d_cache/din	10101010 / 10000000 / 10000001 / 10000010 / 10000011 / 10000100
/d_cache/addr	00 / 01 / 02 / 03 / 04
/d_cache/data_write	
/d_cache/reset	
/d_cache/clk	
/d_cache/dout	00000000 / 10000000 / 00000000 / 10000001 / 00000000 / 10000010 / 00000000 / 10000011 / 00000000

0 ns   2 ns   4 ns   6 ns   8 ns   10 ns   12 ns   14 ns   16 ns   18 ns   20 ns

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/d_cache/din	10000100	10000101	10000110	10000111	10001000	10001001
/d_cache/addr	04	05	06	07	08	09

/d_cache/data_write

/d_cache/reset

/d_cache/clk

| /d_cache/dout | 10000100 | 00000000 | 10000101 | 00000000 | 10000110 | 00000000 | 10000111 | 00000000 | 10001000 | 00000000 |

22 ns 24 ns 26 ns 28 ns 30 ns 32 ns 34 ns 36 ns 38 ns 40 ns

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/d_cache/din	10001001	10001010		10001011		10001100		10001101		10001110
/d_cache/addr	09	0A		0B		0C		0D		0E
/d_cache/data_write										
/d_cache/reset										
/d_cache/clk										
/d_cache/dout	10001001	00000000	10001010	00000000	10001011	00000000	10001100	00000000	10001101	00000000

42 ns   44 ns   46 ns   48 ns   50 ns   52 ns   54 ns   56 ns   58 ns   60 ns

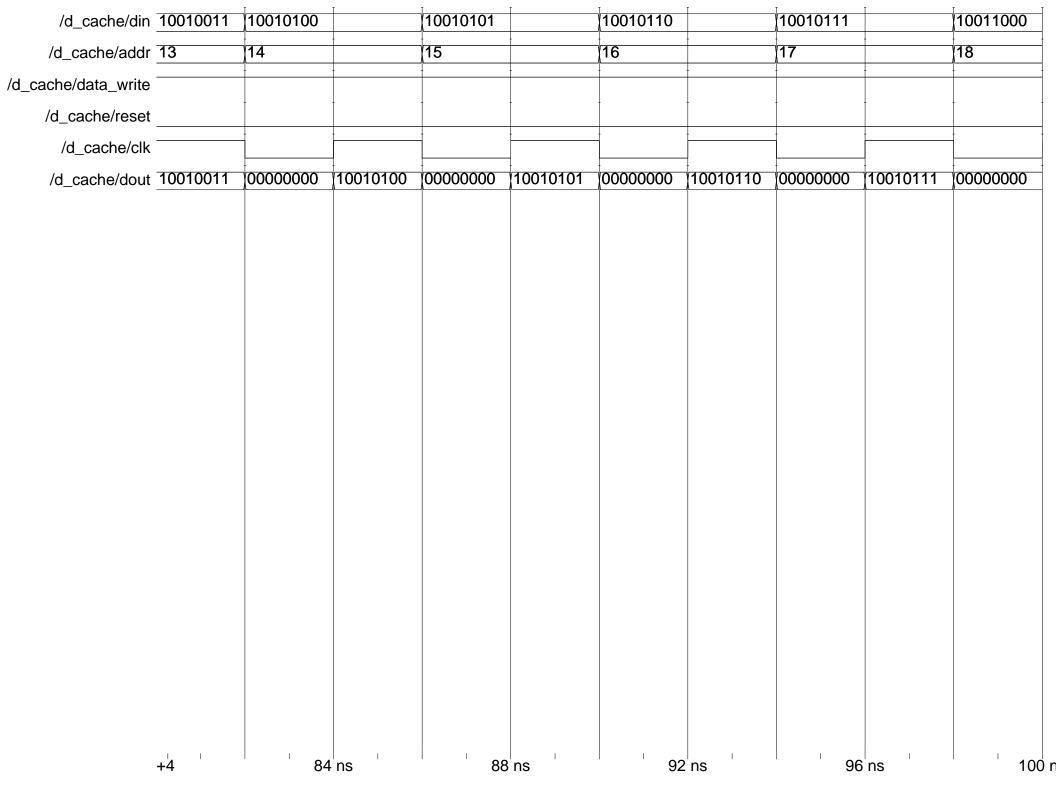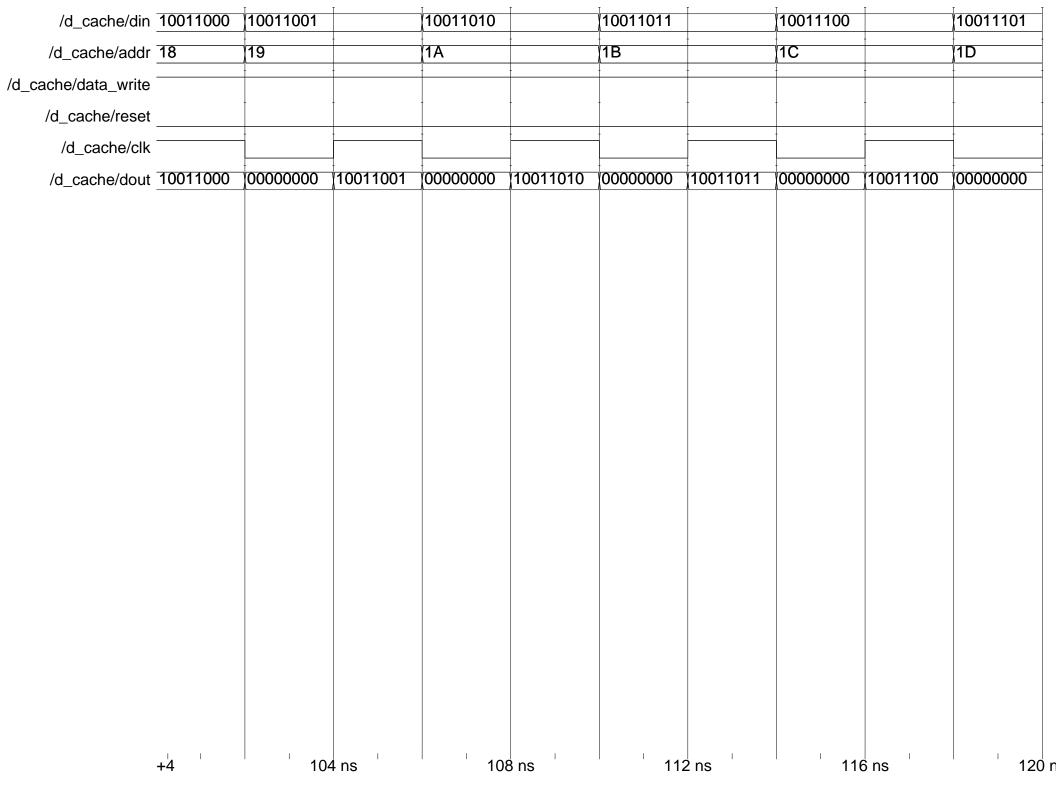Entity:d_cache Architecture:d_cache_arch Date: Tue Nov 23 01:49:34 PM EST 2021   Row: 3 Page: 3

/d_cache/din	10001110	10001111		10010000		10010001		10010010		10010011
/d_cache/addr	0E	0F		10		11		12		13
/d_cache/data_write										
/d_cache/reset										
/d_cache/clk										
/d_cache/dout	10001110	00000000	10001111	00000000	10010000	00000000	10010001	00000000	10010010	00000000

62 ns   64 ns   66 ns   68 ns   70 ns   72 ns   74 ns   76 ns   78 ns   80 ns

/d_cache/din	10010011	10010100		10010101		10010110		10010111		10011000
/d_cache/addr	13	14		15		16		17		18
/d_cache/data_write										
/d_cache/reset										
/d_cache/clk										
/d_cache/dout	10010011	00000000	10010100	00000000	10010101	00000000	10010110	00000000	10010111	00000000

+4   84 ns   88 ns   92 ns   96 ns   100 n

Signal	Values
/d_cache/din	10011000	10011001	10011010	10011011	10011100	10011101
/d_cache/addr	18	19	1A	1B	1C	1D
/d_cache/data_write	
/d_cache/reset	
/d_cache/clk	
/d_cache/dout	10011000	00000000	10011001	00000000	10011010	00000000	10011011	00000000	10011100	00000000

+4   104 ns   108 ns   112 ns   116 ns   120 n

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Signal	Values
/d_cache/din	10011101, 10011110, 10011111
/d_cache/addr	1D, 1E, 1F, 05, 04
/d_cache/data_write	
/d_cache/reset	
/d_cache/clk	
/d_cache/dout	10011101, 00000000, 10011110, 00000000, 10011111, 10000101, 00000000

+4   124 ns   128 ns   132 ns   136 ns   140 n

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