/cpu_board/reset

/cpu_board/clk

/cpu_board/pc_out: 0 1 2 3 4 5 6 3 4 5 6 3 4 5 6 3 4 5 6 3 4 5 7 8 9 10 11 12 13 14 15 0 1 2 3 4 5

/cpu_board/rs_out: 0 5 4 0 5 4 3 0 9 3 2 0 12 2 1 0 14 1 0 15 10 11 0

/cpu_board/rt_out: 0 5 0 4 0 3 0 2 0 1 0 15 0 15 10 11 0

0 ns   20 ns   40 ns   60 ns   80 ns   100 ns   120 ns   140 ns

Entity:cpu_board Architecture:cpu_board_arch Date: Thu Dec 02 03:23:32 PM EST 2021   Row: 1 Page: 1