# eSolid - Real-Time Kernel 1.0BetaR01

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CONTENTS

# **Contents**

1	eSol	lid Real-	Time Kernel	1
	1.1	eSolid	RT Kernel Specification	1
		1.1.1	Source code	1
		1.1.2	Consistent Application Programming Interface	1
		1.1.3	Preemptive multi-threading	2
		1.1.4	Round-Robin scheduling	2
		1.1.5	Deterministic	2
		1.1.6	Configurable	2
		1.1.7	Portable	2
		1.1.8	Static object allocation	2
		1.1.9	Unlimited number of threads	2
		1.1.10	Error checking	2
		1.1.11	Profiling	3
2	Dire	ctory ar	nd file organization	3
_	2.1	_		3
		2.1.1	What is a port?	3
	2.2	Code S	Sections	3
		2.2.1	Port independent code	3
		2.2.2	Port dependent code	4
		2.2.3	Template and example code	4
3	Kerr	nel state	es es	4
	3.1	Intro .		4
	3.2	eSolid	RT Kernel states	5
4	Thre	ead Man	agement	6
	4.1	Intro .		6
		4.1.1	eSolid RT Kernel thread	6
		4.1.2	Thread states	6
5	Criti	cal sect	ions	7
	5.1	Intro .		7
		5.1.1	eSolid RT Kernel internal critical sections	7
	5.2	Implem	nentation	8
		5.2.1	Disabling interrupts	8
		5.2.2	Disabling Kernel scheduler	9
		5.2.3	Using semaphores	9
6	Time	e compl	ovity	9
6	1111116	. combl	CALLY	9

CONTENTS

	6.1	Intro .		9
		6.1.1	Big O notation	10
	6.2	Consta	ant time	10
		6.2.1	eSolid RT Kernel time complexity	10
7	Sche	eduler		10
	7.1		um	10
	7.2		ds List	10
	7.3		ds Queue	10
		7.3.1	Simple Ready Threads Queue	11
		7.3.2	Complex Ready Threads Queue	12
	7.4	_	Threads Queue	12
		ouu,		-
8	Erro	r check	ing	12
	8.1	Intro .		12
9	Mod	ule Doc	cumentation	12
	9.1	Kernel		12
		9.1.1	Detailed Description	13
	9.2	Interfa	00	14
		9.2.1	Detailed Description	17
		9.2.2	Macro Definition Documentation	17
		9.2.3	Typedef Documentation	19
		9.2.4	Enumeration Type Documentation	19
		9.2.5	Function Documentation	19
		9.2.6	Variable Documentation	30
	9.3	Interna	als	31
		9.3.1	Detailed Description	33
		9.3.2	Macro Definition Documentation	33
		9.3.3	Typedef Documentation	35
		9.3.4	Enumeration Type Documentation	35
		9.3.5	Function Documentation	35
		9.3.6	Variable Documentation	37
	9.4	Config	uration	39
		9.4.1	Detailed Description	39
		9.4.2	Macro Definition Documentation	40
	9.5	Port te	mplate	42
		9.5.1	Detailed Description	42
	9.6	Compi	ler port	43
		9.6.1	Detailed Description	44
		9.6.2	Macro Definition Documentation	44

CONTENTS

		9.6.3	Typedef Documentation	45
		9.6.4	Enumeration Type Documentation	46
	9.7	CPU po	ort interface	47
		9.7.1	Detailed Description	50
		9.7.2	Macro Definition Documentation	51
		9.7.3	Typedef Documentation	54
		9.7.4	Function Documentation	55
		9.7.5	Variable Documentation	57
	9.8	CPU po	ort internals	58
		9.8.1	Detailed Description	58
		9.8.2	Function Documentation	58
		9.8.3	Variable Documentation	59
	9.9	CPU po	ort configuration	60
		9.9.1	Detailed Description	60
		9.9.2	Macro Definition Documentation	60
	9.10	Kernel	configuration	61
		9.10.1	Detailed Description	61
		9.10.2	Macro Definition Documentation	62
10	Data	Structu	ure Documentation	64
				64
	10.1			65
		コロコン		65
	10.2			65 65
	10.2	esThd	Struct Reference	65
	10.2	esThd :	Struct Reference	65 67
		esThd : 10.2.1 10.2.2	Struct Reference	65 67 67
		esThd : 10.2.1 10.2.2 esThd0	Struct Reference	65 67 67 68
		esThd : 10.2.1 10.2.2 esThd0 10.3.1	Struct Reference	65 67 67 68 69
	10.3	esThd : 10.2.1	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation	65 67 67 68 69
	10.3	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference	65 67 68 69 69
	10.3	esThd 0.2.1 10.2.2 esThd 0.3.1 10.3.2 esTmr 10.4.1	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Struct Reference  Detailed Description	65 67 68 69 69 70
	10.3	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description  Field Documentation	65 67 68 69 69 70 71
	10.3	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Struct Reference  Struct Reference  Struct Reference  Struct Reference	65 67 68 69 69 70 71
	10.3	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1	Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description	65 67 68 69 69 70 71 71
	10.3 10.4 10.5	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1 10.5.2	Struct Reference  Detailed Description Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Field Documentation  Field Documentation  Field Documentation	65 67 68 69 69 70 71 71 71 72
	10.3 10.4 10.5	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1 10.5.2 portStd	Struct Reference  Detailed Description Field Documentation  O Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference	65 67 68 69 69 71 71 71 72 72
	10.3 10.4 10.5	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1 10.5.2 portStd (10.6.1 10.6.1	Detailed Description Field Documentation  Detailed Description  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  k Struct Reference  Detailed Description	65 67 68 69 69 71 71 71 72 72
	10.3 10.4 10.5	esThd (10.2.1 10.2.2 esThd (10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1 10.5.2 portSto 10.6.1 10.6.2	Struct Reference Detailed Description Field Documentation  Struct Reference Detailed Description Field Documentation  Struct Reference  Detailed Description Field Documentation  Struct Reference  Detailed Description Field Documentation  Struct Reference  Detailed Description Field Documentation  k Struct Reference  Detailed Description  Field Documentation  k Struct Reference  Detailed Description  Field Documentation  k Struct Reference  Detailed Description  Field Documentation	65 67 68 69 69 70 71 71 72 72 72
	10.3 10.4 10.5	esThd (10.2.1 10.2.2 esThd(10.3.1 10.3.2 esTmr 10.4.1 10.4.2 portCtx 10.5.1 10.5.2 portStd (10.6.1 10.6.2 esThd(10.6.2 esT	Detailed Description Field Documentation  Detailed Description  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Documentation  Struct Reference  Detailed Description  Field Documentation  Struct Reference  Detailed Description  Field Documentation  k Struct Reference  Detailed Description	65 67 68 69 69 71 71 71 72 72

97

		10.7.2 Field Documentation	73
	10.8	sysTmr Struct Reference	73
		10.8.1 Detailed Description	74
		10.8.2 Field Documentation	74
	10.9	esThd::thdL Struct Reference	74
		10.9.1 Detailed Description	75
		10.9.2 Field Documentation	75
	10.10	0esTmr::tmrL Struct Reference	76
		10.10.1 Detailed Description	77
		10.10.2 Field Documentation	77
11	Eilo I	Documentation	77
''			
	11.1		77
	44.0	•	79
	11.2		80
		11.2.1 Detailed Description	80
	11.3	cpu.h File Reference	80
		11.3.1 Detailed Description	84
		11.3.2 Variable Documentation	84
	11.4	. – .	85
		11.4.1 Detailed Description	85
		kernel-example.dox File Reference	85
	11.6	kernel.c File Reference	86
		11.6.1 Detailed Description	90
		11.6.2 Variable Documentation	90
	11.7	kernel.dox File Reference	91
	11.8	kernel.h File Reference	91
		11.8.1 Detailed Description	95
	11.9	kernel_cfg.h File Reference	96
		11.9.1 Detailed Description	97
	11.10	0kernel_cfg.h File Reference	97
		11.10.1 Detailed Description	97

# 1 eSolid Real-Time Kernel

Index

# 1.1 eSolid RT Kernel Specification

#### 1.1.1 Source code

The source code of the kernel and all of it's ports are published under **free software license**, which guarantees end users (individuals, organizations, companies) the freedoms to use, study, share (copy), and modify the software.

The GPL grants the recipients of a computer software the rights of the Free Software Definition (written by Richard Stallman) and uses copyleft to ensure the freedoms are preserved whenever the work is distributed, even when the work is changed or added to. The GPL is a copyleft license, which means that derived works can only be distributed under the same license terms.

For more details visit: https://gnu.org/licenses/gpl.html

## 1.1.2 Consistent Application Programming Interface

All objects declared in Application Programming Interface are following these naming rules:

- All objects except macros are using CamelCase style names
- All functions, structures and unions are prefixed with: es
- All typedef-ed types are prefixed with: es and postfixed with: T
- All macro names are in UPPERCASE style, words are delimited by underscore sign
- All macro names are prefixed with: ES\_
- All Global variables are prefixed with: q

All API objects are named following this convention: es<group><action><suffix>()

- · Group:
  - Kern General Kernel services
  - Thd Thread management
  - ThdQ Thread Queue management
  - Sched Scheduler invocation
  - SchedRdy Scheduler Ready Thread Queue management
- Suffix:
  - none normal API object
  - I I class Regular Interrupts are locked

All Port Interface objects are named using the rules stated above with certain differences:

- All functions, structures and unions are prefixed with: port
- All macro names are prefixed with: PORT\_

## 1.1.3 Preemptive multi-threading

eSolid RT Kernel uses a **preemptive scheduler**, which has the power to preempt, or interrupt, and later resume, other threads in the system. The scheduler always runs ready thread with the highest priority.

#### 1.1.4 Round-Robin scheduling

Round-Robin scheduling is very **simple algorithm** to implement and it is **starvation free**. It employs time-sharing, giving to each thread a time slice or quantum. Processor's time is shared between a number of threads, giving the illusion that it is dealing with these threads **concurrently**. This scheduling is only used when there are two or more threads of the same priority ready for execution.

#### 1.1.5 Deterministic

All algorithms used in eSolid RT Kernel implementation are belonging to **Constant Time Complexity** category. Constant Time O(1) functions needs fixed amount of time to execute an algorithm. Their execution time does not depend on number of inputs. For more information see Time complexity.

#### 1.1.6 Configurable

The kernel provides two configuration files kernel\_cfg.h and cpu\_cfg.h which can be used to tailor the kernel to application needs.

In addition, the kernel implements a number of hooks which can alter or augment the behavior of the kernel or applications, by intercepting function calls between software components.

## 1.1.7 Portable

During the design stage of the kernel a special attention was given to achieve high portability of the kernel. Some data types and algorithms are tailored to exploit new hardware features.

#### 1.1.8 Static object allocation

All objects used in eSolid RT Kernel can be statically allocated. There is no need to use any memory management functionality which makes it very easy to verify the application.

#### 1.1.9 Unlimited number of threads

eSolid RT Kernel allows applications to have any number of threads. The only limiting factors for the maximum number of threads are the amount of RAM and ROM memory capacity and required processing time.

## 1.1.10 Error checking

All eSolid software is using design methods very similar to approaches of **contract programming** paradigm for software design. The contract programming prescribes that Application Programming Interface should have formal, precise and verifiable specifications, which extend the ordinary definition of abstract data types with preconditions and postconditions. These specifications are referred to as "contracts". The contract for each method will normally contain the following pieces of information:

- · Acceptable and unacceptable input values
- · Return values and their meanings
- · Error and exception condition values that can occur during the execution
- · Side effects
- Preconditions
- Postconditions
- Invariants

The contract validations are done by **assert** macros. They have the responsibility of informing the programmer when a contract can not be validated.

#### 1.1.11 Profiling

Note

This feature is not implemented

## 2 Directory and file organization

Details about directory and file organization

#### 2.1 Intro

The directory structure of eSolid RT Kernel is fairly easy to understand. Once the organization of directories and files is understood it is fairly easy to integrate eSolid RT Kernel into application.

#### 2.1.1 What is a port?

Porting is a process of adapting software to an architecture that is different from the one for which it was originally designed. The term is also used when software is changed to make it usable in different environments. Software is portable when the cost of porting it to a new platform is less than the cost of writing it from beginning.

#### 2.2 Code Sections

The kernel is divided into three sections. One section is port independent code, the second one is port dependent code and the third sections is code templates.

## 2.2.1 Port independent code

Port independent code is the code which does not change from port to port, e.g. when the CPU is changed this code is not changed at all and it is still correctly executed. Code can be developed and tested on another machine, which greatly reduces design efforts. It provides API and some common data structures. Port independent code lives under /inc and /src directories:

- inc/kernel.h
- inc/kernel\_cfg.h
- src/kernel.c

Click on file name for further description of the file.

## 2.2.2 Port dependent code

Second section is the port dependent code. This code provides low-level functions which are needed to interact with interrupt controllers, manipulate CPU settings and do the context switching. They are highly CPU/compiler bounded and are often written in assembly language.

Each port has it's name which is also the name of directory which holds all the port files. Usually each port has some kind of variant. In that case each variant is a subdirectory of the containing port. Common code for all variants will be in common subdirectory. Each eSolid RT Kernel port will have at least the following files:

- port/[port\_name]/common/compiler.h
- port/[port\_name]/[variant\_name]/cpu\_cfq.h

3 Kernel states 5

```
• port/[port_name]/[variant_name]/cpu.h
```

```
• port/[port_name]/[variant_name]/cpu.c
```

Note

Port dependent code is separately described in documentation for relevant port.

## 2.2.3 Template and example code

Templates are some predefined configuration settings for various scenarios where eSolid RT Kernel can be used. Templates also contain some example code for how to write new ports.

In the example below is given <code>Generic</code> template which holds files with default configuration settings and some example code for new ports. New port files are in template/generic/port directory. When porting to a new architecture/compiler use provided template files for starters. This will greatly reduce the time needed to become familiar with the kernel port requirements. Generic template files are the following:

- template/generic/port/compiler.h
- template/generic/port/cpu\_cfg.h
- template/generic/port/cpu.h
- template/generic/port/cpu.c
- template/generic/kernel\_cfg.h

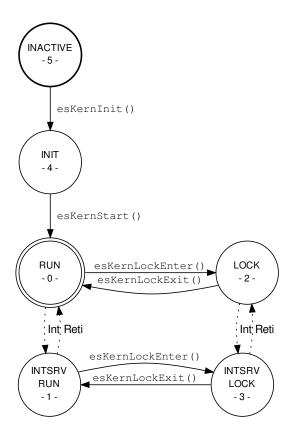
## 3 Kernel states

Details about kernel states

## 3.1 Intro

A Kernel state machine is a behavior model of the kernel core. Each state defines which functions can be called and what methods are allowed.

## 3.2 eSolid RT Kernel states



## **INACTIVE**

Inactive state of the kernel - Level 5

This state is entered after a physical reset. When the system is in this state all the maskable interrupt sources are disabled. In this state none of kernel internal data structures are initialized. In this state it is not possible to use any Kernel API except esKernInit().

INIT

Initialization state of the kernel - Level 4

In this state all internal data structures are initialized but the kernel is still not running. In this stage new threads can be created by calling esThdInit() function. Also, the application is allowed to use API which is used to create kernel structures like Thread Queues esThdQ. All the maskable interrupt sources are DISABLED.

RUN

Normal, running state of the kernel - Level 0

To start multi-threading just call the esKernStart() function. This function will switch the kernel into RUN state and multi-threading of created threads will commence. During the RUN state you are allowed to create other task as well. All the interrupt sources are enabled and the system APIs are accessible, threads are running. All the maskable interrupt sources are ENABLED.

**LOCK** 

Scheduler locked state, no context switching - Level 2

4 Thread Management 7

The running state of the kernel can be switched to LOCK state where the scheduler is locked and no context switching is allowed. LOCK state is one way of preventing the access to a shared resource. One more reason to lock the scheduler would be during the accessing of special hardware (like programming the FLASH memory) which does not allow interruption of the running operation. Usage of scheduler locks should be kept at minimum. All the maskable interrupt sources are ENABLED.

## INTSRV

Interrupt Service state, no context switching - Levels 1 and 3

During the both states RUN and LOCK, an interrupt event can occur. When Interrupt Service Routine is executing the kernel is in INTSRV\_\* state. Each INTSRV state corresponds to the state where the execution was interrupted from and the kernel will return to it's original state.

Note

The level of state INACTIVE is the highest. As the kernel boots up the level is decremented. The running state is level 0.

## 4 Thread Management

Introduction to threads and how to use them

## 4.1 Intro

A thread, also called a thread of execution is the smallest sequence of program instructions that can be managed by an operating system scheduler. Multi-threading is usually implemented by time-division multiplexing where the processor switches between threads. Context switching occurs fast enough that the user perceives the threads as running at the same time. By using threads a programmer can split the work into the threads, each responsible for a smaller portion of the problem. From a threads view he thinks it has the processor all to itself.

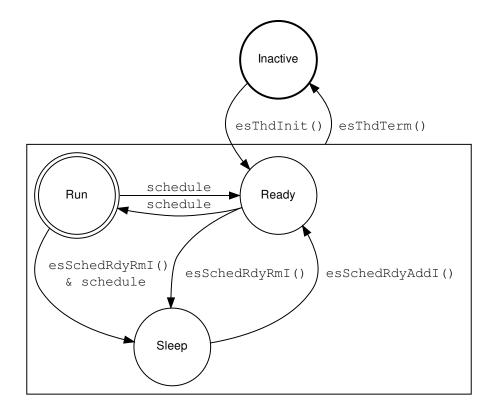
## 4.1.1 eSolid RT Kernel thread

eSolid RT Kernel supports multi-threading and allows applications to have any number of threads. The only limiting factors for the maximum number of threads are the amount of RAM and ROM memory and processing time.

#### 4.1.2 Thread states

A thread can be in one of the following states:

5 Critical sections 8



## 5 Critical sections

How to deal with critical sections in an application

## 5.1 Intro

In concurrent programming, a critical section is a piece of code that accesses a shared resource (data structure or device) that must not be concurrently accessed by more than one thread of execution. A critical section will usually terminate in fixed time, and a thread will have to wait for a fixed time to enter it (aka bounded waiting). Some synchronization mechanism is required at the entry and exit of the critical section to ensure exclusive use, for example a semaphore.

#### 5.1.1 eSolid RT Kernel internal critical sections

In contrast to application code in kernel code there is no other mechanism to protect critical code except disabling interrupts. Fortunately, some ports have ability to mask certain interrupts with low priority and allow interrupts with higher priority. By masking low priority interrupts the kernel can protect its critical sections. However for this scheme to work its forbidden to call any OS service function from a high priority interrupt. If this rule is not followed then the high priority interrupt with an OS service function call can preempt the kernel low priority interrupt which will in that case corrupt the kernel internal data structures.

5.2 Implementation 9

Note

1) It is forbidden to call any OS service function from an interrupt with the priority higher than the kernel interrupt priority.

2) On some ports the kernel never completely disables interrupts.

## 5.2 Implementation

There are multiple ways how are critical sections implemented:

- The simplest method is to prevent interrupts on entry into the critical section, and restoring interrupts to their
  previous state on exit from critical section. Any thread of execution entering any critical section anywhere in
  the system, with this implementation, will prevent any other thread, including an interrupt, from being executed
  on the CPU.
- This approach can be improved upon by using semaphores. To enter a critical section, a thread must obtain
  a semaphore, which it releases on leaving the section. Other threads are prevented from entering the critical
  section at the same time as the original thread, but are free to gain control of the CPU and execute other
  code, including other critical sections that are protected by different semaphores.

## 5.2.1 Disabling interrupts

In order to properly disable interrupts the application must follow these steps:

- · declare an auto variable which will hold interrupt state
- save interrupt status into auto variable and disable interrupts
- · access the shared resource
- · restore previously saved interrupt state

For auto variable declaration macro ES\_CRITICAL\_DECL() is used. This macro will declare a temporary interrupt status variable. Then by using the macro ES\_CRITICAL\_ENTER() the state of enabled interrupts will be saved in auto variable declared earlier. Immediately after saving the interrupt state the macro will lock interrupts. Now the code can safely access and use the shared resource. When code finishes using the resource it will call ES\_CRITICAL\_EXIT() macro. This macro will restore interrupts from the previously saved interrupt state.

When to use this scheme

- If interrupt service routine *changes* the shared resource state.
- If the processing time of critical section is very small.

When not to use this scheme

If interrupt service routine takes a lot of CPU time to process critical section. If a critical section is long, then
the system clock will drift every time a critical section is executed because the system timer interrupt is no
longer serviced, so tracking time is impossible during the critical section. Also, if a program execution halts
during its critical section, control will never be returned to another thread, effectively halting the entire system.

6 Time complexity 10

## 5.2.2 Disabling Kernel scheduler

Another way to implement a critical section and protect your data is by locking the kernel scheduler. The kernel locking can be used only if you know that protected data will be modified only by other threads. This protection scheme can not be used when data is modified by interrupt service routines.

When to use this scheme

- If interrupt service routine never changes the shared resource state.
- If the processing time of critical section is very small.

When not to use this scheme

• If interrupt service routine takes a lot of CPU time to process critical section. If a critical section is long, then the system will be partially responsive to other events since interrupt service routines can be invoked, but note that any further processing by other threads is still disabled.

## 5.2.3 Using semaphores

## 6 Time complexity

About time categories of algorithms

## 6.1 Intro

In computer science, the time complexity of an algorithm quantifies the amount of time taken by an algorithm to run as a function of the length of the input. The time complexity of an algorithm is commonly expressed using **big O** notation, which excludes coefficients and lower order terms. When expressed this way, the time complexity is said to be described asymptotically, i.e., as the input size goes to infinity. For example, if the time required by an algorithm on all inputs of size n is at most  $5n^3 + 3n$ , the asymptotic time complexity is  $0 (n^3)$ .

Time complexity is commonly estimated by counting the number of elementary operations performed by the algorithm, where an elementary operation takes a fixed amount of time to perform. Thus the amount of time taken and the number of elementary operations performed by the algorithm differ by at most a constant factor.

Since an algorithm's performance time may vary with different inputs of the same size, one commonly uses the worst-case time complexity of an algorithm, denoted as  $\mathbf{T}(\mathbf{n})$ , which is defined as the maximum amount of time taken on any input of size  $\mathbf{n}$ . Time complexities are classified by the nature of the function  $\mathbf{T}(\mathbf{n})$ . For instance, an algorithm with  $\mathbf{T}(\mathbf{n}) = O(\mathbf{n})$  is called a linear time algorithm, and an algorithm with  $\mathbf{T}(\mathbf{n}) = O(2^{\Lambda}\mathbf{n})$  is said to be an exponential time algorithm.

Note

Worst-case time-complexity  $\mathbb{T}(n)$  indicates the longest running time performed by an algorithm given any input of size n, and thus this guarantees that the algorithm finishes on time.

6.2 Constant time 11

#### 6.1.1 Big O notation

Big O notation describes the limiting behavior of a function when the argument tends towards a particular value or infinity, usually in terms of simpler functions and it is used to classify algorithms by how they respond (e.g., in their processing time or working space requirements) to changes in input size.

#### 6.2 Constant time

An algorithm is said to be constant time (also written as O(1) time) if the value of T(n) is bounded by a value that does not depend on the size of the input.

Despite the name *constant time*, the running time does not have to be independent of the problem size, but an upper bound for the running time has to be bounded independently of the problem size.

Note

Constant time effectively means that there is a constant upper bound to how long the function will take to run which isn't affected by any of the input argument.

#### 6.2.1 eSolid RT Kernel time complexity

All eSolid RT Kernel functions are using constant time O(1) algorithms. This is especially important for Real Time applications.

### 7 Scheduler

About the scheduler and Ready Threads Queue

#### 7.1 Quantum

The period of time for which a thread is allowed to execute in a preemptive multi-threading system is generally called the time slice, or quantum. The scheduler is run once every quantum to choose the next thread for execution. If the quantum is too short then the scheduler overhead may become high.

An interrupt is used to allow the kernel to switch between threads when their quantum expires, effectively allowing the processor's time to be shared between a number of threads, giving the illusion that it is dealing with these threads concurrently.

#### 7.2 Threads List

## 7.3 Threads Queue

Based on the number of configured priority levels (see CFG\_SCHED\_PRIO\_LVL) and on the number of data register bits (see PORT\_DATA\_WIDTH\_VAL) of the used CPU, two configurations are possible:

- · Simple Ready Threads Queue
- · Complex Ready Threads Queue

Simple Ready Threads Queue configuration is used when the number of configured priority levels is lower or equal to the number of bits in general purpose data register. For example if application is using 9 priority levels on 32-bit CPU than simple Ready Threads Queue configuration is used. In contrast, when using 9 priority levels on an 8-bit CPU than the kernel is forced to use the Complex Ready Threads Queue configuration since 8-bit register cannot carry 9 bits of data.

7.3 Threads Queue 12

## 7.3.1 Simple Ready Threads Queue

Each bit in bit[0] variable represents each priority level. The number of bits used in this variable depends on CFG\_SCHED\_PRIO\_LVL value. If a bit at Nth position is set then there is a thread inserted in Thread List at Nth priority.

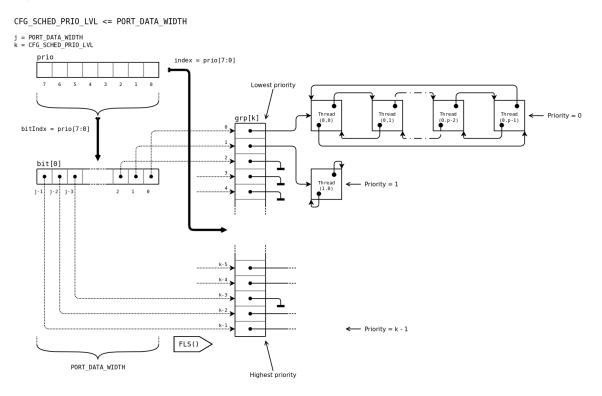


Figure 1: Ready Threads Queue - low number of priority levels

## 7.3.2 Complex Ready Threads Queue

CFG\_SCHED\_PRIO\_LVL > PORT\_DATA\_WIDTH

i = PRIO\_BM\_GRP\_INDX = round\_up(CFG\_SCHED\_PRIO\_LVL / PORT\_DATA\_WIDTH)

j = PORT\_DATA\_WIDTH

k = CFG\_SCHED\_PRIO\_LVL

l = PRIO\_BM\_DATA\_WIDTH\_LOG2 = log2(PORT\_DATA\_WIDTH)

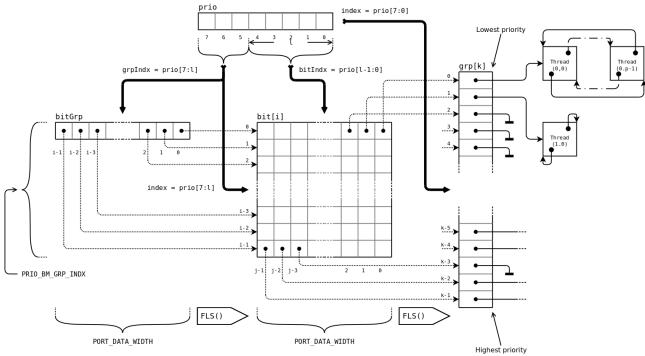


Figure 2: Ready Threads Queue - high number of priority levels

## 7.4 Ready Threads Queue

Ready Threads Queue holds threads that are ready for execution.

# 8 Error checking

How errors are detected

## 8.1 Intro

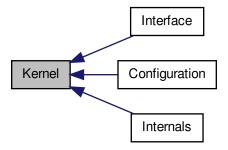
## 9 Module Documentation

## 9.1 Kernel

Overview.

9.1 Kernel 14

Collaboration diagram for Kernel:



## Modules

• Configuration

Configuration settings.

Interface

Application programming interface.

Internals

Kernel inner work.

## 9.1.1 Detailed Description

Overview.

## 9.2 Interface

Application programming interface.

Collaboration diagram for Interface:



#### **Data Structures**

struct esThd

Thread structure.

struct esTmr

Timer structure.

struct esThdQ

Thread Queue structure.

struct esKernCtrl

Kernel control block structure.

## Kernel identification and version number

• #define ES\_KERNEL\_VER 0x10000UL

Identifies the underlying kernel version number.

#define ES\_KERNEL\_ID "eSolid Kernel v1.0"

Kernel identification string.

## **Critical section management**

These macros are used to prevent interrupts on entry into the critical section, and restoring interrupts to their previous state on exit from critical section.

For more details see Critical sections.

void esKernLockEnterl (void)

Lock the scheduler.

void esKernLockExitI (void)

Unlock the scheduler.

void esKernLockEnter (void)

Lock the scheduler.

void esKernLockExit (void)

Unlock the scheduler.

#define ES\_CRITICAL\_DECL() PORT\_CRITICAL\_DECL()

Critical section status variable declaration.

#define ES\_CRITICAL\_ENTER() PORT\_CRITICAL\_ENTER()

Enter a critical section.

#define ES\_CRITICAL\_EXIT() PORT\_CRITICAL\_EXIT()

Exit from critical section.

#### **Error checking**

Some basic infrastructure for error checking

These macros provide basic detection of errors. For more datails see Error checking.

#define ES\_ASSERT(expr)

Generic assert macro.

#define ES API OBLIGATION(expr) expr

Execute code to fulfill the contract.

#define ES\_API\_REQUIRE(expr) ES\_ASSERT(expr)

Make sure the caller has fulfilled all contract preconditions.

#define ES\_API\_ENSURE(expr) ES\_ASSERT(expr)

Make sure the callee has fulfilled all contract postconditions.

## Thread management

Basic thread management services

For more details see Thread Management.

typedef struct esThd esThd\_T

Thread type.

typedef portStck\_T esStck\_T

Stack type.

• void esThdInit (esThd\_T \*thd, void(\*thdf)(void \*), void \*arg, portStck\_T \*stck, size\_t stckSize, uint8\_t prio)

Initialize the specified thread.

void esThdTerm (esThd\_T \*thd)

Terminate the specified thread.

static PORT\_C\_INLINE esThd\_T \* esThdGetId (void)

Get the current thread ID.

• static PORT\_C\_INLINE uint8\_t esThdGetPrio (esThd\_T \*thd)

Get the priority of a thread.

void esThdSetPriol (esThd\_T \*thd, uint8\_t prio)

Set the priority of a thread.

void esThdPostI (esThd\_T \*thd)

Post to thread semaphore.

void esThdPost (esThd\_T \*thd)

Post to thread semaphore.

void esThdWaitI (void)

Wait for thread semaphore.

void esThdWait (void)

Wait for thread semaphore.

#define ES\_STCK\_SIZE(elem) PORT\_STCK\_SIZE(elem)

Converts the required stack elements into the stack array index.

## Timer services

typedef struct esTmr esTmr\_T

Timer type.

void esTmrAddl (esTmr\_T \*tmr, esTick\_T tick, void(\*fn)(void \*), void \*arg)

Add a new timer.

## **Thread Queue management**

typedef struct esThdQ esThdQ\_T

Thread queue type.

void esThdQInit (esThdQ\_T \*thdQ)

Initialize Thread Queue.

void esThdQAddI (esThdQ\_T \*thdQ, esThd\_T \*thd)

Add a thread to the tail of the Thread Queue.

void esThdQRmI (esThdQ\_T \*thdQ, esThd\_T \*thd)

Removes the thread from the Thread Queue.

esThd T \* esThdQFetchl (const esThdQ T \*thdQ)

Fetch the first high priority thread from the Thread Queue.

esThd\_T \* esThdQFetchRotatel (esThdQ\_T \*thdQ, uint\_fast8\_t prio)

Fetch the next thread and rotate linked list.

bool\_T esThdQIsEmpty (const esThdQ\_T \*thdQ)

Is thread queue empty.

#define PRIO\_BM\_GRP\_INDX ((CFG\_SCHED\_PRIO\_LVL + PORT\_DATA\_WIDTH\_VAL - 1U) / PORT\_DATA\_WIDTH\_VAL)

Priority Bit Map Group Index.

#### Kernel control block

```
enum esKernState {
```

 $ES_KERN_RUN = 0x00U$ ,

 $ES_KERN_INTSRV_RUN = 0x01U$ ,

ES KERN LOCK = 0x02U,

ES\_KERN\_INTSRV\_LOCK = 0x03U,

 $ES_KERN_INIT = 0x04U$ ,

ES KERN INACTIVE = 0x05U }

Kernel state enumeration.

typedef enum esKernState esKernState\_T

Kernel state type.

typedef struct esKernCtrl esKernCtrl\_T

Kernel control block type.

const volatile esKernCtrl\_T gKernCtrl

Kernel control block.

### General kernel functions

There are several groups of functions:

- · kernel initialization and start
- ISR prologue and epilogue
- void esKernInit (void)

Initialize kernel internal data structures.

PORT\_C\_NORETURN void esKernStart (void)

Start the multi-threading.

void esKernSysTmrl (void)

Process the system timer event.

void esKernIsrProloguel (void)

Enter Interrupt Service Routine.

void esKernIsrEpiloguel (void)

Exit Interrupt Service Routine.

#### Scheduler notification and invocation

void esSchedRdyAddI (esThd\_T \*thd)

Add thread thd to the ready thread list and notify the scheduler.

void esSchedRdyRmI (esThd\_T \*thd)

Remove thread thd from the ready thread list and notify the scheduler.

void esSchedYieldI (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

· void esSchedYieldIsrl (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

## System timer

void esSysTmrEnable (void)

Enable system timer tick events.

void esSysTmrDisable (void)

Disable system timer tick events.

#### Kernel hook functions

PORT C NORETURN void userAssert (const char \*fnName, const char \*expr)

An assertion has failed. This function should inform the user about failed assertion.

void userSysTmr (void)

System timer hook function, called from system system timer ISR function.

· void userKernInit (void)

Kernel initialization hook function, called from esKernInit() function.

void userKernStart (void)

Kernel start hook function, called from esKernStart() function.

void userThdInitEnd (void)

Thread initialization end hook function, called from esThdInit() function.

void userThdTerm (void)

Thread terminate hook function, called from esThdTerm() or when a thread terminates itself.

void userCtxSw (esThd T \*oldThd, esThd T \*newThd)

Kernel context switch hook function, called from esSchedYieldI() and esSchedYieldIsrI() functions.

## 9.2.1 Detailed Description

Application programming interface.

#### 9.2.2 Macro Definition Documentation

## 9.2.2.1 #define ES\_KERNEL\_VER 0x10000UL

Identifies the underlying kernel version number.

RTOS identification and version (main [31:16] .sub [15:0])

9.2.2.2 #define ES\_KERNEL\_ID "eSolid Kernel v1.0"

Kernel identification string.

```
9.2.2.3 #define ES_CRITICAL_DECL( ) PORT_CRITICAL_DECL()
```

Critical section status variable declaration.

9.2.2.4 #define ES\_CRITICAL\_ENTER( ) PORT\_CRITICAL\_ENTER()

Enter a critical section.

9.2.2.5 #define ES\_CRITICAL\_EXIT( ) PORT\_CRITICAL\_EXIT()

Exit from critical section.

9.2.2.6 #define ES\_ASSERT( expr )

#### Value:

```
do {
     if (!(expr)) {
         userAssert(PORT_C_FUNC, #expr);
     }
} while (0U)
```

Generic assert macro.

#### **Parameters**

expr | Expression which must be TRUE

## 9.2.2.7 #define ES\_API\_OBLIGATION( expr ) expr

Execute code to fulfill the contract.

## **Parameters**

expr Expression to be executed only if contracts need to be validated.
--

## 9.2.2.8 #define ES\_API\_REQUIRE( expr ) ES\_ASSERT(expr)

Make sure the caller has fulfilled all contract preconditions.

### **Parameters**

expr	Expression which must be satisfied

## 9.2.2.9 #define ES\_API\_ENSURE( expr ) ES\_ASSERT(expr)

Make sure the callee has fulfilled all contract postconditions.

## **Parameters**

expr Expression which must be satisfied
---

## 9.2.2.10 #define ES\_STCK\_SIZE( elem ) PORT\_STCK\_SIZE(elem)

Converts the required stack elements into the stack array index.

```
9.2.2.11 #define PRIO_BM_GRP_INDX ((CFG_SCHED_PRIO_LVL + PORT_DATA_WIDTH_VAL - 1U) / PORT_DATA_WIDTH_VAL)
```

Priority Bit Map Group Index.

Object class:

**Not API** object, this object is not part of the application programming interface and it is intended for internal use only.

9.2.3 Typedef Documentation

9.2.3.1 typedef struct esThd esThd\_T

Thread type.

9.2.3.2 typedef portStck\_T esStck\_T

Stack type.

9.2.3.3 typedef struct esTmr esTmr\_T

Timer type.

9.2.3.4 typedef struct esThdQ esThdQ\_T

Thread queue type.

9.2.3.5 typedef enum esKernState esKernState\_T

Kernel state type.

9.2.3.6 typedef struct esKernCtrl esKernCtrl\_T

Kernel control block type.

9.2.4 Enumeration Type Documentation

9.2.4.1 enum esKernState

Kernel state enumeration.

For more details see: Kernel states

Object class:

Regular API object, this object is part of the application programming interface.

**Enumerator** 

ES\_KERN\_RUN Kernel is active

ES\_KERN\_INTSRV\_RUN Servicing an interrupt return to ES\_KERN\_RUN state

ES\_KERN\_LOCK Kernel is locked

ES\_KERN\_INTSRV\_LOCK Servicing an interrupt, return to ES\_KERN\_LOCK state

ES\_KERN\_INIT Kernel is in initialization state

ES\_KERN\_INACTIVE Kernel data structures are not initialized

9.2.5 Function Documentation

9.2.5.1 void esKernInit (void)

Initialize kernel internal data structures.

This is the function which must be called first before any other kernel API. It initializes internal data structures which all other kernel API use.

#### Precondition

```
1) The kernel state == ES_KERN_INACTIVE, see Kernel states.
```

#### **Postcondition**

```
1) The kernel state == ES_KERN_INIT.
```

#### Note

1) This function may be invoked only once.

## Object class:

Regular API object, this object is part of the application programming interface.

```
9.2.5.2 PORT_C_NORETURN void esKernStart ( void )
```

Start the multi-threading.

This function will start multi-threading. Once the multi-threading has started the execution will never return to this function again (this function never returns).

## Precondition

- 1) The kernel state == ES\_KERN\_INIT, see Kernel states.
- 2) At least one thread must be initialized and be in Threads Ready Queue before starting multi-threading, see esThdInit().

## Postcondition

- 1) The kernel state == ES\_KERN\_RUN
- 2) The multi-threading execution will commence.

#### Note

1) Once this function is called the execution of threads will start and this function will never return.

## Object class:

Regular API object, this object is part of the application programming interface.

```
9.2.5.3 void esKernSysTmrl (void)
```

Process the system timer event.

This function will be called only by port system timer interrupt.

## Precondition

```
1) The kernel state < ES_KERN_INIT, see Kernel states.
```

## Object class:

**Not API** object, this object is not part of the application programming interface and it is intended for internal use only.

#### 9.2.5.4 void esKernlsrProloguel (void)

Enter Interrupt Service Routine.

Function will notify kernel that you are about to enter interrupt service routine (ISR). This allows kernel to keep track of interrupt nesting and then only perform rescheduling at the last nested ISR.

#### Precondition

```
1) The kernel state < ES_KERN_INIT, see Kernel states.
```

#### Note

- 1) You must call esKernIsrEpiloqueI() at the exit of ISR.
- 2) You must invoke esKernlsrProloguel() and esKernlsrEpiloguel() in pair. In other words, for every call to esKernlsrProloguel() at the beginning of the ISR you must have a call to esKernlsrEpiloguel() at the end of the ISR.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

### 9.2.5.5 void esKernlsrEpiloguel (void)

Exit Interrupt Service Routine.

This function is used to notify kernel that you have completed servicing an interrupt. When the last nested ISR has completed, the function will call the scheduler to determine whether a new, high-priority task, is ready to run.

#### Precondition

```
1) The kernel state < ES_KERN_INIT, see Kernel states.
```

## Note

- 1) You must invoke esKernIsrPrologueI() and esKernIsrEpilogueI() in pair. In other words, for every call to esKernIsrPrologueI() at the beginning of the ISR you must have a call to esKernIsrEpilogueI() at the end of the ISR
- 2) Rescheduling is prevented when the scheduler is locked (see esKernLockEnterl())

## **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.6 void esKernLockEnterl (void)
```

Lock the scheduler.

#### **Function class:**

I class API function, this function can be called from application and interrupt service routine only with interrupts locked

## 9.2.5.7 void esKernLockExitl ( void )

Unlock the scheduler.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

9.2.5.8 void esKernLockEnter ( void )

Lock the scheduler.

Object class:

Regular API object, this object is part of the application programming interface.

9.2.5.9 void esKernLockExit (void)

Unlock the scheduler.

Object class:

Regular API object, this object is part of the application programming interface.

9.2.5.10 void esThdInit ( esThd\_T \* thd, void(\*)(void \*) thdf, void \* arg, portStck\_T \* stck, size\_t stckSize, uint8\_t prio )
Initialize the specified thread.

#### **Parameters**

thd	Thread: is a pointer to the thread structure, esThd_T. The structure will be used as information container for the thread. It is assumed that storage for the esThd_T structure is allocated by
	the user code.
thdf	Thread Function: is a pointer to thread function. Thread function must have the following
	<pre>signature: void thread (void * arg).</pre>
arg	Argument: is a void pointer to an optional data area. It's usage is application defined and it is
	intended to pass arguments to thread when it is started for the first time.
stck	Stack: is a pointer to a allocated memory for thread stack. The pointer always points to the
	first element in the array, regardless of what type of stack the CPU is using. The thread's stack
	is used to store local variables, function parameters, return addresses. Each thread has its
	own stack and different sized stack. The stack type must be an array of portStck_T.
stckSize	Stack Size: specifies the size of allocated stack memory. Size is expressed in bytes. Please
	see port documentation about minimal stack size.
prio	Priority: is the priority of the thread. The higher the number, the higher the priority (the impor-
	tance) of the thread. Several threads can have the same priority.

Threads must be created in order for kernel to recognize them as threads. Initialize a thread by calling esThd-Init() and provide arguments specifying to kernel how the thread will be managed. Threads are always created in the ready-to-run state. Threads can be created either prior to the start of multi-threading (before calling esKernStart()), or by a running thread.

## Precondition

```
1) The kernel state ES_KERN_INACTIVE, see Kernel states.
```

- 2) thd != NULL
- 3) thdf != NULL
- 4) stckSize >= PORT\_STCK\_MINSIZE\_VAL, see PORT\_STCK\_MINSIZE\_VAL.
- 5) 0 <= prio <= CFG\_SCHED\_PRIO\_LVL, see CFG\_SCHED\_PRIO\_LVL.

### **Postcondition**

1) thd->signature == THD\_CONTRACT\_SIGNATURE, each esThd\_T structure will have valid signature after initialization.

## Object class:

Regular API object, this object is part of the application programming interface.

```
9.2.5.11 void esThdTerm ( esThd_T * thd )
```

Terminate the specified thread.

#### **Parameters**

```
thd Thread: is a pointer to the thread structure, esThd_T.
```

#### Precondition

- 1) The kernel state ES\_KERN\_INACTIVE, see Kernel states.
- 2) thd != NULL
- 3) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 4) (thd->thdL.q == NULL) OR (thd->thdL.q == gRdyQueue), thread must be either in Ready Threads Queue or not be in any queue (e.g. not waiting for a synchronization mechanism).

```
9.2.5.12 static PORT_C_INLINE esThd_T* esThdGetId ( void ) [static]
```

Get the current thread ID.

## Returns

Pointer to current thread ID structure esThd.

#### Note

This is inline function.

## Object class:

Regular API object, this object is part of the application programming interface.

```
9.2.5.13 static PORT_C_INLINE uint8_t esThdGetPrio ( esThd_T * thd ) [static]
```

Get the priority of a thread.

## **Parameters**

thd	Thread: is pointer to the thread structure, esThd_T.
-----	--

## Returns

The priority of the thread pointed by thd.

#### Note

This is inline function.

## Object class:

Regular API object, this object is part of the application programming interface.

9.2.5.14 void esThdSetPriol ( esThd\_T \* thd, uint8\_t prio )

Set the priority of a thread.

## Parameters

thd	Thread: is pointer to the thread structure, esThd_T.
prio	Priority: is new priority of the thread pointed by thd.

#### Precondition

- 1) The kernel state < ES\_KERN\_INACTIVE, see Kernel states.
- 2) thd != NULL
- 3) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 4) 0 <= prio <= CFG\_SCHED\_PRIO\_LVL, see CFG\_SCHED\_PRIO\_LVL.

### **Function class:**

I class API function, this function can be called from application and interrupt service routine only with interrupts locked.

9.2.5.15 void esThdPostl ( esThd\_T \* thd )

Post to thread semaphore.

## **Parameters**

thd Pointer to the thread ID structure

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

9.2.5.16 void esThdPost ( esThd\_T \* thd )

Post to thread semaphore.

#### **Parameters**

thd	Pointer to the thread ID structure
-----	------------------------------------

## Object class:

Regular API object, this object is part of the application programming interface.

9.2.5.17 void esThdWaitI (void)

Wait for thread semaphore.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

9.2.5.18 void esThdWait (void)

Wait for thread semaphore.

## Object class:

Regular API object, this object is part of the application programming interface.

9.2.5.19 void esThdQInit ( esThdQ\_T \* thdQ )

Initialize Thread Queue.

### **Parameters**

thdQ Thread Queue: is a pointer to thread queue structure, esThdQ.

#### Precondition

```
1) thdQ != NULL
```

## Postcondition

1) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, each esThdQ\_T structure will have valid signature after initialization.

## Object class:

Regular API object, this object is part of the application programming interface.

```
9.2.5.20 void esThdQAddl ( esThdQ_T*thdQ, esThd_T*thd )
```

Add a thread to the tail of the Thread Queue.

#### **Parameters**

thdQ	Thread Queue: is a pointer to thread queue structure, esThdQ.
thd	Thread: is a pointer to the thread ID structure, esThd_T.

This function adds a thread at the tail of the specified Thread Queue.

#### Precondition

- 1) thdQ != NULL
- 2) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, the pointer must point to a esThdQ\_T structure.
- 3) thd != NULL
- 4) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 5)  $thd \rightarrow thdL.q == NULL$ , thread must not be in any queue.
- 6) 0 <= thd->prio <= CFG\_SCHED\_PRIO\_LVL, see CFG\_SCHED\_PRIO\_LVL.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.21 void esThdQRml ( esThdQ_T*thdQ, esThd_T*thd )
```

Removes the thread from the Thread Queue.

#### **Parameters**

thdQ T	Thread Queue: is a pointer to thread queue structure, esThdQ.
thd T	Thread: is a pointer to the thread ID structure, esThd_T.

#### Precondition

- 1) thd != NULL
- 2) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 3) thdQ != NULL
- 4) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, the pointer must point to a esThdQ\_T structure.
- 5)  $thd \rightarrow thdL.q == thdQ$ , thread must be in the thdQ queue.
- 6) 0 <= thd->prio <= CFG\_SCHED\_PRIO\_LVL, see CFG\_SCHED\_PRIO\_LVL.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.22 esThd_T* esThdQFetchl ( const esThdQ_T* thdQ )
```

Fetch the first high priority thread from the Thread Queue.

#### **Parameters**

thdQ Thread Queue: is a p	ointer to thread queue structure, esThdQ.
---------------------------	---

#### Returns

A pointer to the thread ID structure with the highest priority.

#### Precondition

- 1) thdQ != NULL
- 2) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, the pointer must point to a esThdQ\_T structure.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.23 esThd_T* esThdQFetchRotatel( esThdQ_T * thdQ, uint_fast8_t prio )
```

Fetch the next thread and rotate linked list.

## **Parameters**

thdQ	The second secon
	fetch from.
prio	Priority: is the priority level to fetch and rotate.

## Returns

Pointer to the next thread in queue.

### Precondition

- 1) thdQ != NULL
- 2) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, the pointer must point to a esThdQ\_T structure.
- 3) 0 <= prio <= CFG\_SCHED\_PRIO\_LVL, see CFG\_SCHED\_PRIO\_LVL.

## 9.2.5.24 bool\_T esThdQlsEmpty ( const esThdQ\_T \* thdQ )

Is thread queue empty.

#### **Parameters**

thdQ	Thread Queue: is a pointer to thread queue structure, esThdQ.	

#### Returns

The state of thread queue

#### Return values

TRUE	- thread queue is empty
FALSE	- thread queue is not empty

## Precondition

- 1) thdQ != NULL
- 2) thdQ->signature == THDQ\_CONTRACT\_SIGNATURE, the pointer must point to a esThdQ\_T structure.

## 9.2.5.25 void esSchedRdyAddl ( esThd\_T \* thd )

Add thread  ${\tt thd}$  to the ready thread list and notify the scheduler.

#### **Parameters**

thd	Pointer to the initialized thread ID structure, esThd_T.
-----	--

## Precondition

- 1) The kernel state < ES\_KERN\_INACTIVE, see Kernel states.
- 2) thd != NULL
- 3) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 3) thd->thdL.q == NULL, thread must not be in a queue.

### **Function class:**

I class API function, this function can be called from application and interrupt service routine only with interrupts locked.

## 9.2.5.26 void esSchedRdyRml ( esThd\_T \* thd )

Remove thread thd from the ready thread list and notify the scheduler.

#### **Parameters**

```
thd Pointer to the initialized thread ID structure, esThd_T.
```

### Precondition

- 1) The kernel state < ES\_KERN\_INACTIVE, see Kernel states.
- 2) thd != NULL
- 3) thd->signature == THD\_CONTRACT\_SIGNATURE, the pointer must point to a esThd\_T structure.
- 4) thd->thdL.q == &gRdyQueue, thread must be in Ready Threads queue.

#### **Function class:**

I class API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.27 void esSchedYieldI (void)
```

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

#### Precondition

```
1) The kernel state < ES_KERN_INACTIVE, see Kernel states.
```

#### Warning

Scheduler will have undefined behavior if there is no ready thread to run (e.g. empty gRdyQueue) at the time it is invoked.

#### **Function class:**

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.28 void esSchedYieldIsrl (void)
```

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

#### Precondition

```
1) The kernel state < ES_KERN_INACTIVE, see Kernel states.
```

## Warning

Scheduler will have undefined behavior if there is no ready thread to run (e.g. empty gRdyQueue) at the time it is invoked.

## Function class:

**I class** API function, this function can be called from application and interrupt service routine only with interrupts locked.

```
9.2.5.29 void esSysTmrEnable (void)
```

Enable system timer tick events.

This function will override scheduler power savings algorithm and force the system timer into running (active) state.

```
9.2.5.30 void esSysTmrDisable (void)
```

Disable system timer tick events.

This function will try to switch off the system timer. If the system timer is used by scheduler than the scheduler will take control of the system timer.

```
9.2.5.31 void esTmrAddl ( esTmr_T * tmr, esTick_T tick, void(*)(void *) fn, void * arg )
```

Add a new timer.

#### **Parameters**

tmr	Timer: is pointer to the timer ID structure, esTmr.
tick	Tick: the timer delay expressed in system ticks
fn	Function: is pointer to the callback function
arg	Argument: is pointer to the arguments of callback function

#### 9.2.5.32 PORT\_C\_NORETURN void userAssert ( const char \* fnName, const char \* expr )

An assertion has failed. This function should inform the user about failed assertion.

#### **Parameters**

fnName	Function name: is pointer to the function name string where the assertion has failed. Macro will automatically fill in the function name.
expr	Expression: is pointer to the string containing the expression that failed to evaluate to TRUE.

Function will just print the information which was given by the macros. After the function informs the user it **must** go into infinite loop or HALT the processor.

## Precondition

```
1) NULL != fnName
2) NULL != expr
```

## Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_API\_VALIDATION is active.

## 9.2.5.33 void userSysTmr (void)

System timer hook function, called from system system timer ISR function.

This function is called whenever a system event is generated.

### Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_SYSTMR\_EVENT is active.

## 9.2.5.34 void userKernInit (void)

Kernel initialization hook function, called from esKernInit() function.

This function is called before kernel initialization.

### Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_KERN\_INIT is active.

## 9.2.5.35 void userKernStart (void)

Kernel start hook function, called from esKernStart() function.

This function is called before kernel start.

#### Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_KERN\_START is active.

## 9.2.5.36 void userThdInitEnd (void)

Thread initialization end hook function, called from esThdInit() function.

This function is called after the thread initialization.

Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_THD\_INIT\_END is active.

```
9.2.5.37 void userThdTerm (void)
```

Thread terminate hook function, called from esThdTerm() or when a thread terminates itself.

Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_THD\_TERM is active.

```
9.2.5.38 void userCtxSw ( esThd_T * oldThd, esThd_T * newThd )
```

Kernel context switch hook function, called from esSchedYieldI() and esSchedYieldIsrI() functions.

This function is called at each context switch.

## **Parameters**

oldThd	Pointer to the thread being switched out.
newThd	Pointer to the thread being switched in.

Note

- 1) The definition of this function must be written by the user.
- 2) This function is called only if CFG\_HOOK\_CTX\_SW is active.

## 9.2.6 Variable Documentation

9.2.6.1 const volatile esKernCtrl\_T gKernCtrl

Kernel control block.

Note

This variable has Read-Only access rights for application.

9.3 Internals 32

#### 9.3 Internals

Kernel inner work.

Collaboration diagram for Internals:



## **Data Structures**

struct sysTmr

Main System Timer structure.

#### **Macros**

#define PRIO BM DATA WIDTH LOG2

Priority Bit Map log base 2: log2 (PORT\_DATA\_WIDTH\_VAL)

#define SCHED STATE INTSRV MSK (1U << 0)</li>

Kernel state variable bit position which defines if kernel is in interrupt servicing state.

#define SCHED\_STATE\_LOCK\_MSK (1U << 1)</li>

Kernel state variable bit position which defines if the kernel is locked or not.

#define SYSTMR\_SCHED\_QM\_MSK (1U << 0)</li>

Scheduler is using system timer Quantum mask.

#define SYSTMR\_USR\_QM\_MSK (1U << 1)</li>

User is using system timer Quantum mask.

#define SCHED\_POWER\_SAVE 0U

Enable/disable scheduler power savings mode.

• #define THD\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEFU)

Thread structure signature.

#define THDQ\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEEU)

Thread Queue structure signature.

• #define DLIST\_IS\_ENTRY\_FIRST(list, entry) ((entry) == (entry)->list.next)

DList macro: is the thread the first one in the list.

• #define DLIST\_IS\_ENTRY\_LAST(list, entry) DLIST\_IS\_ENTRY\_FIRST(list, entry)

DList macro: is the thread the last one in the list.

• #define DLIST\_IS\_ENTRY\_SINGLE(list, entry) DLIST\_IS\_ENTRY\_FIRST(list, entry)

DList macro: is the thread single in the list.

#define DLIST\_ENTRY\_PREV(list, entry) (entry)->list.prev

Get the previous entry.

• #define DLIST\_ENTRY\_NEXT(list, entry) (entry)->list.next

Get the next entry.

#define DLIST\_ENTRY\_INIT(list, entry)

Initialize entry.

• #define DLIST\_ENTRY\_ADD\_AFTER(list, current, entry)

```
Add new entry after current entry.
```

#define DLIST\_ENTRY\_RM(list, entry)

Remove the entry from a list.

#define KTMR\_STCK\_SIZE PORT\_STCK\_SIZE(40U)

System Timer kernel thread stack size.

#define KIDLE\_STCK\_SIZE PORT\_STCK\_SIZE(40U)

Idle kernel thread stack size.

#### **Variables**

• static esThdQ\_T gRdyQueue

Ready Thread queue.

#### System timer

enum sysTmrState {
 SYSTMR\_ACTIVE,
 SYSTMR\_INACTIVE }

System timer state enumeration.

typedef struct sysTmr sysTmr\_T

System Timer type.

static void sysTmrInit (void)

Initialize system timer hardware.

static void sysTmrTryDeactivate (void)

Try to deactivate system timer.

static void sysTmrTryActivate (void)

Try to activate system timer.

#### **Priority Bit Map**

typedef struct prioBM prioBM\_T

Priority Bit Map type.

static PORT\_C\_INLINE void prioBMInit (prioBM\_T \*bm)

Initialize bitmap.

static PORT\_C\_INLINE void prioBMSet (prioBM\_T \*bm, uint\_fast8\_t prio)

Set the bit corresponding to the prio argument.

static PORT\_C\_INLINE void prioBMClear (prioBM\_T \*bm, uint\_fast8\_t prio)

Clear the bit corresponding to the prio argument.

static PORT\_C\_INLINE uint\_fast8\_t prioBMGet (const prioBM\_T \*bm)

Get the highest priority set.

static PORT\_C\_INLINE bool\_T prioBMIsEmpty (const prioBM\_T \*bm)

Is bit map empty?

#### Scheduler

• static PORT\_C\_INLINE void schedInit (void)

Initialize Ready Thread Queue structure gRdyQueue and Kernel control structure esKernCtrl.

static PORT\_C\_INLINE void schedStart (void)

Set scheduler data structures ready for multi-threading.

static void schedRdyAddInitI (esThd T\*thd)

Initialize scheduler ready structure during the thread add operation.

static void schedQml (void)

Do the Quantum (Round-Robin) scheduling.

static PORT\_C\_INLINE void schedQmActivate (void)

Activate system timer Quantum mode.

static PORT\_C\_INLINE void schedQmDeactivate (void)

Deactivate system timer Quantum mode.

static void schedQmEvaluateI (esThd\_T \*thd)

Evaluate if the system timer Quantum mode is needed.

#### System timer kernel thread

- static void tmrListAddSort (esTmr\_T \*list, esTmr\_T \*tmr)
- static void kTmrInit (void)

Initialization of System Timer Thread.

static void kTmr (void \*arg)

System timer thread code.

#### Idle kernel thread

• static void kldleInit (void)

Initialization of Idle thread.

static void kldle (void \*arg)

Idle thread code.

## 9.3.1 Detailed Description

Kernel inner work.

#### 9.3.2 Macro Definition Documentation

#### 9.3.2.1 #define PRIO\_BM\_DATA\_WIDTH\_LOG2

## Value:

Priority Bit Map log base 2: log2 (PORT\_DATA\_WIDTH\_VAL)

## 9.3.2.2 #define SCHED\_STATE\_INTSRV\_MSK (1U << 0)

Kernel state variable bit position which defines if kernel is in interrupt servicing state.

# 9.3.2.3 #define SCHED\_STATE\_LOCK\_MSK (1U << 1)

Kernel state variable bit position which defines if the kernel is locked or not.

9.3.2.4 #define SYSTMR\_SCHED\_QM\_MSK (1U << 0)

Scheduler is using system timer Quantum mask.

```
9.3.2.5 #define SYSTMR_USR_QM_MSK (1U << 1)
```

User is using system timer Quantum mask.

9.3.2.6 #define SCHED\_POWER\_SAVE 0U

Enable/disable scheduler power savings mode.

9.3.2.7 #define THD\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEFU)

Thread structure signature.

The signature is used to confirm that a structure passed to a kernel function is indeed a esThd\_T thread structure.

9.3.2.8 #define THDQ\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEEU)

Thread Queue structure signature.

The signature is used to confirm that a structure passed to a kernel function is indeed a esThdQ\_T thread queue structure.

```
9.3.2.9 #define DLIST_IS_ENTRY_FIRST( list, entry ) ((entry) == (entry)->list.next)
```

DList macro: is the thread the first one in the list.

```
9.3.2.10 #define DLIST_IS_ENTRY_LAST( list, entry ) DLIST_IS_ENTRY_FIRST(list, entry)
```

DList macro: is the thread the last one in the list.

```
9.3.2.11 #define DLIST_IS_ENTRY_SINGLE( list, entry ) DLIST_IS_ENTRY_FIRST(list, entry)
```

DList macro: is the thread single in the list.

```
9.3.2.12 #define DLIST_ENTRY_PREV( list, entry ) (entry)->list.prev
```

Get the previous entry.

```
9.3.2.13 #define DLIST_ENTRY_NEXT( list, entry ) (entry)->list.next
```

Get the next entry.

```
9.3.2.14 #define DLIST_ENTRY_INIT( list, entry )
```

Value:

Initialize entry.

9.3.2.15 #define DLIST\_ENTRY\_ADD\_AFTER( list, current, entry )

Value:

```
do {
          (entry)->list.next = (current);
          (entry)->list.prev = (entry)->list.next->list.prev;
          (entry)->list.next->list.prev = (entry);
          (entry)->list.prev->list.next = (entry);
} while (OU)
```

Add new entry after current entry.

9.3.2.16 #define DLIST\_ENTRY\_RM( list, entry )

Value:

```
do {
     (entry) -> list.next-> list.prev = (entry) -> list.prev;
     (entry) -> list.prev-> list.next = (entry) -> list.next;
} while (OU)
```

Remove the entry from a list.

9.3.2.17 #define KTMR\_STCK\_SIZE PORT\_STCK\_SIZE(40U)

System Timer kernel thread stack size.

9.3.2.18 #define KIDLE\_STCK\_SIZE PORT\_STCK\_SIZE(40U)

Idle kernel thread stack size.

9.3.3 Typedef Documentation

9.3.3.1 typedef struct sysTmr sysTmr\_T

System Timer type.

9.3.3.2 typedef struct prioBM prioBM\_T

Priority Bit Map type.

9.3.4 Enumeration Type Documentation

9.3.4.1 enum sysTmrState

System timer state enumeration.

**Enumerator** 

**SYSTMR\_ACTIVE** System timer is running. SYSTMR\_ACTIVE **SYSTMR\_INACTIVE** System timer is stopped. SYSTMR\_INACTIVE

9.3.5 Function Documentation

9.3.5.1 static PORT\_C\_INLINE void prioBMInit ( prioBM\_T \* bm ) [static]

Initialize bitmap.

#### **Parameters**

bm	Pointer to the bit map structure

9.3.5.2 static PORT\_C\_INLINE void prioBMSet (  $prioBM_T * bm$ ,  $uint_fast8_t prio$  ) [static]

Set the bit corresponding to the prio argument.

## Parameters

bm	bm Pointer to the bit map structure	
prio Priority which will be marked as used		

### 9.3.5.3 static PORT\_C\_INLINE void prioBMClear ( prioBM\_T \* bm, uint\_fast8\_t prio ) [static]

Clear the bit corresponding to the prio argument.

#### **Parameters**

bm Pointer to the bit map structure		Pointer to the bit map structure
prio Priority which will be marked as unused		Priority which will be marked as unused

## 9.3.5.4 static PORT C INLINE uint\_fast8\_t prioBMGet ( const prioBM T \* bm ) [static]

Get the highest priority set.

### **Parameters**

bm	Pointer to the bit map structure

#### **Returns**

The number of the highest priority marked as used

```
9.3.5.5 static PORT C INLINE bool T prioBMIsEmpty ( const prioBM T * bm ) [static]
```

Is bit map empty?

#### **Parameters**

bm	Pointer to the bit map structure

## Returns

The status of the bit map

## Return values

TRUE	- bit map is empty
FALSE	- there is at least one bit set

### 9.3.5.6 static PORT\_C\_INLINE void schedlnit ( void ) [static]

Initialize Ready Thread Queue structure gRdyQueue and Kernel control structure esKernCtrl.

```
9.3.5.7 static PORT_C_INLINE void schedStart ( void ) [static]
```

Set scheduler data structures ready for multi-threading.

This function is called just before multi-threading has commenced.

```
9.3.5.8 static void schedRdyAddInitl( esThd_T * thd ) [static]
```

Initialize scheduler ready structure during the thread add operation.

### **Parameters**

thd	Pointer to the thread currently being initialized.

Function will initialize scheduler structures during the init phase of the kernel.

```
9.3.5.9 static void schedQml( void ) [static]
```

Do the Quantum (Round-Robin) scheduling.

9.3.5.10 static PORT\_C\_INLINE void schedQmActivate ( void ) [static]

Activate system timer Quantum mode.

9.3.5.11 static PORT\_C\_INLINE void schedQmDeactivate (void ) [static]

Deactivate system timer Quantum mode.

**9.3.5.12** static void schedQmEvaluatel(esThd\_T \* thd) [static]

Evaluate if the system timer Quantum mode is needed.

#### **Parameters**

thd | Pointer to the thread which is ready to be executed

9.3.5.13 static void sysTmrInit ( void ) [static]

Initialize system timer hardware.

9.3.5.14 static void sysTmrTryDeactivate( void ) [static]

Try to deactivate system timer.

9.3.5.15 static void sysTmrTryActivate( void ) [static]

Try to activate system timer.

9.3.5.16 static void kTmrlnit ( void ) [static]

Initialization of System Timer Thread.

9.3.5.17 static void kTmr (void \* arg ) [static]

System timer thread code.

## **Parameters**

arg | NO ARGUMENTS - thread does not use argument

**9.3.5.18** static void kldlelnit ( void ) [static]

Initialization of Idle thread.

9.3.5.19 static void kldle ( void \* arg ) [static]

Idle thread code.

#### **Parameters**

arg NO ARGUMENTS - thread does not use argument

9.3.6 Variable Documentation

9.3.6.1 esThdQ\_T gRdyQueue [static]

Ready Thread queue.

9.4 Configuration 40

# 9.4 Configuration

Configuration settings.

Collaboration diagram for Configuration:



## Kernel configuration options and settings

• #define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

• #define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

#define CFG\_SCHED\_TIME\_QUANTUM 10U

Scheduler Round-Robin time quantum.

• #define CFG\_SYSTMR\_MODE 1U

System timer mode.

#define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

• #define CFG\_SYSTMR\_TICK\_TYPE 2U

The size of the system timer counter.

## Kernel hooks

• #define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

• #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

• #define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

• #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization end hook function.

• #define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

• #define CFG\_HOOK\_CTX\_SW 0U

Context switch hook function.

## 9.4.1 Detailed Description

Configuration settings.

9.4 Configuration 41

#### 9.4.2 Macro Definition Documentation

### 9.4.2.1 #define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

9.4.2.2 #define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

9.4.2.3 #define CFG\_SCHED\_TIME\_QUANTUM 10U

Scheduler Round-Robin time quantum.

9.4.2.4 #define CFG\_SYSTMR\_MODE 1U

System timer mode.

Possible values are:

- 0U fixed mode
- 1U inhibited mode
- · 2U adaptive mode

### 9.4.2.5 #define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

Note

This setting is valid only if configuration option CFG\_SYSTMR\_CLOCK\_FREQUENCY is properly set in port configuration file cpu\_cfg.h

## 9.4.2.6 #define CFG\_SYSTMR\_TICK\_TYPE 2U

The size of the system timer counter.

Possible values are:

- 0U 8 bit counter
- 1U 16 bit counter
- 2U 32 bit counter

## 9.4.2.7 #define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

9.4.2.8 #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

9.4.2.9 #define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

9.4.2.10 #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization end hook function.

9.4 Configuration 42

9.4.2.11 #define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

9.4.2.12 #define CFG\_HOOK\_CTX\_SW 0U

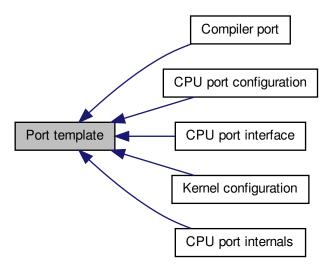
Context switch hook function.

9.5 Port template 43

# 9.5 Port template

Templates.

Collaboration diagram for Port template:



## Modules

• CPU port configuration

CPU port specific configuration options.

• CPU port interface

CPU port macros and functions.

CPU port internals

CPU port inner work.

· Compiler port

Compiler provided macros and data types.

Kernel configuration

Default Kernel configuration options.

# 9.5.1 Detailed Description

Templates.

## 9.6 Compiler port

Compiler provided macros and data types.

Collaboration diagram for Compiler port:



### Compiler provided macros

Port interface macros and port specific macros

These macros are used to ease the writing of ports. All macros prefixed with PORT\_ are part of the port interface.

• #define PORT C INLINE inline

C extension - make a function inline.

#define PORT\_C\_INLINE\_ALWAYS inline

C extension - make a function inline - always.

#define PORT\_C\_NAKED

Omit function prologue/epilogue sequences.

• #define PORT\_C\_FUNC "unknown"

Provides function name for assert macros.

• #define PORT\_C\_WEAK

Declares a weak function.

#define PORT\_C\_ALIGNED(expr)

This attribute specifies a minimum alignment (in bytes) for variables of the specified type.

• #define PORT\_HWREG\_SET(reg, mask, val)

A standardized way of properly setting the value of HW register.

Compiler provided data types

The compiler port must provide some C90 (C99) data types

The compiler port must:

 declare sets of integer types having specified widths, standard type definitions and shall define corresponding sets of macros.

Types are defined in the following categories:

- · Integer types having certain exact widths
- · Fastest integer types having at least certain specified widths
- · Integer types wide enough to hold pointers to objects
- · standard type definitions

The following exact-width integer types are required:

- int8\_t
- int16 t
- int32 t
- uint8\_t
- uint16\_t
- uint32\_t

The following fastest minimum-width integer types are required:

- · int\_fast8\_t
- int\_fast16\_t
- · int fast32 t
- · uint\_fast8\_t
- · uint\_fast16\_t
- · uint\_fast32\_t

The following integer types capable of holding object pointers are required:

- intptr\_t
- · uintptr\_t

The following standard type definitions are required:

- NULL
- ptrdiff t
- · size t

```
    enum boolType {
        TRUE = 1U,
        FALSE = 0U }
        Bool data type.
    typedef enum boolType bool_T
```

9.6.1 Detailed Description

Compiler provided macros and data types.

9.6.2 Macro Definition Documentation

Bool data type.

9.6.2.1 #define PORT\_C\_INLINE inline

C extension - make a function inline.

The point of making a function inline is to hint to the compiler that it is worth making some form of extra effort to call the function faster than it would otherwise - generally by substituting the code of the function into its caller. As well as eliminating the need for a call and return sequence, it might allow the compiler to perform certain optimizations between the bodies of both functions.

#### 9.6.2.2 #define PORT\_C\_INLINE\_ALWAYS inline

C extension - make a function inline - always.

Generally, functions are not inlined unless optimization is specified. For functions declared inline, this attribute inlines the function even if no optimization level was specified.

#### 9.6.2.3 #define PORT\_C\_NAKED

Omit function prologue/epilogue sequences.

This attribute will indicate that the specified function does not need prologue/epilogue sequences generated by the compiler. It is up to the programmer to provide these sequences. The only statements that can be safely included in naked functions are <code>asm</code> statements that do not have operands. All other statements, including declarations of local variables, <code>if</code> statements, and so forth, should be avoided. Naked functions should be used to implement the body of an assembly function, while allowing the compiler to construct the requisite function declaration for the assembler.

```
9.6.2.4 #define PORT_C_FUNC "unknown"
```

Provides function name for assert macros.

```
9.6.2.5 #define PORT_C_WEAK
```

Declares a weak function.

The weak attribute causes the declaration to be emitted as a weak symbol rather than a global. This is primarily useful in defining library functions that can be overridden in user code, though it can also be used with non-function declarations.

```
9.6.2.6 #define PORT_C_ALIGNED( expr )
```

This attribute specifies a minimum alignment (in bytes) for variables of the specified type.

Note

The alignment of any given struct or union type is required by the ISO C standard to be at least a perfect multiple of the lowest common multiple of the alignments of all of the members of the struct or union in question.

```
9.6.2.7 #define PORT_HWREG_SET( reg, mask, val )
```

#### Value:

```
do {
    portReg_T tmp;
    tmp = (reg);
    tmp &= ~(mask);
    tmp |= ((mask) & (val));
    (reg) = tmp;
} while (00)
```

A standardized way of properly setting the value of HW register.

## **Parameters**

reg Register which will be written to	
mask	The bit mask which will be applied to register and val argument
val	Value to be written into the register

#### 9.6.3 Typedef Documentation

9.6.3.1 typedef enum boolType bool\_T

Bool data type.

9.6.4 Enumeration Type Documentation

9.6.4.1 enum boolType

Bool data type.

Enumerator

TRUE TRUE. TRUE
FALSE FALSE. FALSE

## 9.7 CPU port interface

CPU port macros and functions.

Collaboration diagram for CPU port interface:



# **Data Structures**

struct portStck

Stack structure used for stack declaration in order to force the alignment.

struct portCtx

Port context structure.

### **Typedefs**

typedef uint8\_t portReg\_T

Data type which corresponds to the general purpose register.

typedef struct portStck portStck\_T
 Stack type.

## **Functions**

struct portStck \_\_attribute\_\_ ((aligned(1)))

Alignment of stack structure.

#### Variables

- struct portCtx \_\_attribute\_\_
- portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

const PORT\_C\_ROM portReg\_T pwr2LKP [PORT\_DATA\_WIDTH\_VAL]

Look up table for:  $2^{\wedge}$ n expression.

## Port constants

• #define PORT\_DATA\_WIDTH\_VAL 8U

This macro specifies the bit width of CPU data registers.

#define PORT\_STCK\_MINSIZE\_VAL sizeof(struct portCtx)

This macro specifies the minimal size of the thread stack.

 #define PORT\_SYSTMR\_RELOAD\_VAL (CFG\_SYSTMR\_CLOCK\_FREQUENCY / CFG\_SYSTMR\_EVEN-T\_FREQUENCY)

System timer reload value for one tick.

#define PORT\_SYSTMR\_MAX\_VAL 0xFFU

System timer maximum value.

#define PORT\_SYSTMR\_MAX\_TICKS\_VAL (PORT\_SYSTMR\_MAX\_VAL / PORT\_SYSTMR\_RELOAD\_V-AL)

Maximum number of ticks the system timer can accept.

### Interrupt management

void portIntDisable (void)

Disable interrupts.

portReg\_T portIntGet\_ (void)

Get the current status of enabled/disabled interrupts.

void portIntSet\_ (portReg\_T status)

Set the status of interrupts according to the status argument.

• #define PORT INT DISABLE() portIntDisable ()

Disable all interrupt sources.

#define PORT\_ISR\_ENTER()

Enter ISR. Increment gPortIsrNesting\_ variable to keep track of ISR nesting.

#define PORT ISR EXIT()

Exit ISR. Decrement gPortIsrNesting\_ variable to keep track of ISR nesting.

#define PORT\_ISR\_IS\_LAST() (0U == gPortIsrNesting ? TRUE : FALSE)

If isrNesting variable is zero then the last ISR is executing and scheduler should be invoked.

#### **Critical section management**

Disable/enable interrupts by preserving the status of interrupts.

Generally speaking these macros would store the status of the interrupt disable flag in the local variable declared by PORT\_CRITICAL\_DECL and then disable interrupts. Local variable is allocated in all of eSolid RTOS functions that need to disable interrupts. Macros would restore the interrupt status by copying back the allocated variable into the CPU's status register.

#define PORT\_CRITICAL\_DECL() portReg\_T intStatus\_

Declare the interrupt status variable.

• #define PORT CRITICAL ENTER()

Enter critical section.

#define PORT\_CRITICAL\_EXIT() portIntSet\_(intStatus\_)

Exit critical section.

## Scheduler support

#### Note

These functions are extensively used by the scheduler and therefore they should be optimized for the architecture being used.

• uint\_fast8\_t portFindLastSet\_ (portReg\_T val)

Find last set bit in a word.

void portSysTmrInit\_ (void)

Initialize systick timer and associated interrupt.

void portSysTmrTerm (void)

Stop the sistem timer.

void portSysTmrReload\_ (esTick\_T ticks)

Reload the system timer.

void portSysTmrEnable\_ (void)

Enable the system timer.

void portSysTmrDisable\_ (void)

Disable the system timer.

void portSysTmrlsrEnable\_ (void)

Disable the system timer interrupt.

void portSysTmrlsrDisable\_ (void)

Enable the system timer interrupt.

void portThdStart\_ (void)

Start the first thread.

#define PORT\_FIND\_LAST\_SET(val) portFindLastSet\_(val)

Find last set bit in a word.

#define PORT\_PWR2(pwr) (1U << (pwr))</li>

Helper macro: calculate  $2^{\wedge}$  pwr expression.

#define PORT\_SYSTMR\_INIT() portSysTmrInit\_()

Initialize system timer and associated interrupt.

#define PORT\_SYSTMR\_TERM() portSysTmrTerm\_()

Stop the timer if it is running and disable associated interrupt.

#define PORT\_SYSTMR\_RELOAD(ticks) portSysTmrReload\_(ticks)

Reload the system timer with specified number of ticks.

#define PORT\_SYSTMR\_ENABLE() portSysTmrEnable\_()

Enable the system timer.

#define PORT\_SYSTMR\_DISABLE() portSysTmrDisable\_()

Disable the system timer.

• #define PORT\_SYSTMR\_ISR\_ENABLE() portSysTmrlsrEnable\_()

Enable the system timer interrupt.

#define PORT\_SYSTMR\_ISR\_DISABLE() portSysTmrlsrDisable\_()

Disable the system timer interrupt.

## Dispatcher context switching

void \* portCtxInit\_ (void \*stck, size\_t stckSize, void(\*thdf)(void \*), void \*arg)

Initialize the thread context.

void portCtxSw\_ (void)

Do the context switch - invoked from API.

void portCtxSwlsr\_ (void)

Do the context switch - invoked from ISR.

#define PORT\_CTX\_INIT(stck, stackSize, thread, arg) portCtxInit\_(stck, stackSize, thread, arg)

Initialize the thread context.

#define PORT\_CTX\_SW() portCtxSw\_()

Do the context switch - invoked from API level.

• #define PORT\_CTX\_SW\_ISR() portCtxSwlsr\_()

Do the context switch - invoked from ISR level.

#define PORT\_THD\_START() portThdStart\_()

Start the first thread.

## General port macros

#define PORT\_STCK\_SIZE(size)

Calculate the stack size.

• #define PORT CRITICAL EXIT SLEEP() PORT CRITICAL EXIT()

TODO

#define PORT\_INIT\_EARLY() portInitEarly\_()

Early port initialization.

#define PORT\_INIT() portInit\_()

Port initialization.

#define PORT\_INIT\_LATE() portInitLate\_()

Late port initialization.

#### **General port functions**

void portInitEarly (void)

Early port initialization.

void portInit\_ (void)

Port initialization.

void portInitLate\_ (void)

Late port initialization.

### 9.7.1 Detailed Description

CPU port macros and functions. Since this header file is included with the API of the kernel a few naming conventions are defined in order to avoid name clashing with the names of objects from libraries included by application code.

## 1) Macro naming conventions

For macro naming try to follow these rules:

- All standard PORT API macro names are prefixed with: PORT\_.
- All other macros which are specific to the port used are prefixed with: CPU .

## 2) Type declaration naming conventions

For type declaration naming try to follow these rules:

- All type declaration names are prefixed with: cpu.
- 3) Global variable naming conventions

For global variable naming try to follow these rules:

• All global variable names are prefixed with: cpu.

## 4) Funcion naming convetions

For functions naming try to follow these rules:

- All function names are prefixed with: **port** and postfixed with: \_ (underscore).
- All other functions which are specific to the port used are prefixed with: cpu and postfixed with: \_\_ (underscore).
- The exception to above two rules are the names of functions used for Interrupt Service Routines. They can have any name required by port.

#### 9.7.2 Macro Definition Documentation

#### 9.7.2.1 #define PORT\_DATA\_WIDTH\_VAL 8U

This macro specifies the bit width of CPU data registers.

9.7.2.2 #define PORT\_STCK\_MINSIZE\_VAL sizeof(struct portCtx)

This macro specifies the minimal size of the thread stack.

Generally minimal stack size is equal to the size of context structure

```
9.7.2.3 #define PORT_SYSTMR_RELOAD_VAL (CFG_SYSTMR_CLOCK_FREQUENCY / CFG_SYSTMR_EVENT_FREQUENCY)
```

System timer reload value for one tick.

This is a calculated value for one system tick period

9.7.2.4 #define PORT\_SYSTMR\_MAX\_VAL 0xFFU

System timer maximum value.

This macro specifies maximum value that can be reloaded into system timer counter. For example, if the system timer is a 8-bit counter than this macro would have the value of 0xFFU.

```
9.7.2.5 #define PORT_SYSTMR_MAX_TICKS_VAL (PORT_SYSTMR_MAX_VAL / PORT_SYSTMR_RELOAD_VAL)
```

Maximum number of ticks the system timer can accept.

```
9.7.2.6 #define PORT_INT_DISABLE( ) portIntDisable_()
```

Disable all interrupt sources.

```
9.7.2.7 #define PORT_ISR_ENTER( )
```

## Value:

Enter ISR. Increment gPortIsrNesting\_variable to keep track of ISR nesting.

Variable gPortIsrNesting\_ is needed only if the port does not support any other method of detecting when the last ISR is executing.

```
9.7.2.8 #define PORT_ISR_EXIT( )
```

## Value:

Exit ISR. Decrement gPortIsrNesting\_ variable to keep track of ISR nesting.

Variable gPortIsrNesting\_ is needed only if the port does not support any other method of detecting when the last ISR is executing.

```
9.7.2.9 #define PORT_ISR_IS_LAST( ) (0U == gPortIsrNesting ? TRUE : FALSE)
```

If isrNesting variable is zero then the last ISR is executing and scheduler should be invoked.

#### Returns

Is the currently executed ISR the last one?

#### Return values

TRUE	- this is last ISR
FALSE	- this is not the last ISR

9.7.2.10 #define PORT\_CRITICAL\_DECL( ) portReg\_T intStatus\_

Declare the interrupt status variable.

This variable is used to store the current state of enabled ISRs.

```
9.7.2.11 #define PORT_CRITICAL_ENTER( )
```

#### Value:

```
do {
    intStatus_ = portIntGet_();
    portIntDisable_();
} while (OU)
```

Enter critical section.

9.7.2.12 #define PORT\_CRITICAL\_EXIT( ) portIntSet\_(intStatus\_)

Exit critical section.

9.7.2.13 #define PORT\_FIND\_LAST\_SET( val ) portFindLastSet\_(val)

Find last set bit in a word.

This function is used by the scheduler to efficiently determine the highest priority of thread ready for execution. For algorithm details see: http://en.wikipedia.org/wiki/Find\_first\_set.

#### **Returns**

The position of the last set bit in a word

```
9.7.2.14 #define PORT_PWR2( pwr ) (1U << (pwr))
```

Helper macro: calculate 2<sup>^</sup>pwr expression.

Some ports may want to use look up tables instead of shifting operation

```
9.7.2.15 #define PORT_SYSTMR_INIT( ) portSysTmrInit_()
```

Initialize system timer and associated interrupt.

This macro will only initialize system timer and associated interrupt. It MUST NOT start the system timer in this stage. Responsibility:

- · initialize system timer
- · initialize system timer interrupt

## 9.7.2.16 #define PORT\_SYSTMR\_TERM( ) portSysTmrTerm\_()

Stop the timer if it is running and disable associated interrupt.

Responsibility:

- · disable system timer interrupt
- · stop and disable system timer

## 9.7.2.17 #define PORT\_SYSTMR\_RELOAD( ticks ) portSysTmrReload\_(ticks)

Reload the system timer with specified number of ticks.

Responsibility:

- · calculate the reload value based on PORT\_SYSTMR\_RELOAD\_VAL
- · reload the system timer

## 9.7.2.18 #define PORT\_SYSTMR\_ENABLE( ) portSysTmrEnable\_()

Enable the system timer.

Responsibility:

• enable (run) the system timer counter

## 9.7.2.19 #define PORT\_SYSTMR\_DISABLE( ) portSysTmrDisable\_()

Disable the system timer.

Responsibility:

· disable (stop) the system timer counter

## 9.7.2.20 #define PORT\_SYSTMR\_ISR\_ENABLE( ) portSysTmrlsrEnable\_()

Enable the system timer interrupt.

Responsibility:

· allow system timer interrupt to occur

# $9.7.2.21 \quad \hbox{\#define PORT\_SYSTMR\_ISR\_DISABLE(} \quad \hbox{) portSysTmrlsrDisable\_()}$

Disable the system timer interrupt.

Responsibility:

disallow system timer interrupt to occur

# 9.7.2.22 #define PORT\_CTX\_INIT( stck, stackSize, thread, arg ) portCtxInit\_(stck, stackSize, thread, arg)

Initialize the thread context.

### **Parameters**

in,out	stck	Pointer to the allocated thread stck. The pointer points to the beginning of the
		memory as defined per C language. It's up to port function to adjust the pointer
		according to the stck type: full descending or full ascending one.
	stackSize	The size of allocated stck in bytes.
in	thread	Pointer to the thread function.
in	arg	Argument that will be passed to thread function at the starting of execution.

#### Returns

The new top of stck after thread context initialization.

```
9.7.2.23 #define PORT_CTX_SW( ) portCtxSw_()
```

Do the context switch - invoked from API level.

```
9.7.2.24 #define PORT_CTX_SW_ISR( ) portCtxSwlsr_()
```

Do the context switch - invoked from ISR level.

```
9.7.2.25 #define PORT_THD_START( ) portThdStart_()
```

Start the first thread.

```
9.7.2.26 #define PORT_STCK_SIZE( size )
```

### Value:

Calculate the stack size.

This macro is used when specifying the size of thread stack. Responsibility:

- add to size the minimal stack size specified by PORT\_STCK\_MINSIZE\_VAL.
- · if it is needed by the port make sure the alignment is correct.

```
9.7.2.27 #define PORT_CRITICAL_EXIT_SLEEP( ) PORT_CRITICAL_EXIT()
```

TODO.

```
9.7.2.28 #define PORT_INIT_EARLY( ) portInitEarly ()
```

Early port initialization.

This macro will be called at early initialization stage from esKernInit() function. It is called before any kernel data initialization. Usually this macro would be used to setup memory space, fill the memory with debug value or something similar.

```
9.7.2.29 #define PORT_INIT( ) portInit_()
```

Port initialization.

This macro will be called after kernel data structure initialization from esKernInit() function.

```
9.7.2.30 #define PORT_INIT_LATE( ) portInitLate_()
```

Late port initialization.

This macro will be called just a moment before the multitasking is started. The macro is called from esKernStart() function.

## 9.7.3 Typedef Documentation

## 9.7.3.1 typedef uint8\_t portReg\_T

Data type which corresponds to the general purpose register.

9.7.3.2 typedef struct portStck portStck\_T

Stack type.

9.7.4 Function Documentation

9.7.4.1 struct portStck \_\_attribute\_\_ ( (aligned(1)) )

Alignment of stack structure.

9.7.4.2 void portIntDisable\_( void )

Disable interrupts.

9.7.4.3 portReg\_T portIntGet\_( void )

Get the current status of enabled/disabled interrupts.

Returns

Interrupt status

9.7.4.4 void portIntSet\_( portReg\_T status )

Set the status of interrupts according to the status argument.

#### **Parameters**

status The status of interrupts that will be set by the function.

9.7.4.5 uint\_fast8\_t portFindLastSet\_( portReg\_T val )

Find last set bit in a word.

#### **Parameters**

val Value which needs to be evaluated

This function is used by the scheduler to efficiently determine the highest priority of thread ready for execution. For algorithm details see: http://en.wikipedia.org/wiki/Find\_first\_set.

Returns

The position of the last set bit in a word

9.7.4.6 void portSysTmrInit\_ (void)

Initialize systick timer and associated interrupt.

This function will be called just a moment before the multitasking is started. The function is called from esKernStart() function. It should setup:

- · systick timer (scheduler uses tick event to switch between threads of same priority)
- · systick timer interrupt

Note

This function MUST NOT enable system timer events. System timer events are enabled/disabled by portSysTmrEnable\_() and portSysTmrDisable\_() functions.

9.7.4.7 void portSysTmrTerm\_ ( void )

Stop the sistem timer.

9.7.4.8 void portSysTmrReload\_ ( esTick\_T ticks )

Reload the system timer.

## **Parameters**

ticks	How much ticks is needed to delay

9.7.4.9 void portSysTmrEnable\_( void )

Enable the system timer.

9.7.4.10 void portSysTmrDisable\_( void )

Disable the system timer.

9.7.4.11 void portSysTmrlsrEnable\_ ( void )

Disable the system timer interrupt.

9.7.4.12 void portSysTmrlsrDisable\_ ( void )

Enable the system timer interrupt.

9.7.4.13 void portThdStart\_ ( void )

Start the first thread.

9.7.4.14  $void* portCtxInit_( void* stck, size_t stckSize, void(*)(void*) thdf, void* arg)$ 

Initialize the thread context.

#### **Parameters**

in,out	stck	Pointer to the allocated thread stck. The pointer points to the beginning of the
		memory as defined per C language. It's up to port function to adjust the pointer
		according to the stck type: full descending or full ascending one.
	stckSize	The size of allocated stck in bytes.
in	thdf	Pointer to the thread function.
in	arg	Argument that will be passed to thread function at the starting of execution.

#### Returns

The new top of stck after thread context initialization.

9.7.4.15 void portCtxSw\_( void )

Do the context switch - invoked from API.

9.7.4.16 void portCtxSwlsr\_( void )

Do the context switch - invoked from ISR.

9.7.4.17 void portInitEarly\_ (void)

Early port initialization.

This function will be called at early initialization stage from esKernInit() function. It is called before any kernel data initialization. Usually this function would be used to setup memory space, fill the memory with debug value or something similar.

9.7.4.18 void portInit\_ (void)

Port initialization.

This function will be called after kernel data structure initialization from esKernInit() function.

9.7.4.19 void portInitLate\_ ( void )

Late port initialization.

This function will be called just a moment before the multitasking is started. The function is called from esKernStart() function.

9.7.5 Variable Documentation

9.7.5.1 portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

9.7.5.2 const PORT\_C\_ROM portReg\_T pwr2LKP[PORT\_DATA\_WIDTH\_VAL]

Look up table for:  $2^{n}$  expression.

This look up table can be used to accelerate the Logical Shift Left operations which are needed to set bits inside the priority bit map. In plain C this operation would be written as: (1U << n), but in many 8-bit CPUs this operation can be lengthy. If there is a need for faster operation than this table can be used instead of the mentioned C code.

To use the look up table change PORT\_PWR2 macro implementation from: (1U << (pwr)) to pwr2LK-P[pwr]

# 9.8 CPU port internals

CPU port inner work.

Collaboration diagram for CPU port internals:



## **Functions**

uint\_fast8\_t portFindLastSet\_ (portReg\_T val)

Find last set bit in a word.

void \* portCtxInit\_ (void \*stck, size\_t stckSize, void(\*thdf)(void \*), void \*arg)
 Initialize the thread context.

### **Variables**

portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

const PORT\_C\_ROM portReg\_T pwr2LKP [PORT\_DATA\_WIDTH\_VAL]
 Look up table for: 2<sup>^</sup>n expression.

## 9.8.1 Detailed Description

CPU port inner work.

#### 9.8.2 Function Documentation

9.8.2.1 uint\_fast8\_t portFindLastSet\_( portReg\_T val )

Find last set bit in a word.

## **Parameters**

val Value which needs to be evaluated

This function is used by the scheduler to efficiently determine the highest priority of thread ready for execution. For algorithm details see: http://en.wikipedia.org/wiki/Find\_first\_set.

#### Returns

The position of the last set bit in a word

9.8.2.2 void\* portCtxInit\_ ( void \* stck, size\_t stckSize, void(\*)(void \*) thdf, void \* arg )

Initialize the thread context.

#### **Parameters**

in,out	stck	Pointer to the allocated thread stck. The pointer points to the beginning of the
		memory as defined per C language. It's up to port function to adjust the pointer
		according to the stck type: full descending or full ascending one.
	stckSize	The size of allocated stck in bytes.
in	thdf	Pointer to the thread function.
in	arg	Argument that will be passed to thread function at the starting of execution.

#### Returns

The new top of stck after thread context initialization.

9.8.3 Variable Documentation

9.8.3.1 portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

9.8.3.2 const PORT\_C\_ROM portReg\_T pwr2LKP[PORT\_DATA\_WIDTH\_VAL]

#### Initial value:

```
= {
    (1U << 0), (1U << 1), (1U << 2), (1U << 3), (1U << 5), (1U << 6), (1U << 7),
```

Look up table for:  $2^n$  expression.

This look up table can be used to accelerate the Logical Shift Left operations which are needed to set bits inside the priority bit map. In plain C this operation would be written as: (1U << n), but in many 8-bit CPUs this operation can be lengthy. If there is a need for faster operation than this table can be used instead of the mentioned C code.

To use the look up table change PORT\_PWR2 macro implementation from: (1U << (pwr)) to pwr2LK-P[pwr]

## 9.9 CPU port configuration

CPU port specific configuration options.

Collaboration diagram for CPU port configuration:



### Port General configuration

Configuration options and settings which are available for every port.

#### Note

- 1) All port General configuration macros are prefixed with CFG\_ string.
- 2) All port specific options and constants are prefixed with CPU string.
- #define CFG\_CRITICAL\_PRIO 1U

Priority of critical sections in kernel.

• #define CFG\_SYSTMR\_CLOCK\_FREQUENCY 1000000UL

The frequency of clock which is used for the system timer.

## 9.9.1 Detailed Description

CPU port specific configuration options. Each configuration option or setting has its own default value when not defined by the application. When application needs to change a setting it just needs to define a configuration macro with another value and the default configuration macro will be overridden.

#### 9.9.2 Macro Definition Documentation

## 9.9.2.1 #define CFG\_CRITICAL\_PRIO 1U

Priority of critical sections in kernel.

This option varies with the MCU used. In the simplest case when the MCU does not support interrupt priorities than only one priority level is available. In that case critical section will simply disable interrupts on entry and enable them at exit.

### 9.9.2.2 #define CFG\_SYSTMR\_CLOCK\_FREQUENCY 1000000UL

The frequency of clock which is used for the system timer.

Specify here the clock value so the kernel can properly manage system tick event generation. Usually system timer will use the clock of the processor. A hardware timer is configured to generate an interrupt at a rate between 10 and 1000 Hz which provides the system tick. The rate of interrupt is application specific and depends on the desired resolution system tick time source. However, the faster the tick rate, the higher the overhead will be imposed on the system.

## 9.10 Kernel configuration

Default Kernel configuration options.

Collaboration diagram for Kernel configuration:



Kernel configuration options and settings

Kernel default configuration

• #define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

• #define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

#define CFG\_SCHED\_TIME\_QUANTUM 10U

Scheduler Round-Robin time quantum.

#define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

#### Kernel hooks

#define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

• #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

• #define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

• #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization hook function.

• #define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

• #define CFG\_HOOK\_CTX\_SW 0U

Kernel context switch hook function.

## 9.10.1 Detailed Description

Default Kernel configuration options. Each configuration option or setting has its own default value when not defined by the application. When application needs to change a setting it just needs to define a configuration macro with another value and the default configuration macro will be overridden.

#### 9.10.2 Macro Definition Documentation

#### 9.10.2.1 #define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

During the development cycle of the application this option should be turned on. When this configuration option is turned on the kernel API functions will also check arguments passed to them. If an invalid argument is detected the execution of the application will stop and the user will be informed about the error condition.

#### Possible values:

- · 0U API validation is OFF
- · 1U API validation is ON

#### Note

The error checking use userAssert() hook function to provide the information about the error condition.

### 9.10.2.2 #define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

#### Possible values:

- Min: 2U (two priority levels)
- Max: 256U

#### Warning

Scheduler will have undefined behavior if there is no ready thread to run (e.g. empty Ready Threads Queue) at the time it is invoked.

## 9.10.2.3 #define CFG\_SCHED\_TIME\_QUANTUM 10U

Scheduler Round-Robin time quantum.

This constant is the number of system ticks allowed for the threads before preemption occurs. Setting this value to zero disables the preemption for threads with equal priority and the round robin becomes cooperative. Note that higher priority threads can still preempt, the kernel is always preemptive.

#### Note

Disabling the round robin preemption makes the kernel more compact and generally faster.

## 9.10.2.4 #define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

Specify the desired resolution system tick time source. This setting is valid only if configuration option CFG\_SYST-MR\_CLOCK\_FREQUENCY is properly set in port configuration file cpu\_cfg.h

#### 9.10.2.5 #define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

This hook is called just a moment before a system timer event is processed.

#### Note

This hook will call userSysTmr() function.

9.10.2.6 #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

This hook is called at the beginning of esKernInit() function.

Note

This hook will call userKernInit() function.

9.10.2.7 #define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

This hook is called at the beginning of esKernStart() function.

Note

This hook will call userKernStart() function.

9.10.2.8 #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization hook function.

This hook is called at the end of esThdInit() function.

Note

This hook will call userThdInitEnd() function.

9.10.2.9 #define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

This hook is called when a thread terminates.

Note

This hook will call userThdTerm() function.

9.10.2.10 #define CFG\_HOOK\_CTX\_SW 0U

Kernel context switch hook function.

This hook is called at each context switch.

Note

This hook will call userCtxSw() function.

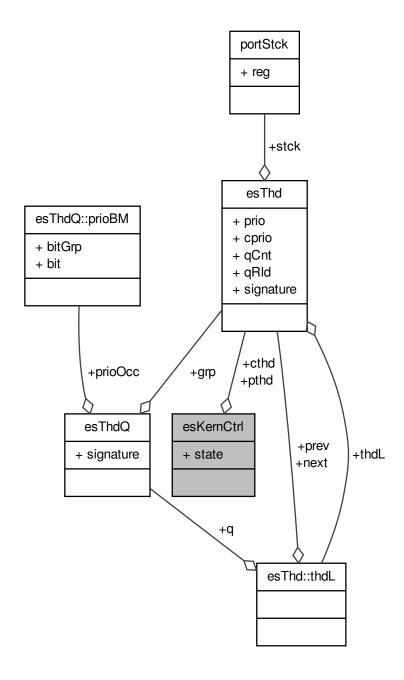
# 10 Data Structure Documentation

# 10.1 esKernCtrl Struct Reference

Kernel control block structure.

#include <kernel.h>

Collaboration diagram for esKernCtrl:



### **Data Fields**

struct esThd \* cthd

Pointer to the Current Thread.

struct esThd \* pthd

Pointer to the Pending Thread to be switched.

• enum esKernState state

State of kernel.

### 10.1.1 Detailed Description

Kernel control block structure.

This structure holds important status data about the kernel. Since all data within the structure is somewhat related and accessed within the same pieces of code it was decided it is better to group all kernel data into the structure. This way the compiler can generate code that gets the address of the structure and then use relative indirect addressing to access all members of the structure. This results in more efficient code on architectures that have relative indirect addressing capability.

## Object class:

Regular API object, this object is part of the application programming interface.

10.1.2 Field Documentation

10.1.2.1 struct esThd\* esKernCtrl::cthd

Pointer to the Current Thread.

10.1.2.2 struct esThd\* esKernCtrl::pthd

Pointer to the Pending Thread to be switched.

10.1.2.3 enum esKernState esKernCtrl::state

State of kernel.

The documentation for this struct was generated from the following file:

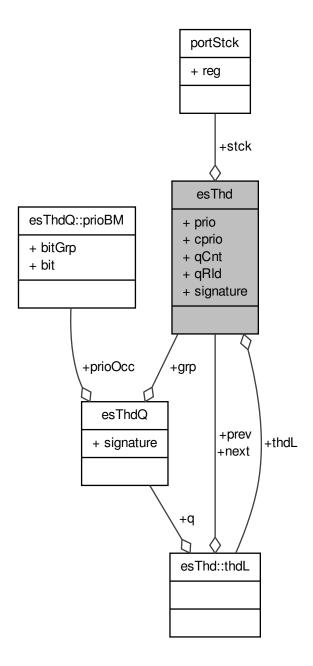
· kernel.h

## 10.2 esThd Struct Reference

Thread structure.

#include <kernel.h>

Collaboration diagram for esThd:



### **Data Structures**

• struct thdL

Thread linked List structure.

# Data Fields

portStck\_T \* stck

Pointer to thread's Top Of Stack.

struct esThd::thdL thdL

Thread linked list.

uint\_fast8\_t prio

Thread current priority level.

uint\_fast8\_t cprio

Constant Thread Priority level.

uint\_fast8\_t qCnt

Quantum counter.

uint\_fast8\_t qRld

Quantum counter reload value.

portReg\_T signature

Thread structure signature, see Error checking.

#### 10.2.1 Detailed Description

Thread structure.

A thread structure is a data structure used by kernel to maintain information about a thread. Each thread requires its own ID structure and the structure is allocated in user memory space (RAM). The address of the thread's ID structure is provided to OS thread-related services.

Thread structure is used as thread ID and a thread is always referenced using this structure.

Object class:

Regular **API** object, this object is part of the application programming interface.

10.2.2 Field Documentation

10.2.2.1 portStck\_T\* esThd::stck

Pointer to thread's Top Of Stack.

10.2.2.2 struct esThd::thdL esThd::thdL

Thread linked list.

10.2.2.3 uint\_fast8\_t esThd::prio

Thread current priority level.

10.2.2.4 uint\_fast8\_t esThd::cprio

Constant Thread Priority level.

10.2.2.5 uint\_fast8\_t esThd::qCnt

Quantum counter.

10.2.2.6 uint\_fast8\_t esThd::qRld

Quantum counter reload value.

10.2.2.7 portReg\_T esThd::signature

Thread structure signature, see Error checking.

The documentation for this struct was generated from the following file:

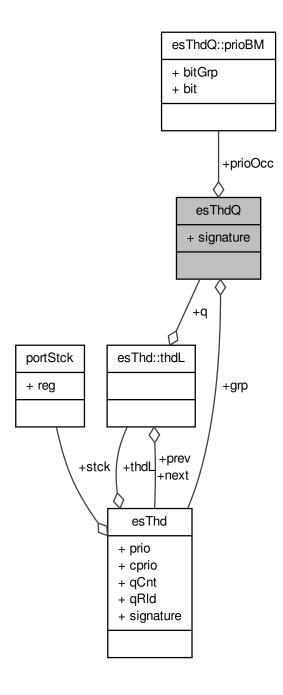
· kernel.h

### 10.3 esThdQ Struct Reference

Thread Queue structure.

#include <kernel.h>

Collaboration diagram for esThdQ:



#### **Data Structures**

struct prioBM

Priority Bit Map structure.

### **Data Fields**

• struct esThdQ::prioBM prioOcc

Priority Occupancy.

struct esThd \* grp [CFG\_SCHED\_PRIO\_LVL]

Array of Group Head pointers to priority groups.

• portReg\_T signature

Thread Queue struct signature, see Error checking.

### 10.3.1 Detailed Description

Thread Queue structure.

Object class:

Regular API object, this object is part of the application programming interface.

10.3.2 Field Documentation

10.3.2.1 struct esThdQ::prioBM esThdQ::prioOcc

Priority Occupancy.

10.3.2.2 struct esThd\* esThdQ::grp[CFG\_SCHED\_PRIO\_LVL]

Array of Group Head pointers to priority groups.

10.3.2.3 portReg\_T esThdQ::signature

Thread Queue struct signature, see Error checking.

The documentation for this struct was generated from the following file:

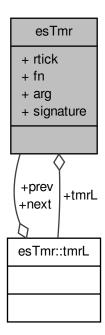
· kernel.h

### 10.4 esTmr Struct Reference

Timer structure.

#include <kernel.h>

Collaboration diagram for esTmr:



### **Data Structures**

• struct tmrL

Timer linked list structure.

### Data Fields

struct esTmr::tmrL tmrL

Timer linked List.

esTick\_T rtick

Relative tick value.

void(\* fn )(void \*)

Callback function pointer.

void \* arg

Callback function argument.

• portReg\_T signature

Timer structure signature, see Error checking.

# 10.4.1 Detailed Description

Timer structure.

10.4.2 Field Documentation

10.4.2.1 struct esTmr::tmrL esTmr::tmrL

Timer linked List.

10.4.2.2 esTick\_T esTmr::rtick

Relative tick value.

10.4.2.3 void(\* esTmr::fn)(void \*)

Callback function pointer.

10.4.2.4 void\* esTmr::arg

Callback function argument.

10.4.2.5 portReg\_T esTmr::signature

Timer structure signature, see Error checking.

The documentation for this struct was generated from the following file:

kernel.h

# 10.5 portCtx Struct Reference

Port context structure.

#include <cpu.h>

Collaboration diagram for portCtx:



### **Data Fields**

• portReg\_T r0

Data pushed on stack during context switching.

10.5.1 Detailed Description

Port context structure.

### 10.5.2 Field Documentation

# 10.5.2.1 portReg\_T portCtx::r0

Data pushed on stack during context switching.

The documentation for this struct was generated from the following file:

• cpu.h

# 10.6 portStck Struct Reference

Stack structure used for stack declaration in order to force the alignment.

```
#include <cpu.h>
```

Collaboration diagram for portStck:



# Data Fields

portReg\_T reg

A structure field representing stack data.

# 10.6.1 Detailed Description

Stack structure used for stack declaration in order to force the alignment.

10.6.2 Field Documentation

10.6.2.1 portReg\_T portStck::reg

A structure field representing stack data.

The documentation for this struct was generated from the following file:

• cpu.h

# 10.7 esThdQ::prioBM Struct Reference

Priority Bit Map structure.

#include <kernel.h>

Collaboration diagram for esThdQ::prioBM:

esThdQ::prioBM + bitGrp + bit

### **Data Fields**

portReg\_T bitGrp

Bit group indicator.

portReg\_T bit [PRIO\_BM\_GRP\_INDX]

Bit priority indicator.

10.7.1 Detailed Description

Priority Bit Map structure.

10.7.2 Field Documentation

10.7.2.1 portReg\_T esThdQ::prioBM::bitGrp

Bit group indicator.

10.7.2.2 portReg\_T esThdQ::prioBM::bit[PRIO\_BM\_GRP\_INDX]

Bit priority indicator.

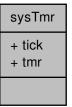
The documentation for this struct was generated from the following file:

• kernel.h

# 10.8 sysTmr Struct Reference

Main System Timer structure.

Collaboration diagram for sysTmr:



### **Data Fields**

esTick\_T tick

Current system tick counter.

• uint\_fast16\_t tmr

The number of timers in system.

# 10.8.1 Detailed Description

Main System Timer structure.

10.8.2 Field Documentation

10.8.2.1 esTick\_T sysTmr::tick

Current system tick counter.

10.8.2.2 uint\_fast16\_t sysTmr::tmr

The number of timers in system.

The documentation for this struct was generated from the following file:

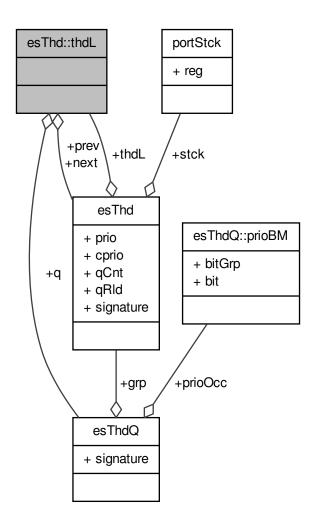
kernel.c

# 10.9 esThd::thdL Struct Reference

Thread linked List structure.

#include <kernel.h>

Collaboration diagram for esThd::thdL:



### **Data Fields**

struct esThdQ \* q

Indicates which queue is used.

struct esThd \* next

Next thread in linked list.

struct esThd \* prev

Previous thread in linked list.

# 10.9.1 Detailed Description

Thread linked List structure.

# 10.9.2 Field Documentation

10.9.2.1 struct esThdQ\* esThd::thdL::q

Indicates which queue is used.

10.9.2.2 struct esThd\* esThd::thdL::next

Next thread in linked list.

10.9.2.3 struct esThd\* esThd::thdL::prev

Previous thread in linked list.

The documentation for this struct was generated from the following file:

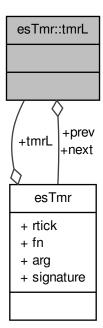
kernel.h

### 10.10 esTmr::tmrL Struct Reference

Timer linked list structure.

#include <kernel.h>

Collaboration diagram for esTmr::tmrL:



### **Data Fields**

struct esTmr \* next

Next thread in timer linked list.

struct esTmr \* prev

Previous thread in timer linked list.

11 File Documentation 78

10.10.1 Detailed Description

Timer linked list structure.

10.10.2 Field Documentation

10.10.2.1 struct esTmr\* esTmr::tmrL::next

Next thread in timer linked list.

10.10.2.2 struct esTmr\* esTmr::tmrL::prev

Previous thread in timer linked list.

The documentation for this struct was generated from the following file:

kernel.h

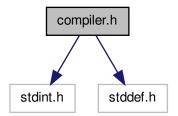
# 11 File Documentation

# 11.1 compiler.h File Reference

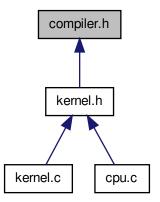
Interface of Compiler port - Template.

#include <stdint.h>
#include <stddef.h>

Include dependency graph for compiler.h:



This graph shows which files directly or indirectly include this file:



#### **Macros**

### Compiler provided macros

Port interface macros and port specific macros

These macros are used to ease the writing of ports. All macros prefixed with **PORT**\_ are part of the port interface.

- #define PORT\_C\_INLINE inline
  - C extension make a function inline.
- #define PORT\_C\_INLINE\_ALWAYS inline
  - C extension make a function inline always.
- #define PORT\_C\_NAKED
  - Omit function prologue/epilogue sequences.
- #define PORT\_C\_FUNC "unknown"
  - Provides function name for assert macros.
- #define PORT\_C\_WEAK
  - Declares a weak function.
- #define PORT\_C\_ALIGNED(expr)
  - This attribute specifies a minimum alignment (in bytes) for variables of the specified type.
- #define PORT\_HWREG\_SET(reg, mask, val)
  - A standardized way of properly setting the value of HW register.

### Compiler provided data types

The compiler port must provide some C90 (C99) data types

The compiler port must:

 declare sets of integer types having specified widths, standard type definitions and shall define corresponding sets of macros.

Types are defined in the following categories:

· Integer types having certain exact widths

- · Fastest integer types having at least certain specified widths
- · Integer types wide enough to hold pointers to objects
- · standard type definitions

The following exact-width integer types are required:

- int8\_t
- int16\_t
- int32\_t
- · uint8 t
- uint16\_t
- uint32\_t

The following fastest minimum-width integer types are required:

- · int\_fast8\_t
- · int\_fast16\_t
- · int\_fast32\_t
- · uint\_fast8\_t
- · uint\_fast16\_t
- · uint\_fast32\_t

The following integer types capable of holding object pointers are required:

- intptr\_t
- uintptr\_t

The following standard type definitions are required:

- NULL
- ptrdiff\_t
- size\_t

```
    enum boolType {
        TRUE = 1U,
        FALSE = 0U }
        Bool data type.
    typedef enum boolType bool_T
```

11.1.1 Detailed Description

Bool data type.

Interface of Compiler port - Template.

**Author** 

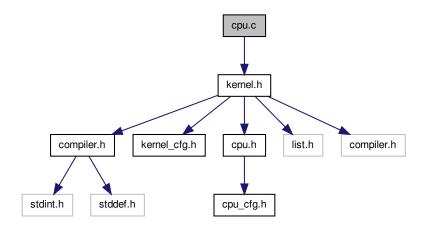
Nenad Radulovic

# 11.2 cpu.c File Reference

Implementation of CPU port - Template.

#include "kernel.h"

Include dependency graph for cpu.c:



### **Functions**

- uint\_fast8\_t portFindLastSet\_ (portReg\_T val)
  - Find last set bit in a word.
- void \* portCtxInit\_ (void \*stck, size\_t stckSize, void(\*thdf)(void \*), void \*arg)
   Initialize the thread context.

### **Variables**

portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

const PORT\_C\_ROM portReg\_T pwr2LKP [PORT\_DATA\_WIDTH\_VAL]
 Look up table for: 2<sup>n</sup> n expression.

### 11.2.1 Detailed Description

Implementation of CPU port - Template.

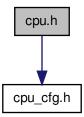
### Author

Nenad Radulovic

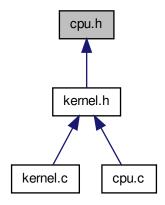
# 11.3 cpu.h File Reference

Interface of CPU port - Template.

#include "cpu\_cfg.h"
Include dependency graph for cpu.h:



This graph shows which files directly or indirectly include this file:



# **Data Structures**

struct portStck

Stack structure used for stack declaration in order to force the alignment.

struct portCtx

Port context structure.

### Macros

# Port constants

• #define PORT\_DATA\_WIDTH\_VAL 8U

This macro specifies the bit width of CPU data registers.

#define PORT\_STCK\_MINSIZE\_VAL sizeof(struct portCtx)

This macro specifies the minimal size of the thread stack.

 #define PORT\_SYSTMR\_RELOAD\_VAL (CFG\_SYSTMR\_CLOCK\_FREQUENCY / CFG\_SYSTMR\_EV-ENT\_FREQUENCY)

System timer reload value for one tick.

• #define PORT\_SYSTMR\_MAX\_VAL 0xFFU

System timer maximum value.

 #define PORT\_SYSTMR\_MAX\_TICKS\_VAL (PORT\_SYSTMR\_MAX\_VAL / PORT\_SYSTMR\_RELOAD-\_VAL)

Maximum number of ticks the system timer can accept.

#### **Critical section management**

Disable/enable interrupts by preserving the status of interrupts.

Generally speaking these macros would store the status of the interrupt disable flag in the local variable declared by PORT\_CRITICAL\_DECL and then disable interrupts. Local variable is allocated in all of eSolid RTOS functions that need to disable interrupts. Macros would restore the interrupt status by copying back the allocated variable into the CPU's status register.

```
    #define PORT_CRITICAL_DECL() portReg_T intStatus_
```

Declare the interrupt status variable.

#define PORT\_CRITICAL\_ENTER()

Enter critical section.

#define PORT\_CRITICAL\_EXIT() portIntSet\_(intStatus\_)

Exit critical section.

#### General port macros

#define PORT\_STCK\_SIZE(size)

Calculate the stack size.

#define PORT\_CRITICAL\_EXIT\_SLEEP() PORT\_CRITICAL\_EXIT()

TODC

#define PORT\_INIT\_EARLY() portInitEarly\_()

Early port initialization.

#define PORT\_INIT() portInit\_()

Port initialization.

• #define PORT\_INIT\_LATE() portInitLate ()

Late port initialization.

### **Typedefs**

typedef uint8\_t portReg\_T

Data type which corresponds to the general purpose register.

typedef struct portStck portStck\_T

Stack type.

### **Functions**

• struct portStck \_\_attribute\_\_ ((aligned(1)))

Alignment of stack structure.

# **General port functions**

void portInitEarly\_ (void)

Early port initialization.

void portInit\_ (void)

Port initialization.

void portInitLate\_ (void)

Late port initialization.

#### **Variables**

portReg\_T reg

A structure field representing stack data.

- struct portCtx \_\_attribute\_\_
- portReg\_T gPortIsrNesting\_

Variable to keep track of ISR nesting.

const PORT\_C\_ROM portReg\_T pwr2LKP [PORT\_DATA\_WIDTH\_VAL]

Look up table for:  $2^{\wedge}$ n expression.

#### Interrupt management

#define PORT\_INT\_DISABLE() portIntDisable\_()

Disable all interrupt sources.

#define PORT ISR ENTER()

Enter ISR. Increment gPortIsrNesting\_variable to keep track of ISR nesting.

#define PORT ISR EXIT()

Exit ISR. Decrement gPortIsrNesting\_ variable to keep track of ISR nesting.

• #define PORT\_ISR\_IS\_LAST() (0U == gPortIsrNesting ? TRUE : FALSE)

If isrNesting variable is zero then the last ISR is executing and scheduler should be invoked.

void portIntDisable\_ (void)

Disable interrupts.

portReg\_T portIntGet\_ (void)

Get the current status of enabled/disabled interrupts.

void portIntSet\_ (portReg\_T status)

Set the status of interrupts according to the status argument.

# Scheduler support

#### Note

These functions are extensively used by the scheduler and therefore they should be optimized for the architecture being used.

• #define PORT\_FIND\_LAST\_SET(val) portFindLastSet\_(val)

Find last set bit in a word.

#define PORT\_PWR2(pwr) (1U << (pwr))</li>

Helper macro: calculate  $2^{\wedge}$  pwr expression.

#define PORT SYSTMR INIT() portSysTmrInit ()

Initialize system timer and associated interrupt.

#define PORT\_SYSTMR\_TERM() portSysTmrTerm\_()

Stop the timer if it is running and disable associated interrupt.

#define PORT SYSTMR RELOAD(ticks) portSysTmrReload (ticks)

Reload the system timer with specified number of ticks.

• #define PORT\_SYSTMR\_ENABLE() portSysTmrEnable\_()

Enable the system timer.

#define PORT\_SYSTMR\_DISABLE() portSysTmrDisable\_()

Disable the system timer.

#define PORT\_SYSTMR\_ISR\_ENABLE() portSysTmrlsrEnable\_()

Enable the system timer interrupt.

• #define PORT SYSTMR ISR DISABLE() portSysTmrlsrDisable ()

Disable the system timer interrupt.

```
uint_fast8_t portFindLastSet_ (portReg_T val)
           Find last set bit in a word.

    void portSysTmrInit_ (void)

           Initialize systick timer and associated interrupt.
    • void portSysTmrTerm_ (void)
           Stop the sistem timer.

    void portSysTmrReload_ (esTick_T ticks)

           Reload the system timer.

    void portSysTmrEnable_ (void)

           Enable the system timer.

    void portSysTmrDisable_ (void)

           Disable the system timer.

    void portSysTmrlsrEnable_ (void)

           Disable the system timer interrupt.

    void portSysTmrlsrDisable_ (void)

           Enable the system timer interrupt.

    void portThdStart_ (void)

           Start the first thread.
Dispatcher context switching

    #define PORT_CTX_INIT(stck, stackSize, thread, arg) portCtxInit_(stck, stackSize, thread, arg)

           Initialize the thread context.
    • #define PORT_CTX_SW() portCtxSw_()
           Do the context switch - invoked from API level.

    #define PORT_CTX_SW_ISR() portCtxSwlsr_()

           Do the context switch - invoked from ISR level.

    #define PORT_THD_START() portThdStart_()

           Start the first thread.

    void * portCtxInit_ (void *stck, size_t stckSize, void(*thdf)(void *), void *arg)

           Initialize the thread context.

    void portCtxSw_ (void)

          Do the context switch - invoked from API.

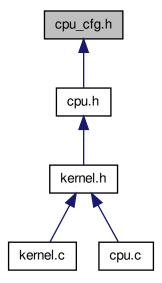
    void portCtxSwlsr_ (void)

          Do the context switch - invoked from ISR.
11.3.1 Detailed Description
Interface of CPU port - Template.
Author
    Nenad Radulovic
11.3.2 Variable Documentation
11.3.2.1 portReg_T reg
A structure field representing stack data.
```

### 11.4 cpu\_cfg.h File Reference

Configuration of CPU port - Template.

This graph shows which files directly or indirectly include this file:



### Macros

# Port General configuration

Configuration options and settings which are available for every port.

Note

- 1) All port General configuration macros are prefixed with CFG\_ string.
- 2) All port specific options and constants are prefixed with CPU\_ string.
- #define CFG\_CRITICAL\_PRIO 1U

Priority of critical sections in kernel.

• #define CFG\_SYSTMR\_CLOCK\_FREQUENCY 1000000UL

The frequency of clock which is used for the system timer.

# 11.4.1 Detailed Description

Configuration of CPU port - Template.

Author

Nenad Radulovic

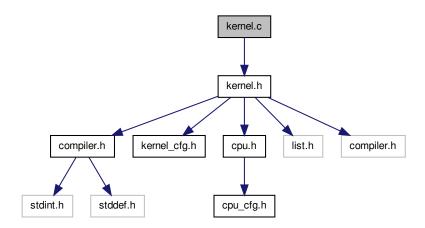
# 11.5 kernel-example.dox File Reference

#### 11.6 kernel.c File Reference

Implementation of port independent code.

#include "kernel.h"

Include dependency graph for kernel.c:



### **Data Structures**

struct sysTmr

Main System Timer structure.

#### **Macros**

• #define PRIO\_BM\_DATA\_WIDTH\_LOG2

Priority Bit Map log base 2: log2 (PORT\_DATA\_WIDTH\_VAL)

#define SCHED\_STATE\_INTSRV\_MSK (1U << 0)</li>

Kernel state variable bit position which defines if kernel is in interrupt servicing state.

#define SCHED STATE LOCK MSK (1U << 1)</li>

Kernel state variable bit position which defines if the kernel is locked or not.

#define SYSTMR SCHED QM MSK (1U << 0)</li>

Scheduler is using system timer Quantum mask.

#define SYSTMR USR QM MSK (1U << 1)</li>

User is using system timer Quantum mask.

• #define SCHED\_POWER\_SAVE 0U

Enable/disable scheduler power savings mode.

#define THD\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEFU)

Thread structure signature.

• #define THDQ\_CONTRACT\_SIGNATURE ((portReg\_T)0xFEEDBEEEU)

Thread Queue structure signature.

#define DLIST\_IS\_ENTRY\_FIRST(list, entry) ((entry) == (entry)->list.next)

DList macro: is the thread the first one in the list.

#define DLIST\_IS\_ENTRY\_LAST(list, entry) DLIST\_IS\_ENTRY\_FIRST(list, entry)

DList macro: is the thread the last one in the list.

 #define DLIST\_IS\_ENTRY\_SINGLE(list, entry) DLIST\_IS\_ENTRY\_FIRST(list, entry) DList macro: is the thread single in the list. • #define DLIST\_ENTRY\_PREV(list, entry) (entry)->list.prev Get the previous entry. #define DLIST\_ENTRY\_NEXT(list, entry) (entry)->list.next Get the next entry. • #define DLIST\_ENTRY\_INIT(list, entry) Initialize entry. #define DLIST\_ENTRY\_ADD\_AFTER(list, current, entry) Add new entry after current entry. #define DLIST\_ENTRY\_RM(list, entry) Remove the entry from a list. • #define KTMR\_STCK\_SIZE PORT\_STCK\_SIZE(40U) System Timer kernel thread stack size. #define KIDLE\_STCK\_SIZE PORT\_STCK\_SIZE(40U) Idle kernel thread stack size. **Functions**  void esKernInit (void) Initialize kernel internal data structures. • PORT\_C\_NORETURN void esKernStart (void) Start the multi-threading. void esKernSysTmrl (void) Process the system timer event. void esKernLockEnterl (void) Lock the scheduler. void esKernLockExitI (void) Unlock the scheduler. void esKernLockEnter (void) Lock the scheduler. void esKernLockExit (void) Unlock the scheduler. void esKernIsrProloguel (void) Enter Interrupt Service Routine. void esKernIsrEpiloguel (void) Exit Interrupt Service Routine. void esThdInit (esThd\_T \*thd, void(\*thdf)(void \*), void \*arg, portStck\_T \*stck, size\_t stckSize, uint8\_t prio) Initialize the specified thread. void esThdTerm (esThd\_T \*thd) Terminate the specified thread. void esThdSetPriol (esThd\_T \*thd, uint8\_t prio) Set the priority of a thread. void esThdPostI (esThd\_T \*thd) Post to thread semaphore. void esThdPost (esThd T \*thd)

Post to thread semaphore.

Wait for thread semaphore.

void esThdWaitI (void)

void esThdWait (void)

Wait for thread semaphore.

void esThdQInit (esThdQ\_T \*thdQ)

Initialize Thread Queue.

void esThdQAddI (esThdQ T \*thdQ, esThd T \*thd)

Add a thread to the tail of the Thread Queue.

void esThdQRml (esThdQ\_T \*thdQ, esThd\_T \*thd)

Removes the thread from the Thread Queue.

esThd T \* esThdQFetchl (const esThdQ T \*thdQ)

Fetch the first high priority thread from the Thread Queue.

esThd\_T \* esThdQFetchRotatel (esThdQ\_T \*thdQ, uint\_fast8\_t prio)

Fetch the next thread and rotate linked list.

bool\_T esThdQIsEmpty (const esThdQ\_T \*thdQ)

Is thread queue empty.

void esSchedRdyAddI (esThd T \*thd)

Add thread thd to the ready thread list and notify the scheduler.

void esSchedRdyRmI (esThd\_T \*thd)

Remove thread thd from the ready thread list and notify the scheduler.

void esSchedYieldI (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

· void esSchedYieldIsrl (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

void esSysTmrEnable (void)

Enable system timer tick events.

void esSysTmrDisable (void)

Disable system timer tick events.

void esTmrAddl (esTmr T \*tmr, esTick T tick, void(\*fn)(void \*), void \*arg)

Add a new timer.

#### **Scheduler**

• static PORT\_C\_INLINE void schedInit (void)

Initialize Ready Thread Queue structure gRdyQueue and Kernel control structure esKernCtrl.

static PORT\_C\_INLINE void schedStart (void)

Set scheduler data structures ready for multi-threading.

static void schedRdyAddInitI (esThd\_T \*thd)

Initialize scheduler ready structure during the thread add operation.

• static void schedQml (void)

Do the Quantum (Round-Robin) scheduling.

static PORT C INLINE void schedQmActivate (void)

Activate system timer Quantum mode.

static PORT\_C\_INLINE void schedQmDeactivate (void)

Deactivate system timer Quantum mode.

static void schedQmEvaluateI (esThd\_T \*thd)

Evaluate if the system timer Quantum mode is needed.

### **Variables**

• static esThdQ\_T gRdyQueue

Ready Thread queue.

static uint\_fast8\_t gKernLockCnt

Kernel Lock Counter.

const volatile esKernCtrl\_T gKernCtrl

Kernel control initialization.

### System timer

```
    enum sysTmrState {
        SYSTMR_ACTIVE,
        SYSTMR_INACTIVE }
```

System timer state enumeration.

typedef struct sysTmr sysTmr\_T

System Timer type.

static void sysTmrInit (void)

Initialize system timer hardware.

static void sysTmrTryDeactivate (void)

Try to deactivate system timer.

static void sysTmrTryActivate (void)

Try to activate system timer.

### **Priority Bit Map**

typedef struct prioBM prioBM\_T

Priority Bit Map type.

• static PORT\_C\_INLINE void prioBMInit (prioBM\_T \*bm)

Initialize bitmap.

• static PORT\_C\_INLINE void prioBMSet (prioBM\_T \*bm, uint\_fast8\_t prio)

Set the bit corresponding to the prio argument.

• static PORT\_C\_INLINE void prioBMClear (prioBM\_T \*bm, uint\_fast8\_t prio)

Clear the bit corresponding to the prio argument.

static PORT\_C\_INLINE uint\_fast8\_t prioBMGet (const prioBM\_T \*bm)

Get the highest priority set.

• static PORT\_C\_INLINE bool\_T prioBMIsEmpty (const prioBM\_T \*bm)

Is bit map empty?

### System timer kernel thread

static sysTmr\_T gSysTmr

Main System Timer structure.

static esTmr\_T gTmrWait

Waiting list of timers to expire.

static esTmr\_T gTmrPend

Timers pending to be inserted in waiting list.

static esThd\_T gKTmrld

System timer thread Id.

• static portStck\_T gKTmrStck [KTMR\_STCK\_SIZE]

System timer thread stack.

- static void tmrListAddSort (esTmr\_T \*list, esTmr\_T \*tmr)
- static void kTmrlnit (void)

Initialization of System Timer Thread.

static void kTmr (void \*arg)

System timer thread code.

Idle kernel thread

```
    static esThd_T gKldleId
```

Idle thread Id.

static portStck\_T gKldleStck [KIDLE\_STCK\_SIZE]

Idle thread stack.

• static void kldleInit (void)

Initialization of Idle thread.

• static void kldle (void \*arg)

Idle thread code.

### 11.6.1 Detailed Description

Implementation of port independent code.

Author

Nenad Radulovic

### 11.6.2 Variable Documentation

```
11.6.2.1 sysTmr_T gSysTmr [static]
```

### Initial value:

```
= {
    OU,
    OU,
```

Main System Timer structure.

```
11.6.2.2 esTmr_T gTmrWait [static]
```

### Initial value:

Waiting list of timers to expire.

```
11.6.2.3 esTmr_T gTmrPend [static]
```

### Initial value:

Timers pending to be inserted in waiting list.

```
11.6.2.4 esThd_T gKTmrld [static]
```

System timer thread Id.

```
11.6.2.5 portStck_T gKTmrStck[KTMR_STCK_SIZE] [static]
```

System timer thread stack.

```
11.6.2.6 esThd_T gKldleld [static]
```

Idle thread Id.

```
11.6.2.7 portStck_T gKldleStck[KIDLE_STCK_SIZE] [static]
```

Idle thread stack.

```
11.6.2.8 uint_fast8_t gKernLockCnt [static]
```

Kernel Lock Counter.

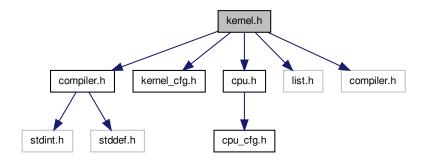
### 11.7 kernel.dox File Reference

### 11.8 kernel.h File Reference

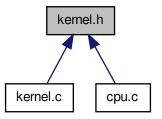
Interface of kernel.

```
#include "compiler.h"
#include "kernel_cfg.h"
#include "cpu.h"
#include "list.h"
```

Include dependency graph for kernel.h:



This graph shows which files directly or indirectly include this file:



#### **Data Structures**

struct esThd

Thread structure.

struct esThd::thdL

Thread linked List structure.

struct esTmr

Timer structure.

struct esTmr::tmrL

Timer linked list structure.

struct esThdQ

Thread Queue structure.

struct esThdQ::prioBM

Priority Bit Map structure.

struct esKernCtrl

Kernel control block structure.

### Macros

#### Kernel identification and version number

- #define ES\_KERNEL\_VER 0x10000UL
  - Identifies the underlying kernel version number.
- #define ES\_KERNEL\_ID "eSolid Kernel v1.0"

Kernel identification string.

### Error checking

Some basic infrastructure for error checking

These macros provide basic detection of errors. For more datails see Error checking.

• #define ES\_ASSERT(expr)

Generic assert macro.

#define ES\_API\_OBLIGATION(expr) expr

Execute code to fulfill the contract.

#define ES\_API\_REQUIRE(expr) ES\_ASSERT(expr)

Make sure the caller has fulfilled all contract preconditions.

#define ES\_API\_ENSURE(expr) ES\_ASSERT(expr)

Make sure the callee has fulfilled all contract postconditions.

#### **Functions**

#### General kernel functions

There are several groups of functions:

- · kernel initialization and start
- · ISR prologue and epilogue
- · void esKernInit (void)

Initialize kernel internal data structures.

PORT C NORETURN void esKernStart (void)

Start the multi-threading.

void esKernSysTmrl (void)

Process the system timer event.

void esKernIsrProloguel (void)

Enter Interrupt Service Routine.

void esKernIsrEpiloguel (void)

Exit Interrupt Service Routine.

### Scheduler notification and invocation

void esSchedRdyAddI (esThd\_T \*thd)

Add thread thd to the ready thread list and notify the scheduler.

void esSchedRdyRmI (esThd T \*thd)

Remove thread thd from the ready thread list and notify the scheduler.

void esSchedYieldI (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

· void esSchedYieldIsrl (void)

Force the scheduler invocation which will evaluate all ready threads and switch to ready thread with the highest priority.

### System timer

void esSysTmrEnable (void)

Enable system timer tick events.

void esSysTmrDisable (void)

Disable system timer tick events.

### Kernel hook functions

PORT\_C\_NORETURN void userAssert (const char \*fnName, const char \*expr)

An assertion has failed. This function should inform the user about failed assertion.

void userSysTmr (void)

System timer hook function, called from system system timer ISR function.

void userKernInit (void)

Kernel initialization hook function, called from esKernInit() function.

void userKernStart (void)

Kernel start hook function, called from esKernStart() function.

void userThdInitEnd (void)

Thread initialization end hook function, called from esThdlnit() function.

void userThdTerm (void)

Thread terminate hook function, called from esThdTerm() or when a thread terminates itself.

void userCtxSw (esThd\_T \*oldThd, esThd\_T \*newThd)

Kernel context switch hook function, called from esSchedYieldI() and esSchedYieldIsrI() functions.

**Critical section management** 

These macros are used to prevent interrupts on entry into the critical section, and restoring interrupts to their previous state on exit from critical section.

For more details see Critical sections.

```
• #define ES_CRITICAL_DECL() PORT_CRITICAL_DECL()
```

Critical section status variable declaration.

#define ES\_CRITICAL\_ENTER() PORT\_CRITICAL\_ENTER()

Enter a critical section.

#define ES\_CRITICAL\_EXIT() PORT\_CRITICAL\_EXIT()

Exit from critical section.

void esKernLockEnterl (void)

Lock the scheduler.

void esKernLockExitI (void)

Unlock the scheduler.

· void esKernLockEnter (void)

Lock the scheduler.

void esKernLockExit (void)

Unlock the scheduler.

#### Thread management

Basic thread management services

For more details see Thread Management.

#define ES\_STCK\_SIZE(elem) PORT\_STCK\_SIZE(elem)

Converts the required stack elements into the stack array index.

typedef struct esThd esThd\_T

Thread type.

typedef portStck\_T esStck\_T

Stack type.

void esThdInit (esThd\_T \*thd, void(\*thdf)(void \*), void \*arg, portStck\_T \*stck, size\_t stckSize, uint8\_t prio)

Initialize the specified thread.

void esThdTerm (esThd\_T \*thd)

Terminate the specified thread.

static PORT\_C\_INLINE esThd\_T \* esThdGetId (void)

Get the current thread ID.

static PORT\_C\_INLINE uint8\_t esThdGetPrio (esThd\_T \*thd)

Get the priority of a thread.

void esThdSetPriol (esThd\_T \*thd, uint8\_t prio)

Set the priority of a thread.

void esThdPostI (esThd\_T \*thd)

Post to thread semaphore.

void esThdPost (esThd\_T \*thd)

Post to thread semaphore.

void esThdWaitI (void)

Wait for thread semaphore.

void esThdWait (void)

Wait for thread semaphore.

#### **Timer services**

```
• typedef struct esTmr esTmr T
```

Timer type.

void esTmrAddl (esTmr T \*tmr, esTick T tick, void(\*fn)(void \*), void \*arg)

Add a new timer.

#### **Thread Queue management**

```
    #define PRIO_BM_GRP_INDX ((CFG_SCHED_PRIO_LVL + PORT_DATA_WIDTH_VAL - 1U) / PORT_DATA_WIDTH_VAL)
```

Priority Bit Map Group Index.

• typedef struct esThdQ esThdQ\_T

Thread queue type.

void esThdQInit (esThdQ\_T \*thdQ)

Initialize Thread Queue.

void esThdQAddI (esThdQ\_T \*thdQ, esThd\_T \*thd)

Add a thread to the tail of the Thread Queue.

void esThdQRml (esThdQ\_T \*thdQ, esThd\_T \*thd)

Removes the thread from the Thread Queue.

esThd\_T \* esThdQFetchl (const esThdQ\_T \*thdQ)

Fetch the first high priority thread from the Thread Queue.

esThd T \* esThdQFetchRotatel (esThdQ T \*thdQ, uint fast8 t prio)

Fetch the next thread and rotate linked list.

bool\_T esThdQIsEmpty (const esThdQ\_T \*thdQ)

Is thread queue empty.

### Kernel control block

```
    enum esKernState {
        ES_KERN_RUN = 0x00U,
        ES_KERN_INTSRV_RUN = 0x01U,
        ES KERN LOCK = 0x02U,
```

ES KERN INTSRV LOCK = 0x03U,

 $ES_KERN_INIT = 0x04U,$ 

ES\_KERN\_INACTIVE = 0x05U }

Kernel state enumeration.

• typedef enum esKernState esKernState\_T

Kernel state type.

typedef struct esKernCtrl esKernCtrl\_T

Kernel control block type.

const volatile esKernCtrl\_T gKernCtrl

Kernel control block.

#### 11.8.1 Detailed Description

Interface of kernel.

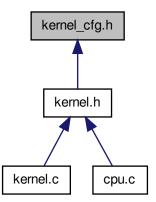
Author

Nenad Radulovic

### 11.9 kernel\_cfg.h File Reference

Configuration of Kernel.

This graph shows which files directly or indirectly include this file:



#### Macros

#### Kernel configuration options and settings

#define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

• #define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

#define CFG SCHED TIME QUANTUM 10U

Scheduler Round-Robin time quantum.

• #define CFG\_SYSTMR\_MODE 1U

System timer mode.

#define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

#define CFG\_SYSTMR\_TICK\_TYPE 2U

The size of the system timer counter.

### Kernel hooks

#define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

• #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

#define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

• #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization end hook function.

#define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

• #define CFG\_HOOK\_CTX\_SW 0U

Context switch hook function.

### 11.9.1 Detailed Description

Configuration of Kernel.

**Author** 

Nenad Radulovic

# 11.10 kernel\_cfg.h File Reference

Configuration of Kernel - Template.

**Macros** 

### Kernel configuration options and settings

Kernel default configuration

• #define CFG\_API\_VALIDATION 1U

Enable/disable API arguments validation.

#define CFG\_SCHED\_PRIO\_LVL 8U

Scheduler priority levels.

#define CFG\_SCHED\_TIME\_QUANTUM 10U

Scheduler Round-Robin time quantum.

#define CFG\_SYSTMR\_EVENT\_FREQUENCY 100UL

The frequency of system tick event.

### Kernel hooks

• #define CFG\_HOOK\_SYSTMR\_EVENT 0U

System timer event hook function.

• #define CFG\_HOOK\_KERN\_INIT 0U

Kernel initialization hook function.

• #define CFG\_HOOK\_KERN\_START 0U

Kernel start hook function.

• #define CFG\_HOOK\_THD\_INIT\_END 0U

Thread initialization hook function.

#define CFG\_HOOK\_THD\_TERM 0U

Thread termination hook function.

• #define CFG\_HOOK\_CTX\_SW 0U

Kernel context switch hook function.

### 11.10.1 Detailed Description

Configuration of Kernel - Template.

**Author** 

Nenad Radulovic

# Index

attribute	PORT_SYSTMR_INIT, 52
CPU port interface, 55	PORT_SYSTMR_RELOAD, 53
	PORT_SYSTMR_TERM, 52
arg	PORT_THD_START, 54
esTmr, 71	portCtxInit_, 56
h.i+	portCtxSw_, 56
bit	portCtxSwlsr_, 56
esThdQ::prioBM, 73	portFindLastSet_, 55
bitGrp	portInit_, 57
esThdQ::prioBM, 73	portInitEarly_, <mark>56</mark>
bool_T	portInitLate_, 57
Compiler port, 45	portIntDisable_, 55
boolType	portIntGet_, 55
Compiler port, 46	portIntSet_, 55
CEC ADI MALIDATIONI	portReg_T, 54
CFG_API_VALIDATION	portStck_T, 54
Configuration, 40	portSysTmrDisable_, 56
Kernel configuration, 62	portSysTmrEnable_, 56
CFG_CRITICAL_PRIO	portSysTmrInit_, 55
CPU port configuration, 60	portSysTmrlsrDisable , 56
CFG_HOOK_CTX_SW	portSysTmrlsrEnable_, 56
Configuration, 41	portSysTmrReload_, 56
Kernel configuration, 63	portSysTmrTerm_, 55
CFG_HOOK_KERN_INIT	portThdStart_, 56
Configuration, 40	pwr2LKP, 57
Kernel configuration, 62	CPU port internals, 58
CFG_HOOK_KERN_START	gPortIsrNesting_, 59
Configuration, 40	portCtxInit_, 58
CFG_HOOK_THD_TERM	portFindLastSet_, 58
Configuration, 40	pwr2LKP, 59
Kernel configuration, 63	Compiler port, 43
CFG_SCHED_PRIO_LVL	bool T, 45
Configuration, 40	boolType, 46
Kernel configuration, 62	FALSE, 46
CFG_SYSTMR_MODE	PORT C ALIGNED, 45
Configuration, 40	PORT_C_FUNC, 45
CPU port configuration, 60	PORT C INLINE, 44
CFG_CRITICAL_PRIO, 60	PORT_C_NAKED, 45
CPU port interface, 47	PORT_C_WEAK, 45
attribute, 55	PORT HWREG SET, 45
gPortIsrNesting_, 57	TRUE, 46
PORT_CRITICAL_DECL, 52	compiler.h, 77
PORT_CRITICAL_EXIT, 52	Configuration, 39
PORT_CTX_INIT, 53	CFG API VALIDATION, 40
PORT_CTX_SW, 54	CFG HOOK CTX SW, 41
PORT_CTX_SW_ISR, 54	CFG_HOOK_KERN_INIT, 40
PORT_INIT, 54	CFG_HOOK_KERN_START, 40
PORT_INIT_EARLY, 54	CFG_HOOK_THD_TERM, 40
PORT_INIT_LATE, 54	CFG_SCHED_PRIO_LVL, 40
PORT_INT_DISABLE, 51	CFG SYSTMR MODE, 40
PORT_ISR_ENTER, 51	cprio
PORT_ISR_EXIT, 51	esThd, 67
PORT_ISR_IS_LAST, 51	· ·
PORT_PWR2, 52	cpu.c, 80
PORT_STCK_SIZE, 54	cpu.h, 80
PORT_SYSTMR_ENABLE, 53	reg, 84

cpu_cfg.h, 85	esKernLockEnter
cthd	Interface, 21
esKernCtrl, 65	esKernLockEnterl
	Interface, 21
DLIST_ENTRY_INIT	esKernLockExit
Internals, 34	Interface, 22
DLIST ENTRY NEXT	esKernLockExitI
Internals, 34	
	Interface, 21
DLIST_ENTRY_PREV	esKernStart
Internals, 34	Interface, 20
DLIST_ENTRY_RM	esKernState
Internals, 34	Interface, 19
DLIST_IS_ENTRY_LAST	esKernState_T
Internals, 34	Interface, 19
	esKernSysTmrl
ES_KERN_INACTIVE	Interface, 20
Interface, 19	esSchedRdyAddI
ES_KERN_INIT	Interface, 27
Interface, 19	esSchedRdyRmI
ES KERN INTSRV LOCK	•
Interface, 19	Interface, 27
ES KERN INTSRV RUN	esSchedYieldI
Interface, 19	Interface, 28
	esSchedYieldIsrI
ES_KERN_LOCK	Interface, 28
Interface, 19	esStck_T
ES_KERN_RUN	Interface, 19
Interface, 19	esSysTmrDisable
ES_API_ENSURE	Interface, 28
Interface, 18	esSysTmrEnable
ES_API_OBLIGATION	Interface, 28
Interface, 18	esThd, 65
ES_API_REQUIRE	cprio, 67
Interface, 18	
ES ASSERT	prio, 67
Interface, 18	qCnt, 67
ES_CRITICAL_DECL	qRld, 67
Interface, 17	signature, 67
ES_CRITICAL_ENTER	stck, 67
Interface, 18	thdL, 67
	esThd::thdL, 74
ES_CRITICAL_EXIT	next, 76
Interface, 18	prev, 76
ES_KERNEL_ID	q, <mark>75</mark>
Interface, 17	esThd T
ES_KERNEL_VER	Interface, 19
Interface, 17	esThdGetId
ES_STCK_SIZE	Interface, 23
Interface, 18	esThdGetPrio
esKernCtrl, 64	
cthd, 65	Interface, 23
pthd, 65	esThdInit
state, 65	Interface, 22
esKernCtrl T	esThdPost
<del>-</del>	Interface, 24
Interface, 19	esThdPostI
esKernInit	Interface, 24
Interface, 19	esThdQ, 68
esKernIsrEpiloguel	grp, <mark>69</mark>
Interface, 21	prioOcc, 69
esKernlsrProloguel	signature, 69
Interface, 20	, ,

a The IO was in DM 70	a D. d. O
esThdQ::prioBM, 72	gRdyQueue
bit, 73	Internals, 37
bitGrp, 73	gSysTmr
esThdQ_T	kernel.c, 90
Interface, 19	gTmrPend
esThdQAddl	kernel.c, 90
Interface, 25	gTmrWait
esThdQFetchl	kernel.c, 90
Interface, 26	grp
esThdQFetchRotateI	esThdQ, 69
Interface, 26	Interface, 14
esThdQInit	ES KERN INACTIVE, 19
Interface, 24	ES KERN INIT, 19
esThdQlsEmpty	ES KERN INTSRV LOCK, 19
Interface, 26	ES_KERN_INTSRV_RUN, 19
esThdQRmI	ES KERN LOCK, 19
Interface, 25	ES KERN RUN, 19
esThdSetPriol	ES API ENSURE, 18
Interface, 23	ES API OBLIGATION, 18
esThdTerm	ES_AFI_OBLIGATION, 18 ES API REQUIRE, 18
Interface, 22	
esThdWait	ES_ASSERT, 18
Interface, 24	ES_CRITICAL_DECL, 17
esThdWaitI	ES_CRITICAL_ENTER, 18
Interface, 24	ES_CRITICAL_EXIT, 18
esTmr, 69	ES_KERNEL_ID, 17
arg, 71	ES_KERNEL_VER, 17
fn, 71	ES_STCK_SIZE, 18
rtick, 71	esKernCtrl_T, 19
signature, 71	esKernInit, 19
tmrL, 71	esKernIsrEpilogueI, 21
esTmr::tmrL, 76	esKernIsrPrologueI, 20
next, 77	esKernLockEnter, 21
prev, 77	esKernLockEnterl, 21
esTmr_T	esKernLockExit, 22
Interface, 19	esKernLockExitI, 21
esTmrAddI	esKernStart, 20
Interface, 28	esKernState, 19
	esKernState_T, 19
FALSE	esKernSysTmrl, 20
Compiler port, 46	esSchedRdyAddl, 27
fn	esSchedRdyRml, 27
esTmr, 71	esSchedYieldI, 28
	esSchedYieldIsrI, 28
gKldleld	esStck_T, 19
kernel.c, 91	esSysTmrDisable, 28
gKldleStck	esSysTmrEnable, 28
kernel.c, 91	esThd_T, 19
gKTmrld	esThdGetId, 23
kernel.c, 91	esThdGetPrio, 23
gKTmrStck	esThdInit, 22
kernel.c, 91	esThdPost, 24
gKernCtrl	esThdPostI, 24
Interface, 30	esThdQ_T, 19
gKernLockCnt	esThdQAddI, 25
kernel.c, 91	esThdQFetchI, 26
gPortIsrNesting_	esThdQFetchRotateI, 26
CPU port interface, 57	esThdQInit, 24
CPU port internals, 59	esThdQIsEmpty, 26

esThdQRmI, 25	KTMR_STCK_SIZE
esThdSetPrioI, 23	Internals, 35
esThdTerm, 22	kTmr
esThdWait, 24	Internals, 37
esThdWaitI, 24	kTmrlnit
esTmr_T, 19	Internals, 37
esTmrAddI, 28	Kernel, 12
gKernCtrl, 30	Kernel configuration, 61
PRIO_BM_GRP_INDX, 18	CFG_API_VALIDATION, 62
userAssert, 29	CFG_HOOK_CTX_SW, 63
userCtxSw, 30	CFG_HOOK_KERN_INIT, 62
userKernInit, 29	CFG_HOOK_THD_TERM, 63
userKernStart, 29	CFG_SCHED_PRIO_LVL, 62
userSysTmr, 29	kernel-example.dox, 85
userThdInitEnd, 29	kernel.c, 86
userThdTerm, 30	gKldleld, 91
Internals, 31	gKldleStck, 91
DLIST ENTRY INIT, 34	gKTmrld, 91
DLIST ENTRY NEXT, 34	gKTmrStck, 91
DLIST ENTRY PREV, 34	gKernLockCnt, 91
DLIST ENTRY RM, 34	gSysTmr, 90
DLIST_IS_ENTRY_LAST, 34	gTmrPend, 90
gRdyQueue, 37	gTmrWait, 90
KIDLE_STCK_SIZE, 35	kernel.dox, 91
kldle, 37	kernel.h, 91
kldleInit, 37	kernel_cfg.h, 96, 97
KTMR_STCK_SIZE, 35	_ 3 , ,
kTmr, 37	next
kTmrlnit, 37	esThd::thdL, 76
prioBM_T, 35	esTmr::tmrL, 77
prioBMClear, 35	
prioBMGet, 36	PORT_C_ALIGNED
prioBMInit, 35	Compiler port, 45
prioBMIsEmpty, 36	PORT_C_FUNC
prioBMSet, 35	Compiler port, 45
SYSTMR_ACTIVE, 35	PORT_C_INLINE
SYSTMR_INACTIVE, 35	Compiler port, 44
SCHED_POWER_SAVE, 34	PORT_C_NAKED
SYSTMR_SCHED_QM_MSK, 33	Compiler port, 45
SYSTMR_USR_QM_MSK, 33	PORT_C_WEAK
schedlnit, 36	Compiler port, 45
schedQmActivate, 37	PORT_CRITICAL_DECL
schedQmDeactivate, 37	CPU port interface, 52
schedQmEvaluateI, 37	PORT_CRITICAL_ENTER
schedQmI, 36	CPU port interface, 52
schedRdyAddInitI, 36	PORT_CRITICAL_EXIT
schedStart, 36	CPU port interface, 52
sysTmr_T, 35	PORT_CTX_INIT
sysTmrInit, 37	CPU port interface, 53
sysTmrState, 35	PORT_CTX_SW
sysTmrTryActivate, 37	CPU port interface, 54
sysTmrTryDeactivate, 37	PORT_CTX_SW_ISR
KIDLE OTOK OLZE	CPU port interface, 54
KIDLE_STCK_SIZE	PORT_FIND_LAST_SET
Internals, 35	CPU port interface, 52
kldle	PORT_HWREG_SET
Internals, 37	Compiler port, 45 PORT INIT
kldleInit	CPU port interface, 54
Internals, 37	or o port interface, 54

PORT_INIT_EARLY	reg, 72
CPU port interface, 54	portStck_T
PORT_INIT_LATE	CPU port interface, 54
CPU port interface, 54	portSysTmrDisable_
PORT_INT_DISABLE	CPU port interface, 56
CPU port interface, 51	portSysTmrEnable_
PORT_ISR_ENTER	CPU port interface, 56
CPU port interface, 51	portSysTmrInit_
PORT ISR EXIT	CPU port interface, 55
CPU port interface, 51	portSysTmrlsrDisable_
PORT ISR IS LAST	CPU port interface, 56
CPU port interface, 51	portSysTmrlsrEnable_
PORT PWR2	CPU port interface, 56
CPU port interface, 52	portSysTmrReload_
PORT STCK SIZE	CPU port interface, 56
CPU port interface, 54	portSysTmrTerm_
•	-
PORT_SYSTMR_DISABLE	CPU port interface, 55
CPU port interface, 53	portThdStart_
PORT_SYSTMR_ENABLE	CPU port interface, 56
CPU port interface, 53	prev
PORT_SYSTMR_INIT	esThd::thdL, 76
CPU port interface, 52	esTmr::tmrL, 77
PORT_SYSTMR_RELOAD	prio
CPU port interface, 53	esThd, 67
PORT_SYSTMR_TERM	prioBM_T
CPU port interface, 52	Internals, 35
PORT_THD_START	prioBMClear
CPU port interface, 54	Internals, 35
PRIO_BM_GRP_INDX	prioBMGet
Interface, 18	Internals, 36
Port template, 42	prioBMInit
portCtx, 71	Internals, 35
r0, 72	prioBMIsEmpty
portCtxInit_	Internals, 36
CPU port interface, 56	prioBMSet
CPU port internals, 58	Internals, 35
•	
portCtxSw_	prioOcc
CPU port interface, 56	esThdQ, 69
portCtxSwlsr_	pthd
CPU port interface, 56	esKernCtrl, 65
portFindLastSet_	pwr2LKP
CPU port interface, 55	CPU port interface, 57
CPU port internals, 58	CPU port internals, 59
portInit_	
CPU port interface, 57	q
portInitEarly_	esThd::thdL, 75
CPU port interface, 56	qCnt
portInitLate_	esThd, 67
CPU port interface, 57	qRld
portIntDisable_	esThd, 67
CPU port interface, 55	
portIntGet_	r0
CPU port interface, 55	portCtx, 72
portIntSet_	reg
CPU port interface, 55	cpu.h, 84
•	portStck, 72
portReg_T	rtick
CPU port interface, 54	esTmr, 71
portStck, 72	

SYSTMR ACTIVE	userCtxSw
Internals, 35	Interface, 30
SYSTMR INACTIVE	userKernInit
Internals, 35	Interface, 29
SCHED_POWER_SAVE	userKernStart
Internals, 34	
	Interface, 29
SYSTMR_SCHED_QM_MSK	userSysTmr
Internals, 33	Interface, 29
SYSTMR_USR_QM_MSK	userThdInitEnd
Internals, 33	Interface, 29
schedInit	userThdTerm
Internals, 36	Interface, 30
schedQmActivate	
Internals, 37	
schedQmDeactivate	
Internals, 37	
schedQmEvaluatel	
Internals, 37	
schedQml	
Internals, 36	
schedRdyAddInitI	
Internals, 36	
schedStart	
Internals, 36	
signature	
esThd, 67	
esThdQ, 69	
esTmr, 71	
state	
esKernCtrl, 65	
stck	
esThd, 67	
sysTmr, 73	
tick, 74	
tmr, 74	
sysTmr T	
Internals, 35	
sysTmrInit	
Internals, 37	
sysTmrState	
Internals, 35	
sysTmrTryActivate	
Internals, 37	
sysTmrTryDeactivate	
Internals, 37	
TRUE	
Compiler port, 46	
thdL	
esThd, 67	
tick	
sysTmr, 74	
tmr	
sysTmr, 74	
tmrL	
esTmr, 71	
,	
userAssert	

Interface, 29