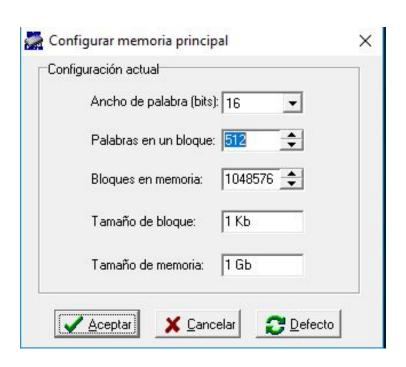
PRACTICA 2 AC



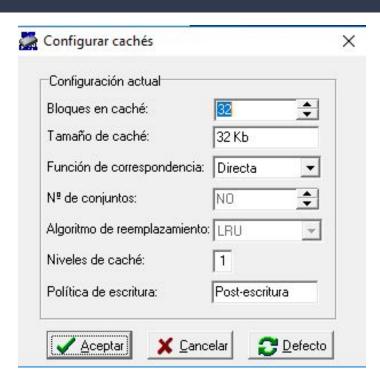
Pablo Cordón Hidalgo Manuel Jesús Reyes Capelo

FUNCIONES DE CORRESPONDENCIA

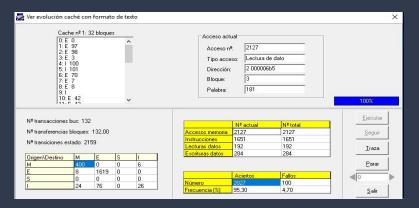
CONFIGURACIÓN DE LA MP

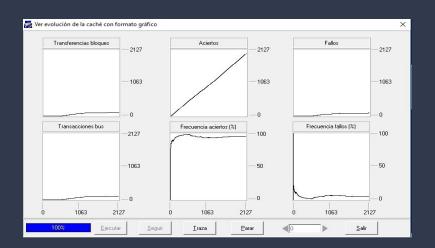


CONFIGURACIÓN DE LA MC (32 DIR)



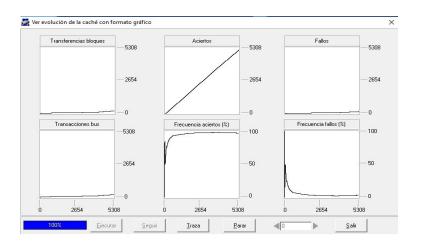
HYDRO

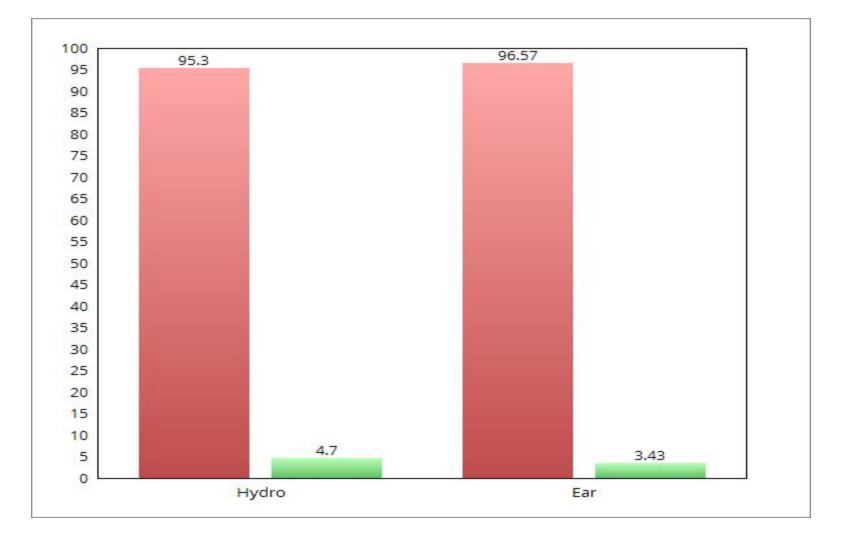




EAR



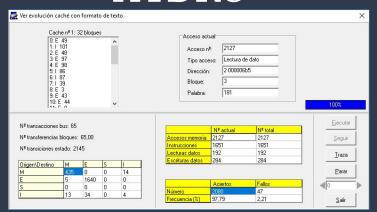


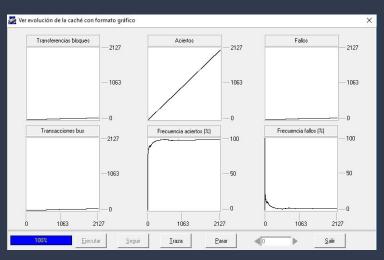


CONFIGURACIÓN DE LA MC (32 TOTAS)

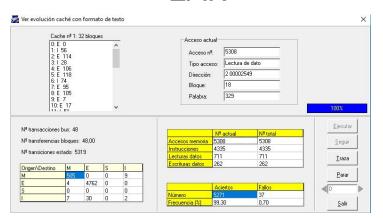


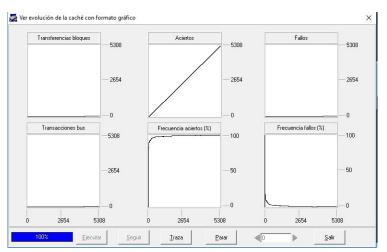
HYDRO

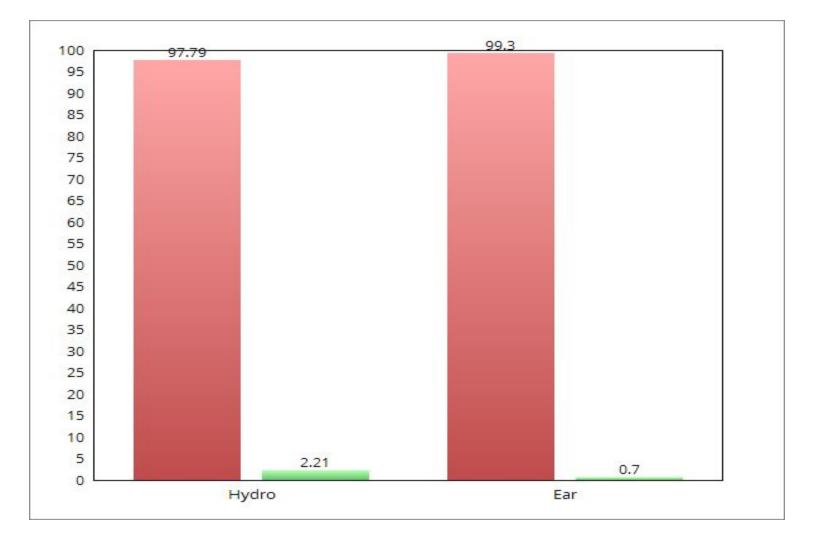




EAR



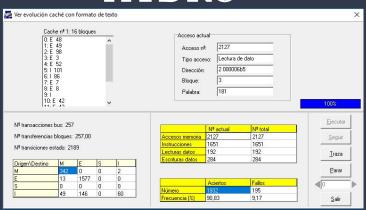


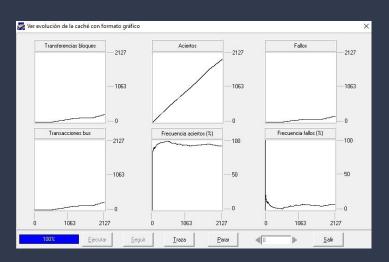


CONFIGURACIÓN DE LA MC (16 DIR)

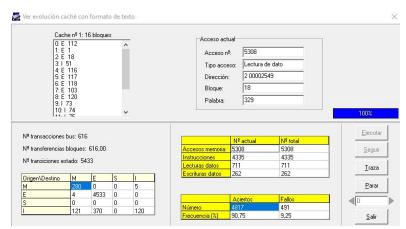


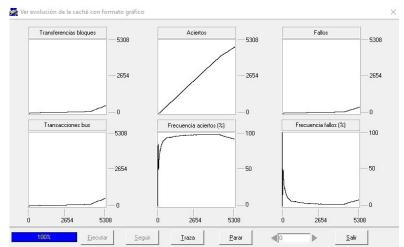
HYDRO

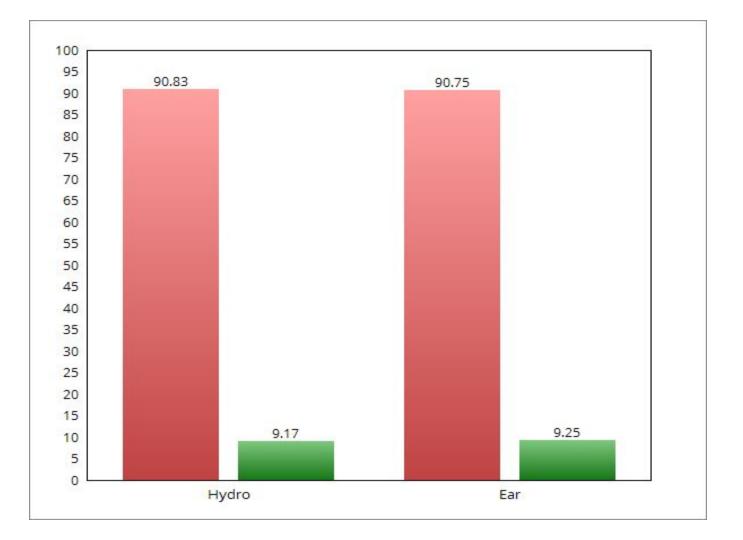




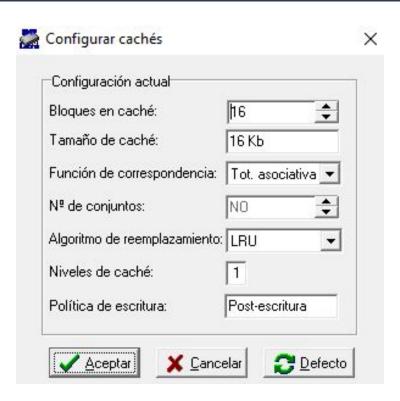
EAR





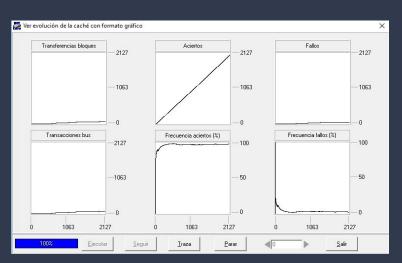


CONFIGURACIÓN DE LA MC (16 TOTAS)

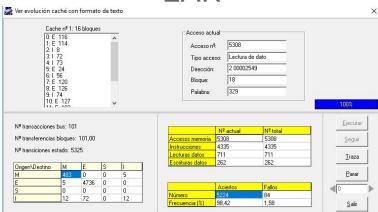


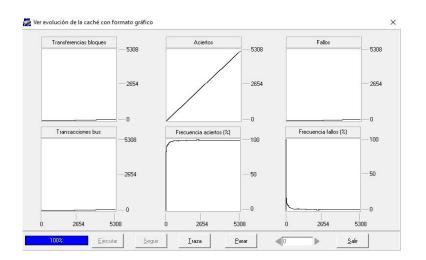
HYDRO

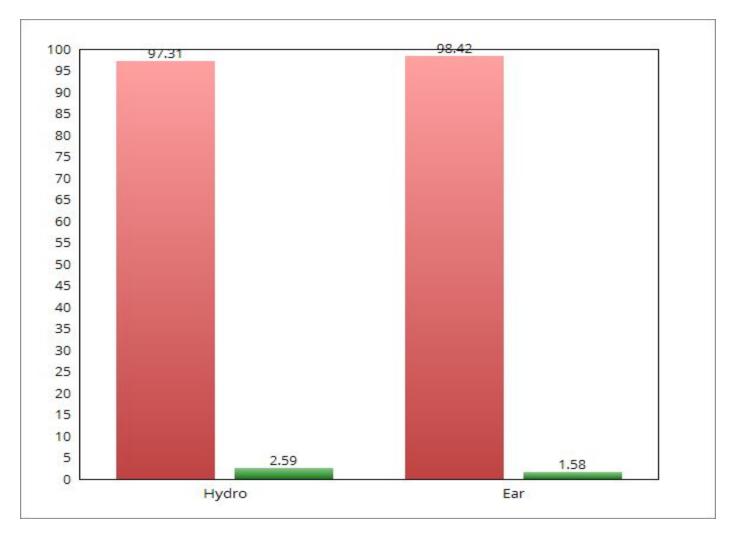




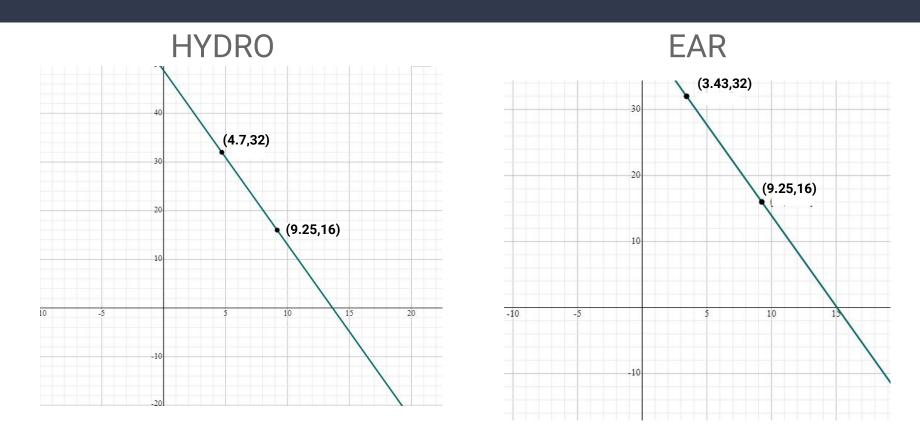
EAR



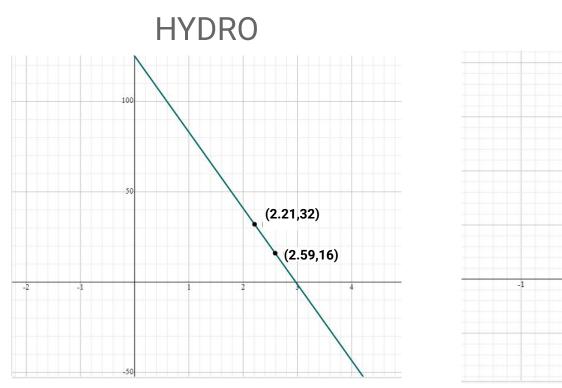


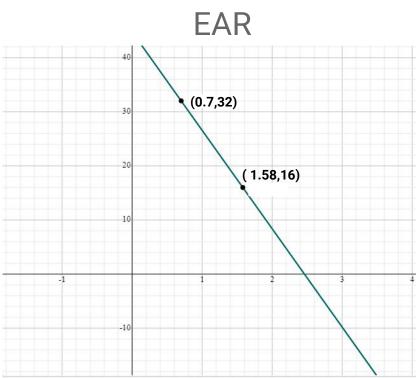


REPRESENTACIÓN DE RECTAS: DIRECTA



REPRESENTACIÓN DE RECTAS: TOT. ASOC.



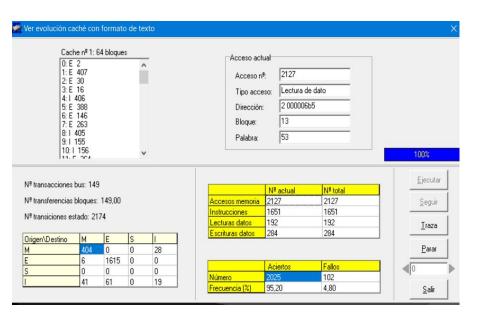


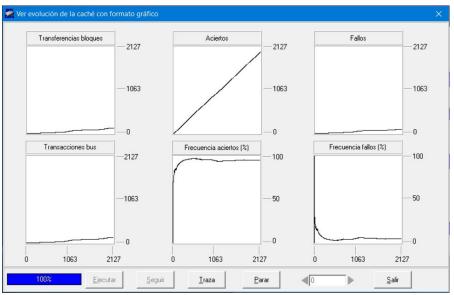
ALGORITMOS DE REEMPLAZO

CONFIGURACIÓN DE LA MC

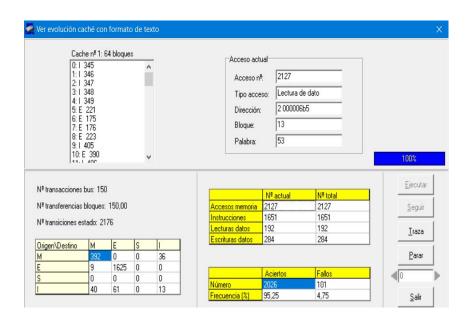


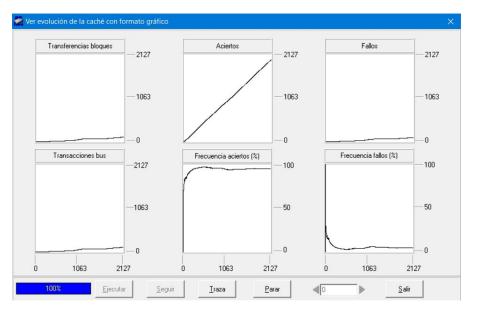
ALEATORIO



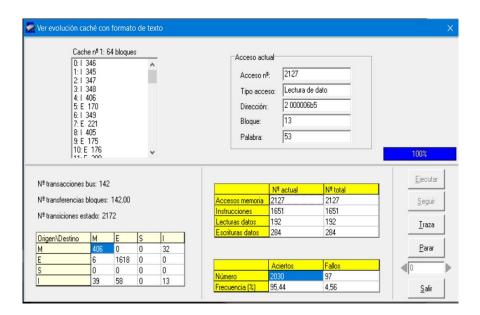


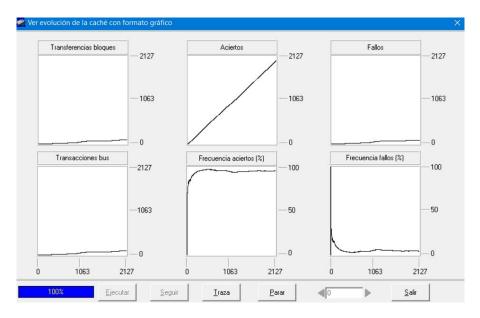
FIFO

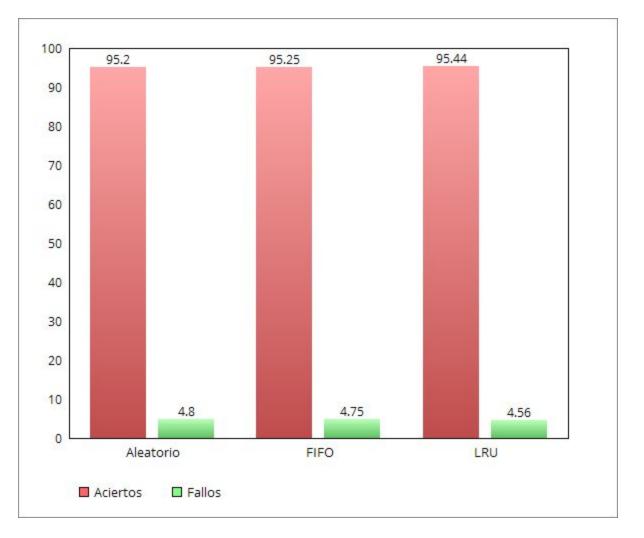




LRU







ALGORITMOS DE REEMPLAZO

- ¿Qué algoritmo de reemplazo es el que obtiene mejores resultados?.

El mejor es el LRU (Tasa de Fallos 4.56)

- ¿Y el segundo mejor?.

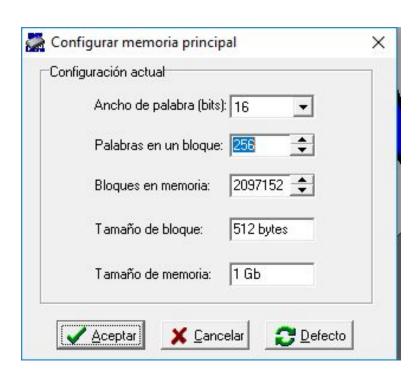
FIFO (Tasa de Fallos 4.75)

- Empleando la opción de visión de la evolución de la caché en modo gráfico, ¿se mantiene constante la tasa de fallos?, ¿por qué?.

Se mantiene prácticamente constante,. Sin contar el inicio, el número de fallos apenas aumenta con respecto al número de aciertos, luego de media a la larga ejecución parece que ha habido muy pocos fallos.

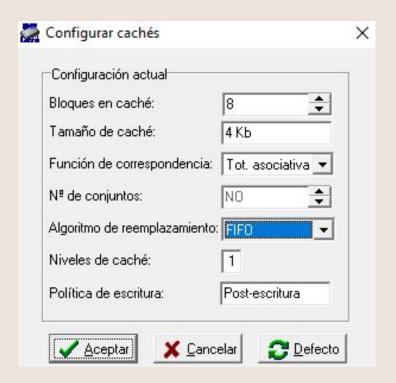
DISEÑO TRAZAS DE MEMORIA

CONFIGURACIÓN DE LA MP



CONFIGURACIÓN DE LA MC - FICH1





FICHERO 1

DIRECTA

B0

B1

B2

B3

B4

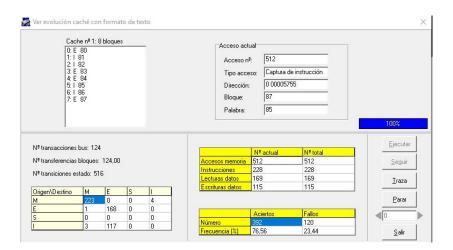
B5

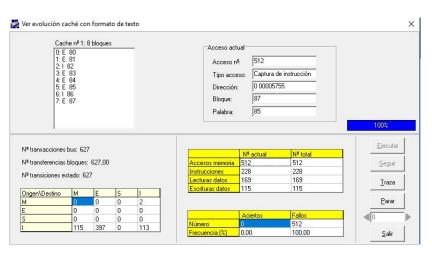
B6

B7

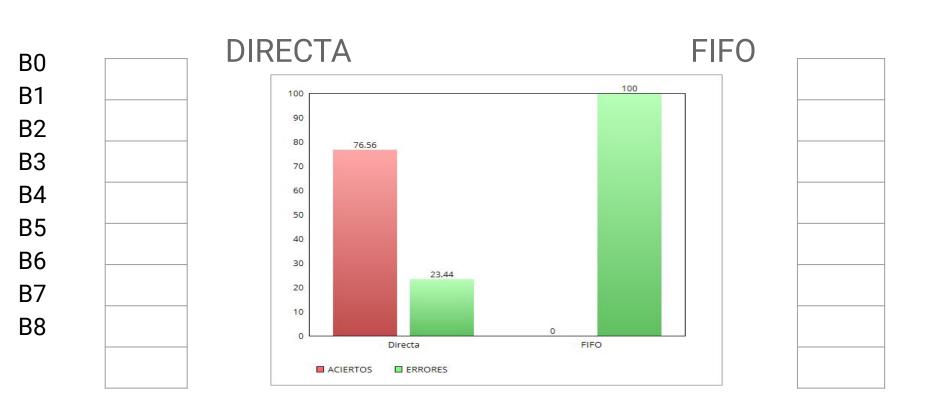
B8

FIFO

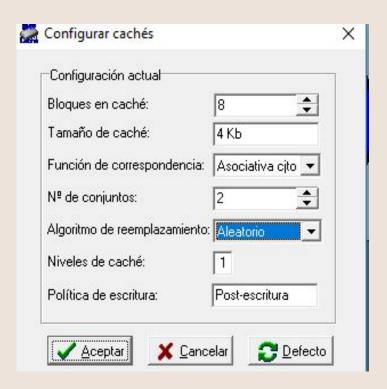


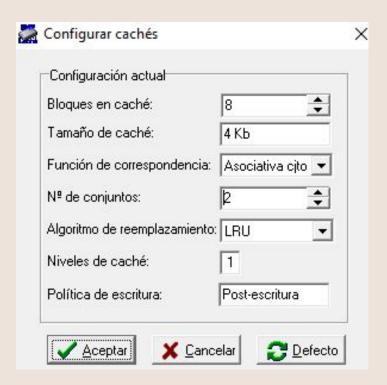


FICHERO 1



CONFIGURACIÓN DE LA MC - FICH2





FICHERO 2

B0 B6 B1 B7 B2 B3 B8

B1

B2

B3

B4

B5

B6

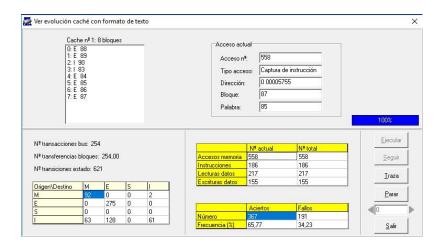
B12

B5

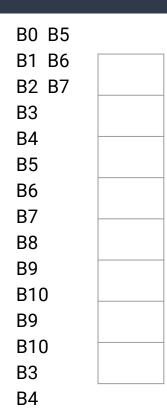
ALEATORIO

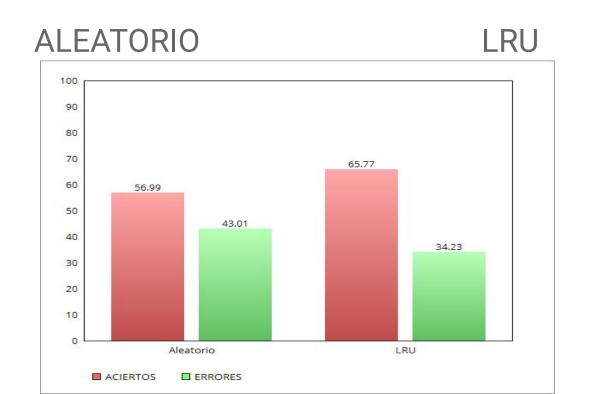
Ver evolución caché con formato de texto X Cache nº 1: 8 bloques Acceso actual 0: E 87 1: E 86 Acceso nº: 2: I 90 3: E 89 Captura de instrucción Tipo acceso: 0 00005755 Dirección: 6:1 83 7:1 82 Bloque: 85 Palabra: Ejecutar Nº transacciones bus: 302 Nº actual Nº total Nº transferencias bloques: 302,00 558 558 Accesos memoria Seguir 186 Nº transiciones estado: 620 217 Lecturas datos 217 Traza 155 155 Origen\Destino Parar 225 0 Fallos 240 178 0 59 Frecuencia (%) 56,99 43,01 Salir

LRU

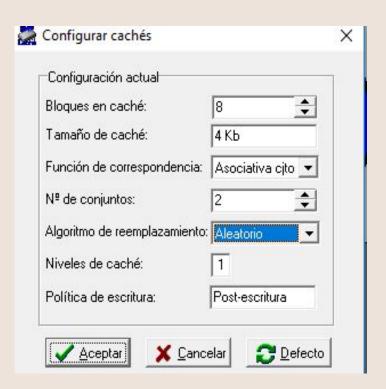


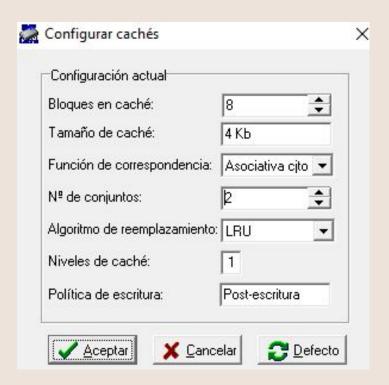
FICHERO 2





CONFIGURACIÓN DE LA MC - FICH3





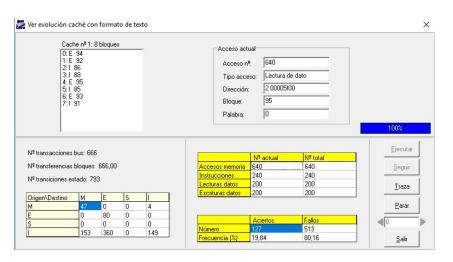
FICHERO 3

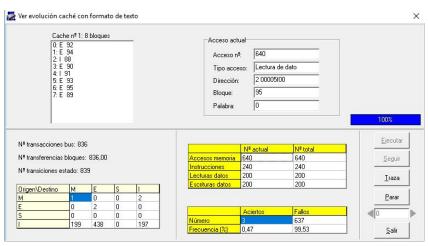
B0 B15 **B**1 B2 **B**3 **B4 B5 B6** B7 B8 **B9** B10 B11 B12 B13

B14

ALEATORIO

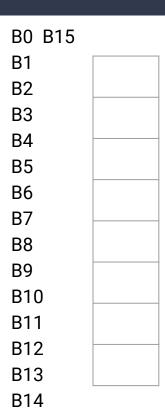
LRU



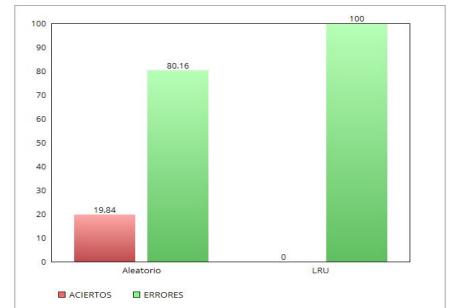


FICHERO 3

LRU







¡MUCHAS GRACIAS!