HowTo Write Fast Numerical Code Exercise 1

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1 Cost Analysis

We consider the matlab function given in the exercise sheet (listing 1) provided on the course homepage:

```
function z = func (x1, x2, y)

m = length(x1);
n = length(y);

if m == 1

z = x1(1) + sum(y);

return
k = m/2;
t = func(x1(1:k), x2(1:k), y) * func(x1(k+1:m), x2(k+1:m), y)

;
y1 = pi*y;
z = t*func(x3(1:k), x3(k+1:m), y1);
end
```

Listing 1: Matlab code given in the exercise

1.1 Floating Point Additions

We observe that there are in total $A_1 = n$ floating point additions for the case length(x1) = m = 1 and

$$A_m = 3 \cdot A_{m/2} + m \tag{1}$$

floating point additions per recursion step. Using the formula provided in the course we are able to solve the recursion by first substituting m with 2^k ,

$$F_k = 3 \cdot F_{k-1} + 2^k. \tag{2}$$

Then solving the recursion:

$$F_k = 3^k n + \sum_{i=0}^{k-1} 3^i \cdot 2^{k-i} = 3^k n - 2(2^k - 3^k).$$
 (3)

Doing a back substitution gives us a total amount of floating point additions:

$$A_m = 3^{\log_2 m} n + 2(m - 3^{\log_2 m}). \tag{4}$$

1.2 Floating Point Multiplications

We observe that there are in total $M_1 = 0$ floating point additions for the case length(x1) = m = 1 and

$$M_m = 3 \cdot M_{m/2} + n + 2 \tag{5}$$

floating point multiplications per recursion step. Using the formula provided in the course we are able to solve the recursion:

$$G_{0} = 0,$$

$$G_{k} = 3 \cdot G_{k-1} + n + 2$$
Substitution of m with 2^{k}

$$= 3^{k} \cdot 0 + \sum_{i=0}^{k-1} 3^{i} \cdot (2+n)$$

$$= \sum_{i=0}^{k-1} 3^{i} \cdot (2+n) = \frac{1}{2} (3^{k} - 1) (n+2).$$
(7)

By substituting back we get the total amount of floating point operations:

$$M_m = \frac{1}{2} \left(3^{\log_2 k} - 1 \right) (n+2). \tag{8}$$

1.3 Total Floating Point Operations

Adding the equations (4) and (7) together we get the total amount of floating point operations in listing 1:

$$C_m = A_m + M_m$$

$$= 3^{\log_2 m} n + 2(m - 3^{\log_2 m}) + \frac{1}{2} (3^{\log_2 k} - 1) (n + 2).$$
(9)

2 Machine Information

The computer is running a Mac OSX version 10.8.2 using a Intel Core i7-3720QM CPU using a frequency of 2.60 GHz per core. There are 4 cores (8 threads) available (Datasheet¹).

Using an Intel Sandy Bridge architecture slideset² and the architecture slides³ we are able to compute the available floating point operations:

Floating Point additions/cycle: 1 Floating Point multiplications/cycle: 1 GFlops/s: $2.6GHz \cdot 2Flops/cycle$ = 5.6 GFlops/s per Core

¹http://ark.intel.com/products/64891

²https://www.cesga.es/gl/paginas/descargaDocumento/id/135

³http://www.inf.ethz.ch/personal/markusp/teaching/263-2300-ETH-spring13/slides/arch.pdf

3 Matrix Multiplication

In order to get accurate timings on OSX we installed and activated the DisableTurboBoost kernel module⁴ on the computer. The sysct1 now shows:

```
# sysctl -a hw | grep cpufrequency
hw.cpufrequency: 2600000000
hw.cpufrequency_min: 2600000000
hw.cpufrequency_max: 2600000000
hw.cpufrequency = 2600000000
```

Listing 2: Bash output of the sysctl command

The code shows $n \cdot m \cdot k$ floating point adds and $n \cdot m \cdot k$ floating point multiplications. **Total Floating Point operations: 2nmk**.

In order to get consistent timings we had to set the frequency define in mmm.c to 2.6e9. Resulting in the following output:

```
_{1} m=1000 k=1000 n=1000
2 RDTSC instruction:
  18399246918.000000 cycles measured => 7.076633 seconds,
     assuming frequency is 2600.000000 MHz. (change in source
     file if different)
5 C clock() function:
 7117748.000000 cycles measured. On some systems, this number
     seems to be actually computed from a timer in seconds then
     transformed into clock ticks using the variable
     CLOCKS_PER_SEC. Unfortunately, it appears that
     CLOCKS_PER_SEC is sometimes set improperly. (According to
     this variable, your computer should be running at 1.000000
     MHz). In any case, dividing by this value should give a
     correct timing: 7.117748 seconds.
8 C gettimeofday() function:
 7.117292 seconds measured
```

Listing 3: Execution of the mmm.c code compiled with GCC 4.7.2 and the flags -O3 -m64 -fno -tree-vectorize.

 $^{^4 {\}tt https://github.com/nanoant/DisableTurboBoost.kext}$

3.1 Plots

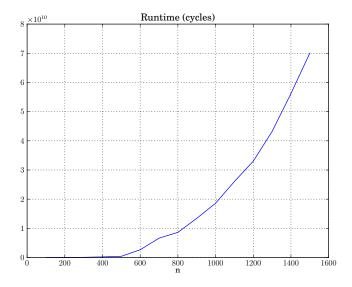


Figure 1: Runtime of $\mathtt{mmm.c}$ in cycles. Compiler: GCC 4.7.2, Flags: -03 -m64 -fno-tree-vectorize.

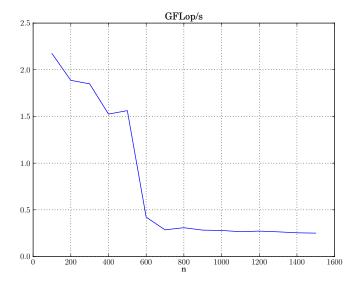


Figure 2: Achieved GFlop/s of mmm.c in cycles. Compiler: $GCC\ 4.7.2,\ Flags:\ -03\ -m64\ -fnotree-vectorize.$

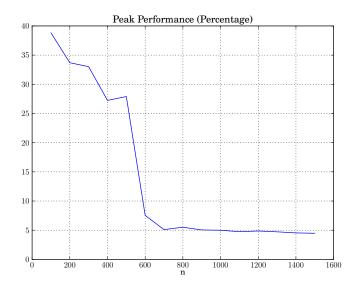


Figure 3: Achieved peak performance (percentage) of mmm.c in cycles. Compiler: GCC 4.7.2, Flags: -03 -m64 -fno-tree-vectorize.

We observe that we don't manage to use the utilise all the available floating point units.

4 Daxpy

The implementation of the "daxpy" function was straightforward. We copied the $\mathtt{mmm.c}$ code and replaced the matrix multiplication with the vector addition.

4.1 Plots

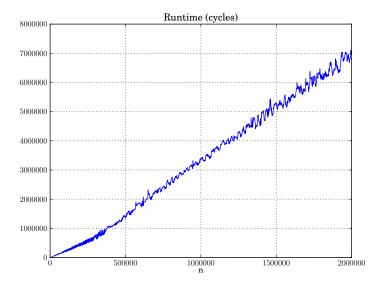


Figure 4: Runtime of daxpy.c in cycles. Compiler: GCC 4.7.2, Flags: -O3 -m64 -fno-tree-vectorize.

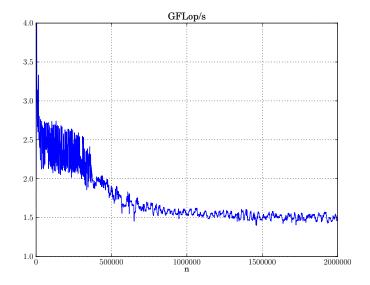


Figure 5: Achieved GFlop/s of daxpy.c in cycles. Compiler: $GCC\ 4.7.2$, $Flags: -03\ -m64\ -fno$ -tree-vectorize.

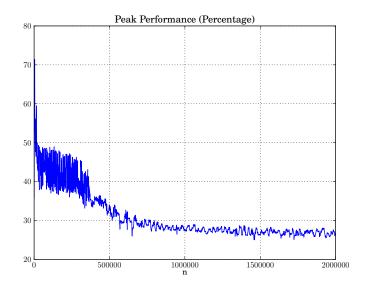


Figure 6: Achieved peak performance (percentage) of daxpy.c in cycles. Compiler: GCC 4.7.2, Flags: -03 -m64 -fno-tree-vectorize.

We notice that we were able to utilize 72% of the available floating point peak performance using a vector length of n=1000.

5 Bounds