## **HITACHI CONFIDENTIAL**

# Hitachi Finger Vein Authentication Device

# Data sheet

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#### **PRELIMINARY**

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## **Revision History**

No.	Date	Chapter	Description	Remarks
0.1	June 8, 2009	-	New release	Based on Japanese Ver.
				02
0.2	Oct. 2, 2010	1,3,4	Revised model name	Based on Japanese Ver.
			(PCT-KCC5001 to PCT-KCC50*1)	03
		3,4,	Changed marking of finger guide	
		3	Added remarks about EMI.	
		4.2	Added maximum number of	
			enrollment fingers	
		4.2.15, 4.3	Added verification time of	
			PCT-KCC5031/9031.	
		4.4	Added remarks for usage	
			condition	
0.21	June 17, 2011	5,	Changed firmware version from	Based on Japanese Ver.
			02-00 to 02-01	04

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### 1. Introduction

This document describes the specifications of embedded type finger vein authentication device PCT-KCC50\*1 and PCT-KCC90\*1.

## 2. Features

The small size embedded type finger vein authentication device uses transmissive optical method to read vein pattern and it is suitable for various security products.

#### 1. Easy to use

- The shape of the finger guide is designed to fit user's finger
- Marking on the finger guide lead user's finger to the right place
- Open type structure is accomplished by placing LEDs both sides.

#### 2. High accuracy

- Transmissive method enables to capture clear vein pattern
- The stable finger guide reduces false rejection by a positioning error.
- Two touch sensors reduce floating error of a finger.

#### 3. Low cost

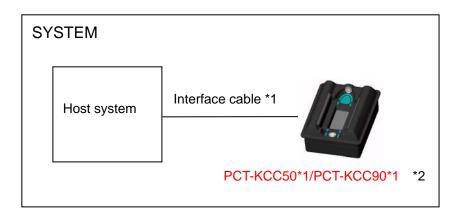
- Use one chip custom LSI (Hitachi FVP1000) for finger vein authentication
- Use CMOS sensor

#### 4. Others

- Authentication is done within the device
- Template data of finger vein is memorized in a non-volatile memory in the device
- Use serial interface (3.3V CMOS level) to communicate with a host system

# 3. Connection configuration

This device is supposed to connect as the following figure.



\*1 The interface cable must be less than 200mm.

It is recommended to change the interface signals to RS-232C level if longer cable is required.

And it is recommended to add a ferrite core to the interface cable and turn once.

\*2 Hitachi provides only the finger vein authentication device.

# 4. Configuration and function

#### 4.1 Appearance and dimensions

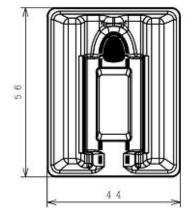
#### (1) Appearance

( · ) · 4-1	
PCT-KCC5001 (F/W Ver.01-00)	PCT-KCC5021 (F/W Ver.01-02)
PCT-KCC9001 (F/W Ver.01-00)	PCT-KCC9021 (F/W Ver.01-02)
PCT-KCC5011 (F/W Ver.01-01)	PCT-KCC5031 (F/W Ver.02-01) * 1
PCT-KCC9011 (F/W Ver.01-01)	PCT-KCC9031 (F/W Ver.02-01) * 1

Fig. 4.1 Appearance

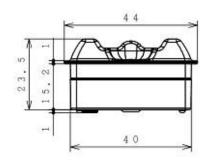
\* 1 : The template data of PCT-KC5031/9031 is not compatible with other models.

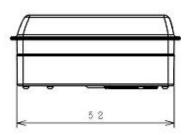
## (2) Dimensions

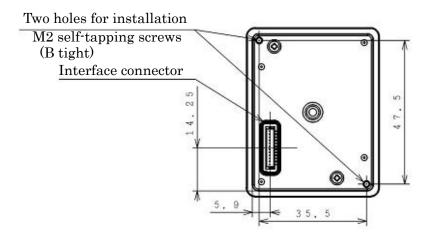


- The dimensions in the drawing are for reference. Hitachi can provide
   3D data if necessary.
  - -Tolerance: +/- 0.5mm









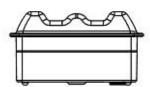
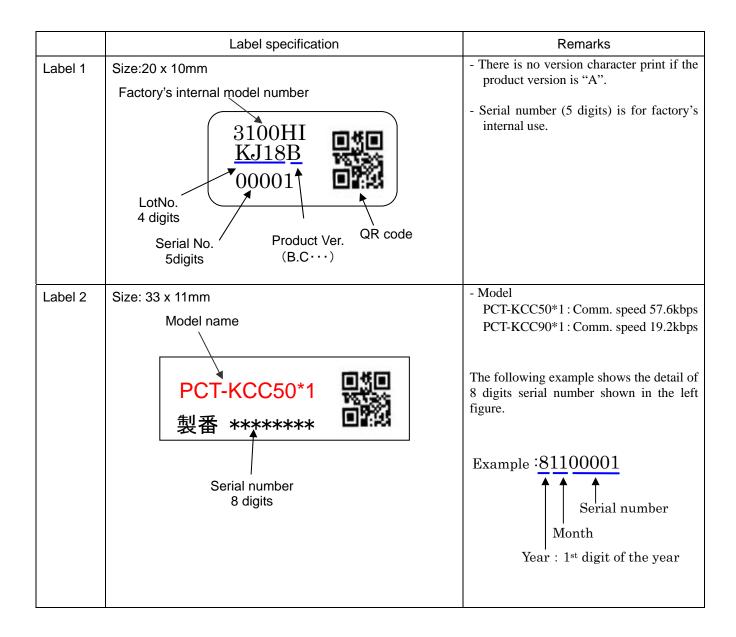


Fig. 4.2 Dimensions

#### (3) Label specification





## 4.2 Basic specifications

No.			Specification	on			Re	mark	is .
1	Outer dir	mensions			Except part	for	the	protruding	
2	Weight		32g						
3	Material	- Case	- PC+ABS		(Mitsubishi				
			Engineerin	g-Pl	astics Co. MB1700)				
	- Filter		- PMMA (Mi	tsub	oishi Rayon Co., Ltd.				
	- LED cover		PF079)						
4	Waterpro	oof	Corresponds	to	IEC IPX3	The h	ost	syste	em needs
						waterpr	oof	mec	hanism to
						assemb	le th	e dev	rice.
5	Power	supply	DC 5.0V +/-	5%					
	voltage								
6	Power	Stand-by	MAIN_PWR_	_O	120mA Typ.				
	supply		N: High						
	current		MAIN_PWR_	_O	75micro A Typ. *1	When	sen	sor	does not
			N: Low			detect.			
		Enrollment,	230mA Typ. *2						
		Verification	470mA Max. *3		Referen	ice v	alue		
7	Interface	signal	Asynchronou	IS S	erial communication				
	format								
8	Interface	signal level	3.3V CMOS	inpu	ut/output level	•	for	ope	n collector
						signal			
9	Interface	connector	JST Mfg. Co	., Lt	d. BM12B-SRSS-TB	12pin			
10	Commur	nication	57.6kbps	PC	T-KCC50*1				
	speed		19.2kbps	PC	T-KCC90*1				
11	Finger ve	ein capturing	Infrared LED	+ 0	ptical camera				
	system		Transmissive type						
12	Storage	of template	Stored in the device.		The c	onne	cted	host	
	data		<max. data="" enroll="" template=""></max.>		system	can	stor	e template	
			Twice scanning mode: 150 fingers		data as	well.			
			(360 fingers)		Max. nu	ımbe	r var	ies depend	
			Three times scanning mode: 100 fingers		on enro	ll mo	de.		
					(230 fingers)	The nu	mbe	r in	brackets is
						for PCT	-KCC	2503	1/9031.

	I., ., .			
13	Verification method	1:1 to 1:15 verification.		It is recommended to use
		-Twice enroll mode: '	1:15	1:1 verification for any
		-3 times enroll mode: 1:10		application.
		(In case of group ver	ification)	The device can verify over
				plural groups.
14	Accuracy	False rejection rate:	0.01%	1:1 verification
		False acceptance rat	e: 0.001%	Measurement based on
		Failure to enroll rate:	Less than 0.03%	ISO/IEC 19795-1
15	Verification time	1:1 Verification (reco	mmended):	Timeout: 5 sec.
		Ар	prox. 1 sec.	
		1:N Verification (grou	ıp	
		,	verification):	
		Арр	rox. 1 to 2 sec	
		For your reference,	the following are	
		verification time o	ver plural groups	
		verification with 100	fingers.	
		-Twice enroll mode:		
		Less than 3 sec.		
		(Less	s than 2.5 sec.)	The number in brackets is
		-3 times enroll mode:	:	for PCT-KCC5031/9031
		Les	s than 4 sec.	
		(Less	s than 3.0 sec)	
16	Environmental	0 to 40 deg. C	Operation	No condensation
	conditions	0 to 50 deg. C	Non	
	Ambient temperature		operation	
	and humidity	-20 to 60 deg. C	Storage	
		20 to 80% RH	Operation	
		20 to 80% RH	Non operation	
		10 to 90% RH	Storage	
17	Environmental	Do not use in direct s	sun light	
''	condition in use	Do not use in direct s	our light	
	CONTRIBUTION IN USE			

<sup>\*1</sup> Changes by environmental temperature

<sup>\*2</sup> Changes depends on a finger

<sup>\*3</sup> Measured value when the infrared LED is maximum brightness

#### 4.3 Accuracy

	Item	Specification		
Verification time		Approx. 1 to 2 sec. *1, *2		
		For your reference, verification		
		time over plural groups with 100		
		fingers is 3 to 4 sec. *4		
Verification	FRR (False Rejection rate)	0.01% *3 In case of standard		
accuracy		threshold (i.e. Middle)		
	FAR (False Acceptance Rate)	0.001% *3 In case of standard		
		threshold (i.e. Middle)		
Failure to enroll rate	(FTE)	Less than 0.03%		

<sup>\*1 &</sup>quot;Verification time" is the time between taking image and completion of verification.

Twice enroll mode: 1:15 verification 3 times enroll mode: 1:10 verification

The measurement method is based on international standard for biometric performance testing ISO/IEC 19795-1.

#### 4.4 Usage environment

The accuracy can be kept under the following environment.

Item	Specification	
Sunlight (indirect)	Less than 4000 Lx	
Bulb lamp	Less than 1,000 Lx	
Fluorescence lamp	Less than 2,000 Lx	

Condition: The light does not enter the image capturing area directly.

Do not install the device where it is exposed to intense light, such as under direct sun light or near a window.

If you cannot avoid installing at such places, setting "Strong light mode" for capture mode can make it easier to succeed verification. However, such as placement of a finger, individual difference or light coming into scanning area may cause verification error or false acceptance. Be sure to check if such error does not cause trouble to user's operation before installation.

<sup>\*2</sup> The time varies depend on the number of fingers. The number of fingers is less than 15.

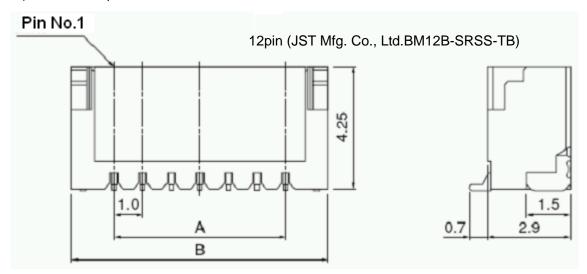
<sup>\*3</sup> In case of 1:1 verification.

<sup>\*4</sup> Approx. 2.5 to 3 seconds for PCT-KCC5031/9031.

<sup>\* &</sup>quot;Strong light mode" is available only for PCT-KCC5031/9031.

## 4.5 Interface specification

## 1) Connector specification



Recommended housing: JST Mfg. Co., Ltd. SHR-12V-S-B or SHR-12V-S

Recommended wire: AWG28

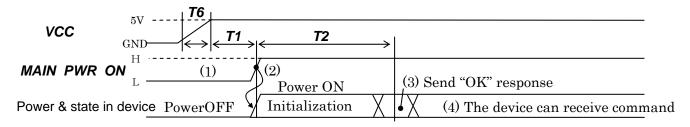
#### 2) Connector pin assignment

No.	Signal name	I/O	Function	Specification
1	VCC	-	Power supply	5.0V +/- 5%
2	VCC	-	Power supply	
3	RX	I	Serial communication data	Asynchronous receiving data. 3.3V CMOS level
4	TX	0	Serial communication data	Asynchronous transmission data. 3.3V CMOS level
5	GND	-	Ground	GND
6	MAIN_PWR_ON	I	Power control input	3.3V CMOS level "H": Power ON, "L": Power OFF * This signal is to control power of the device by a level control. (Not an edge trigger control)
7	BUSY	0	Busy output	3.3V CMOS level  "H" means the device is busy. In this case, do not turn power off. *1  "L": In this case, power can be turned off.  *1 This signal indicates the device is processing internally and ask a host system not to turn power off.
8	TOUCH_OUT1	0	Output of touch sensor (The tip side of the finger guide)	Open collector output Note *1)  Detected a finger being placed: GND Others: Open (Hi-Z) * This signal shows whether a finger is placed on the unit.
9	PASS_DRVER	0	Verification result output	Open collector output Note *2) Verified: GND Others: Open (Hi-Z) * If a finger is verified, this signal shorts to GND.
10	TOUCH_OUT2	-	Output of touch sensor (The root side of the finger guide)	Same as pin #8
11	RESET		Hardware reset input	GND: Reset Open: Normal operation Note *3)
12	GND	-	Ground	GND

- Note \*1) **TOUCH\_OUT1 and TOUCH\_OUT2** signals operate even if the internal power of the device is off state (**MAIN\_PWR\_ON**="L").
- Note \*2) **PASS\_DRVER** signal is used to drive an external mechanical part. This signal goes to Low level when the verification is succeeded, but it won't return Hi-Z automatically. Be sure to send "Release PASS\_DRIVER" command on serial communication after verification in order to return Hi-Z level. Rating specification of "**PASS\_DRVER**" signal is 24V max. and 100mA max.
- Note \*3) The host system must control RESET signal by an open collector device because the signal is pulled up in the device.

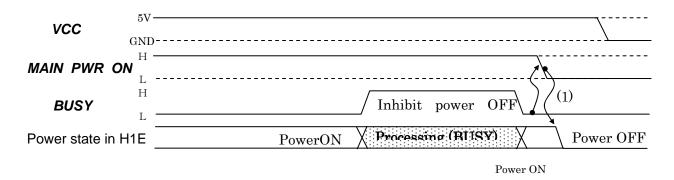
#### 4.6 Control timing

#### 1) Power ON sequence



- (1) VCC must rise from 0 to 5V within T6 time.
- (2) The device turns on when MAIN\_PWR\_ON signal becomes "H" after T1 time from VCC rises to 5V.
- (3) The device sends "OK" response to a host T2 time after power is turned ON. (Refer to serial interface specification)
- (4) After the sequence described above, the device becomes to be able to communicate by serial interface commands.

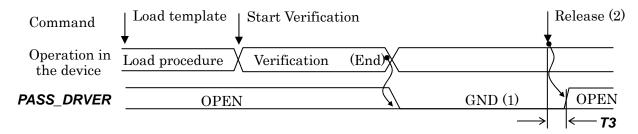
#### 2) Power off sequence



(1) The device goes to power OFF state when *MAIN\_PWR\_ON* becomes "L" level or cut power supply. However, a host system shall not turn off the device when *BUSY* signal is "H" because it means that the device is still processing internally.

Therefore, make sure that **BUSY** signal is "L" before turn **MAIN\_PWR\_ON** to 'L" level. If **BUSY** signal is "H", a host system shall wait until it becomes "L".

#### 3) PASS\_DRVER output specification



- (1) **PASS\_DRVER** signal becomes GND when a finger is verified with an enrolled template. If it fails to verify, it maintains OPEN state.
- (2) **PASS\_DRVER** returns to OPEN state when the device receives "PASS\_DRVER release" command. This is the only way **PASS\_DRVE** goes to OPEN status and there is no other one.

#### 4) Hardware Reset

The host system can recover the device by hardware **RESET** signal to the device if there is no response from the device. This **RESET** signal is valid when **VCC** is supplied and **MAIN\_PWR\_ON** signal is in "H" state. (i.e. Power in the device is ON state)

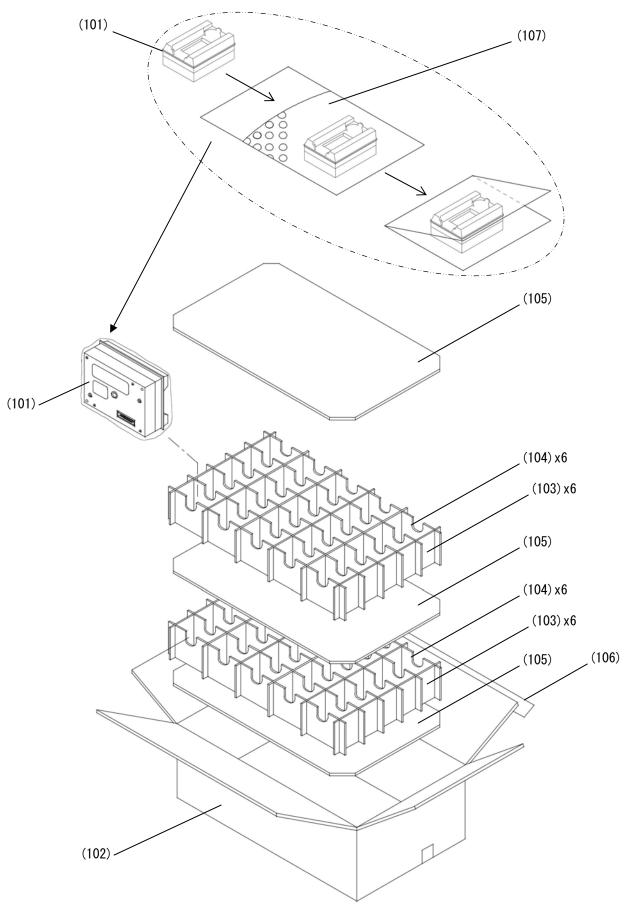
#### 5) Health check

"Get information" command (12h) can be used to know whether the device is operating normally.

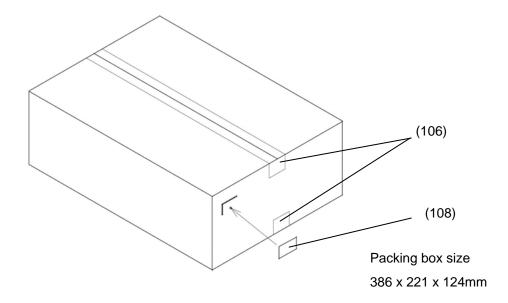
#### 6) Timing specification

		T	T		
No.	Symbol	Minimum	Maximum	Unit	Remarks
1	T1	0	-	ms	
2	T2	-	1000	ms	OK response from the device
					indicates the end of initialization.
					(Reference value)
3	<i>T</i> 3	-	3	ms	Release time of PASS_DRVER
					signal
4	T4	-	20	s	Timeout of serial command
5	T5	5	-	ms	<b>RESET</b> pulse width
6	<i>T</i> 6	-	5	ms	VCC rising time

# 4.7 Packing specification



#### 50 pieces max. in a box



No.	Item	Remarks
101	Finger vein authentication device	
102	Packing case	
103	Partition (big)	
104	Partition (small)	
105	Partition (floor)	
106	KU tape (transparent)	
107	Air bug	
108	Shipping label	

#### 4.8 Others

A host system equipped with the device requires the following functions for security purposes.

- 1) The host system must be equipped with one of the following functions.
  - Invalidate the user.
  - Reject verification requests for specific time frame.
  - Send a warning to the administrator.
  - Record the rejection log.

This function is necessary to avoid illegal verification such usages of a forged finger

- 2) Operations involving template data, such as enrollment and/or deletion of template data, should be performed by the administrator.
  - e.g. setting user identification and/or limiting access to curtain level of information, etc.

3) A host system equipped with the device requires a way to identify whether the module has been intentionally removed or illegally modified.

e.g. adhere a tamper evident seal at the joint of the device and the host system.

Refer to "Serial Interface Specification" for serial communication command.

Refer to "Application note" for designing hardware.

# 5 Conforming regulations

No.	Item
1	RoHS

# 6. Quality

Item	Specification	Remarks
Reliability	Product life: 5 years	
Vibration	- Operating	
	2.45m/s <sup>2</sup> , Frequency: 5 to 55Hz/min	
	X,Y,Z direction	
	No failure under the condition above.	
	- Transportation/Storage	
	7.35 m/s <sup>2</sup> , Frequency: 5 to 500Hz,	
	Sine sweep	
Packing box drop spec.	0.6m drop tests with 1 corner, 3 edges and 6	
	surfaces.	
	No damage, no loose of screws and no failure	
	under the condition above.	