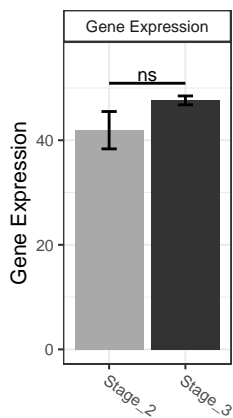
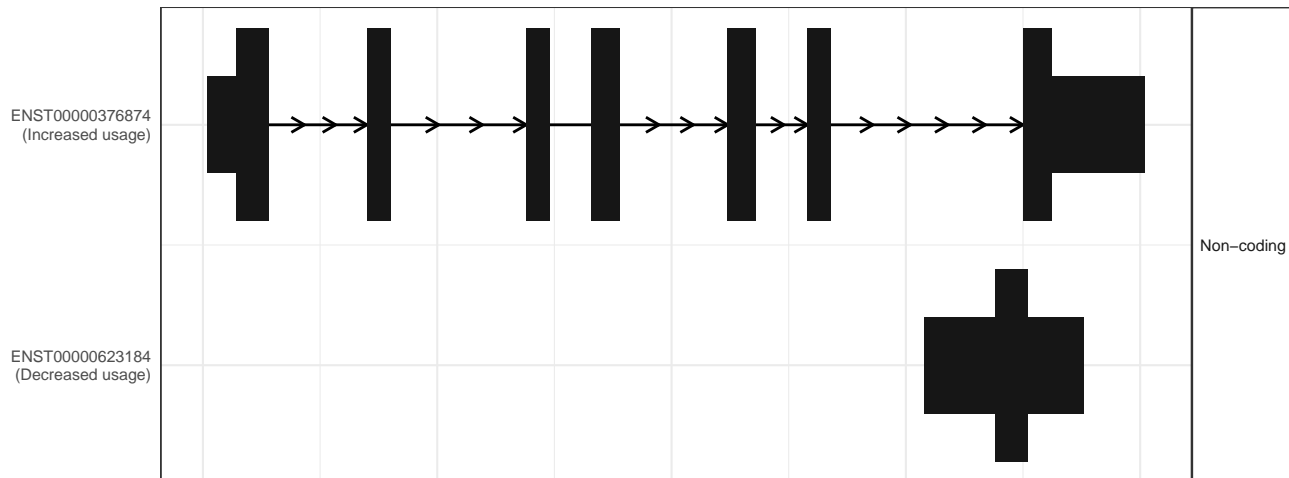
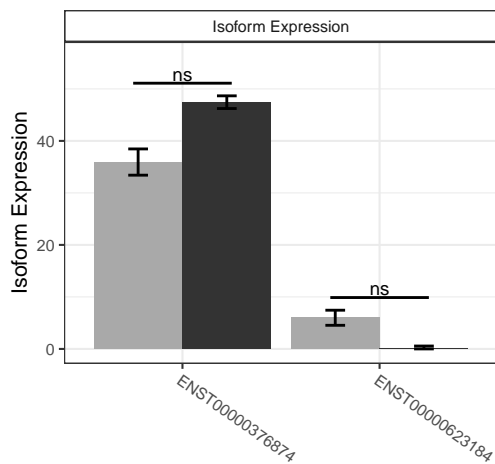


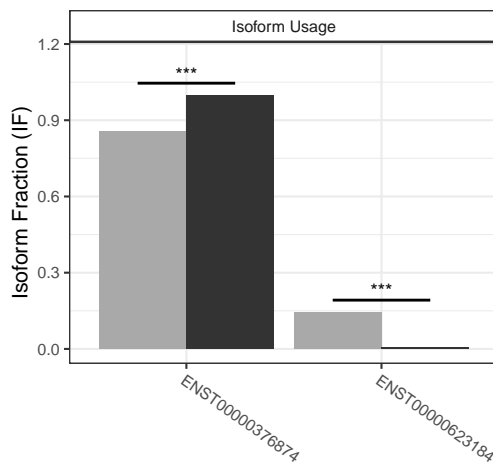
The isoform switch in RILPL1 (Stage_2 vs Stage_3)



Condition



Isoform



Isoform

Condition

