

# Taylor Lloyd

SOFTWARE DEVELOPER · COMPUTER SCIENTIST

Seattle, Washington

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## Work Experience

### Amazon.com, Alexa for Business

Seattle, Washington, USA

SOFTWARE DEVELOPMENT ENGINEER

May '18 - Present

- Designed and implemented Polycom video conferencing integration with Alexa for Business.
- Implemented Full-CD policies for team, including testing requirements and automated deployments.

### University of Alberta, Systems Laboratory

Edmonton, Alberta, Canada

RESEARCH ASSISTANT

May '16 - Dec '17

- Developed and evaluated transformations improving OpenCL performance on Intel FPGAs by up to 700%.
- Evaluated relationships between GPU occupancy and performance, producing a Machine-Learning model capable of improving Clang OpenMP performance up to 6.8 times.
- Mentored summer students, establishing projects, timelines, and deliverables.

### IBM Canada, Compilers Group / Cloud Innovation Lab

Toronto, Ontario, Canada

CAS STUDENT FELLOW / CO-OP SOFTWARE DEVELOPER

Dec. '15 - May '16, May '14 - June '15

- Implemented GPU data transfer for OpenMP 4.5 in the XL compiler.
- Prototyped and shipped novel GPU loop optimizations, speeding GPU execution by 25%.
- Worked with engineering and legal teams to patent new compiler techniques.
- Developed a functional programming language (IBM Dash) and compiler for GPU execution. Wrote language specifications, compiler optimizations, and language runtime extensions.
- Developed a cloud-based javascript framework for big-data processing and workload-aware load balancer for Apache Spark clusters.
- Implemented automated testing and continuous deployment on projects across the team with GitLab and Jenkins.

### Amazon.com

Seattle, Washington, USA

SOFTWARE DEVELOPMENT ENGINEER INTERN

Sept. '13 - Mar. '14, June '15 - Aug. '15

- Designed, implemented, and supported an Android chat application with Microsoft Lync integration.
- Designed and implemented bandwidth-aware security camera tablet application, monitoring thousands of warehouse cameras.
- Created a templating language and Android rendering application to prototype process-oriented workflows and deploy them in days.
- Worked with a small (4-6) person team under an Agile continuous delivery process to support and update thousands of Android devices.
- Ported security libraries from Linux to Android, enabling corporate use of Android tablets.

## Projects

### GPUCheck

STATIC GPU PERFORMANCE ANALYSIS

GPUCheck detects GPU performance problems like non-coalescable memory accesses and divergent branches through LLVM analysis. GPUCheck improves benchmark performance up to 25%, and can be integrated into the build process of any GPU executable compiled using LLVM.

### VRTerminal

GOOGLE VR TERMINAL EMULATOR

VRTerminal is a VT100-compatible 256-color terminal emulator for Android, written in Java & OpenGL and available on the Google Play Store.

### Wedding RFID

TRACKING/GUIDANCE FOR GUESTS

Guests are issued an RFID tag, used at terminals throughout the event. Tags are tied to guests by web API, while touchscreens and RFID sensors allow interaction. The terminals allow sign-in, direct guests to seats, and track engagement.

## Education

### M.Sc. Computing Science

UNIVERSITY OF ALBERTA

- Specialization in Compiler Optimizations for GPU Computing
- GPA 4.0

### B.Sc. Specialization in Computing Science

UNIVERSITY OF ALBERTA

- GPA 3.6

## Skills

### Programming Languages

Golang, Java, CUDA, C/C++, Python, LaTeX, Scala

### Platforms & Frameworks

Lambda, LLVM/Clang, OpenMP, OpenCL, Android

### Areas of Interest

Compiler Optimizations, Heterogeneous Computing, Mobile Development




## Honors & Awards

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2018	<b>Outstanding Thesis Award - Runner-up</b> , University of Alberta	<i>Edmonton, AB</i>
2017	<b>Queen Elizabeth II Graduate Scholarship - Master's Level</b> , University of Alberta	<i>Edmonton, AB</i>
2013, 2015	<b>Dean's Honor Roll</b> , University of Alberta	<i>Edmonton, AB</i>
2013	<b>Undergraduate Student Research Award(USRA)</b> , NSERC	<i>Edmonton, AB</i>
2013	<b>Amdahl Academic Scholarship in Computing Science</b> , University of Alberta	<i>Edmonton, AB</i>
2011, 2012	<b>Jason Lang Academic Excellence</b> , University of Alberta	<i>Edmonton, AB</i>
2010	<b>Academic Excellence</b> , University of Alberta	<i>Edmonton, AB</i>
2009	<b>First Place - Iverson Computing Science Exam</b> , University of Alberta	<i>Edmonton, AB</i>

## Publications & Presentations

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	<b>Program Analysis and Compiler Transformations for Computational Accelerators</b>	<i>Edmonton, AB</i>
UNIVERSITY OF ALBERTA MASTER'S THESIS		<i>May '18</i>
	<b>GPUCheck: Detecting CUDA Thread Divergence with Static Analysis</b>	
NOT YET PUBLISHED		<i>March '18</i>
	<b>Sometimes Machine Learning is Not the Answer: Grid Geometry Tuning for OpenMP GPU Kernels</b>	
NOT YET PUBLISHED		<i>Jan '18</i>
	<b>Divergence and Arithmetic Control Form: Analyzing GPU Applications</b>	<i>Markham, Ontario, Canada</i>
16TH COMPILER-DRIVEN PERFORMANCE WORKSHOP - CASCON		<i>Nov '17</i>
	<b>A Case for Better Integration of Host and Target Compilation When Using OpenCL for FPGAs</b>	<i>Ghent, Belgium</i>
FPGAs FOR SOFTWARE PROGRAMMERS - FSP		<i>Sept '17</i>
	<b>GPGPU Offloading with OpenMP 4.5 in the IBM XL Compiler</b>	<i>Markham, Canada</i>
15TH COMPILER-DRIVEN PERFORMANCE WORKSHOP - CASCON		<i>Nov '16</i>
	<b>Modern analytics with the IBM Dash Compiler</b>	<i>Markham, Ontario, Canada</i>
13TH COMPILER-DRIVEN PERFORMANCE WORKSHOP - CASCON		<i>Nov '14</i>