1. Description

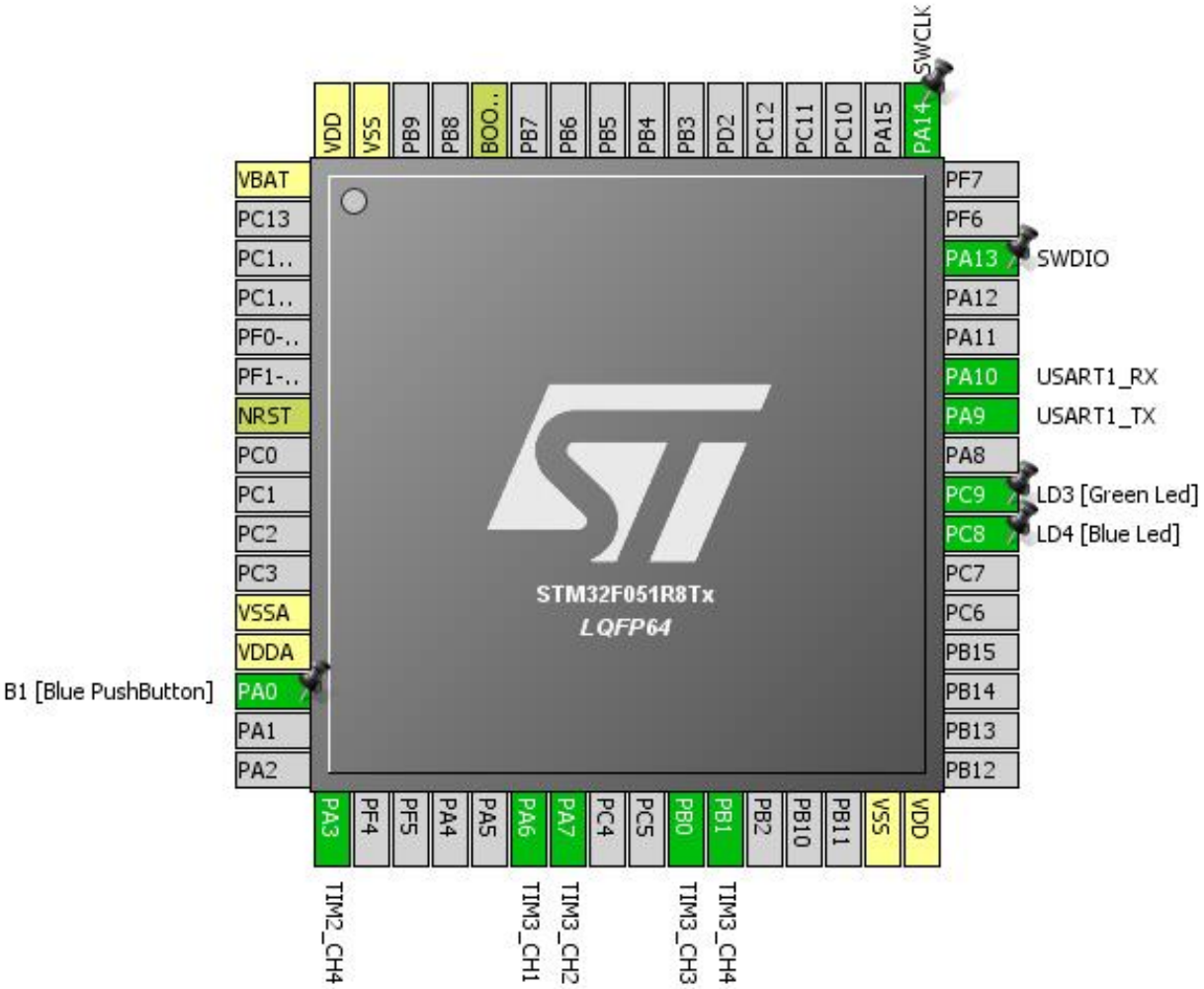
1.1. Project

Project Name	ServoLifter
Generated with:	STM32CubeMX 4.3.0
Date	07/23/2014

1.2. MCU

MCU Serie	STM32F0
MCU Line	STM32F0x1
MCU name	STM32F051R8Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
SYS	Serial-WireDebug	SYS_SWCLK	PA14
		SYS_SWDIO	PA13
TIM2	Clock Source : Internal Clock	N/A	N/A
	Channel4: PWM Generation CH4	TIM2_CH4	PA3
TIM3	Clock Source : Internal Clock	N/A	N/A
	Channel1: PWM Generation CH1	TIM3_CH1	PA6
	Channel2: PWM Generation CH2	TIM3_CH2	PA7
	Channel3: PWM Generation CH3	TIM3_CH3	PB0
	Channel4: PWM Generation CH4	TIM3_CH4	PB1
USART1	Mode: Asynchronous	USART1_RX	PA10
		USART1_TX	PA9

MiddleWare	Mode
FREERTOS	Enabled

4. Pins Configuration

Pin	Pos	Function(s)	Label
PA0	14	GPIO_EXTI0	B1 [Blue PushButton]
PA3	17	TIM2_CH4	
PA6	22	TIM3_CH1	
PA7	23	TIM3_CH2	
PB0	26	TIM3_CH3	
PB1	27	TIM3_CH4	
PC8 *	39	GPIO_Output	LD4 [Blue Led]
PC9 *	40	GPIO_Output	LD3 [Green Led]
PA9	42	USART1_TX	
PA10	43	USART1_RX	
PA13	46	SYS_SWDIO	SWDIO
PA14	49	SYS_SWCLK	SWCLK

* The pin is affected with an I/O function

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F0
Line	STM32F0x1
MCU	STM32F051R8Tx
Datasheet	022265_Rev4

5.2. Parameter Selection

Temperature	25
Vdd	3.6

5.3. Battery Selection

Battery	Not set
Capacity	0.0 mAh
Self discharge	0.0 %/month
Nominal voltage	0.0 V
Max Cont Current	0.0 mA
Max Pulse Current	0.0 mA
Cells in series	1
Cells in parallel	1

6. Software Project

6.1. Project Settings

Name	Value
Project Name	ServoLifter
Project Folder	C:\Users\mmi\Documents\STM32Cube\ServoLifter
Toolchain / IDE	MDK-ARM 4.73
Firmware Package Name and Version	STM32Cube FW_F0 V1.0.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed