

# ESP32 DEVKIT V1 - DOIT

version with 30 GPIOs

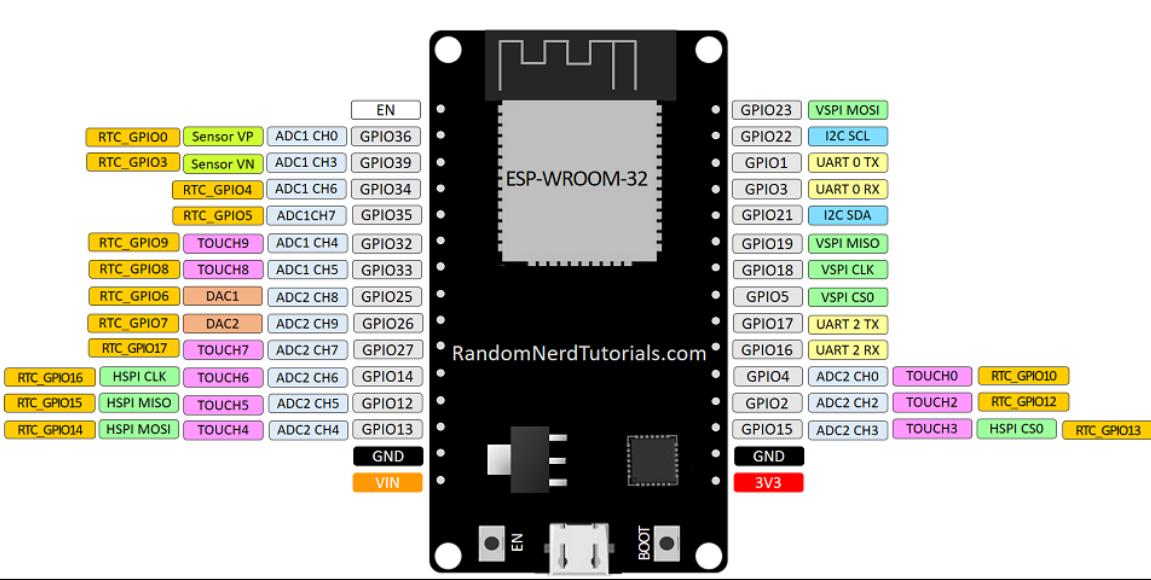
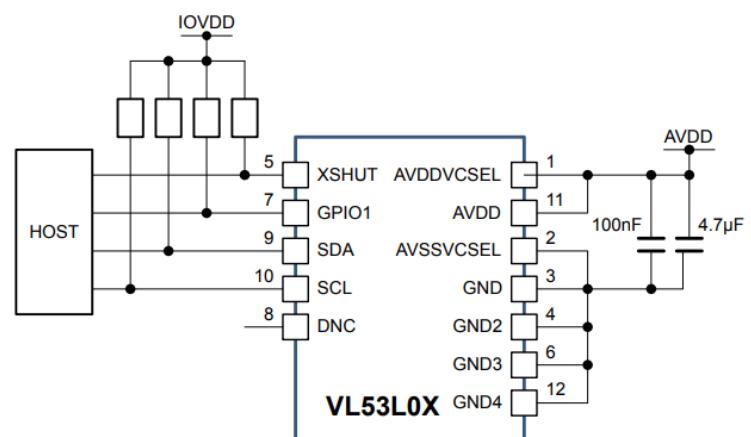


Figure 3. VL53L0X schematic



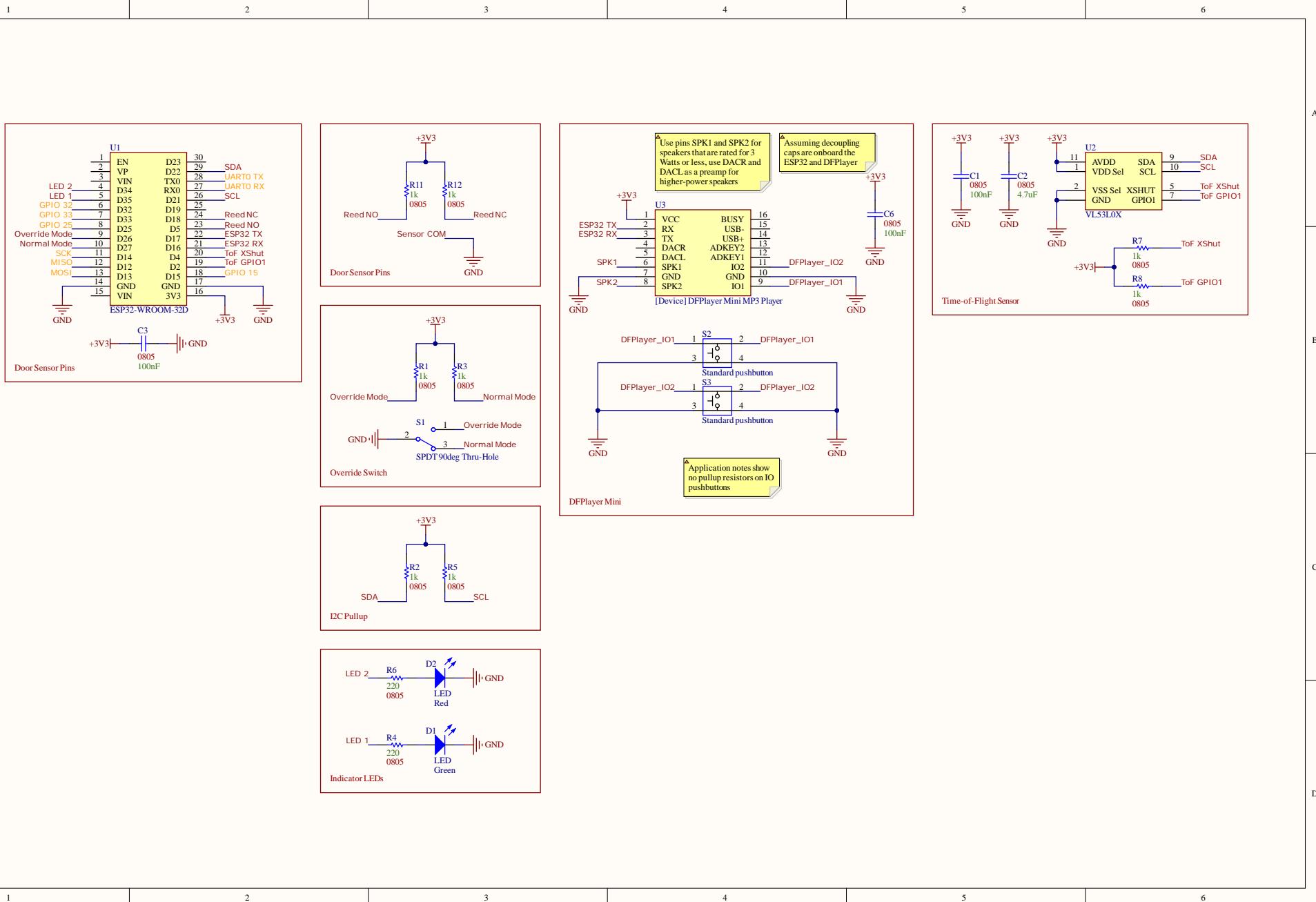
Note: Capacitors on the external supply AVDD should be placed as close as possible to the AVDDVCSEL and AVSSVCSEL module pins.

Note: The external pull-up resistor values can be found in the I<sup>C</sup>-bus specification. Pull-ups are typically fitted only once per bus, near the host. Recommended values for pull-up resistors for an AVDD of 2.8 V and a 400 kHz I<sup>C</sup> clock are 1.5 k to 2 kOhms

Note: The XSHUT pin must always be driven to avoid leakage current. A pull-up is needed if the host state is not known. XSHUT is needed to use hardware standby mode (there is no I<sup>C</sup> communication).

Note: The recommended value of the XSHUT and GPIO1 pull-ups is 10 kOhms.

Note: The GPIO1 should be left unconnected if not used.



A

A

B

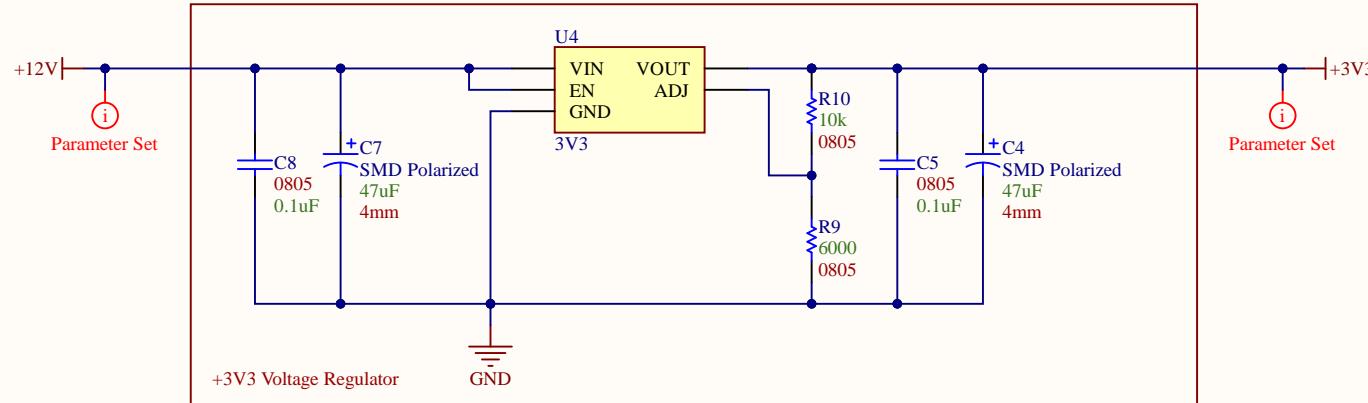
B

C

C

D

D



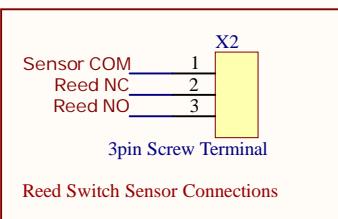
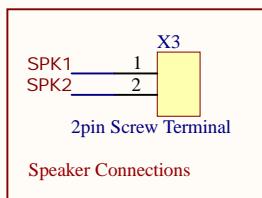
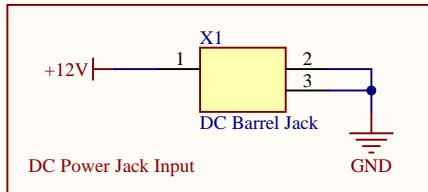
If  $V_{OUT} = 3.3$  and  $R_1 = 10k$ , then  $R_2 = 6035.0$

$$V_{OUT} = 1.242 \times \left( \frac{R_1}{R_2} + 1 \right)$$

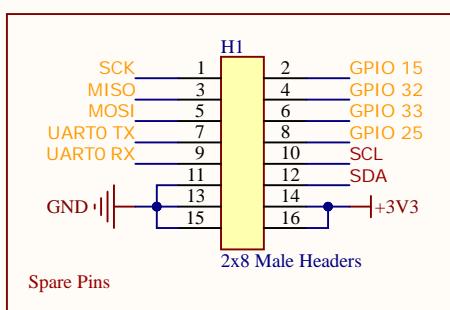
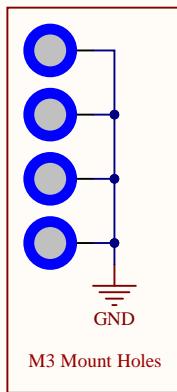
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A



B



A

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