

ESP32 DEVKIT V1 – DOIT

version with 30 GPIOs

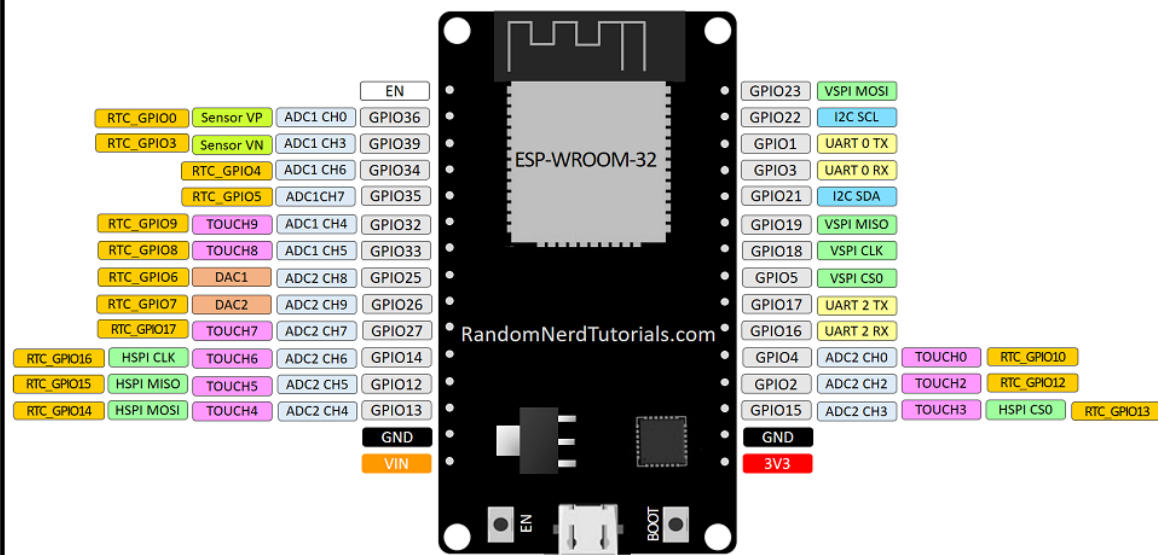
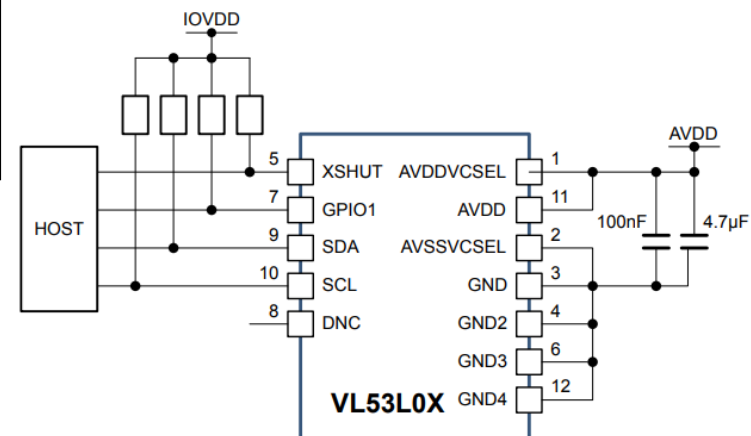
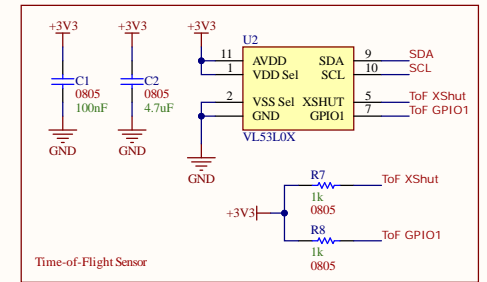
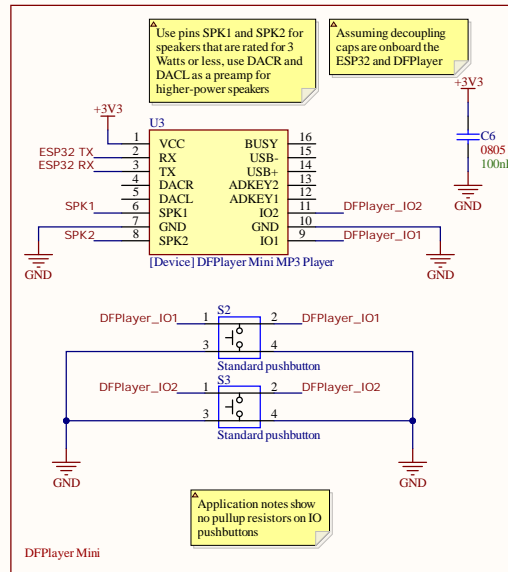
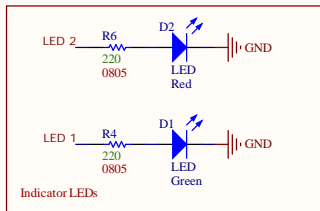
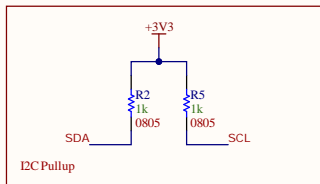
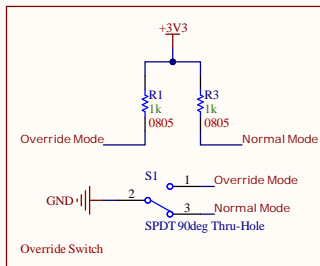
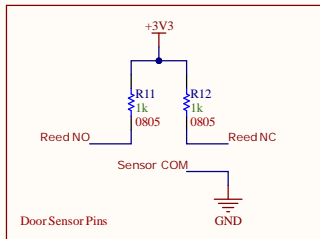
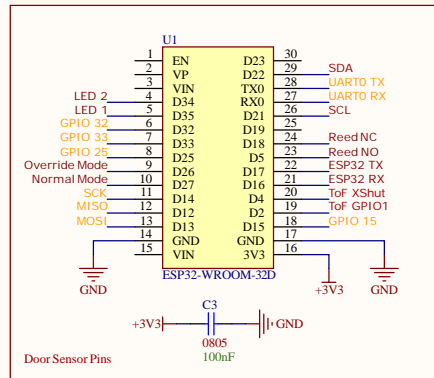
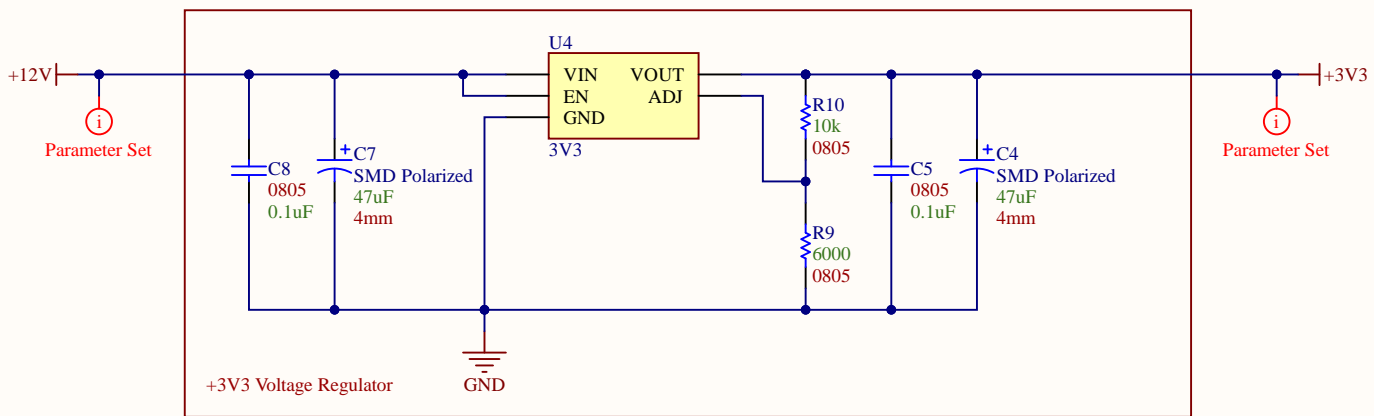


Figure 3. VL53L0X schematic



- Note:** Capacitors on the external supply AVDD should be placed as close as possible to the AVDDVCSEL and AVSSVCSEL module pins.
- Note:** The external pull-up resistor values can be found in the I²C-bus specification. Pull-ups are typically fitted only once per bus, near the host. Recommended values for pull-up resistors for an AVDD of 2.8 V and a 400 kHz I²C clock are 1.5 k to 2 kΩ.
- Note:** The XSHUT pin must always be driven to avoid leakage current. A pull-up is needed if the host state is not known. XSHUT is needed to use hardware standby mode (there is no I²C communication).
- Note:** The recommended value of the XSHUT and GPIO1 pull-ups is 10 kΩ.
- Note:** The GPIO1 should be left unconnected if not used.



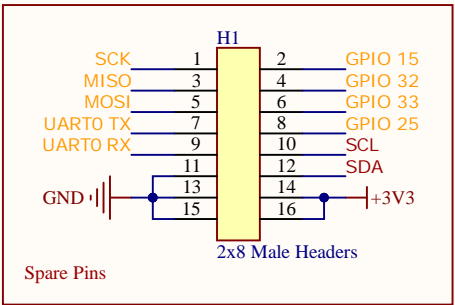
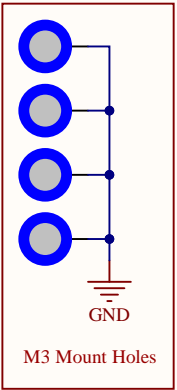
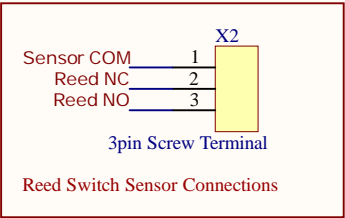
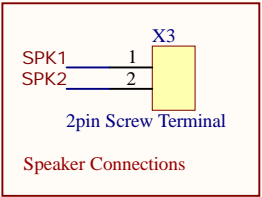
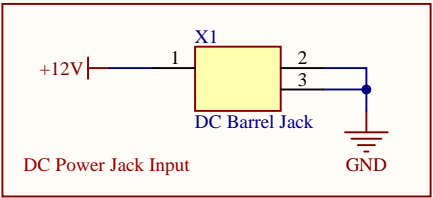


If $V_{OUT} = 3.3$ and $R1 = 10k$, then $R2 = 6035.0$

$$V_{OUT} = 1.242 \times \left(\frac{R1}{R2} + 1 \right)$$



Title		
Size	Number	Revision
A		
Date:	9/22/2025	Sheet of
File:	C:\Users\...\Power.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	9/22/2025	Sheet of
File:	C:\Users\...\Connections.SchDoc	Drawn By: