

Sign-off sheet for P.C. Board Submission

Each team member must perform at least one task in each category

You must initial each line . Sign, date, scan ad return with submission

Team Name: Dugan's Dogs

Schematic submission tasks

- Schematic checked for accuracy of connections JD MM
- Schematic checked for part values MM
- Correct Bypass Capacitors for each active component JD MM
- Correct Footprints in Schematic Database NS
- Verified logical pin names to physical pin numbers NK VL

PCB Submission Tasks

- PCB Footprints checked for accuracy NS
- PCB parts orientation and placement approved MM
- Bypass capacitors located correctly MM
- Correct Power and Ground routing – trace widths appropriate for each. VL
- PCB Design rules verified JD
- Component reference designators checked for placement VL
- Team Name/Student Name on TOP SILKSCREEN JD
- PCB Verification run **Connectivity Check** and **DRC in Ultiboard** and corrected all errors. NK
- CAM submission to FREEDFM NK
- Errors from FREEDFM corrected NK
- Verified Board outline is 10 mil Width. NK MM

- Examined board plots from Freedfm and

verified no global fiducials outside of board outline NK

- Verified that submitted design was the same file that was sent to Freedfm NK

Signatures: Overall Check-off approved : (Signed AND Dated) (Your signature is your pledge)

- Team Member 1 Jesse Dugan 09/29/2019
- Team Member 2 Nayiri Krzysztofowicz 09/29/2019
- Team Member 3 Malcolm Miller 9/29/19
- Team Member 4 Nojan Sheybcani 9/29/19
- Team Member 5 Vivian Lin 9/29/19