a) I modified the algorithm by trying to optimize it with openCL for the fpga. I tried putting in code in the kernel and having it compile. I wrote a .tcl file telling the compiler what to compile and what would be sent to the FPGA. I ran into a couple of problems.
b) The program did not receive any speed up since I could not get the FPGA to work.
c) With my results, since I could not get the FPGA code to work, I did not get any increased speed ups.