LocTypeLang: a layout description language

Dongxu Zhou

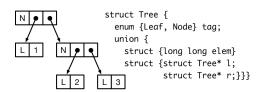
Peking University School of Mathematical Sciences

1 Overview

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1.1 Motivation



(a) Standard representation of a tree structure in C: by default, word-sized tags and pointers.



(b) Serialized version of the same tree. Not to scale: tags take one byte and integers eight.

Figure 1.1: Standard and serialized representations of trees

Figure 1: Two kinds of Tree Representations

As shown in Figure 1, sometimes we need to store a tree as a serialized representation and operate on this representation to achieve better performance. However, the C implementation for this task can be error-prone and cumbersome, especially when using pointers to operate on the serialized tree.

To address this issue, Can we implement the same task using a type-safe language like Haskell, as shown below? By using Haskell's strong type system, we can ensure that the serialized tree is always well-formed and avoid common errors that can occur in C. Additionally, with proper optimization techniques, we want to achieve performance similar to that of C while still maintaining type safety.

```
int sumPacked (byte * &ptr){
    int ret = 0 ;
    if(*ptr == LEAF){
        ptr++;
        ret = *(int*)ptr;
        ptr += sizeof(int);
} else{
        ptr++;
```

```
ret = sumPacked(ptr);
    ret += sumPacked(ptr);
}
return ret;
}
```

1.2 Previous Work

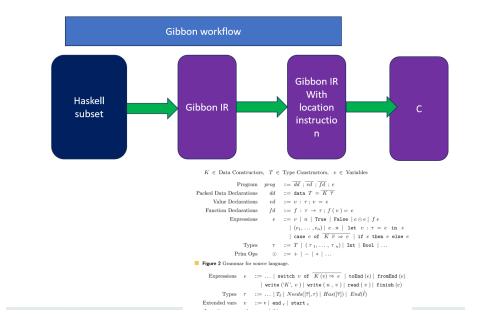


Figure 2: Gibbon workflow

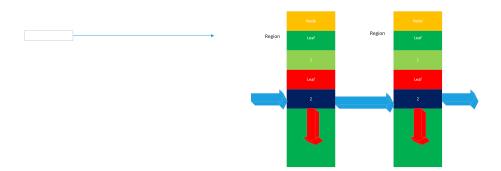


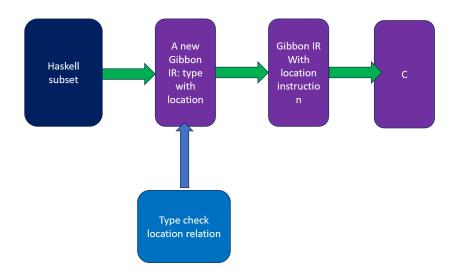
Figure 3: Gibbon memory model

Gibbon workflow : Translate subset of Haskell into Gibbon IR, then into C code.

Gibbon memory model:

- 1. Algebric data types are stored as serialized representation within a region
- 2. There are a lot of regions and each region consists of an array of cells, which contain store values (data constructor tags)
- 3. Each cell in a region is only initialized and written once
- 4. The locations of all the fields of the constructor must also match the expected constraints. That is, the location of the first field should be immediately after the constructor tag, and there should be appropriate after constraints for other fields in the location constraint environment

2 LocTypeLang: Add location information to Type



2.1 Syntax

```
K \in \text{Data Constructors}, \tau \in \text{Type Constructors},
x, y, f \in \text{Variables}, l, l^r \in \text{Location Variables},
r \in \text{ Regions}, i, j \in \text{ Region Indices},
\langle r, i \rangle^l \in \text{Concrete Locations}
                                    top ::=\overrightarrow{dd};\overrightarrow{fd};e
 Top-Level Programs
 Datatype Declarations dd ::= data \tau = \overrightarrow{K}\overrightarrow{\tau}
 Function Declarations fd ::= f : ts; f\vec{x} = e
 Located Types \hat{\tau} ::= \tau atl^r
                             ts ::= \forall_{l^r} \cdot \overrightarrow{\hat{\tau}} \to \hat{\tau}
 Type Scheme
 Values v ::= x \mid \langle r, i \rangle^{l^r}
 Expressions e ::= v
                                                                                    x
|f|\overrightarrow{l^r}\overrightarrow{l^r}\overrightarrow{l}\overrightarrow{v}
 |Kl^r\vec{v}|
 | \det x : \hat{\tau} = e \text{ in } e
 |\text{letloc } l^r = le \text{ in } e
 |letregion r in e
 |case v of \overrightarrow{pat}
 Pattern pat := K(\overrightarrow{x}: \hat{\tau}) \to e
 Location Expressions le ::= (start r)
 |(l^r+1)|
 | (after \hat{\tau}) |
```

 $\langle r,i\rangle^l$ consisting of a region, an index, and symbolic location corresponding to its binding site. The first two components are sufficient to fully describe an address in the store.

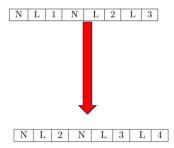
2.2 Design Idea

LocTypeLang is designed to augment Type in Gibbon to track locations within regions (e.g., byte offsets). The basic idea is to first establish what data share which logical memory regions, and in what order those data reside.

As below code, data constructor applications, such as Leaf 1, take an extra location argument, specifying where the data constructor should place the resulting value in memory:(Leaf l_r 1). This location became part of the type of the value Tree@ l_r . Every location resides in a region, and when we want to name that region, we write add r to location l, namely, l_r .

```
data Tree = Node Tree Tree | Leaf Int
buildtree : forall [l_r]. Int
                                  Tree@la_r
buildtree [l_r] n =
    if n == 0 then (leaf l_r 1) -- write to output
   let loc la_r = l_r + 1 in
   let left : Tree@la_r = buildtree [la_r] (n-1) in
   let loc lb_r = after (Tree@la_r) in
    let right : Tree@lb_r = buildtree [lb_r] (n-1) in
    -- write Node tag at l_r
    (Node l_r left right )
add1 : forall [la_r,lb_r]. Tree@la_r
                                          Tree@lb_r
add1 [la_r, lb_r ] t =
    case t of
    Leaf (n : Int) \Rightarrow Leaf (n+1 : Int)
    Node (a : Tree@la_r) (b : Tree@lb_r) =>
    Node (add1 [la_r, lb_r] a) (add1 [la_r, lb_r] b)
```

Build tree produces seriazation tree



Add1 produce another tree from seriazation tree aove

3 Contstrain Based Store typing

Contents

In Fig. 2.2, I extend the grammar with some extra details necessary for describing the type system. The typing rules for expressions are of the form as follows:

$$\Gamma; \Sigma; C; A; N \vdash A'; N'; e : \hat{\tau}$$

Typing Env.
$$\Gamma ::= \{x_1 \mapsto \hat{\tau}_1, \dots, x_n \mapsto \hat{\tau}_n\}$$

Store Typing $\Sigma ::= \{l_1^{r_1} \mapsto \tau_1, \dots, l_n^{r_n} \mapsto \tau_n\}$
Constraint Env. $C ::= \{l_1^{r_1} \mapsto le_1, \dots, l_n^{r_n} \mapsto le_n\}$
Allocation Pointers $A ::= \{r_1 \mapsto ap_1, \dots, r_n \mapsto ap_n\}$
Nursery $N ::= \{l_1^{r_1}, \dots, l_n^{r_n}\}$

The five letters to the left of the turnstile are different environments.

 Γ is a standard typing environment.

 Σ is a store-typing environment, which maps all materialized symbolic locations to their types. That is, every location in Σ has been written and contains a value of type Σ (l^r).

C is a constraint environment, which keeps track of how symbolic locations relate to each other. e.g.

$$C' = C \cup \{l^r \mapsto (\text{ after } \tau'@l_1{}^r)\}$$

A maps each region in scope to a location, and is used to symbolically track the allocation and incremental construction of data structures; A can be thought of as representing the focus within a region of the computation.

N is a nursery of all location VARs that have been allocated, but not yet used. Locations are removed from N upon being written to, as the purpose is to prevent multiple writes to a location.

Store
$$S ::= \{r_1 \mapsto h_1, \dots, r_n \mapsto h_n\}$$

Heap $h ::= \{i_1 \mapsto K_1, \dots, i_n \mapsto K_n\}$
Location Map $M ::= \{l_1^{r_1} \mapsto \langle r_1, i_1 \rangle, \dots, l_n^{r_n} \mapsto \langle r_n, i_n \rangle\}$

S. The store is a map from regions to heaps H, where each heap consists of an array of cells, which contain store values (data constructor tags. e.g. Tree).

M, a map from location var to concrete locations, (r, i).

3.0.1Typing Rule

T-LETREGION

$$\begin{split} \Gamma; \Sigma; C; A'; N \vdash A''; N'; e : \hat{\tau} \\ \overline{\Gamma; \Sigma; C; A; N \vdash A''; N'; \text{letregion } r \text{ in } e : \hat{\tau} & \text{where } A' = A \cup \{r \mapsto \varnothing\} \\ \frac{\text{T-VAR}}{\Gamma; \Sigma; C; A; N \vdash A''; N'; \text{letregion } r \text{ in } e : \hat{\tau} \\ \hline \frac{\Gamma(x) = \tau@l^r \quad \Sigma(l^r) = \tau}{\Gamma; \Sigma; C; A; N \vdash A; N; x : \tau@l^r} \\ \frac{\text{T-CONCRETE-LOC}}{\Gamma; \Sigma; C; A; N \vdash A; N; \langle r, i \rangle^l : \tau@l^r} \end{split}$$

The T-VAR rule ensures that the variable is in scope, and the symbolic location of the variable has been written to. T-CONCrete-LOC is very similar, and also just ensures that the symbolic location has been written to. T-LET is straightforward, but note that along with Γ , it also extends Σ to signify that the location l has materialized.

In T-LetRegion, extending A with an empty allocation pointer brings the region r in scope, and also indicates that a symbolic location has not yet been allocated in this region.

T-LETLOC-START
$$A(r) = \emptyset \quad l^r \notin N'' \quad l^{r'} \neq l^r \qquad \Gamma; \Sigma; C'; A'; N' \vdash A''; N''; e : \tau'@l^{r'}$$

$$\Gamma; \Sigma; C; A; N \vdash A''; N''; \text{ letloc } l^r = (\text{ start } r) \text{ in } e : \tau'@l^{r'}$$
where $C' = C \cup \{l^r \mapsto (\text{start } r)\} \qquad A' = A \cup \{r \mapsto l^r\} \qquad N' = N \cup \{l^r\}$

T-LetLoc-TAG

$$A(r) = l'^r \quad l'^r \in N \quad l^r \notin N'' \quad l^r \neq l'^{r''}$$

$$\Gamma; \Sigma; C'; A'; N' \vdash A''; N''; e : \tau''@l'^{r''}$$

$$\Gamma; \Sigma; C; A; N \vdash A''; N''; \text{ letloc } l^r = (l'^r + 1) \text{ in } e : \tau''@l'^{r''}$$
 where
$$C' = C \cup \{l^r \mapsto (l'^r + 1)\} \qquad A' = A \cup \{r \mapsto l^r\} \qquad N' = N \cup \{l^r\}$$

$$\begin{aligned} & \text{T-LETLOC-AFTER} \\ & A(r) = l_1{}^r \quad \Sigma \left(l_1{}^r \right) = \tau' \quad l_1{}^r \notin N \quad l^r \notin N'' \quad l^r \neq l^{r'} \\ & \quad \Gamma; \Sigma; C'; A'; N' \vdash A''; N''; e : \tau'@l^{r'} \\ \hline & \Gamma; \Sigma; C; A; N \vdash A''; N''; \text{letloc } l^r = \left(\text{ after } \tau'@l_1{}^r \right) \text{ in } e : \tau'@l^{r'} \\ & \text{where } C' = C \cup \left\{ l^r \mapsto \left(\text{ after } \tau'@l_1{}^r \right) \right\} \qquad A' = A \cup \left\{ r \mapsto l^r \right\} \\ & \quad N' = N \cup \left\{ l^r \right\} \end{aligned}$$

T-LEtLoc-StART, T-LETLOC-TAG and TLetLoc-After are corresponding to three ways of allocating a new location in a region. A new location is either: at the start of a region, one cell after an existing location, or after the data structure rooted at an existing location. Additionally, in A, each region is mapped to either the right-most allocated symbolic location in that region (if it is unwritten), or to the symbolic location of the most recently materialized data structure. This mapping in A is used by the typing rules to ensure that: (1)T-LetLoc-StarT may only introduce a location at the start of a region once; (2) T-LETLoc-TAG may only introduce a location if an unwritten location has just been allocated in that region (to correspond to the tag of some soon-to-be-built data structure); and (3) T-LETLOC-AfTER may only introduce a location if a data structure has just been materialized at the end of the region, and the programmer wants to allocate after it.

$$\begin{split} & \text{T-DataConstructor} \\ & \text{TypeOfCon}(K) = \tau \quad \text{TypeOfField } (K,i) = \overrightarrow{\hat{\tau_i}} \\ & l^r \in N \quad A(r) = \overrightarrow{l_n^r} \text{ if } n \neq 0 \quad \text{else } l^r \\ & C\left(\overrightarrow{l_1^r}\right) = l^r + 1 \quad C\left(\overrightarrow{l_{j+1}^r}\right) = \left(\operatorname{after}\left(\overrightarrow{\tau_j} @ \overrightarrow{l_j^r}\right)\right) \\ & \qquad \qquad \Gamma; \Sigma; C; A; N \vdash A; N; \overrightarrow{v_i} : \overrightarrow{\tau_i'} @ l^r \\ & \qquad \qquad \Gamma; \Sigma; C; A; N \vdash A'; N'; K l^r \overrightarrow{v} : \tau @ l^r \\ & \qquad \qquad \text{where } A' = A \cup \{r \mapsto l^r\}; N' = N - \{l^r\} \\ & \qquad n = |\overrightarrow{v}|; i \in I = \{1, \dots, n\}; j \in I - \{n\} \end{split}$$

$$\frac{\Gamma\text{-LET}}{\Gamma; \Sigma; C; A; N \vdash A'; N'; e_1 : \tau_1@l_1{}^{r_1} \qquad \Gamma'; \Sigma'; C; A'; N' \vdash A''; N''; e_2 : \tau_2@l_2{}^{r_2}}{\Gamma; \Sigma; C; A; N \vdash A''; N''; \text{let } x : \tau_1@l_1{}^{r_1} = e_1 \text{ in } e_2 : \tau_2@l_2{}^{r_2}}\\ \text{where } \Gamma' = \Gamma \cup \{x \mapsto \tau_1@l_1{}^{r_1}\}; \Sigma' = \Sigma \cup \left\{l_1{}^{r_1} \mapsto \tau_1\right\}$$

T-DATACONSTRUCTOR starts by ensuring that the tag being written and all the fields have the correct type. Along with that, the locations of all the fields of the constructor must also match the expected constraints. That is, the location of the first field should be immediately after the constructor tag, and there should be appropriate after constraints for other fields in the location constraint environment. After the tag has been written, the location l is removed from the nursery to prevent multiple writes to a location.

As demonstrated by T-DataConstructor, the type system enforces a particular ordering of writes to ensure the resulting tree is serialized in a certain order.

```
code
letloc l_r =
                         \{r \mapsto 1_r\}
                                           Ø
                                                                \{1_r\}
start(r)
                         \{r \mapsto la_r\}
                                          {la_r \mapsto l_r + 1} {l_r, la_r}
letloc la_r = l_r
                                          {la_r \mapsto l_r + 1} {l_r}
let x:T@la_r =
                         {r \mapsto la_r}
Leaf la_r 1
letloc lb_r =
                         {r \mapsto lb\_r} \{la\_r \mapsto l\_r + 1,
                                                                     {l_r, lb_r}
                                         lb_r \mapsto \overline{after(T@la_r)}
after(T@la_r)
                         {r \mapsto lb_r} {la_r \mapsto l_r + 1}
let x:T@la_r =
                                                                     \{1_r\}
Leaf lb_r 2
                                         lb_r → after(T@la_r)
node l_r, x, y
                                                                      Ø
                      {r \mapsto lb_r} la_r \mapsto l_r + 1,
                                         lb_r → after(T@la_r)
```

A simple demonstration of the type system is shown by the figure above, which tracks how A,C, and N change after each line in a simple expression that builds a binary tree with leaf children. Introducing l at the top establishes that it is at the beginning of r,A maps r to l, and N contains l. The location for the left sub-tree, l_a , is defined to be +1 after it, which updates r to point to l_a in A and adds a constraint to C for l_a . Actually constructing the Leaf in the next line removes l_a to N, because it has been written to.

Once l_a has been written, the next line can introduce a new location l_b after it, which updates the mapping in A and adds a new constraint to C . Once l_b has been written and removed from N in the next line, the final Node can be constructed, which expects the constraints to establish that l is before l_a , which is before l_b .

T-PATTERN

$$\frac{\Gamma\text{-CASE}}{\Gamma; \Sigma; C; A; N \vdash A; N; v : \tau'@l^{r'} \qquad \tau'; \Gamma; \Sigma; C; A; N \vdash patA'; N'; \overrightarrow{pat_i} : \hat{\tau}}{\Gamma; \Sigma; C; A; N \vdash A'; N'; \text{ case } v \text{ of } \overrightarrow{pat} : \hat{\tau}}$$

3.0.2 Operation Semantics

D-LETLOC-TAG

$$S; M;$$
 letloc $l^r = l'^r + 1$ in $e \Rightarrow S; M'; e$
where $M' = M \cup \{l^r \mapsto \langle r, i+1 \rangle\}; \langle r, i \rangle = M(l'^r)$

DataConstructor

$$S; M; Kl^r \vec{v} \Rightarrow S'; M; \langle r, i \rangle^{l^r}$$
 where $S' = S \cup \{r \mapsto (i \mapsto K)\}; \langle r, i \rangle = M(l^r)$

D-LETLOC-START

$$S; M;$$
 letloc $lr = ($ start $r)$ in $e \Rightarrow S; M'; e$ where $M' = M \cup lr \mapsto \langle r, 0 \rangle$

D-LETLOC-AFTER

$$S; M;$$
 letloc $l^r = ($ after $\tau @ l_1^r)$ in $e \Rightarrow S; M'; e$ where $M' = M \cup l^r \mapsto \langle r, j \rangle ; \langle r, i \rangle = M (l_1^r)$ $\qquad \tau; \langle r, i \rangle; S \vdash_{eW} \langle r, j \rangle$

D-LET-EXPR
$$S; M; e_1 \Rightarrow S'; M'; e_1' \quad e_1 \neq v$$

$$S; M; \text{ let } x : \hat{\tau} = e_1 \text{ in } e_2 \Rightarrow S'; M'; \text{ let } x : \hat{\tau} = e_1' \text{ in } e_2$$

D-CASE

$$S; M; \operatorname{case} \langle r, i \rangle lr \text{ of } \left[\dots, K \left(\overrightarrow{x} : \tau @ lr \right) \to e, \dots \right] \Rightarrow S; M'; e \left[\langle r, \overrightarrow{w} \rangle \overrightarrow{lr} \mid \overrightarrow{x} \right]$$

$$\text{where } M' = M \cup \overrightarrow{l_1'} \mapsto \langle r, i+1 \rangle, \dots, \overrightarrow{l_{j+1}'} \mapsto \langle r, \overrightarrow{w_{j+1}'} \rangle$$

$$\overrightarrow{\tau_1}; \langle r, i+1 \rangle; S \vdash_{end} \langle r, \overrightarrow{w_1} \rangle \qquad \overrightarrow{\tau_{j+1}}; \langle r, \overrightarrow{w_j} \rangle; S \vdash_{ew} \langle r, \overrightarrow{w_{j+1}} \rangle$$

$$K = S(r)(i); j \in 1, \dots, n-1; n = |x : \overrightarrow{\tau}|$$

```
code
letloc l_r =
                            \{r \mapsto \emptyset \}
                                                                    \{1\_r \mapsto (r,0)\}
start(r)
letloc la_r = l_r
                            \{r \mapsto \emptyset \}
                                                                 \{l_r \mapsto (r,0), la_r \mapsto (r,1)\}
+ 1
let x:T@la_r =
                            {r \mapsto \{1 \mapsto leaf\}}
                                                                 \{l_r \mapsto (r,0), la_r \mapsto (r,1)\}
Leaf la_r 1
letloc lb r =
                            \{r \mapsto \{1 \mapsto leaf\}\}
                                                                 \{l_r \mapsto (r,0), l_r \mapsto (r,1)\}
after(T@la_r)
let x:T@la_r =
                            {r \mapsto \{1 \mapsto leaf}
                                                                 \{l_r \mapsto (r,0), la_r \mapsto (r,1),
                                                                   lb_r \mapsto (r,2) }
Leaf lb_r 2
node l_r, x, y
                          {r \mapsto \{1 \mapsto leaf}
                                                                 \{l_r \mapsto (r,0), la_r \mapsto (r,1),
                           3 \mapsto leaf, 0 \mapsto node}
                                                                        lb_r \mapsto (r,2) }
```

 $S = r \mapsto \emptyset$ and the location l_r maps to $\langle r, 0 \rangle$ in the location map.

After stepping past the first line, the D-LetLoc-Tag step has allocated a cell for the tag of the interior node and bound the location l_{a_r} to $\langle r,1\rangle$. After the next line, the D-DataConstructor transition writes a leaf node to the store at the address represented by l_{1_r} .

$$S = r \mapsto 1 \mapsto \text{Leaf.}$$

The second letloc obtains the starting address for the second leaf node by using the end witness of the previous leaf node. The write of the second leaf node appears in the store after the next line, leaving the following store:

$$S = r \mapsto 1 \mapsto \text{Leaf}, 3 \mapsto \text{Leaf}.$$

Finally, after the D-DataConstructor step taken for the last line, the store contains the finalized allocation:

 $S = r \mapsto 0 \mapsto \text{Node}, 1 \mapsto \text{Leaf}, 3 \mapsto \text{Leaf}.$

4 Type Satety

4.1 Store well formedness

$$\begin{split} \Sigma; C; A; N \vdash_{wf} M; S \\ \text{Definition} \\ 1. \ (l^r \mapsto \tau) \in \Sigma \Rightarrow \\ & \qquad \qquad ((l^r \mapsto \langle r, i_1 \rangle) \in M \land \\ & \qquad \qquad \tau; \langle r, i_1 \rangle; S \vdash_{ew} \langle r, i_2 \rangle) \end{split}$$

- 2. $C \vdash_{wf_{cfc}} M; S$
- 3. $A; N \vdash_{wf_{ca}} M; S$
- 4. $dom(\Sigma) \cap N = \emptyset$

 τ ; $\langle r, i_1 \rangle$; $S \vdash_{ew} \langle r, i_2 \rangle$) mean the store address $\langle r, ie \rangle$ is the position one after the last cell of the tree of type starting at $\langle r, is \rangle$ in store S.

4.1.1 Constructor Application

definition:
$$C \vdash_{wf_{cfc}} M; S$$

 $1.(l^r \mapsto (\text{start } r)) \in C \Rightarrow$
 $(l^r \mapsto \langle r, 0 \rangle) \in M$
 $2. (l^r \mapsto (l'^r + 1)) \in C \Rightarrow$
 $(l'^r \mapsto \langle r, i_l \rangle) \in M \land$
 $(l^r \mapsto \langle r, i_l + 1 \rangle) \in M$
 $3. (l^r \mapsto (\text{after } \tau@l'^{rr})) \in C \Rightarrow$
 $((l'^r \mapsto \langle r, i_1 \rangle) \in M \land$
 $\tau; \langle r, i_1 \rangle; S \vdash_{ew} \langle r, i_2 \rangle \land$

Rule 1 specifies that, if a location corresponding to the first address in a region

 $(l^r \mapsto \langle r, i_2 \rangle) \in M)$

is in the constraint environment, then there is a corresponding entry for that location in the location map. Rule 2 specifies that, if a location corresponding to the address one past a constructor tag is in the constraint environment, then there are corresponding locations for the address of the tag and the address after in the location map.

4.1.2 Safe Allocation

definition:
$$A; N \vdash_{wf_{ca}} M; S$$
1. $((r \mapsto l^r) \in A \land l^r \in N) \Rightarrow$

$$((l^r \mapsto \langle r, i \rangle) \in M \land i \rangle \operatorname{MaxIdx}(r, S))$$
2. $((r \mapsto l^r) \in A \land (l^r \mapsto \langle r, i_s \rangle) \in M \land l^r \notin N \land \tau; \langle r, i_s \rangle; S \vdash_{ew} \langle r, i_e \rangle) \Rightarrow$

$$i_e > \operatorname{Max} Idx(r, S)$$
3. $l^r \in N \Rightarrow$

$$((l^r \mapsto \langle r, i \rangle) \in M \land (r \mapsto (i \mapsto K)) \notin S)$$
4. $(r \mapsto \varnothing) \in A \Rightarrow$

$$r \notin \operatorname{dom}(S)$$

Rule 1 requires that, if a location is in both the allocation and nursery environments, i.e., that address represents an in-flight constructor application, then there is a corresponding location in the location map and the address of that location is the highest address in the store.

Rule 2 requires that, if there is an address in the allocation environment and that address is fully allocated, then the address of that location is the highest address in the store.

Rule 3 requires that, if there is an address in the nursery, then there is a corresponding location in the location map, but nothing at the corresponding address in the store.

4.1.3 Fields Continuity

definition:

4. $i_e = \overrightarrow{w_n}$

$$\tau$$
; $\langle r, i_s \rangle$; $S \vdash_{ew} \langle r, i_e \rangle$)

1. $S(r)(i_s) = K'$ such that

$$\operatorname{data} \tau = \overrightarrow{K_1 \vec{\tau}_1} | \dots | K' \vec{\tau}' | \dots | \overrightarrow{K_m \vec{\tau}_m}$$

2.
$$\overrightarrow{w_0} = i_s + 1$$

3. $\overrightarrow{\tau_1}; \langle r, \overrightarrow{w_0} \rangle; S \vdash_{ew} \langle r, \overrightarrow{w_1} \rangle \land \overrightarrow{\tau_{j+1}}; \langle r, \overrightarrow{w_j} \rangle; S \vdash_{ew} \langle r, \overrightarrow{w_{j+1}} \rangle$
where $j \in \{1, \dots, n-1\}; n = \left| \overrightarrow{\tau_j} \right|$

Rule 1 requires that the first cell store a constructor tag of the appropriate type. Rule 3 specifies the address of the cell one past the tag. Rule 4 recursively specifies the positions of the constructor fields. Finally, Rule 2 specifies that the start of a field is the address one past the end of another field.

4.2 Type Safety Theorem

4.2.1 Progress lemma

if $\varnothing; \Sigma; C; A; N \vdash A'; N'; e: \hat{\tau}$ and $\Sigma; C; A; N \vdash_{wf} M; S$ then e value else $S; M; e \Rightarrow S'; M'; e'$

Proof: T-DataConstructor case

Because K l v is not a value, the proof obligation is to show that there is a rule in the dynamic semantics whose left-hand side matches the machine configuration S;M; e.

The only rule that can match is D-DataConstructor, but to establish the match, there remains one obligation, which is obtained by inversion on D-DataConstructor. The particular obligation is to establish that $(r,i) = M(l_r)$, for some i. To obtain this result, we need to use the well formness of the store, given by the premise of this lemma, and in particular rule Safe Allocation.3.

But a precondition for using Safe Allocation 3 that the location is unwritten, i.e., l_rN . This precondition is satisfied by inversion on T-DataConstructor.

4.2.2 Preservation lemma

If $\varnothing; \Sigma; C; A; N \vdash A'; N'; e : \hat{\tau}$ and $\Sigma; C; A; N \vdash_{wf} M; S$ and $S; M; e \Rightarrow S'; M'; e'$ then for some $\Sigma' \supseteq \Sigma, C' \supseteq C$,

$$\varnothing; \Sigma'; C'; A'; N' \vdash A''; N''; e' : \hat{\tau}$$

and $\Sigma'; C'; A'; N' \vdash_{wf} M'; S'$ Proof:

D-LETLOC-TAG case:

$$S; M; \text{ letloc } l^r = l'^r + 1 \text{ in } e \Rightarrow S; M'; e$$
 where $M' = M \cup \{l^r \mapsto \langle r, i+1 \rangle\}; \langle r, i \rangle = M (l'^r)$

The first of two proof obligations is to show that the result e of the given step of evaluation is well typed, that is,

$$\varnothing; \Sigma; C'; A'; N' \vdash A''; N''; e : \hat{\tau}$$

where $\hat{\tau} = \tau @ l^r, A' = A \cup \{r \mapsto l^r\}$, and $N' = N \cup \{l^r\}$. This proof obligation follows straightforwardly by inversion on T-LetLoc-Tag. - The second obligation for this proof case is to show that

$$\Sigma; C'; A'; N' \vdash_{wf} M'; S$$

The individual requirements, labeled WF 2.2.3.1;1 - WF 2.2.3.1;3, are handled by the following case analysis.

- Case (WF 2.2; 1):

for each $(l^r \mapsto \tau) \in \Sigma$, there exists some i_1, i_2 such that

$$(l'^r \mapsto \langle r, i_1 \rangle) \in M' \land \tau; \langle r, i_1 \rangle; S \vdash_{ew} \langle r, i_2 \rangle$$

By the well formedness of the store given in the premise of this lemma, the above already holds for the location environment M. The obligation discharges by inspecting the only new location in M', namely l^r , which is fresh and therefore cannot be in the domain of Σ .

$$C' \vdash_{wf_{cf_a}} M'; S$$

Of the requirements for this judgement, the only one that is not satisfied immediately by the well formedness of the store given in the premise of the lemma is requirement WF 2.2; 2 The specific requirement is to establish that

$$(l'^r \mapsto \langle r, i \rangle) \in M' \land$$

 $(l^r \mapsto \langle r, i+1 \rangle) \in M',$

which follows immediately by inversion on D-LetLoc-Tag. - Case (WF 2.2; 3):

$$A'; N' \vdash_{wf_{ca}} M'; S$$

* Case (WF 2.2.2; 1):

$$(l^r \mapsto \langle r, i+1 \rangle) \in M' \land i+1 > \text{MaxIdx}(r, S)$$

The first conjunct follows immediately from inversion on D-LetLoc-Tag. To establish the second, however, we first need to establish that the address corresponding to location $l^{\prime r}$ is the highest index in the store S. To do so, we need to

appeal to the well formedness of the store given by the premise of this lemma. In particular, we need to use the same requirement we are trying to prove, namely WF 2.2.2;1, but in this case, instantiating for $l^{\prime r}$ in the original location environment M. By inversion on T-LetLoc-Tag, we have that $A(r) = l^{\prime r}$ and $l^{\prime r} \in N$, and as a consequence of WF 2.2.2;1,

$$(l'^r \mapsto \langle r, i \rangle) \in M \land i > \text{Max Idx}(r, S).$$

Using the second conjunct above, this case discharges immediately.

* Case (WF 2.2.2; 2):

This obligation discharges immediately because, by inversion on T-LetLoc-Tag, $l^r \in N'$.

* Case (WF 2.2.2; 3):

The proof obligation is to establish that, for any constructor tag K,

$$((l^r \mapsto \langle r, i+1 \rangle) \in M' \land (r \mapsto (i+1 \mapsto K)) \notin S)$$

The first conjunct discharges by inversion on D-LetLoc-Tag, and the second as a consequence of having already established just above that $i+1 > \operatorname{MaxIdx}(r,S)$. * Case (WF 2.2.2; 4): The proof obligation is to establish that, for each $(r \mapsto \varnothing) \in A'$, it is the case that $r \notin \operatorname{dom}(S)$. This case discharges because, from the premise of the lemma, this property holds for the original environment A and store S, and, by inversion on T-LetLoc-Tag, continues to hold for A' and S'. - Case (WF 2.2; 4):

$$dom(\Sigma) \cap N' = \emptyset$$

Because it is a bound location, $l \notin \text{dom}(\Sigma)$, and by inversion on T-LetLoc-Tag, $l \in N'$, which discharges the obligation.

5 Implementation Guidance

5.1 Set Up

Set Up

• OS: Ubuntu 20.04

```
$ sudo apt-get install libgc-dev libgmp-dev gcc-7 uthash-dev software-properties-common
$ sudo add-apt-repository ppa:hvr/ghc && sudo apt update && sudo apt install ghc-9.0.1
cabal-install-3.4
$export PATH="/opt/ghc/bin:$PATH"
```

Install haskell stack

```
donzho@LAPTOP-7IE4DBQH ~ % stack --version
Version 2.9.3, Git revision 6cf638947a863f49857f9cfbf72a38a48b183e7e x86_64 hpack-0.35.1
```

• BUild steps

1. run "stack build" from root directory(.\ddpl-final) % stack build

```
gibbon-0.2: unregistering (local file changes: README.md)
gibbon> configure (lib + exe)
Configuring gibbon-0.2...
gibbon> build (lib + exe)
Preprocessing library for gibbon-0.2..
Building library for gibbon-0.2..
[ 1 of 13] Compiling Gibbon.DynFlags
[ 2 of 13] Compiling Gibbon.Common
[ 3 of 13] Compiling Gibbon.Language.Syntax
```

2. run stack test from root directory

5.2 implementation

My implementation is divided into three parts, all of which are located in the ddpl-final/src/Gibbon/MyLocTypeLang directory. These parts include the syntax definition of LocTypeLang, the implementation of static semantics (i.e. typechecker), and the implementation of dynamic semantics (i.e. interpreter).

5.3 examples

You can check the examples in tests/LocTypeCheck , which shows how these samples pass the Typechecker.