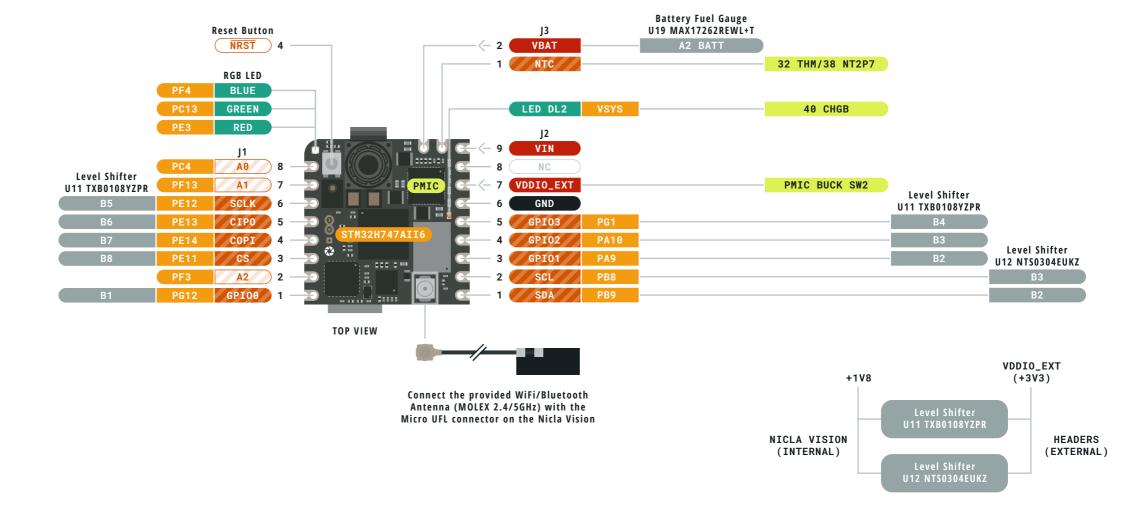
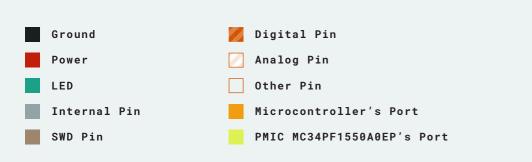


on the bottom of the board

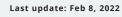




WARNING GPIOs are driven by bidirectional translators powered by VDDIO\_EXT. These translators are meant for low power and can only drive very limited current. Please check translator datasheet for details.

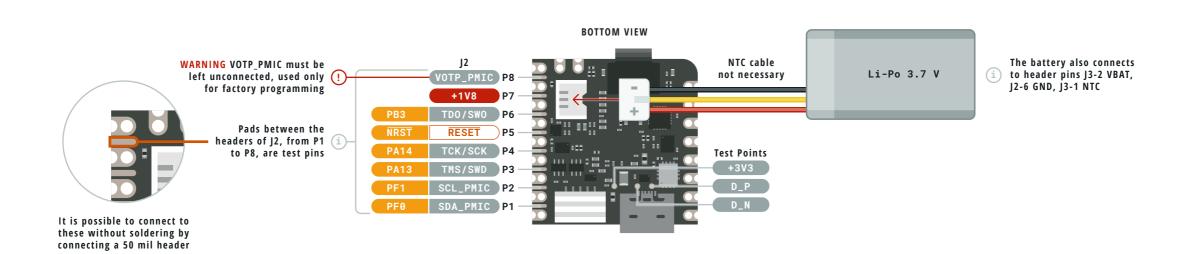
VDDIO\_EXT is software programmable between 1.8 and 3.3V

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Ground Digital Pin

Power Analog Pin

LED Other Pin

Internal Pin Microcontroller's Port

SWD Pin PMIC MC34PF1550A0EP's Port

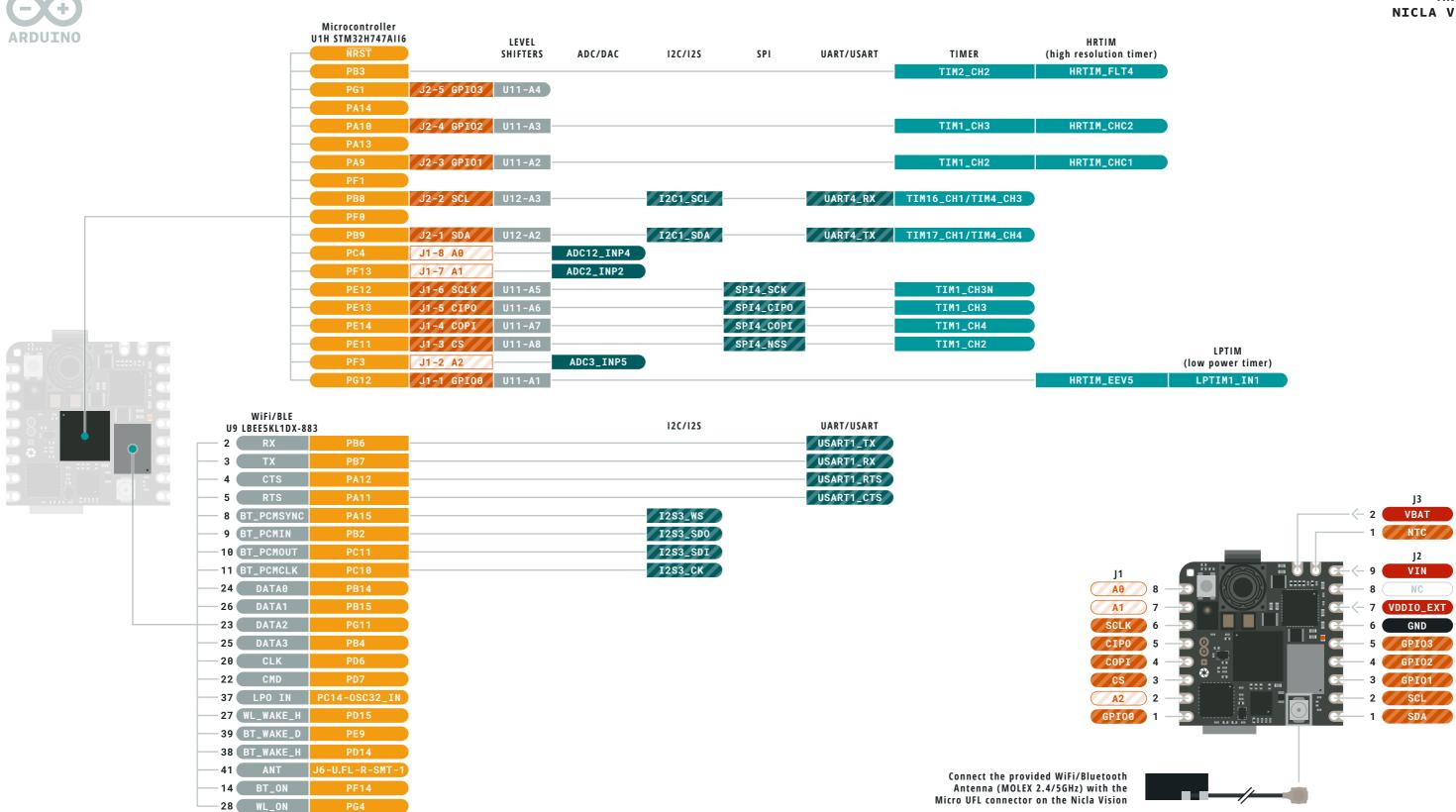
WARNING GPIOs are driven by bidirectional translators powered by VDDIO\_EXT. These translators are meant for low power and can only drive very limited current. Please check translator datasheet for details.

VDDIO\_EXT is software programmable between 1.8 and 3.3V

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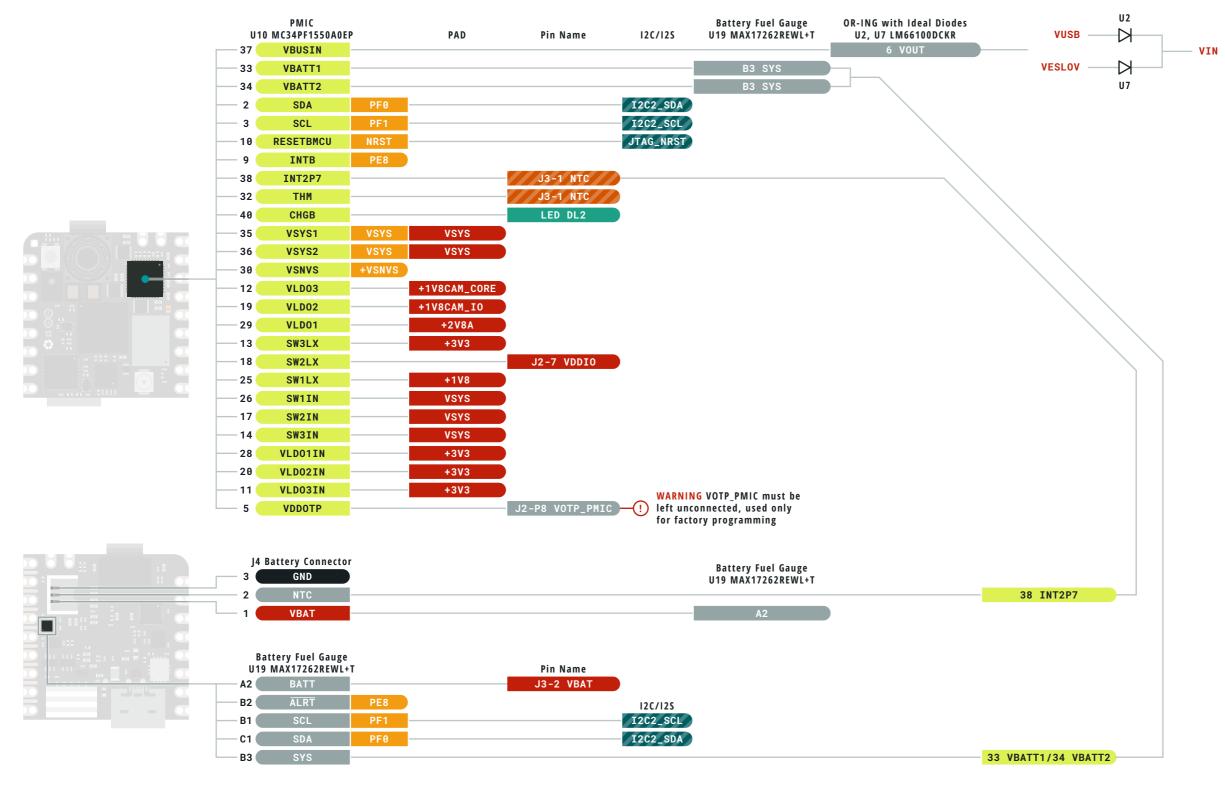
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VDDIO\_EXT is software programmable between 1.8 and 3.3V





# Ground Digital Pin Analog Communication by VDI are me only d Please Internal Pin Microcontroller's Port SWD Pin Debug/Clock MARN bidirect by VDI are me only d Please VDDIO between

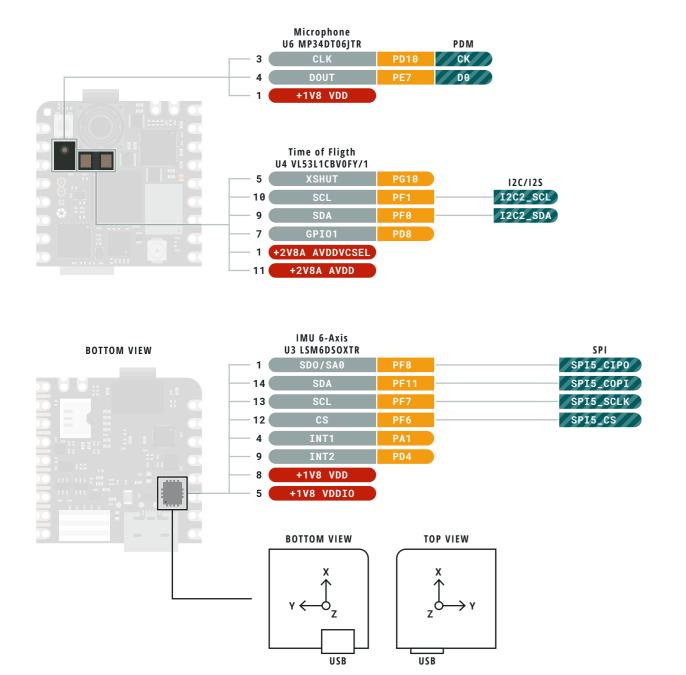
**WARNING** GPIOs are driven by bidirectional translators powered by VDDIO\_EXT. These translators are meant for low power and can only drive very limited current. Please check translator datasheet for details. VDDIO\_EXT is software programmable between 1.8 and 3.3V

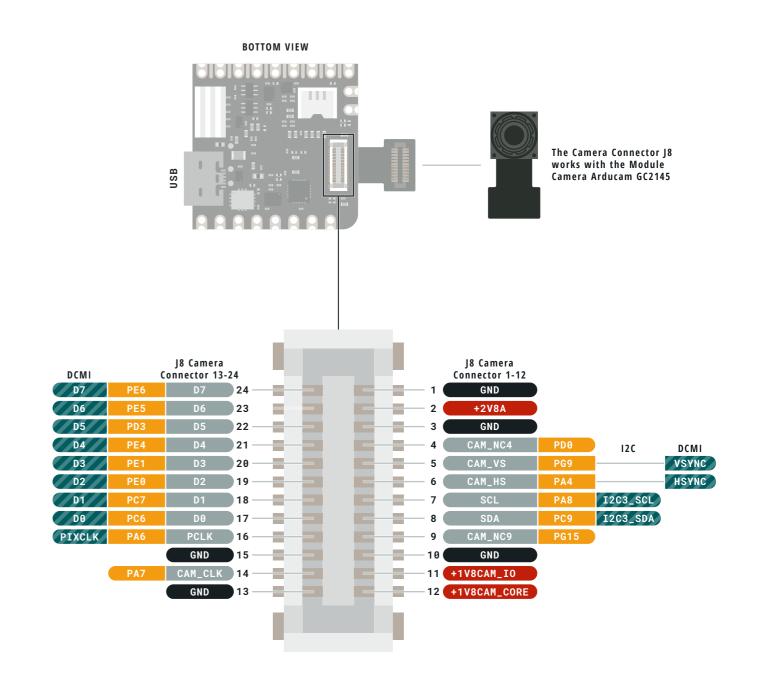
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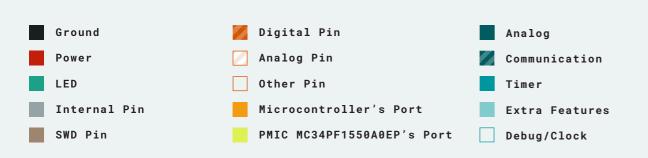
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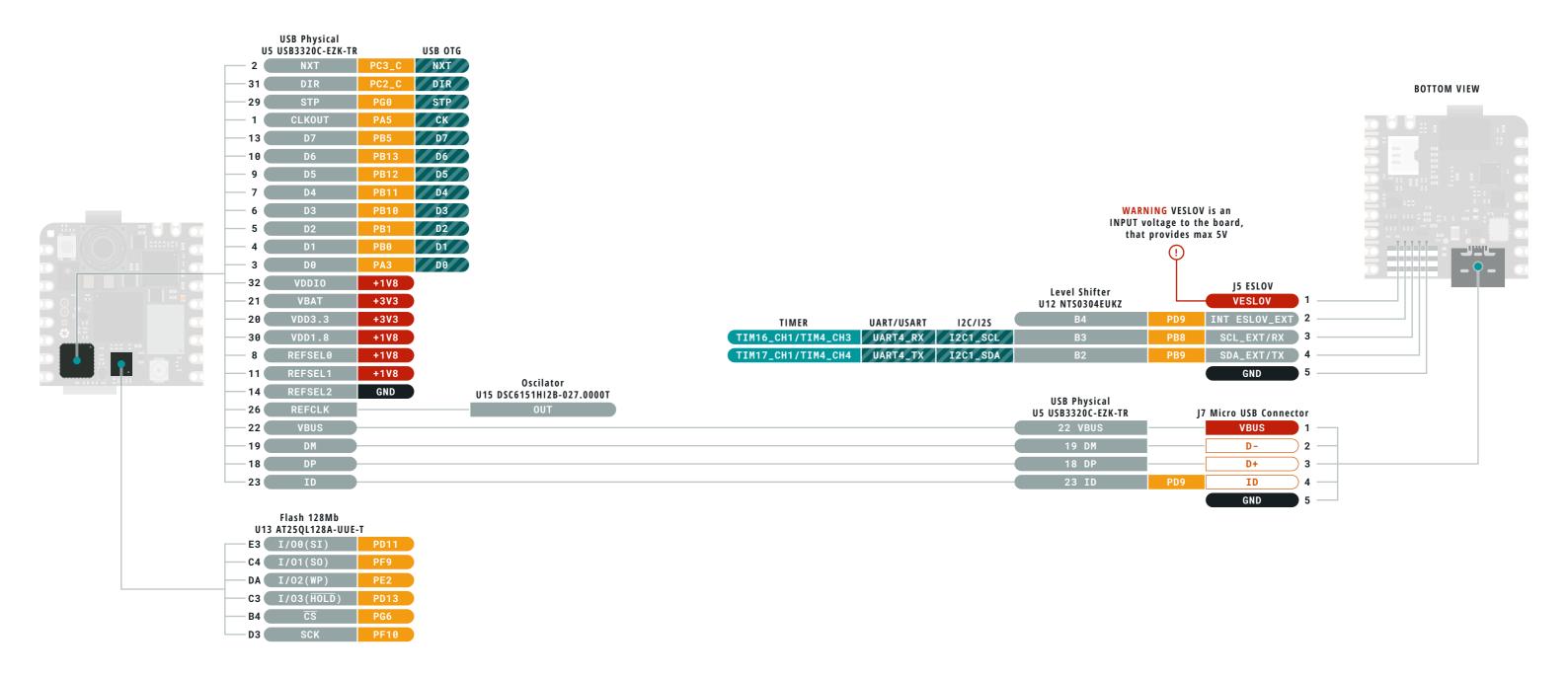
VDDIO\_EXT is software programmable between 1.8 and 3.3V

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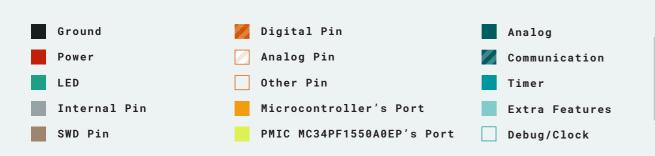


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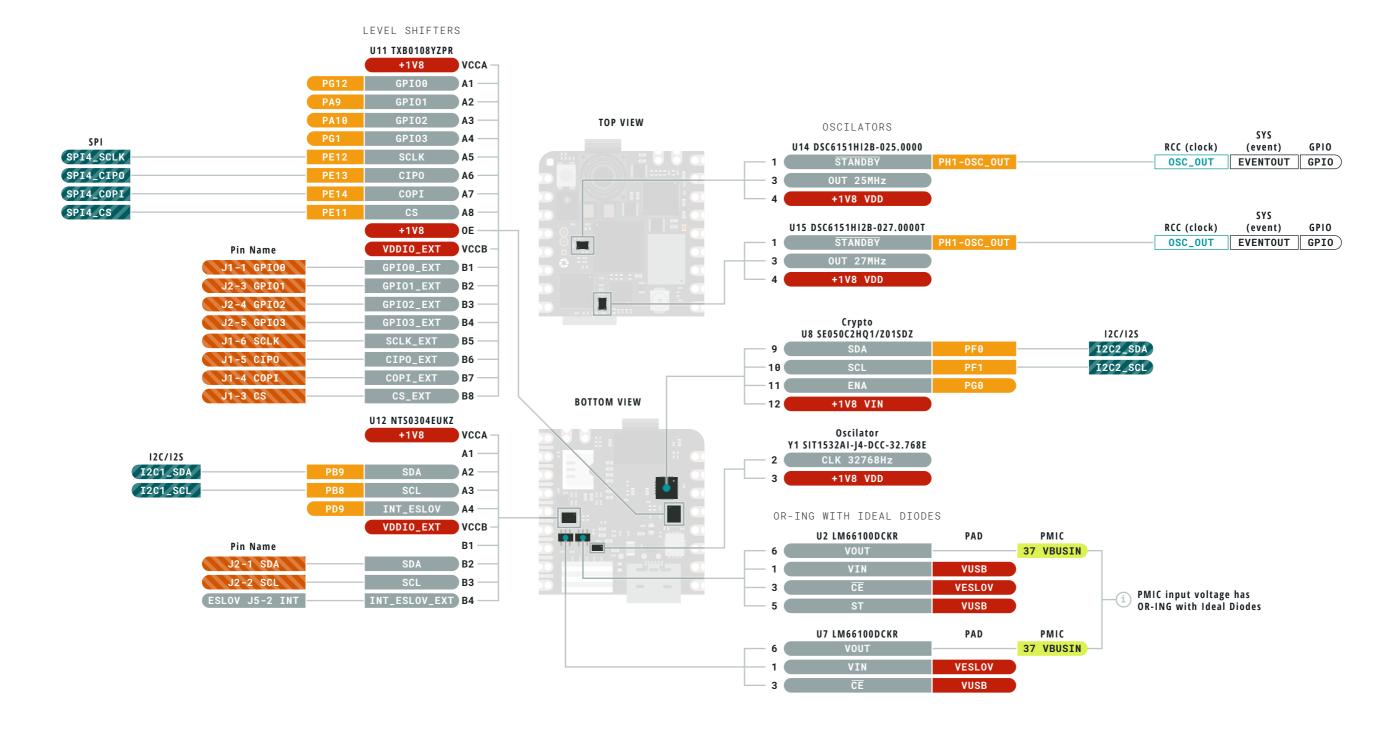


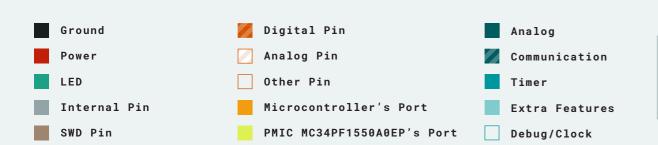
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