COMP130014.02 编译

第七讲:线性IR

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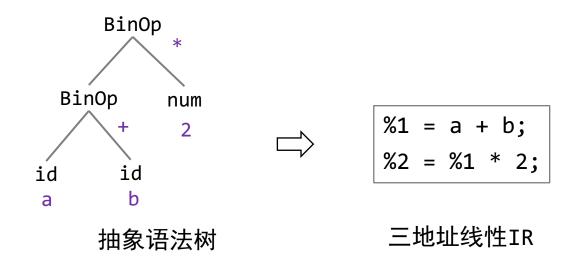
大纲

- ❖一、线性IR
- ❖二、翻译线性IR
- *三、解释执行

一、线性IR定义

线性IR的基本形式

- 指令名 + 地址(一般为两地址或三地址)
 - 地址: 变量名、常量、编译器生成的临时变量或存储单元
- 比较有名的IR: LLVM IR、 GCC GIMPLE、Java Bytecode



TeaPL的IR

- 选取LLVM IR的子集
- 可使用现成工具执行IR: 11i

```
@g = global i32 10
define i32 @fib(i32 %0) {
    %x = alloca i32
    store i32 %0, i32* %x
    %g0 = load i32, i32* @g
    ret i32 %g0
define i32 @main() {
    %r0 = call i32 @fib(i32 1)
    ret i32 %r0;
```

```
#: 11i foo.11
#: echo $?
```

标识符和基础类型

- 全局变量/函数名称: @name
- 局部变量/临时变量: %x、%0(不可重复, 数字编号需连续)

```
→ 全局变量g
\omegag = global i32 10
define i32 @fib(i32 %0) { ——→ 函数fib
   %x = alloca i32
                               → 局部变量%x
   store i32 %0, i32* %x
   %g0 = load i32, i32* @g——→ 临时变量%g0
   ret i32 %g0
define i32 @main() {
   %r0 = call i32 @fib(i32 1)
   ret i32 %r0;
```

数据存取

- 类型: void、i32、i32*、i8、i8*、i1
- 栈空间分配: alloca
- 数据存取: load/store

```
→ 全局变量g:类型为i32*,初始值10
@g = global i32 10
define i32 @fib(i32 %0) {
   %x = alloca i32
                             → 局部变量%x: 类型为i32*
   store i32 %0, i32* %x
   %g0 = load i32, i32* @g — → 临时变量%g0: 类型为i32
   ret i32 %g0
define i32 @main() {
   %r0 = call i32 @fib(i32 1)
   ret i32 %r0;
```

函数

• 定义: define

• 调用: call

• 返回: ret

```
@g = global i32 10
define i32 @fib(i32 %0) { ----
                               → 函数fib:类型为(i32)->i32
   %x = alloca i32
   store i32 %0, i32* %x
   %g0 = load i32, i32* @g
   ret i32 %g0
                                → 返回%g0
                               → 函数main:类型为(void)->i32
define i32 @main() {
   %r0 = call i32 @fib(i32 1)—→ 调用函数fib
   ret i32 %r0;
```

函数声明

- 声明: declare
- 声明和定义不能在一个Ⅱ文件中,使用Ilvm-link工具链接

```
declare i32 @fib( i32 )

define i32 @main() {
    %r0 = call i32 @fib(i32 1)
    ret i32 %r0;
}
```

在a.ll文件中声明函数fib

```
define i32 @fib(i32 %0) {
    %x = alloca i32
    store i32 %0, i32* %x
    %g0 = load i32, i32* @g
    ret i32 %g0
}
```

在b.II文件中定义函数fib

#: llvm-link a.ll b.ll -o c.ll

数组类型存取

• 获取地址: getelementptr

结构体类型数据存取

算数运算

- •加、减、乘法运算: add/sub/mul
- 除法: sdiv(有符号)/udiv(无符号)
- 为什么只有除法需要两个指令?

```
%2 = alloca i32
%3 = add i32 %0, 1
%4 = sub i32 %3, 2
%5 = mul i32 %3, 3
%6 = sdiv i32 %4, 4
store i32 %6, i32* %2
```

浮点数运算用fadd/fsub/fmul/fdiv

关系运算

- 一条指令: icmp
- 多种参数: sgt/sge/slt/sle/eq/ne

```
%4 = load i32, i32* %2
%5 = icmp sgt i32 %4, 0
%6 = icmp sge i32 %4, 0
%7 = icmp slt i32 %4, 0
%8 = icmp sle i32 %4, 0
%9 = icmp eq i32 %4, 0
%10 = icmp ne i32 %4, 0
```

s: signed g: greater l: less e: equal

n: not

类型转换

• 扩充: zext

• 缩短: trunc

```
%a = alloca i32
%b = alloca i8
%t0 = load i32, i32* %a
%t1 = icmp ne i32 %t0, 0
%t2 = zext i1 %t1 to i32
store i32 %t2, i32* %a
%t3 = trunc i32 %t2 to i8 → 类型转换: i32=>i8
```

控制流指令

- 直接跳转: br + 目标
- 条件跳转: br + 条件 + 目标1 + 目标2

```
%2 = alloca i32
  store i32 0, i32* %2
 %3 = load i32, i32* %2
 %4 = icmp \ sgt \ i32 \ %3, 0
  br i1 %4, label %bb1, label %bb2 ——
                                           → 条件跳转
hh1:
  store i32 1, i32* %2
  br label %bb3
                                             ▶ 直接跳转
hh2:
  store i32 0, i32* %2
 br label %bb3
bb3:
 %r0 = phi i32 [ 0, %bb1 ], [ %3, %bb2 ]
  ret i32 %r0
```

数据流指令

• 条件赋值: Phi

```
%2 = alloca i32
  store i32 0, i32* %2
  %3 = load i32, i32* %2
  %4 = icmp sgt i32 %3, 0
  br i1 %4, label %bb1, label %bb2
bb1:
  store i32 1, i32* %2
  br label %bb3
bb2:
  store i32 0, i32* %2
  br label %bb3
bb3:
  %r0 = phi i32 [ 0, %bb1 ], [ %3, %bb2 ]—
  ret i32 %r0
```

如前序代码块为%bb1,则%8=0,如前序代码块为%bb2,则%8=%3

逻辑运算

- 无需定义专门的逻辑运算指令
- 非运算:基于异或(xor)运算实现

%r2 = xor i1 %r1, true → Not运算: !%r1

逻辑运算

• 基于短路控制流实现逻辑或和与运算|

```
bb1:

%5 = xor i1 %4, true

br i1 %5, label %bb2, label %bb3

bb2:

%7 = load i8, i8* %2,

%8 = trunc i8 %7 to i1

br label %3

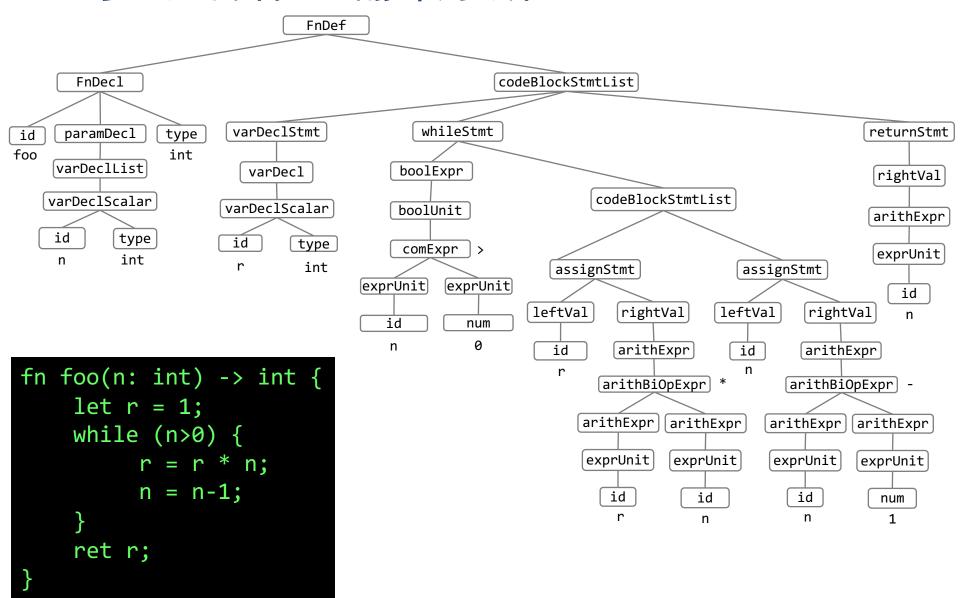
bb3:

%10 = phi i1 [ false, %bb1 ], [ %8, %bb2 ]
```

```
bb1:
br i1 %5, label %bb3, label %bb2
bb2:
    %7 = load i8, i8* %2
    %8 = trunc i8 %7 to i1
br label %bb3
bb3:
    %10 = phi i1 [ true, %bb1 ], [ %8, %bb2 ]
```

二、翻译线性IR

思考:如何将AST翻译为线性IR



AST=>LLVM IR

- 基本思路:
 - 1) 遍历AST,创建全局函数/变量IR
 - 2) 遍历函数AST, 创建代码块编号
 - 3) 翻译每个代码块的内容
- 关键:
 - 代码块编号和引用(br)
 - 变量编号和引用(def-use)

```
struct ProgIR { // 程序IR
    gvlist:list<GlobalVar>;
    fnlist:list<FnIR>;
}
struct FnIR { // 函数组成
    sign:FnSignIR;
    bblist:list<BB>;
}
struct BB { // 代码块组成
    id:int;
    list<InstType> ilist;
}
```

目标IR数据结构示例

代码块编号和引用

• 每个代码块都应以terminator结尾: br/ret

```
define i32 @fac(i32 %0) {
bb0:
    br label %bb1
bb1: ; while cond
    br i1 %cond? label %bb2, label %bb3
bb2: ; while body
    br label %bb1
bb3:
    ret
```

```
fn fac(n: int) -> int {
    let r = 1;
    while (n>0) {
        r = r * n;
        n = n-1;
    }
    ret r;
}
```

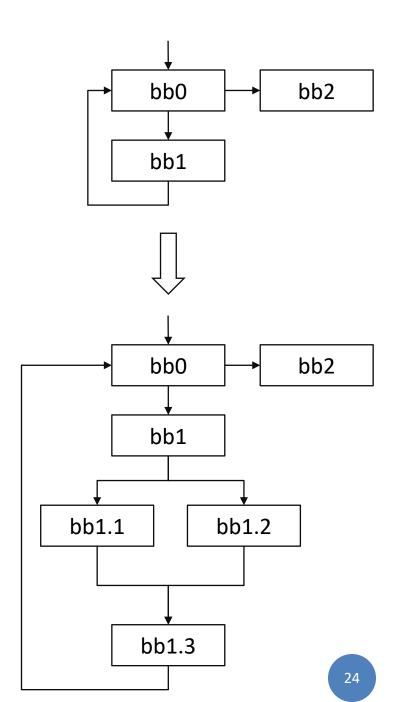
控制流嵌套的例子

```
define i32 @collatz(i32 %0) {
bb0:
   br label %bb1
bb1: ; while condition
    br i1 %7, label %bb2, label %bb6
bb1.1: ; while body; if condition
    br i1 %11, label %bb3, label %bb4
bb1.2: ; true branch
    br label %bb5
bb1.3: ; false branch
    br label %bb5
bb2:
    br label %bb1
bb3:
   ret %r
```

```
fn collatz(n:int) -> int{
    while (n != 1) {
        if (n % 2 == 0) {
            n = n / 2;
        } else {
            n = 3 * n + 1;
        }
     }
    ret n;
}
```

递归下降编号

```
define i32 @collatz(i32 %0) {
bb0:
   br label %bb1
bb1: ; while condition
    br i1 %7, label %bb2, label %bb6
bb1.1: ; while body; if condition
    br i1 %11, label %bb3, label %bb4
bb1.2: ; true branch
    br label %bb5
bb1.3: ; false branch
    br label %bb5
bb2:
    br label %bb1
bb3:
   ret %r
```



变量编号和引用(def-use)

- 使用变量前先load,更新后立即store
- 消除块与块之间的数据依赖关系

```
define i32 @fac(i32 %0) {
bb0:
    %n = alloca i32
    %r = alloca i32
    store i32 %0, i32* %n
    store i32 1, i32* %r
    br label %bb1
bb1:
    %t1 = load i32, i32* %n
    %t2 = icmp sgt i32 %t1, 0
    br i1 %t2, label %bb2, label %bb3
bb2:
    %t3 = load i32, i32* %r
    %t4 = load i32, i32* %n
    %t5 = mul i32 %t3, %t4
    store i32 %t5, i32* %r
```

```
fn fac(n: int) -> int {
    let r = 1;
    while (n>0) {
        r = r * n;
        n = n-1;
    }
    ret r;
}
```

编号要求和方法

- IIi要求:
 - 每个变量(编号)只能定义一次
 - 如果使用纯数字编号,必须从%0开始且连续(代码块和变量名共享)
- 编号方法:
 - 翻译IR时为由于顺序影响,如难以保证编号连续性,避免重复即可
 - 按出现顺序(线性)重命名每一个代码块和变量名
 - 可读性考虑:
 - 代码块用bb编号或纯数字
 - 局部变量用%x名称或纯数字
 - 临时变量用%r1或纯数字

IR翻译结果

```
fn fac(n: int) -> int {
    let r = 1;
    while (n>0) {
        r = r * n;
        n = n-1;
    }
    ret r;
}
```

```
define i32 @fac(i32 %0) {
bb0:
    %n = alloca i32
    %r = alloca i32
    store i32 %0, i32* %n
    store i32 1, i32* %r
    br label %bb1
hh1:
    %t1 = load i32, i32* %n
    %t2 = icmp sgt i32 %t1, 0
    br i1 %t2, label %bb2, label %bb3
bb2:
    %t3 = load i32, i32* %r
    %t4 = load i32, i32* %n
    %t5 = mul i32 %t3, %t4
    store i32 %t5, i32* %r
    %t6 = load i32, i32* %n
    %t7 = sub i32 %t6, 1
    store i32 %t7, i32* %n
    br label %bb1
bb3:
    %t8 = load i32, i32* %r
    ret i32 %t8
```

练习:翻译IR

```
define i32 @collatz(i32 %0) {
bb0:
   br label %bb1
bb1: ; while condition
    br i1 %7, label %bb2, label %bb6
bb1.1: ; while body; if condition
    br i1 %11, label %bb3, label %bb4
bb1.2: ; true branch
    br label %bb5
bb1.3: ; false branch
    br label %bb5
bb2:
   br label %bb1
bb3:
   ret %r
```

```
fn collatz(n:int) -> int{
    while (n != 1) {
        if (n % 2 == 0) {
            n = n / 2;
        } else {
            n = 3 * n + 1;
        }
    }
    ret n;
}
```

练习:翻译IR

```
let a[10]:int = {1,2,3,4,5,6,7,8,9,10};
fn binsearch(x:int) -> int {
    let high:int = 9;
    let low:int = 0;
    let mid:int = (high+low)/2;
    while(a[mid]!=x && low < high) {</pre>
        mid=(high+low)/2;
        if(x<a[mid]) {</pre>
            high = mid-1;
        } else {
            low = mid +1;
    if(x == a[mid]) {
        ret mid;
    else {
        ret -1;
```

```
fn main() -> int {
    let r = binsearch(2);
    ret r;
}
```

三、解释执行

解释执行

- 解释执行对象: 线性IR
- 主要思路:
 - 找到程序入口,按照线性IR指令出现顺序和跳转关系执行
 - 遇到函数创建栈帧,为变量分配空间
 - 为全局变量分配空间

按照IR指令顺序执行

• 通过循环不断获取下一条IR指令并执行

```
enum {
    loadInst,
    addInst,
    subInst,
    mulInst,
    divInst,
    brInst,
    callInst,
    ...
} instType;
```

```
static prog:[instType;n] = { ... };
let pc:*instType = prog;
while(1) {
    match (*pc++) {
        addInst => { ... }
        subInst => { ... }
        ...
    }
}
```

使用Threaded Code

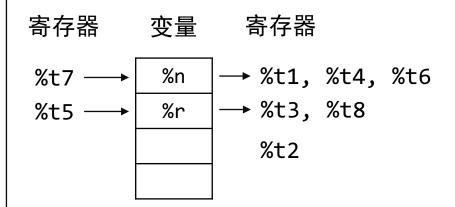
- while-match的问题:需要两次跳转
 - 1: 跳转到分支代码
 - 2: 返回循环入口
- 可否跳转一次?
 - 为每个指令设计一个处理函数或代码块

```
while(1) {
    match (*pc++) {
        addInst => { ... }
        subInst => { ... }
        ...
    }
}
```



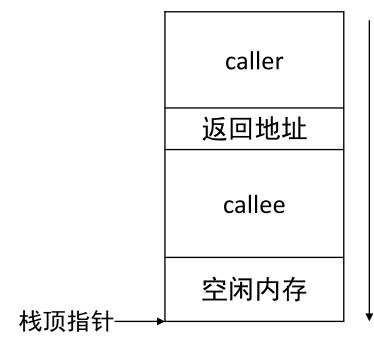
如何保存每条指令的运行效果?

```
define i32 @foo( i32 %0 ) {
bb0:
   %n = alloca i32
   %r = alloca i32
   store i32 %0, i32* %n
   store i32 1, i32* %r
   br label %bb1
bb1:
   %t1 = load i32, i32* %n
   %t2 = icmp sgt i32 %t1, 0
    br i1 %t2, label %bb2, label %bb3
bb2:
   %t3 = load i32, i32* %r
   %t4 = load i32, i32* %n
   %t5 = mul i32 %t3, %t4
    store i32 %t5, i32* %r
   %t6 = load i32, i32* %n
   %t7 = sub i32 %t6, 1
    store i32 %t7, i32* %n
    br label %bb1
bb3:
   %t8 = load i32, i32* %r
    ret i32 %t8
```



函数栈帧: Activation Record

- 栈帧: 为每个函数调用分配一块儿内存空间
- 函数自身所需栈空间可在编译时确定(alloca)
- 栈帧空间在函数返回后收回



栈空间增长方向

```
fn foo() -> &i32(){
    let i:int = 100;
    ret &i;
}
```

Bug!!!

逃逸分析?

传统栈虚拟机/寄存器虚拟机

• LLVM IR为三地址IR,与传统Java Bytecode不同

```
//Java Bytecode
Load a
Load b
Add
Store c
```

```
id = 0;
loadInst => {
    r[id++] = *arg1;
addInst => {
    r[id++] = r[id-1]+r[id-2];
}
storeInst => {
    *arg1 = r[id];
}
```

```
stack s;
loadInst => {
    s.push(*arg1);
addInst => {
    v1 = s.pop();
    v2 = s.pop();
    v2 = v1 + v2;
    s.push(v2);
}
storeInst => {
    v1 = s.pop();
    *arg1 = v1;
```

寄存器虚拟机实现方式

虚拟机

- 为解释执行提供了程序运行抽象
 - 内存管理(栈、堆、垃圾回收)
 - 寄存器
 - 多线程
- 比较有名的虚拟机:
 - Java: HotSpot Dalvik (Android)
 - Javascript: Chrome v8、Chakra、SpiderMonkey
- 虚拟机优化思路:
 - Threaded code
 - JIT优化
 - ...



