

T.E. Computer Sem IV Dec-2019

Paper / Subject Code: 31903 / Computer Networks

T.E. [Computer] SEM IV Choice Base (3 Hours)

[Total Marks: 80]

21/11/2019

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- Note: (1) Question 1 is compulsory
(2) Solve any three questions out of remaining
(3) Assume suitable data wherever necessary

Q.1. (a) Explain the need of layering for communication and networking (5M)
(b) Describe in brief the concept of piggybacking. (5M)
(c) What is subnetting? What are the default subnet masks? (5M)
(d) Differentiate between TCP and UDP (5M)

Q.2. (a) Explain CSMA protocols. Explain how collisions are handled in CSMA/CD. (10 M)
(b) What is traffic shaping? Explain leaky bucket algorithm and compare it with token Bucket algorithm. (10M)

Q.3. (a) Illustrate TCP three way handshake techniques in TCP connection establishment. (10M)
(b) Explain the need for DNS (Domain Name System) and describe the protocol functioning. (10M)

Q.4. (a) Explain the difference between static and dynamic routing. Explain distance vector routing in detail. (10M)

(b) Why does data link protocol always put the CRC in a trailer rather than in a header?
Given the data word "1101011011" and the divisor "10011", show the generation of cyclic redundancy check (CRC) codeword at the sender site. (10M)

Q.5. (a) Write short note on FTP. (10M)
(b) Discuss different types of guided media in detail. (10M)

Q.6. (a) What is a topology? Explain the types of topology. (10M)
(b) Explain the Go-back-N protocol. (10M)

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- N.B. (1) Question No. 1 is compulsory
 (2) Attempt any three out of remaining five questions
 (3) Assumptions made should be clearly stated

1. (a) Explain post correspondence problem. 5
 (b) Differentiate between FA and PDA. 5
 (c) Define Regular Expression and obtain a regular expression such that
 $L(R) = \{ w \mid w \in \{0, 1\}^* \text{ with at most three zeros} \}$ 5
 (d) What is ambiguous grammar? Check whether following grammar is ambiguous or not 5

$$E \rightarrow E+E|E^*E|(E)|id$$
2. (a) Design a Finite State Machine to accept following language over the alphabet {0, 1} 10
 $L(R) = \{ w \mid w \text{ starts with 0 and has odd length or starts with 1 and has even length} \}$
- (b) Give and explain formal definition of Pumping Lemma for Regular Language and prove that following language is not regular. 10

$$L = \{ 0^i \mid i \text{ is prime number} \}$$
3. (a) Construct PDA accepting the language $L = \{ a^{2n}b^n \mid n \geq 0 \}$ 10
 (b) Consider the following grammar 10

$$S \rightarrow iCtS \mid iCtSeS \mid a$$

$$C \rightarrow b$$

 For the string 'ibtaeibta' find the following:
 - (i) Leftmost derivation
 - (ii) Rightmost derivation
 - (iii) Parse tree
 - (iv) Check if above grammar is ambiguous.
4. (a) Construct PDA to check $\{ wcw^r \mid w \in \{a, b\}^* \}$ where w^r is reverse of w & c is a constant. 10
 (b) Convert following CFG to CNF 10

$$S \rightarrow 0A0 \mid 1B1 \mid BB$$

$$A \rightarrow C$$

$$B \rightarrow S \mid A$$

$$C \rightarrow S \mid \epsilon$$
5. (a) Convert $(0+1)(10)^*(0+1)$ into NFA with ϵ -moves and obtain DFA. 10
 (b) Construct Moore and Mealy Machine to convert each occurrence of 101 by 111. 10
6. Write short note on following (any 2) 20
 - (a) Chomsky Hierarchy
 - (b) Halting Problem
 - (c) Rice's Theorem
 - (d) Universal Turing Machine

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T.E. [Computer] SEM IV CBSGS 21/11/2019

Duration: 3 Hrs.



Total Marks: 30

N.B. : 1. Question No. 1 is Compulsory.

2. Attempt any three questions from remaining five questions.

3. Figure to the right indicates full marks.

Q.1 a) What is framing? Explain in short different framing methods. 5

b) Discuss design issues for various layers. 5

c) Compare TCP/IP and OSI reference models. 5

d) Explain various error-reporting messages in Interned Control Message Protocol. 5

Q.2 a) What is congestion control? Explain various congestion prevention policies. 10

b) Explain working of simple mail transfer protocol (SMTP). 10

Q.3 a) Explain in detail IPv4 header format with diagram. 10

b) Explain CSMA protocols. How are collisions handled in CSMA/CD? 10

Q.4 a) Explain working of link state routing algorithm. Explain content and requirements of link state packets. 10

b) Explain congestion control approaches in TCP. 10

Q.5 a) Explain File Transfer Protocol (FTP). 10

b) Explain sliding window protocol using Go Back-N technique. 10

Q. 6 Write short notes on following. 20

a) Simple Network Management Protocol (SNMP)

b) Address Resolution Protocol (ARP)

c) Bluetooth

d) Berkeley Sockets

19/11/2019

1/1

T.E [Computer] SEM II choice base.

[Time: 3 Hrs]

[Marks: 80]

Please check whether you have got the right question paper.

- N.B:
1. Q. 1 is compulsory.
 2. Solve any three from reaming questions.
 3. Draw neat diagrams wherever necessary.

- Q. 1** a) Explain types of users of the database system with suitable example & responsibilities of DBA. (10)
b) Explain extended ER features. (10)
- Q. 2** a) Explain types of join with suitable example. (10)
b) Explain constraints in SQL. (10)
- Q. 3** a) Explain relational algebra operators. (10)
b) What is Log.? Explain Log based recovery. (10)
- Q. 4** a) What is 2PL. Explain 2PL (2- phase) Locking protocol with its types. (10)
b) What is trigger? Write syntax and example. (10)
- Q. 5** a) Explain view serializability with suitable examples. (10)
b) What is Normalization? Explain 1NF, 2NF and 3NF with suitable is example. (10)
- Q. 6** Write note on (Any two) (20)
a) Timestamp Based Protocol
b) Transaction state diagram
c) Functional dependency in dbms

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T.E(Computer) Sem-II CBGS

19/11/19

(3 Hours)

[Total Marks: 80]

111

- NB: 1. Question No.1 Compulsory.
 2. Solve any THREE from Q.2 to Q.6
 3. Assume suitable data whenever necessary with justification.

Q1 Solve any **FOUR**

- (A) Explain Segmentation in 8086 Processor. 05
- (B) Give different bits of Control Register-0 (CR0) of 80386. 05
- (C) Explain Branch Prediction Logic. 05
- (D) Explain Flag Register bits of 8086. 05
- (E) Explain Virtual Mode (VM86) 80386 Processor. 05

- Q2. (A) Draw & Explain architecture of 8086 Processor with example. 10
 (B) Explain Block diagram of PPI 8255. 10

- Q3. (A) Explain Maximum mode of 8086 Processor. 10
 (B) Explain modes of PIT8254. 10

- Q4. (A) Explain Interrupt Structure of 8086 Processor. 10
 (B) Explain Branch Prediction Logic of Pentium Processor. 10

- Q5. (A) Explain TSS in details. 10
 (B) Explain data Cache architecture for Pentium Processor. 10

- Q6. (A) Explain SPARC Processor with block diagram. 10
 (B) Explain block diagram of PIC8259. 10

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Page 1 of 1

T.E (Comp) Sem - II Choice base

(Total Marks : 80)

27/11/19 (10)

(3 Hours)

N.B.: (1) Question No.1 is compulsory.

(2) Attempt **any three** questions from the remaining **five** questions.

(3) Draw neat diagrams wherever necessary.

1. Differentiate :

- a) Multimedia Database and Normal Database.
- b) Printer and Scanner
- c) RTF and TIFF File formats.
- d) Types of video signals.

[20]

2. (a) Give the silent features of different types of Images. What will be the size of digital image if image specification is 800×600 pixels with 8-bit gray level? [10]

(b) Discuss the characteristics of sound wave. Illustrate the steps to digitize audio data. [10]

3. (a) What are the different types of redundancies present in an image? For the phrase "Mississippi river", with each letter will take 8 bits. Apply Huffman Coding Compression technique and find reduction achieved. [10]

(b) Differentiate the different audio compression techniques. Justify DPCM performs better compression than PCM with suitable example. [10]

4. (a) Explain MPEG Video Compression Technique. [10]

(b) Why quality of service is important in network communication for multimedia data. Discuss the protocols to achieve the quality. [10]

5. (a) Chart the navigation through an application for editing a hypermedia mail message. [10]

What menu items are required for this application?

(b) Discuss the importance of steganography. Explain the working of LSB with an example. [10]

6. Write Short Note on Any Two : [20]

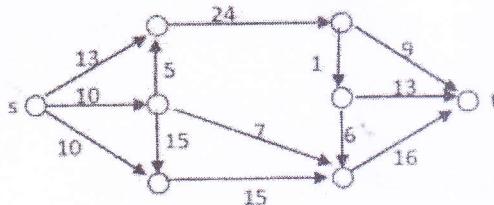
- a) JPEG Compression
- b) RTP
- c) Digital Signature

(3 Hours)

[Total Marks: 80]

- N.B. (1) Question No. 1 is compulsory
 (2) Attempt any three out of the remaining five questions
 (3) Assumptions made should be clearly stated

1. (a) Differentiate between P, NP, NP Complete and NP-Hard classes of Complexity. 05
 (b) Define Red-Black tree. 05
 (c) Write short note on bipartite matching. 05
 (d) Explain recurrences with example. 05
2. (a) Define Maximum flow and Minimum-Cut. Apply Ford Fulkerson algorithm on following. 10



- (b) What is convex hull? Explain Jarvis March in detail. 10
3. (a) Prove that Vertex Cover is NP-Complete. 10
 (b) Explain Master theorem, and apply on the following examples. 10
 - i) $T(n) = 2T(n/2) + n$
 - ii) $T(n) = 4T(n/2) + n^2$
4. (a) Explain steps to prove any problem as NP Complete problem. 10
 (b) Define Binomial Heap, Explain its operations with example. 10
5. (a) Explain DELETE operation in Red-Black Tree. Discuss its time complexity. 10
 (b) Prove that TSP is NP-Complete. 10
6. Write a short note on following (any 4) 20
 - (a) Amortized Analysis
 - (b) Randomized Algorithm
 - (c) Relabel to Front algorithm
 - (d) Line segment properties
 - (e) NP-Completeness

T.E. [Computer] SEM II Choice Paper

15/11/2019

(Time: 3 Hours)

[80 Marks]



N.B. 1) Question No. 1 is compulsory.

2) Attempt any Three questions out of remaining.

3) Assume suitable data wherever necessary and state them clearly.

- Q.1 a) Explain memory banking in 8086 5
 b) Explain VM, RF, IOPL, NT and TF flags of 80386 microprocessor 5
 c) Write addressing modes of the following instructions 5
 i) MOV AX, [BX+SI]
 ii) AND CL,[2000]
 iii) IN AL, DX
 iv) JMP [BX+2]
 v) ADD AX,[BX+SI+5]
 d) Explain BSR mode of 8255 PPI 5
- Q.2 A) Design 8086 based system for the following requirements 10
 i) 8086 working in minimum mode with 8MHz.
 ii) 64 KB EROM using 32 KB * 8 devices
 iii) 128 KB RAM using 64 KB * 8 devices
 B) Draw the block diagram of PIC8259 and discuss its operation 10
- Q.3 A) Draw and explain the block diagram of PIT 8253 10
 B) Draw and explain demultiplexing of address bus in 8086 10
- Q.4 A) Explain how flushing of pipeline problem is minimized in Pentium architecture 10
 B) Draw and explain maximum mode configuration of 8086 system 10
- Q.5 A) Write a 8086 assembly language program with appropriate comments, to find if the given year is a leap year or not 10
 B) Explain Strobed Bi-directional I/O Mode 2 operation of 8255 PPI with control word and timing diagram 10
- Q.6 A) Differentiate real mode, protected mode and virtual mode of 80386 microprocessor 5
 B) Write a short note on mixed language programming 5
 C) Explain the following instructions in 8086 : LAHF and XLAT 5
 D) Explain data transfer modes of DMAC 8257 5

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