

LAB REPORT - 9

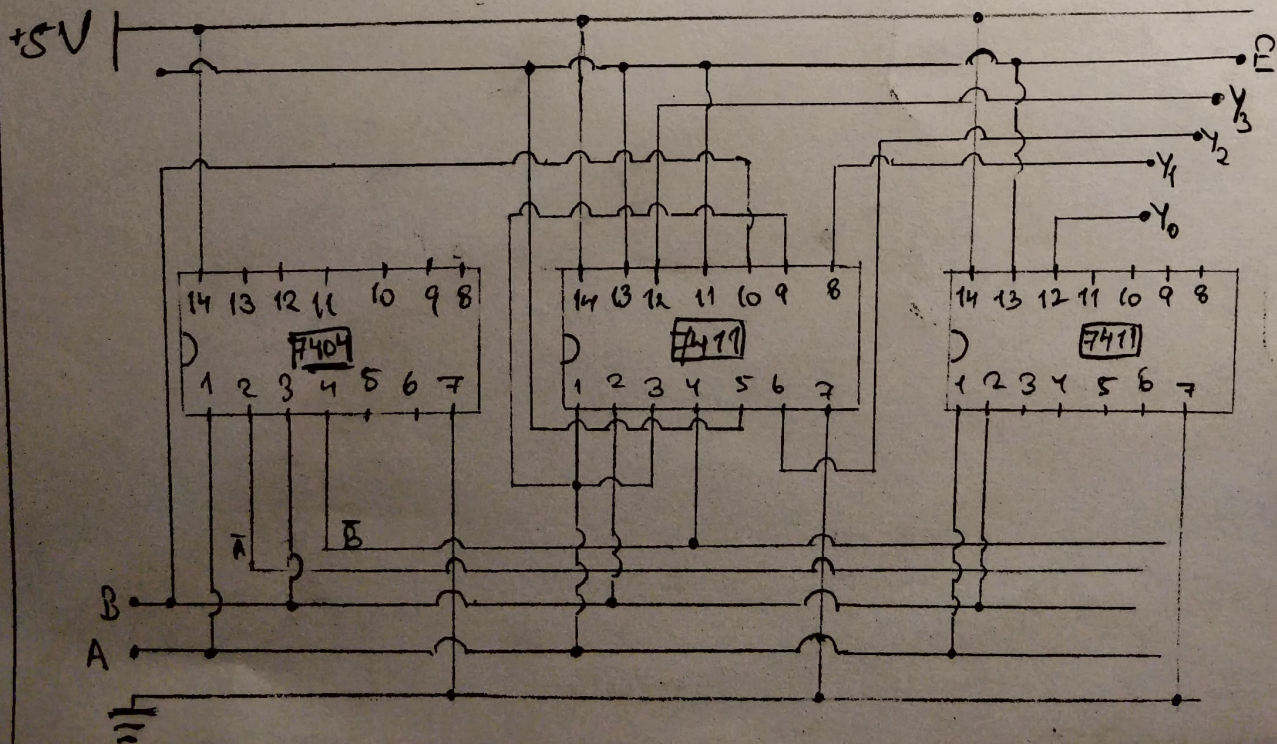
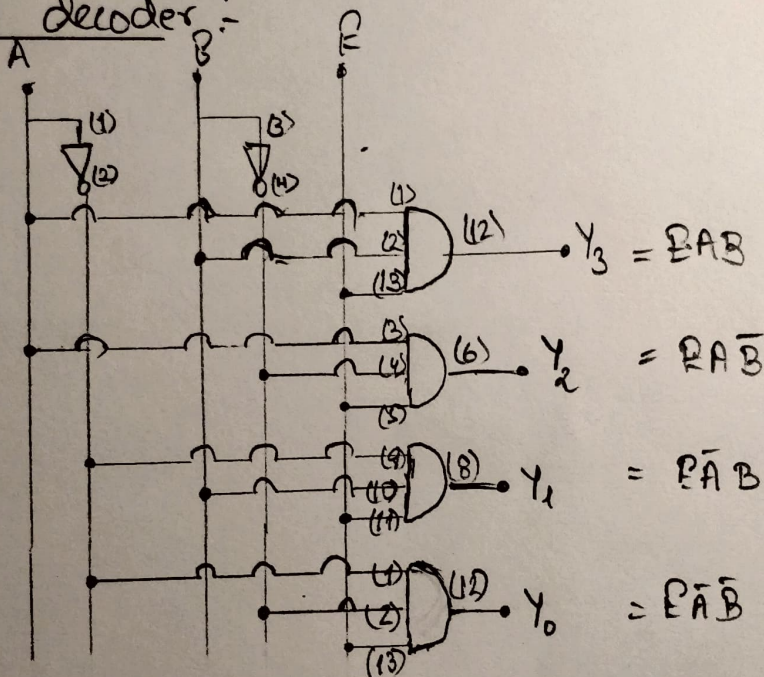
S20210010027
Anushthan Saxena

Physical implementation of circuits.

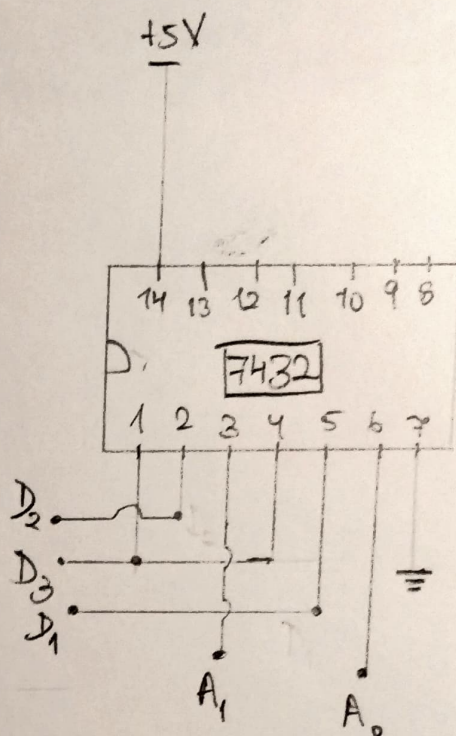
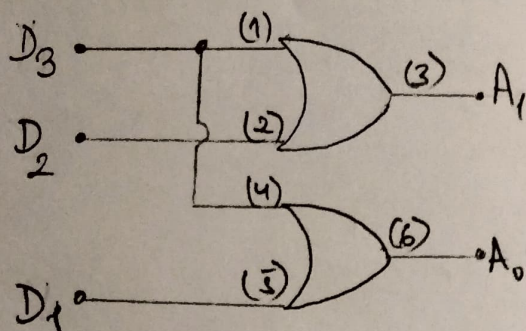
AIM → Implementation of some encoders/decoders using ICs.

ICs used → ~~7408~~ 7408 (2 in AND), 7432 (2 in OR), 7404 (NOT), 7411 (3 in AND).

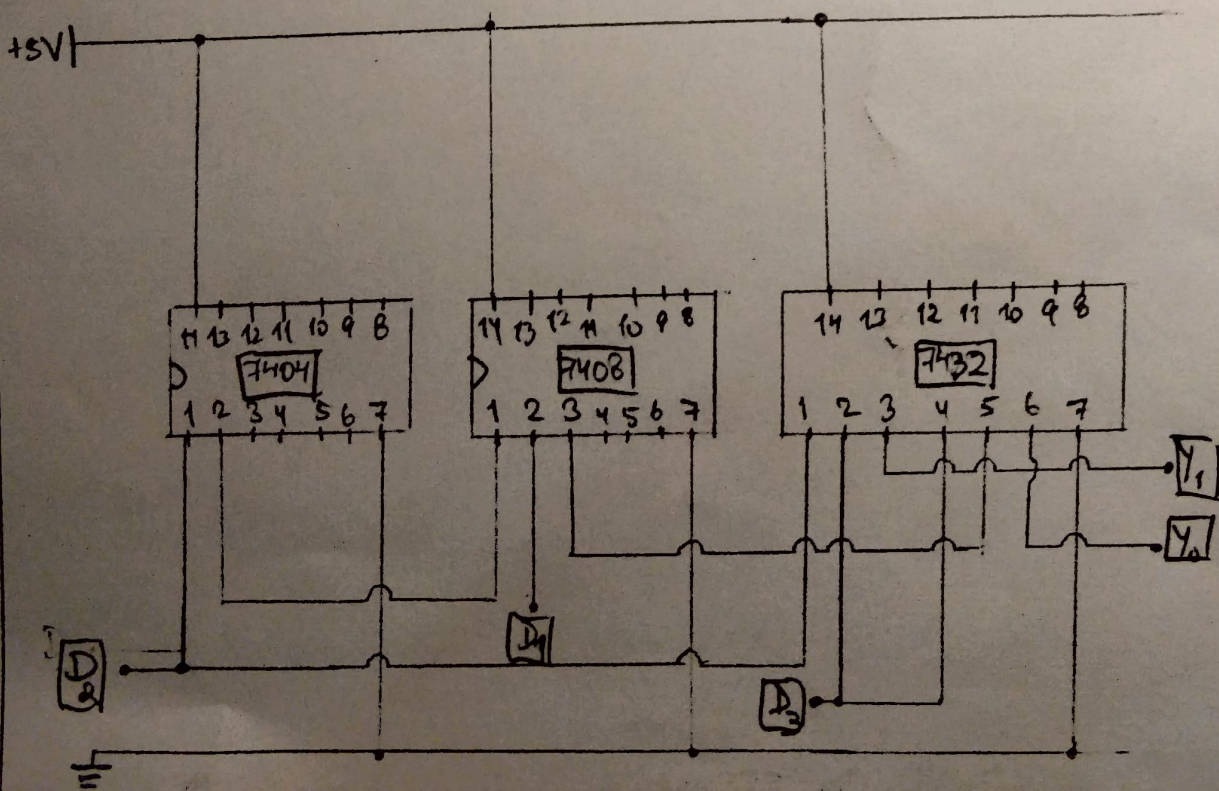
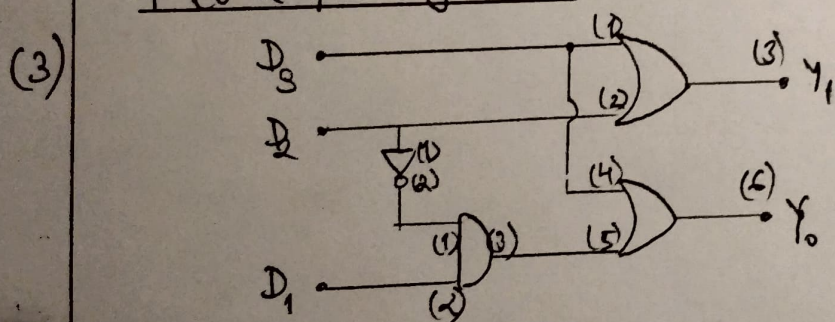
1) 2 to 4 decoder :-



(2) 4 to 2 Encoder →



(3) 4 to 2 priority encoder →



(k) 4x1 Multiplexer →