

Dtd: 11/2/22

Submitted by Sec - B

ASSIGNMENT - 7

S20210010027

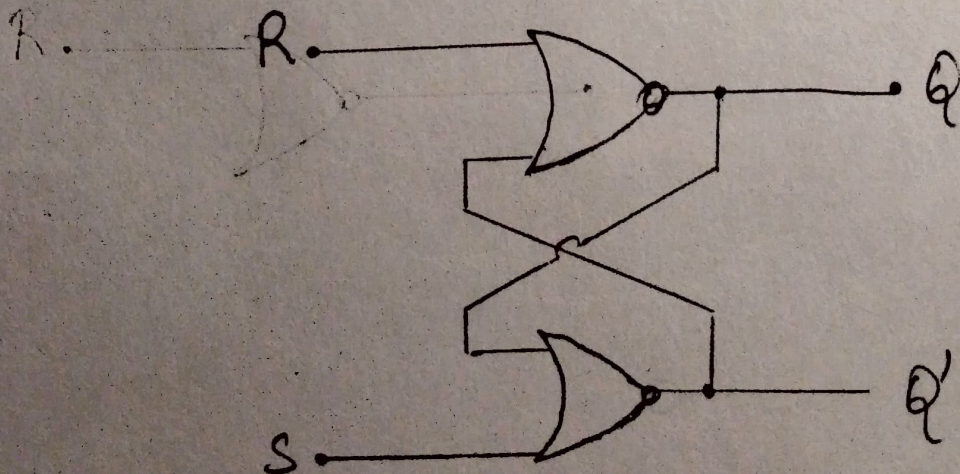
Anushthan Saxena

Latches and Flip-flops.

(I) Latches -

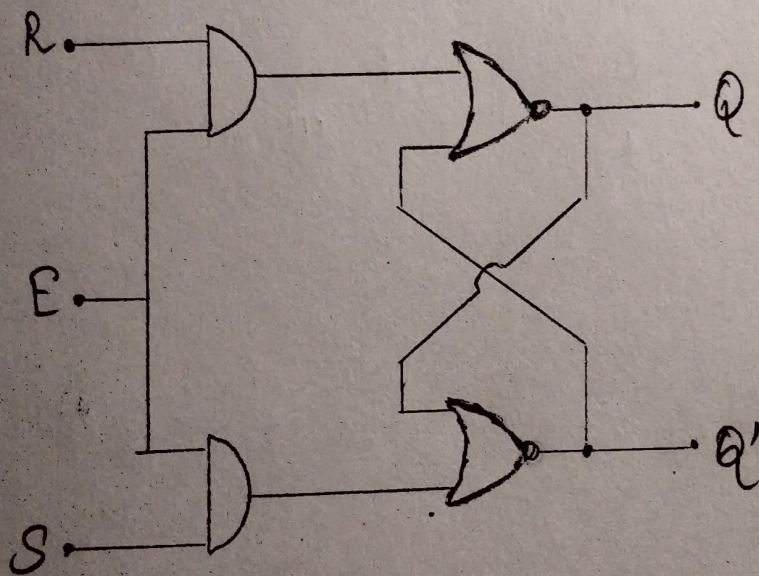
(A) SR Latch:

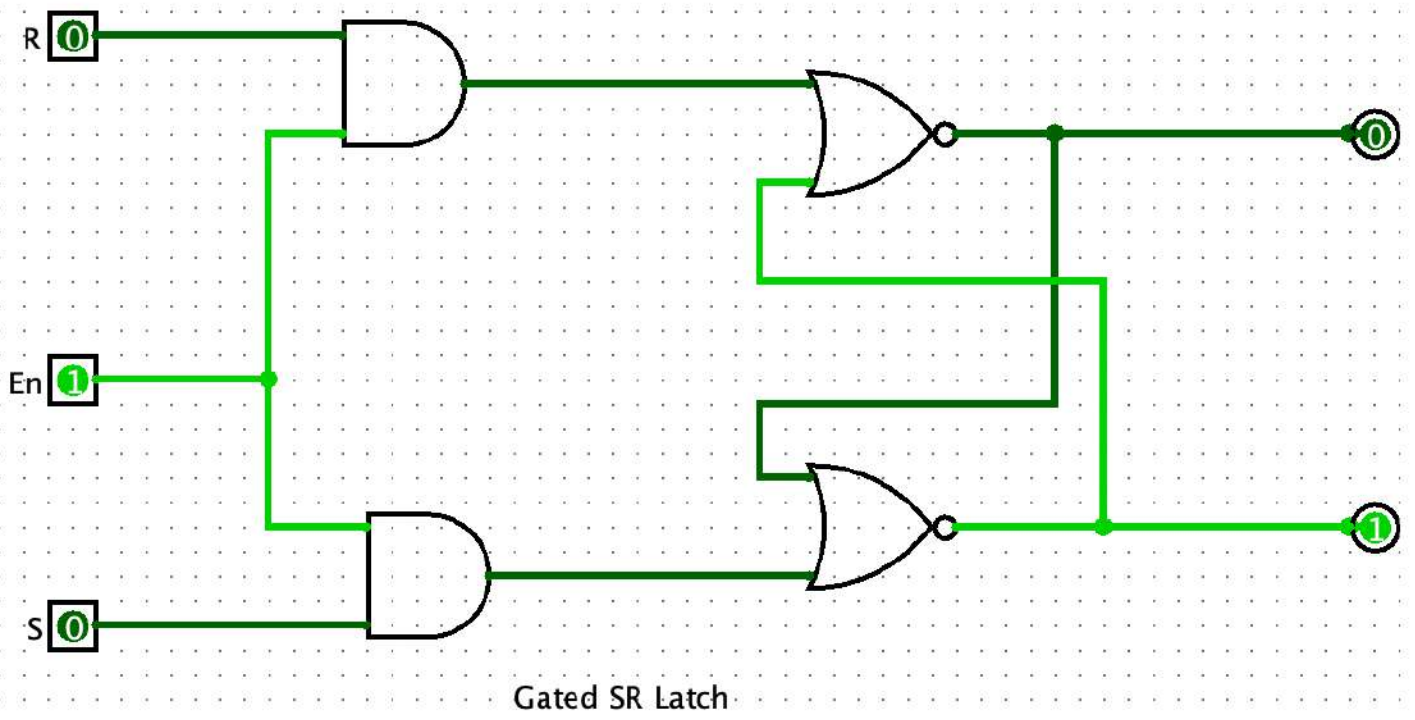
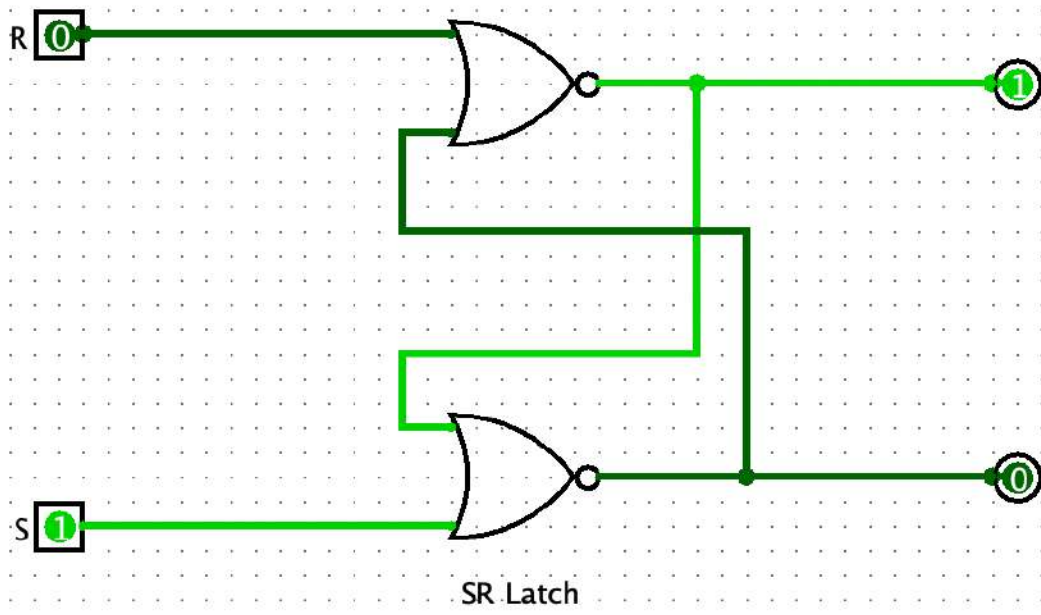
S	R	Q	Q'
0	0	Latched	
0	1	0	1
1	0	1	0
1	1	Illegal	



(B) Gated SR Latch :

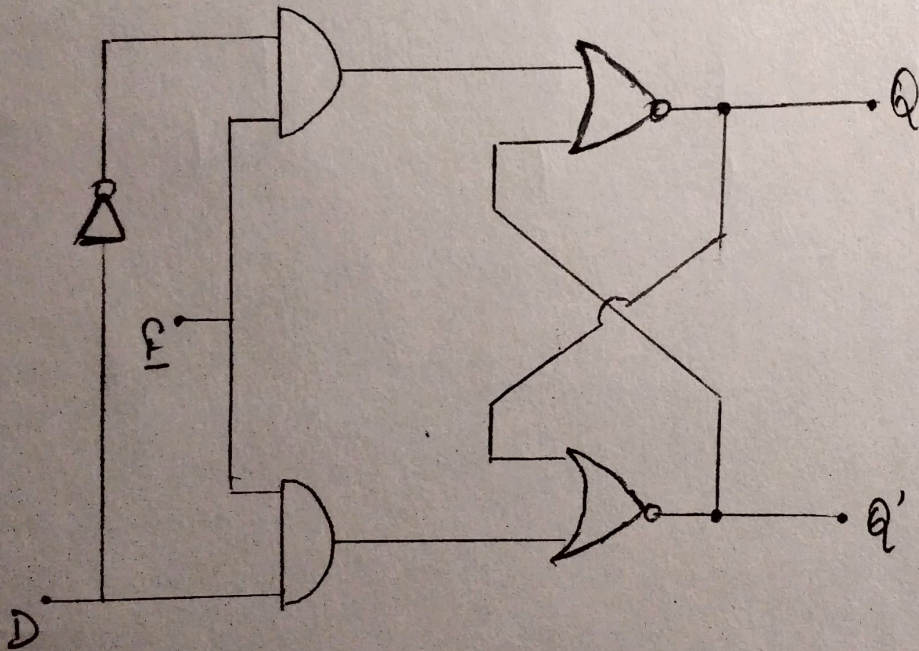
E	S	R	Q	Q'
0	x	x	Latched	
1	0	0	Latched	
1	0	1	0	1
1	1	0	1	0
1	1	1	Illegal	

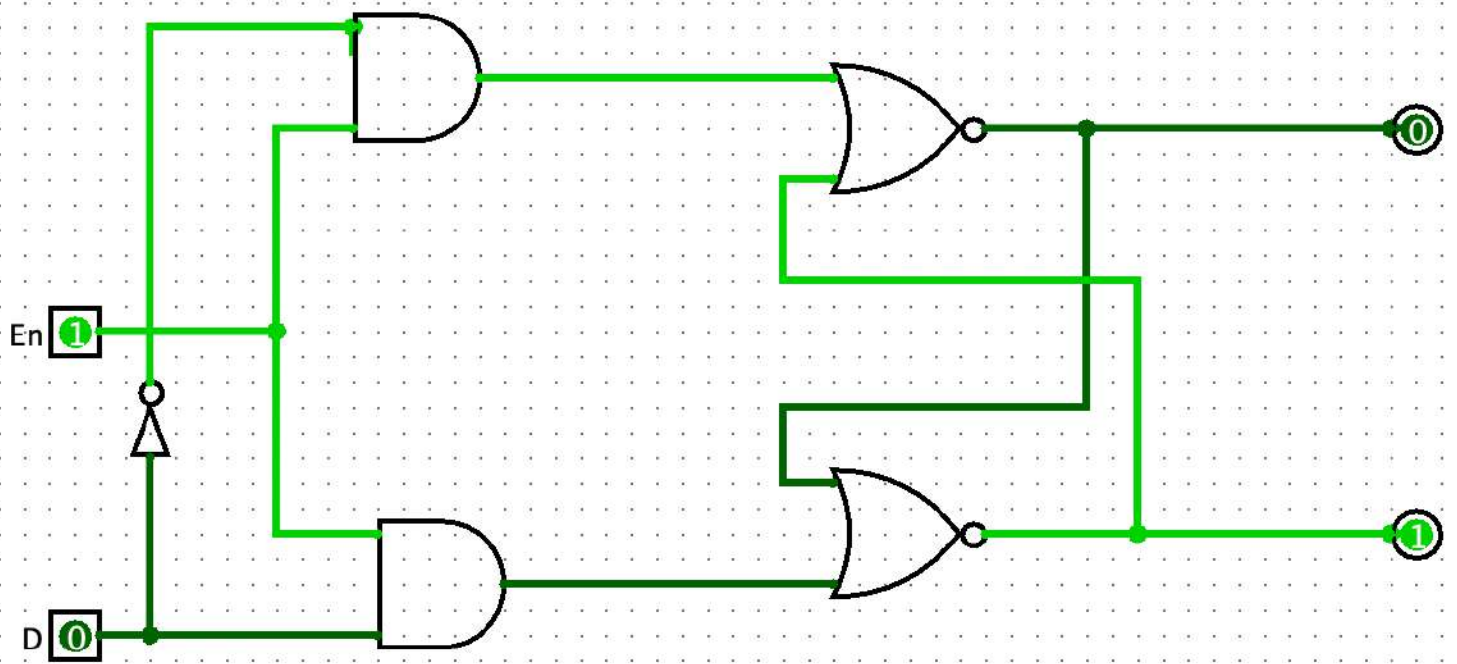




(C) D latch :

<u>E</u>	<u>D</u>	<u>Q</u>	<u>Q'</u>
0	x	Latched	
1	0	0	1
1	1	1	0



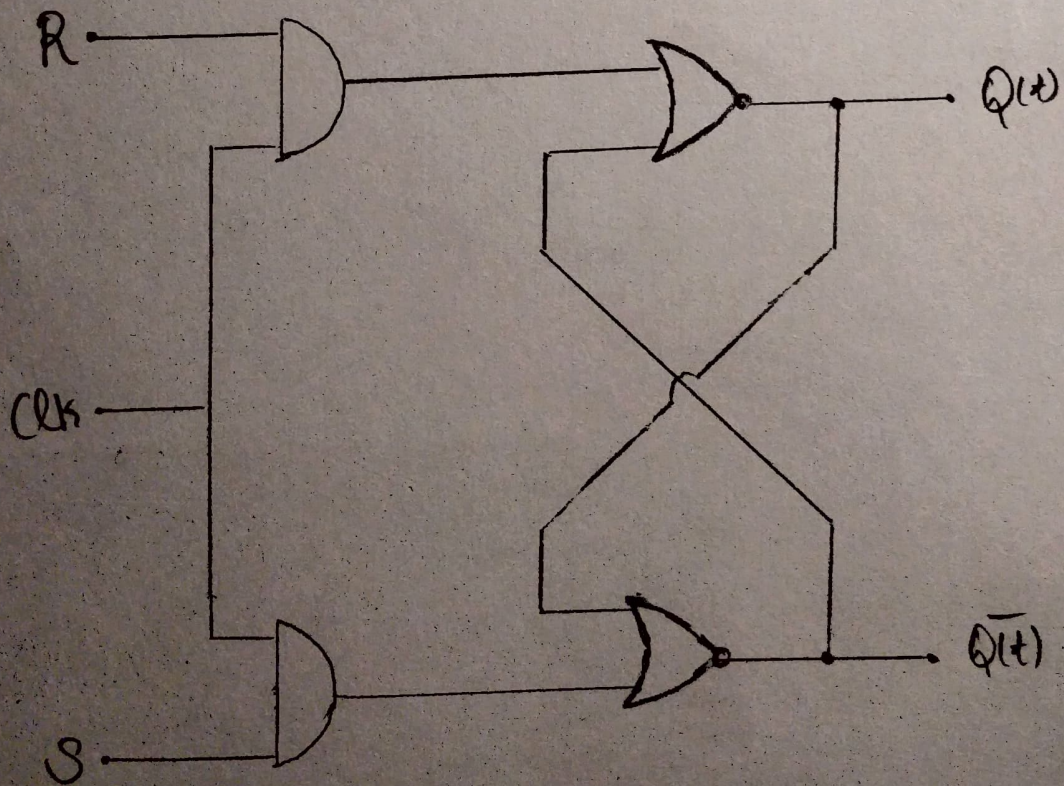


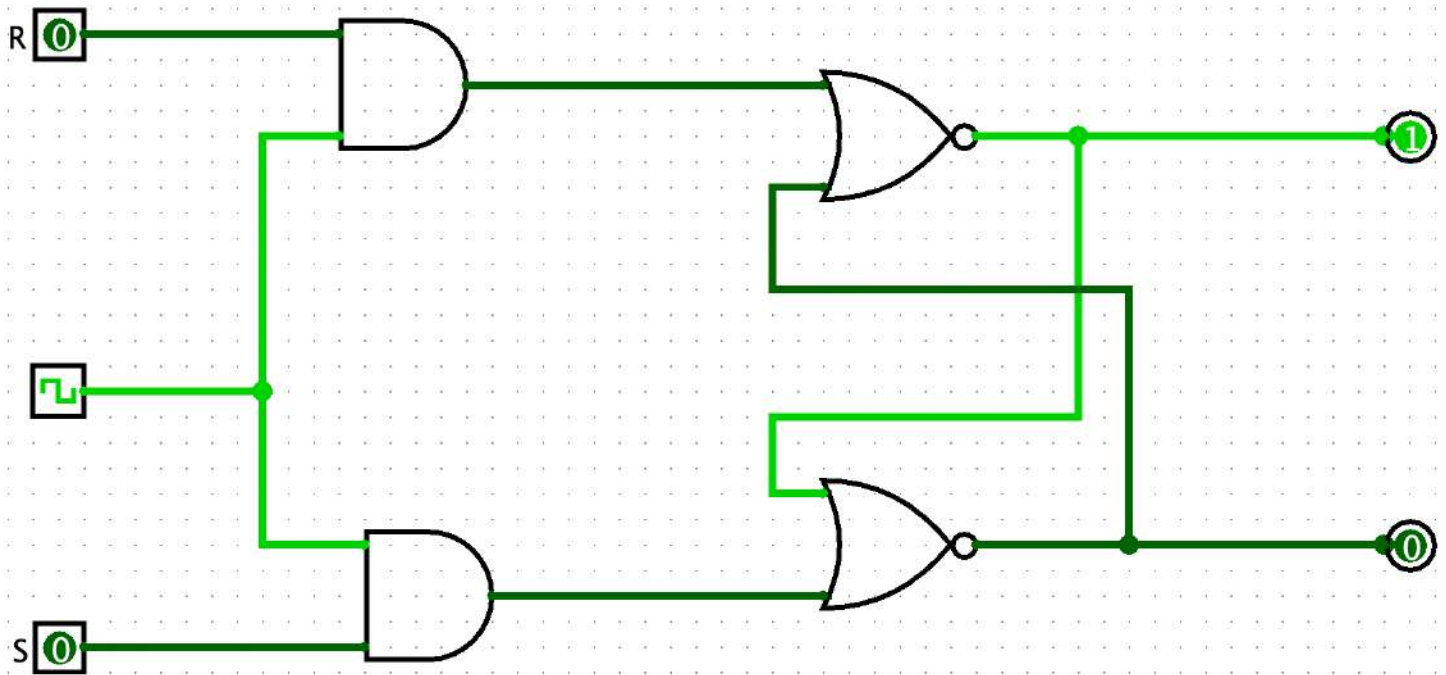
D Latch

(II) Flip-flops-

(A) SR- flip-flop:

clk	S	R	$Q(t+1)$
↑	0	0	$Q(t)$
↑	0	1	0
↑	1	0	1
↑	1	1	Illegal

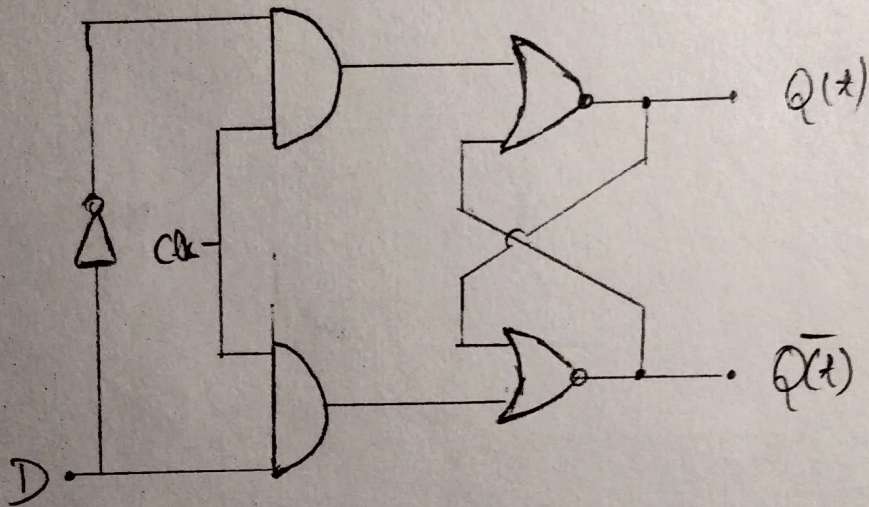




SR Flip Flop

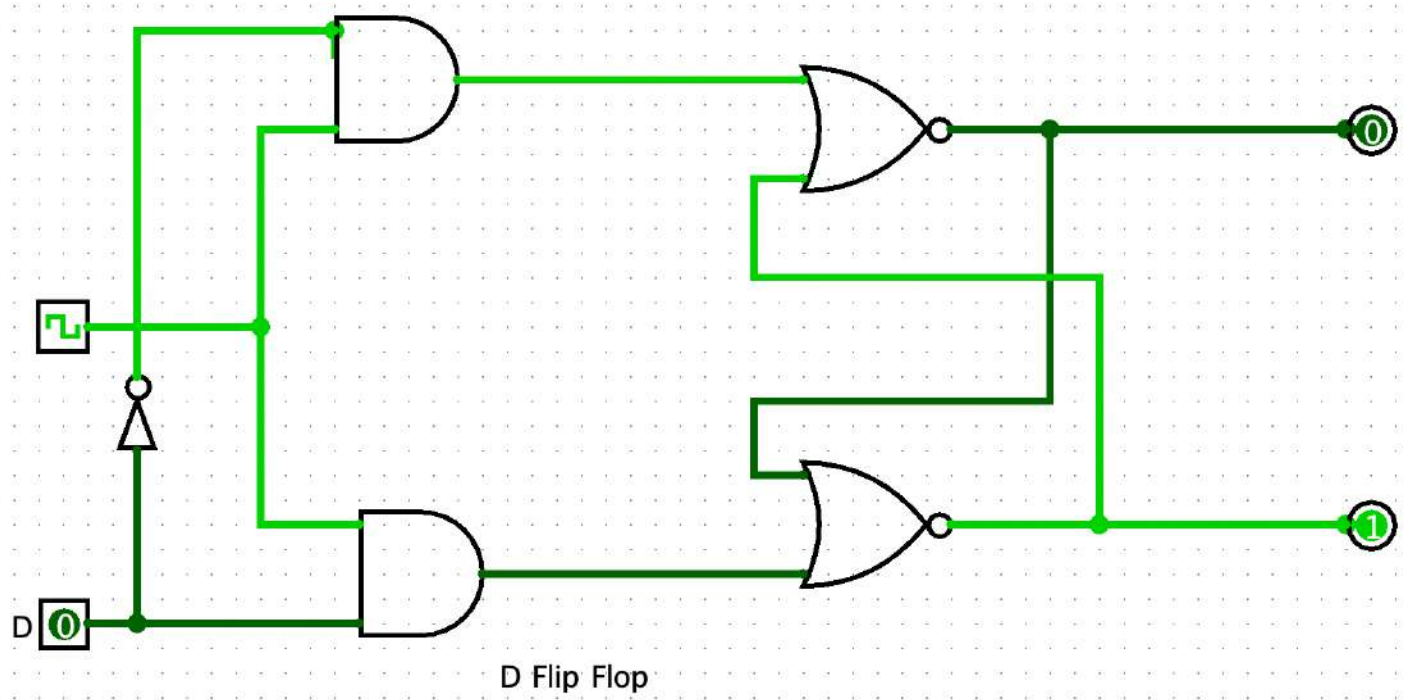
(B) D flip-flop:

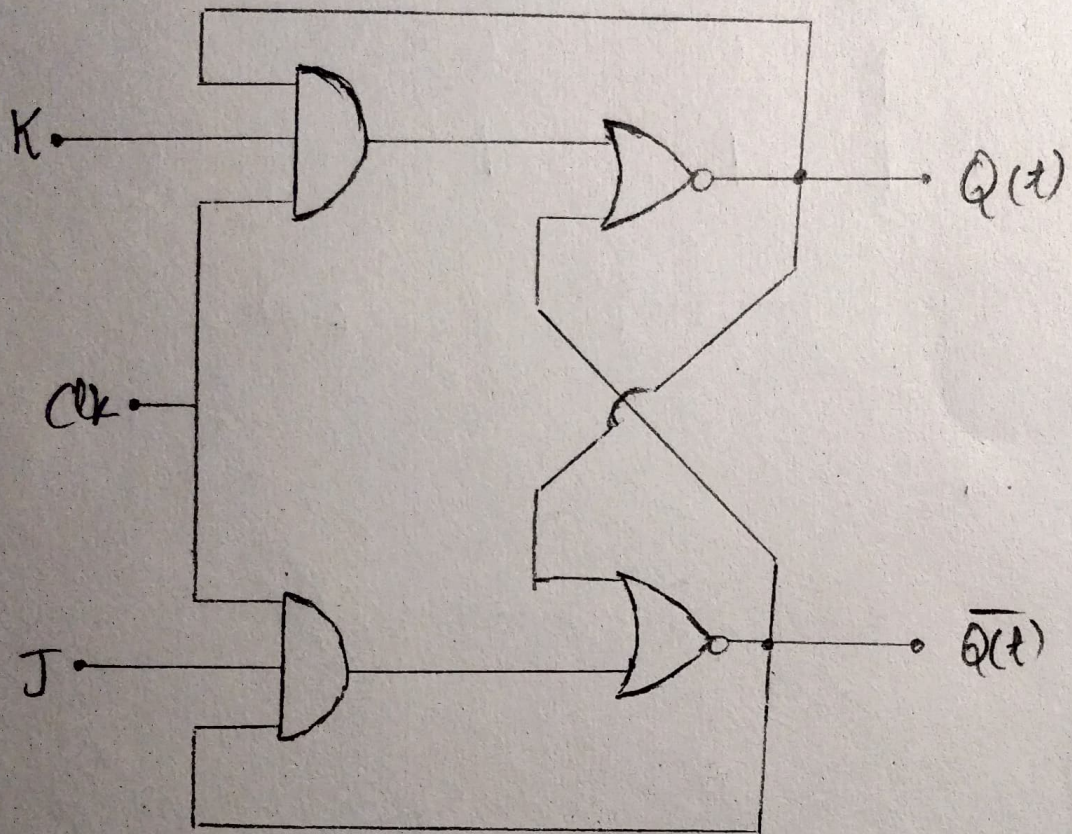
clk	D	$Q(t+1)$
↑	0	0
↑	1	1

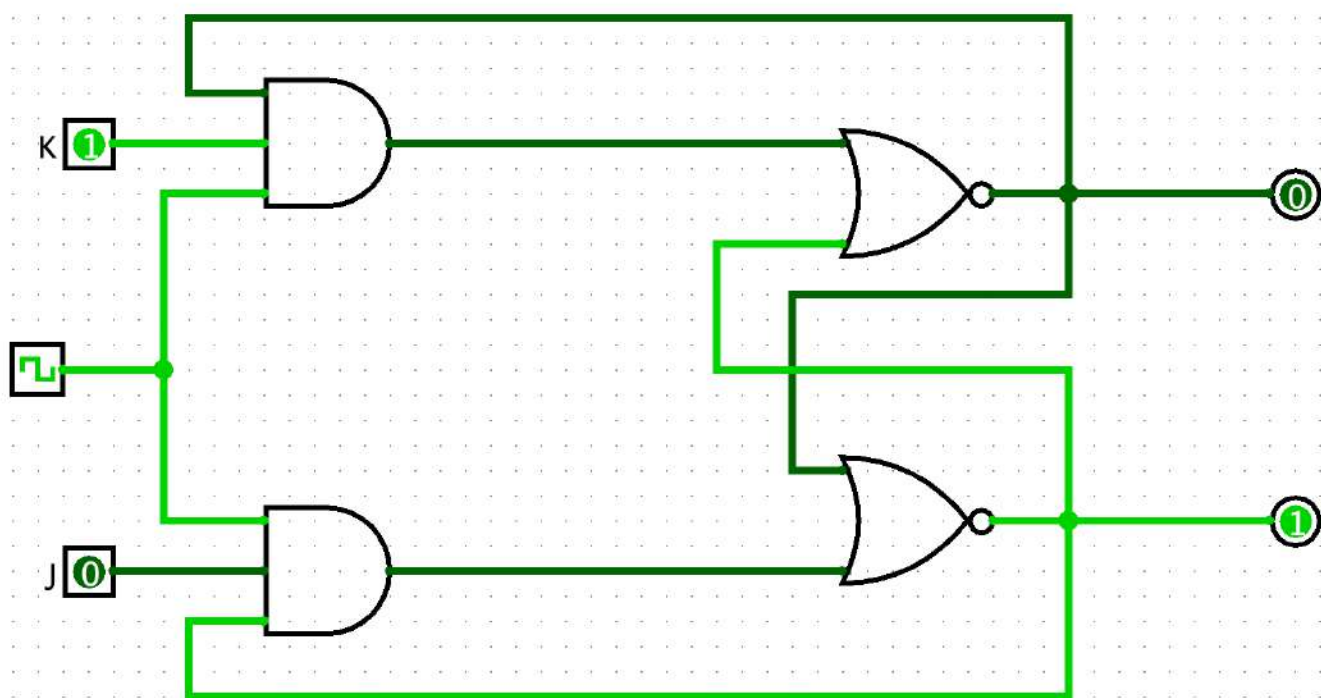


(C) JK flip flop:

clk	J	K	$Q(t+1)$
↑	0	0	$Q(t)$
↑	0	1	0
↑	1	0	1
↑	1	1	$\overline{Q(t)}$







JK Flip Flop

(D) T flip-flop:

Clk	T	$Q(t+1)$
\uparrow	0	$Q(t)$
\uparrow	1	$\bar{Q}(t)$

