Anushthan Saxena

Half and Full Adders and Subtractors

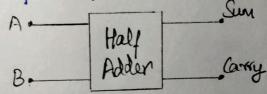
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(I) EXERCISE - A look at HALF-ADPER.

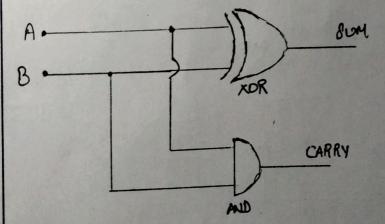
Software USD - Logisin.

DETAILS -> Used to add & single bit numbers.

Schematic for Half Adder:



Circuit diagram:



Truth table:

A	<b>B</b> .	Sun	Comy	· Cun = AB + AB = ABB
00	0	0	0	Coony = AB
1	0	Sun. 0 1 1	0	

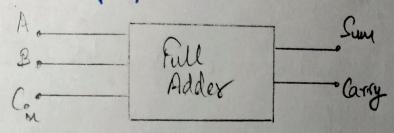
2

# (I) EXERCISE - A look at FULL ADDER.

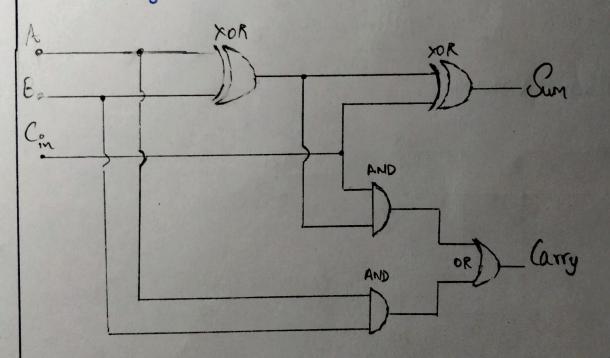
SOFTWARE USED - Logisme

#### DETAILS -

Schematic for full adder:



### Circuit diagram:



Sum= A @ B@Cin

Corry = BC2 + AC + AB

or

Carry = AB + ( (ABB)

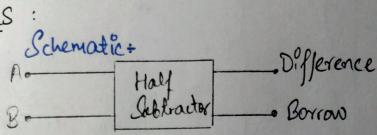
#### Truth stable:

A	B Cm	Sum	Carry
0	0 0	0	0
0	01	1	0
0	10	1	1
0	11	0	
1	00	1	0
1	01	0	
1		O	1
1	11	1	1

(II) EXERCISE: A lookat HALF SUBTRACTOR.

SOPTWARE USED : Logisim.

DETAILS



Circuit diagram:

A Difference

Borrow

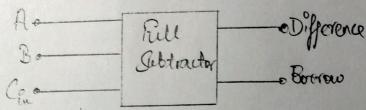
Truth table:

A	8	Difference	Borrow	Difference = ABB
0		0	0	Borrow = $\overline{A}B$
0		1	1	
1		1	0	
1	8 9 9 9	0	0	

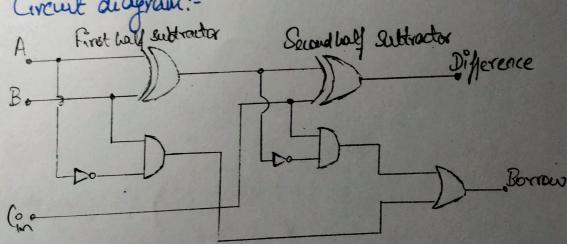
## (IV) EXERCISE: A look at FULL SUBTRACTOR

SOFTWARE USED: Logisim.

## DETAILS : Schematic:



### Circuit diagram:



### Truth table:

A B C 0 0 0 0 0 1	Difference 0 1	Borrow	Difference = A + B + C
010	0100	1000	Boman = BC + AC + AB
1 10	1	1	