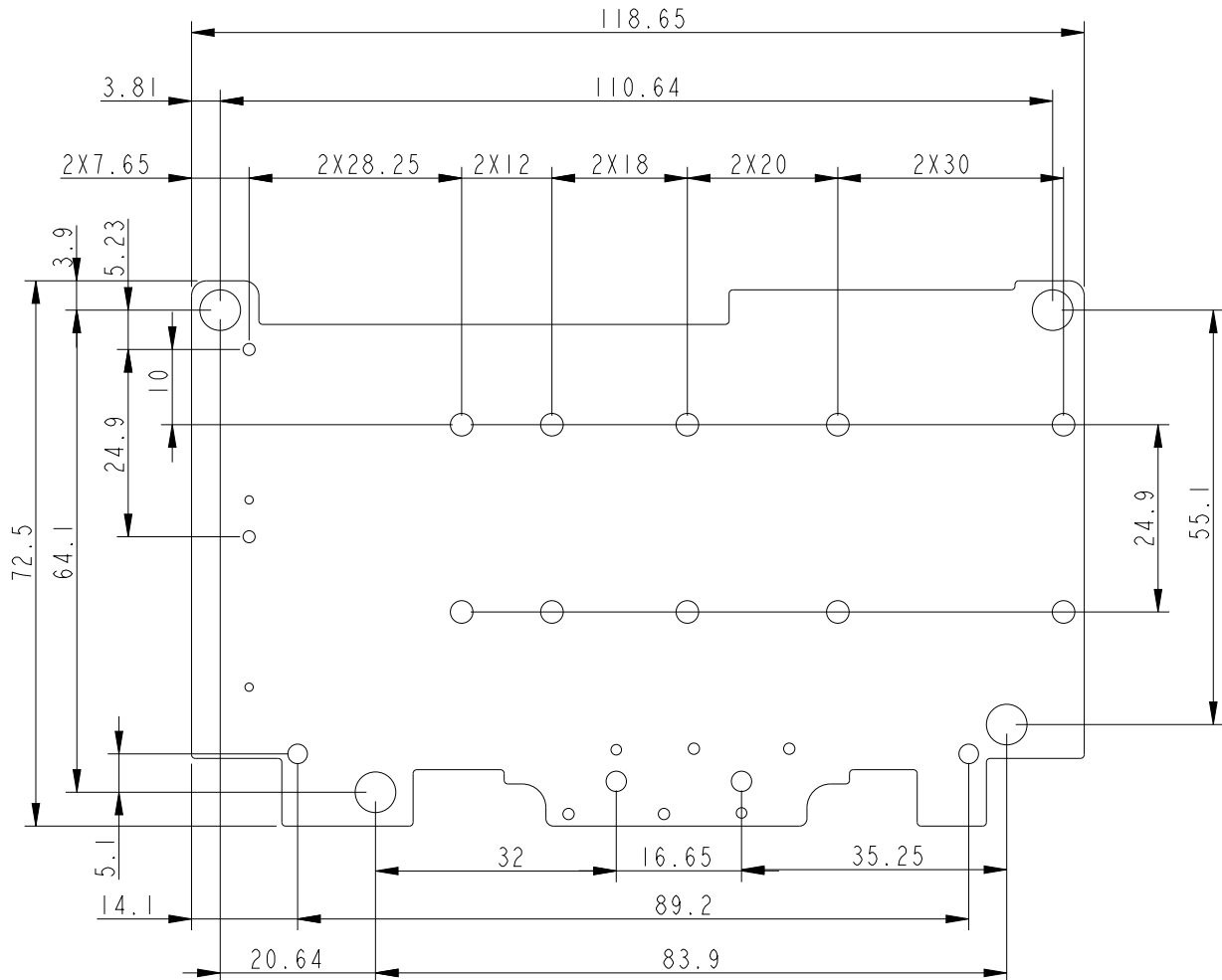
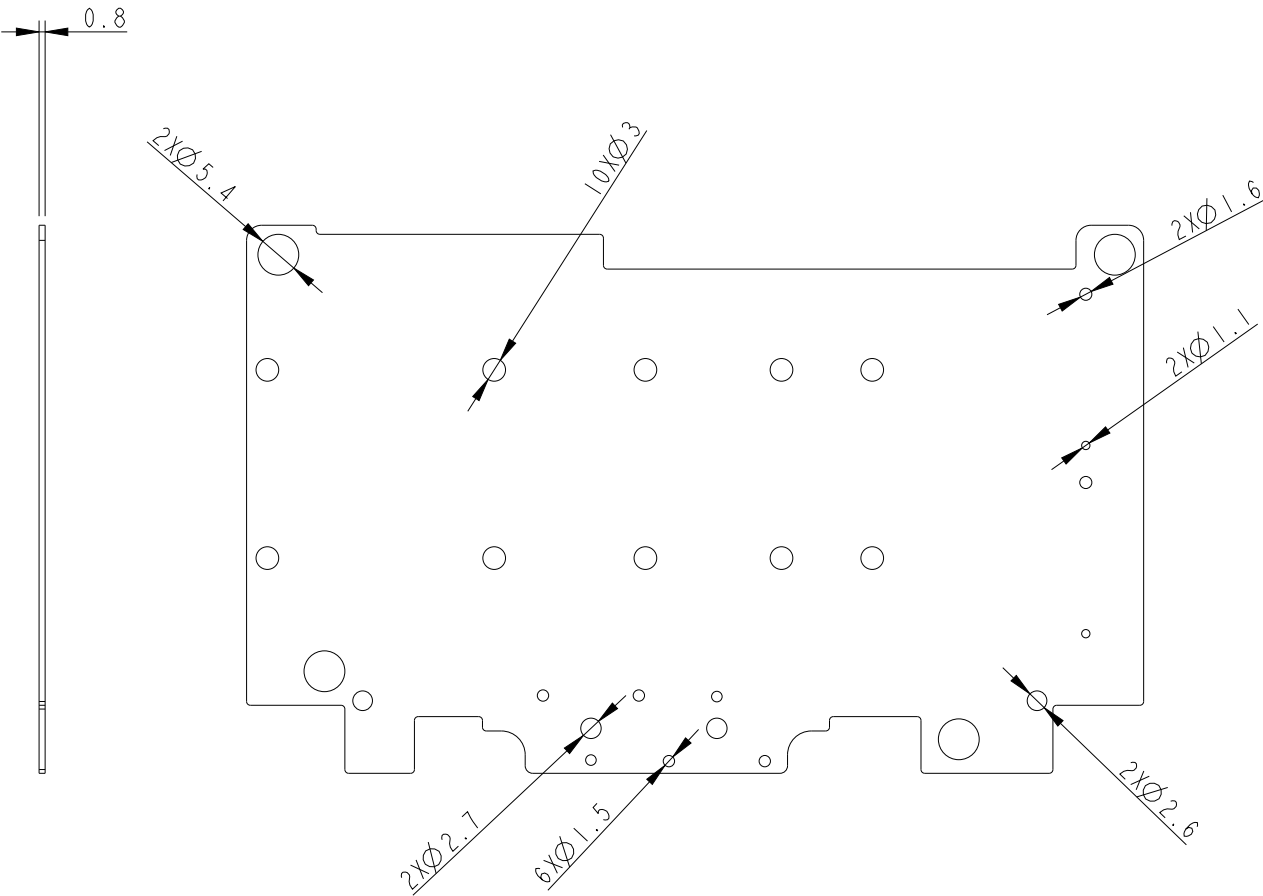


OUTLINE DIMENSION

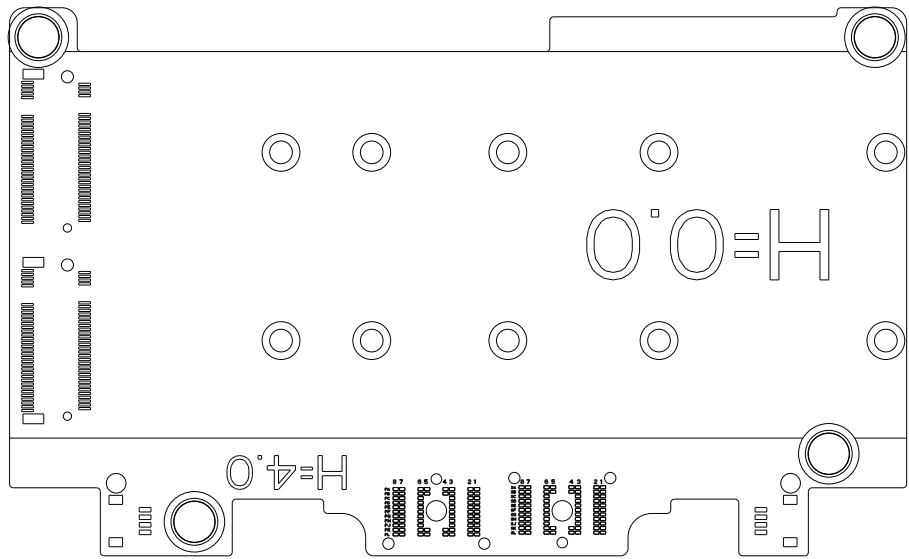


TOP VIEW

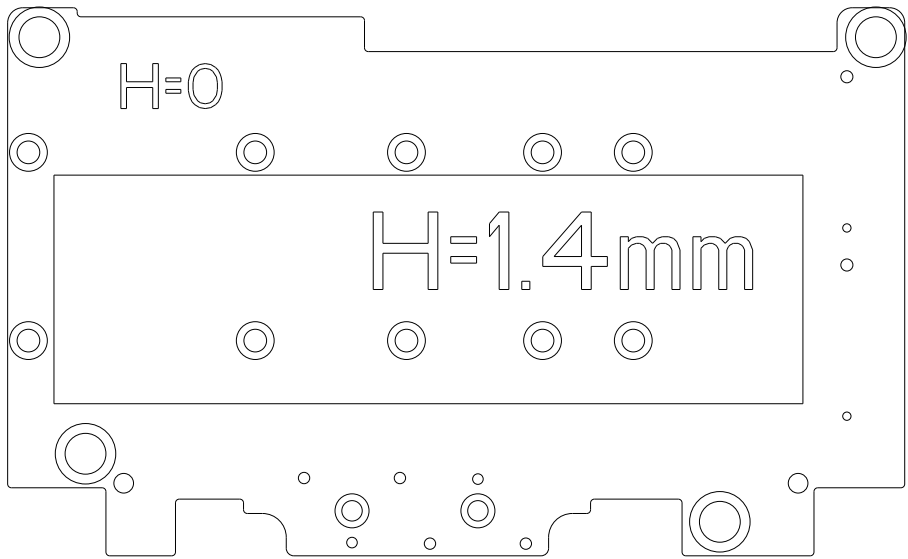


BOTTOM VIEW

LIMIT AREA & FOOT PRINT

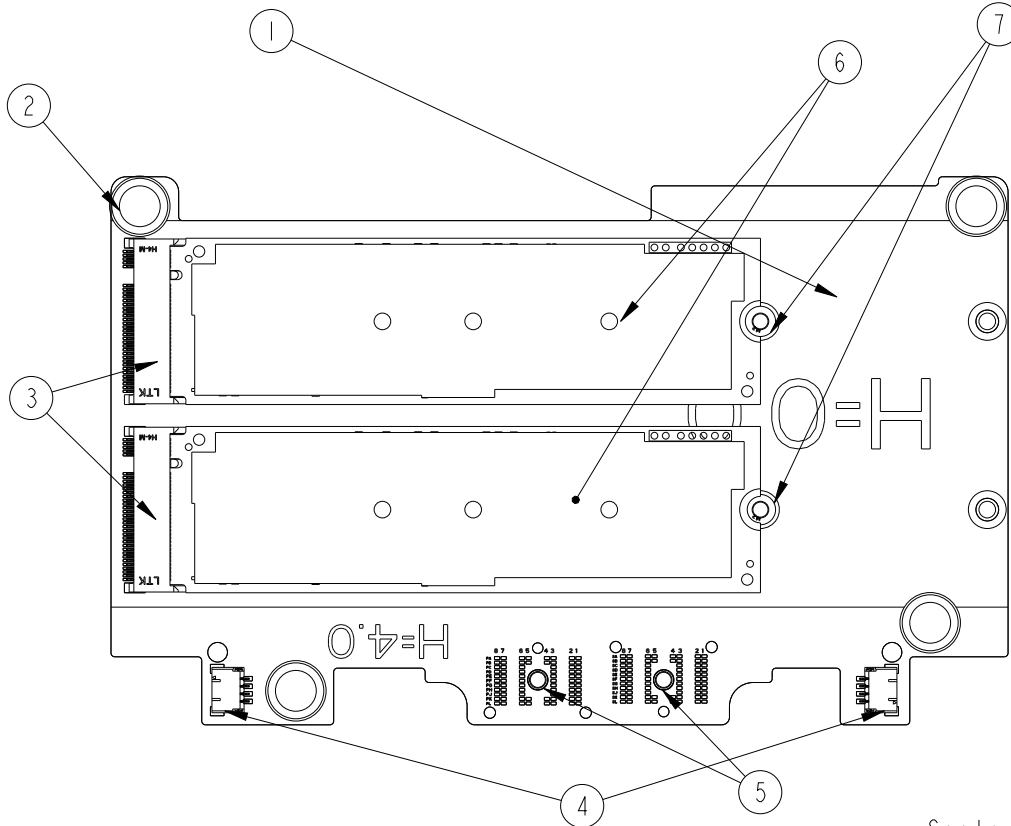


TOP VIEW



BOTTOM VIEW

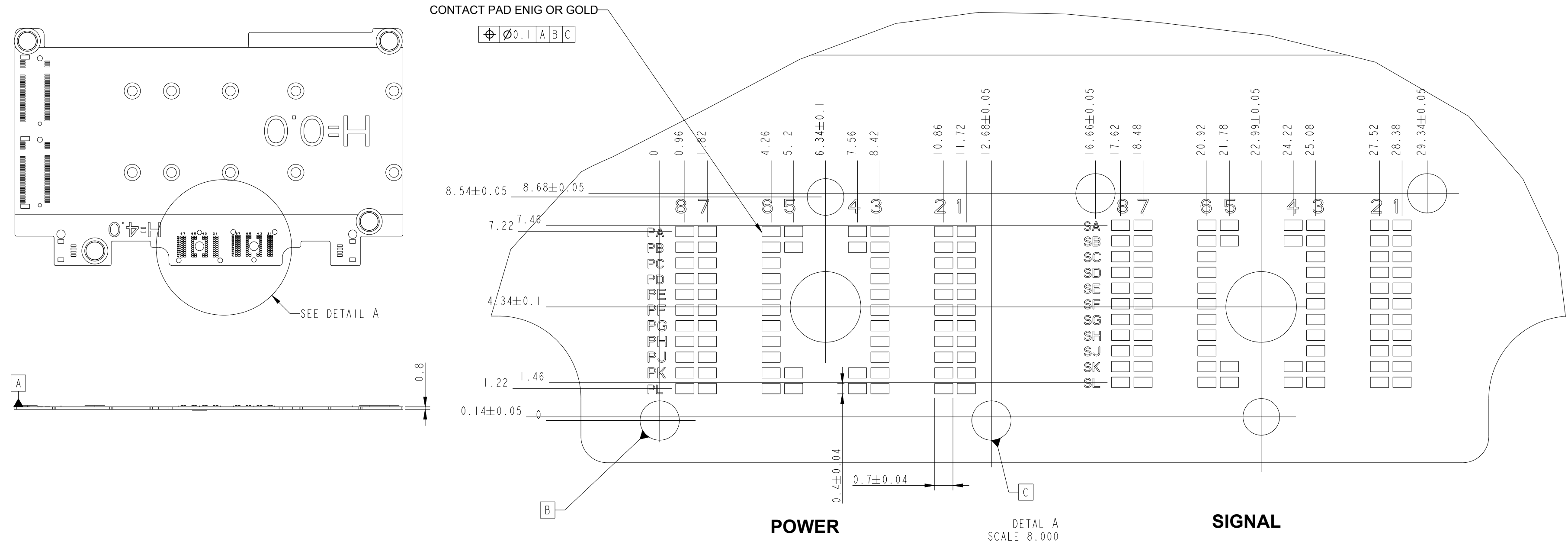
COMPONENTS



Item	Description	Q'ty	Note
1	SSD PCB	1	
2	SSD PCB WASHER	4	
3	SSD CONNECTOR	2	
4	FAN WIRE TO BOARD CONNECTOR	2	
5	FBEAM M2 STANDOFF	2	
6	SSD CARD-2230, 2242, 2260, and 2280	2	
7	SSD M2 NUT+SCREW M2L3.4	2	

UNLESS SPECIFIED TOLERANCE ARE: 300 ABOVE : ± 0.20 300 ≥ X ≥ 150 : ± 0.15 150 > X ≥ 50 : ± 0.10 50 > X ≥ 20 : ± 0.075 20mm BELOW : ± 0.05 ANGLES : ± 1° FLATNESS : ▢ 0.20 CONCENTRICITY : ± 0.05				THIRD ANGLE PROJECTION		DRAWING FILE NAME FW_SSD_PCB_REF_DESIGN		SIZE A2		SHEET 1 OF 2		REVISION 0		VERSION 0	
DESIGNED BY				DRAWN BY		CHECKED BY		APPROVED BY		ISSUED DATE Mar-09-23		SCALE 1000		UNIT mm	
MODEL				FRAMEWORK		DRAWING FILE NAME FW_SSD_PCB_REF_DESIGN		SIZE A2		SHEET 1 OF 2		REVISION 0		VERSION 0	

PIN DEFINITION



	8	7	6	5	4	3	2	1
PA	VSYS_GPU	VSYS_GPU	GND	3.3V_ALW	3.3V_ALW	GND	VADP_GPU	VADP_GPU
PB	VSYS_GPU	VSYS_GPU	GND	3.3V_ALW	3.3V_ALW	GND	VADP_GPU	VADP_GPU
PC	VSYS_GPU	VSYS_GPU	GND			I2C_CLK_ALW	VADP_GPU	VADP_GPU
PD	VSYS_GPU	VSYS_GPU	GND			I2C_DAT_ALW	GND	GND
PE	VSYS_GPU	VSYS_GPU	GND			GND	GND	GND
PF	VSYS_GPU	VSYS_GPU	GND			I2C_CLK_VS	I2C_INT_ALW	GND
PG	VSYS_GPU	VSYS_GPU	GND			I2C_DAT_VS	I2C_INT_VS	GPIO0_EC
PH	GND	GND	GND			GND	FAN0_SPEED	GPIO1_EC
PJ	GND	GND	GND			USB+	FAN1_SPEED	ID0
PK	5V_ALW	5V_ALW	GND	FAN1_PWM	GND	USB-	PWR_EN	DDS_SDA
PL	5V_ALW	5V_ALW	GND	FAN0_PWM	12V_FAN	GND	DDS_CLK	PWR_GOOD

POWER

	8	7	6	5	4	3	2	1
SA	PEX_RX1N	DP_A_HPD	PEX_RX3N	PEX_RST#	PEX_CLK_REQ	PEX_RX4P	GPIO2_EC	PEX_RX6P
SB	PEX_RX1P	GND	PEX_RX3P	TH_ALERT#	DGPU_PWM_SEL	PEX_RX4N	GND	PEX_RX6N
SC	GND	DP_A_L1#	GND			GND	PEX_REFCLK	GND
SD	PEX_RX0N	DP_A_L1	PEX_RX2N			PEX_RX5P	PEX_REFCLK#	PEX_RX7P
SE	PEX_RX0P	GND	PEX_RX2P			PEX_RX5N	GND	PEX_RX7N
SF	GND	DP_A_L0#	GND			GND	DP_A_L3#	GND
SG	PEX_TX0N	DP_A_L0	PEX_TX2N			PEX_TX5P	DP_A_L3	PEX_TX7P
SH	PEX_TX0P	GND	PEX_TX2P			PEX_TX5N	GND	PEX_TX7N
SJ	GND	DP_A_AUX#	GND			GND	DP_A_L2#	GND
SK	PEX_TX1N	DP_A_AUX	PEX_TX3N	PNL_PWR_EN	PNL_BL_PWM	PEX_TX4P	DP_A_L2	PEX_TX6P
SL	PEX_TX1P	GPIO3_EC	PEX_TX3P	TH_OVERT#	PNL_BL_EN	PEX_TX4N	ID1	PEX_TX6N

SIGNAL

UNLESS SPECIFIED  
TOLERANCE ARE:  
300 ABOVE : ± 0.20  
300 ≥ X ≥ 150 : ± 0.15  
150 > X ≥ 50 : ± 0.10  
50 > X ≥ 20 : ± 0.075  
20mm BELOW : ± 0.05  
ANGLES : ± 1°  
FLATNESS : ▱ 0.20  
CONCENTRICITY : ± 0.05

framework

DESIGNED BY  
DRAWN BY  
CHECKED BY  
APPROVED BY  
ISSUED DATE  
SCALE  
UNIT  
DRAWING FILE NAME  
FW\_SSD\_PCB\_REF\_DESIGN  
SIZE  
A2  
SHEET  
2 OF 2  
REVISION  
0  
VERSION  
0