Compilation Time Using Vivado HLS Based Design Method

Legend:
- Compute Kernel Synthesis
- IP Core Generation
- Accelerator Implementation
- Software Compilation

Y-axis: Compilation Time(s), ranging from 0 to 5000

X-axis categories: MM-Small, MM-Medium, MM-Large, FIR-Small, FIR-Medium, FIR-Large, Sobel-Small, Sobel-Medium, Sobel-Large, Kmean-Small, Kmean-Medium, Kmean-Large