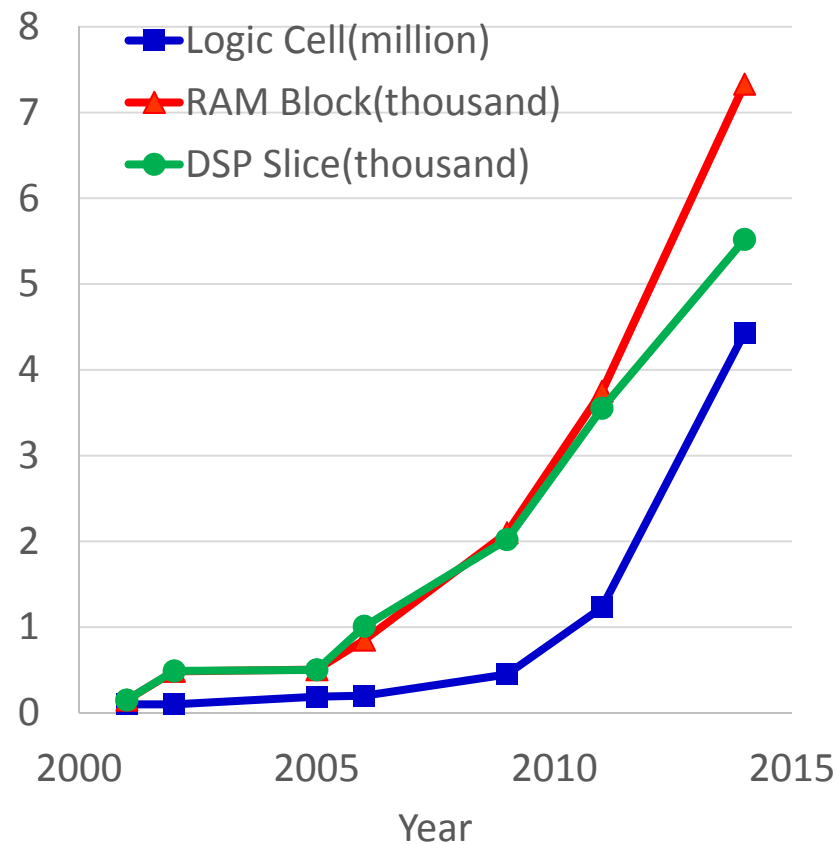


Typical FPGA Architecture



Maximum FPGA Resources of Xilinx FPGA