

Original code

```
#define N 10000  
...  
...  
//Compute kernel  
for(i=0; i<N; i++){  
    c[i] = a[i] x b[i]  
}  
...
```

Compute kernel

```
// Block Size: B  
#define N 10000  
#define B 10  
  
for(i=0; i<N/B; i++){  
    To_FPGA(a[B], b[B])  
    Block_Execution();  
    To_Main_Mem(c[B])  
}
```

Block

```
// Unrolling factor: 2  
#define B 10  
#define U 2  
  
for(i=0; i<B/U; i++){  
    DFG_Execution();  
}
```

DFG

```
#define U 2  
  
for(i=0; i<U; i++){  
    c[i] = a[i] x b[i]  
}
```