

	Parameters	Supported?
Compiling options	Loop grouping	Y
	Loop unrolling	Y
Overlay architecture parameters	SCGRA Size (Row * Col)	Y
	SCGRA topology	Torus
	Input buffer depth	Y
	Output buffer depth	Y
	Input address buffer depth	Y
	Output address buffer depth	Y
	Data memory depth	Y
	Instruction memory depth	Y
	Pipeline depth	Fixed
	Operation set	As needed