Compute Kernel Performance Using Both Design Methods

Legend: ■ Vivado HLS - 2k Buffer ■ Vivado HLS - Max Buffer ■ SCGRA 2x2 ■ SCGRA 5x5

Y-axis: Kernel Speedup Over Performance of Vivado HLS -2k -Buffer

X-axis categories: MM-Small, MM-Medium, MM-Large, FIR-Small, FIR-Medium, FIR-Large, Sobel-Small, Sobel-Medium, Sobel-Large, Kmean-Small, Kmean-Medium, Kmean-Large