```
Original code
                             Compute kernel
#define N 10000
                          // Block Size: B
                                                               Block
                          #define N 10000
                                                       // Unrolling factor: 2
                          #define B 10
                                                                                           DFG
                                                       #define B 10
//Compute kernel
                                                       #define U 2
                          for(i=0; i<N/B; i++){
                                                                                    #define U 2
for(i=0; i<N; i++){
                            To FPGA(a[B], b[B])
  c[i] = a[i] \times b[i]
                                                       for(i=0; i < B/U; i++){
                             Block Execution():
                                                                                   for(i=0; i<U; i++){
                                                          DFG Execution():
                            To Main Mem(c[B])
                                                                                      c[i] = a[i] \times b[i]
```