

Applications

BFS

SSSP

PageRank

...

CC

Runtime & Scheduler

Processing
Components

Optimizations

Parallelism

Memory

Energy

Hardware
Implementation

ASIC

PIM

FPGA

Programming
Model

Vertex-centric

Edge-centric

Hybrid Model

Preprocessing
Components

Graph Compression

Graph Reordering

Graph Partitioning

Evaluation & Testing

Customized Hardware Platforms (ASIC, FPGA, HMC, ReRAM, Flash...)