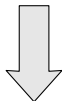
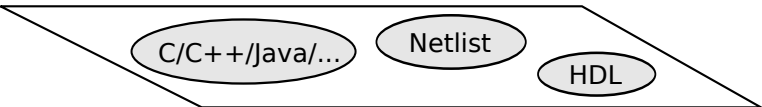


User application



Application compilation

Overlay



Overlay mapping

FPGA

