

ADD4S - Addition for packed BCD strings (vxx)
BRKCS - Break with Context Switch (V25,V35)
BRKEM - Break for Emulation (v20,v30)
BRKN - Break to Native Mode (V25/V35)
BRKS - Break to Security Mode (V25/V35)
BRKXA - Break to Expansion Address (v33)
BTCLR - Bit Test, If it True Clear and Branch (v25,v35)
CALLN - Call Native Mode Routine (v20,v30)
CLEAR1 - Clear one bit (vxx)
CMP4S - Compare for packed BCD strings (vxx)
EXT - Extract Bit Field (vxx)
FINT - Finished Interrupt (v25,v35)
FPO2 - Floating Point Operations 2nd Way (vxx)
INS - Insert Bit String (vxx)
MOVSPA - Move Stack Pointer After Bank Switched (v25,v35)
NOT1 - Invert a Specified bit (vxx)
REPC - Repeat While Carry Flag (vxx)
REPNC - Repeat While Not Carry Flag (vxx)
RETRBI - Return from Register Bank Context (v25,v35)
RETXA - Return from Expansion Address (v33)
ROL4 - Rotate left 4 bits (vxx)
ROR4 - Rotate right 4 bits (vxx)
SET1 - Set a Specified Bit (vxx)
STOP - Stop CPU (v25,v35)
TEST1 - Test a Specified bit (vxx)
TSKSW - Task Switch (v25,v35)