

Instruction processing time

The following table shows instruction processing time of each instruction.

Because the instruction processing time slightly varies depending on the contents of the source and destination, refer to the values in the table as a reference. Since the version supporting the SFC program, the processing time of the some instructions will become longer than the conventional processing time. (☞ Page 1452 Added and Changed Functions)

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
LD	—	1.728	—	1.333	—	1.333	—	3.041	—
LDI	—	1.763	—	1.333	—	1.333	—	3.041	—
AND	—	1.728	—	1.333	—	1.333	—	3.041	—
ANI	—	1.831	—	1.333	—	1.333	—	3.041	—
OR	—	1.764	—	1.333	—	1.333	—	3.041	—
ORI	—	1.831	—	1.333	—	1.333	—	3.041	—
LDP	—	2.062	—	1.767	—	1.759	—	3.628	—
LDF	—	2.035	—	1.767	—	1.759	—	3.628	—
ANDP	—	2.164	—	1.767	—	1.759	—	3.628	—
ANDF	—	2.096	—	1.767	—	1.759	—	3.628	—
ORP	—	2.098	—	1.767	—	1.759	—	3.628	—
ORF	—	2.130	—	1.767	—	1.759	—	3.628	—
LDPI	—	2.131	—	1.759	—	1.759	—	3.628	—
LDFI	—	2.095	—	1.759	—	1.759	—	3.628	—
ANDPI	—	2.092	—	1.759	—	1.759	—	3.628	—
ANDFI	—	2.035	—	1.759	—	1.759	—	3.628	—
ORPI	—	2.100	—	1.759	—	1.759	—	3.628	—
ORFI	—	2.066	—	1.759	—	1.759	—	3.628	—
ANB	—	0.084	—	0.025	—	0.025	—	0.341	—
ORB	—	0.084	—	0.025	—	0.025	—	0.666	—
MPS	—	0.084	—	0.017	—	0.017	—	0.333	—
MRD	—	0.084	—	0.009	—	0.009	—	0.333	—
MPP	—	0.084	—	0.009	—	0.009	—	0.333	—
INV	—	0.092	—	0.017	—	0.017	—	0.333	—
MEP	—	0.100	—	0.042	—	0.042	—	0.700	—
MEF	—	0.100	—	0.050	—	0.050	—	0.700	—
OUT	—	1.334	1.276	1.217	1.217	1.217	1.217	3.023	3.023
OUTH T/ST	—	3.934	3.577	2.973	2.395	2.853	2.361	5.013	4.126
OUT C	—	3.496	3.001	2.875	2.538	2.753	2.446	4.964	4.247
OUT LC	—	3.745	3.764	2.875	2.538	2.753	2.446	4.964	4.247
OUT F	—	25.840	1.548	24.127	0.882	16.448	0.882	19.083	2.136
OUT T/ST	—	4.427	3.852	3.267	2.705	3.160	2.664	5.307	4.426
OUTH S T/ST	—	3.654	3.277	2.960	2.278	2.760	2.211	4.920	3.904
SET	—	1.367	0.110	1.233	0.017	1.233	0.017	3.027	0.674
RST	—	1.367	0.110	1.128	0.017	1.068	0.017	2.721	0.674
SET F	—	1.002	1.006	0.967	0.996	0.959	0.987	2.484	2.198
RST F	—	1.071	1.078	0.967	0.996	0.959	0.987	2.484	2.338
ANS	(n)=K1	4.899	4.633	3.633	3.487	3.416	3.311	6.148	5.887
ANR	—	1.466	0.087	1.359	0.017	1.359	0.017	2.751	0.674
ANRP	—	2.224	0.971	2.224	0.882	2.224	0.882	4.213	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
PLS	—	1.432	1.138	1.199	1.079	1.159	1.064	2.664	2.774
PLF	—	1.383	1.155	1.199	1.079	1.159	1.064	2.664	2.774
FF	—	1.115	0.903	0.969	0.882	0.929	0.870	2.108	2.205
ALT	—	2.564	0.111	2.033	0.017	1.959	0.017	3.995	0.674
ALTP	—	2.284	0.882	2.284	0.882	2.284	0.882	5.457	2.136
SFT	—	4.231	0.087	3.267	0.017	3.241	0.017	4.351	0.674
SFTP	—	4.355	0.970	3.467	0.882	3.385	0.882	4.984	2.136
SFR	(n)=K1	5.366	0.120	4.267	0.017	4.108	0.017	6.431	0.674
	(n)=K15	5.367	0.120	4.267	0.017	4.108	0.017	6.431	0.674
SFRP	(n)=K1	5.132	0.968	5.132	0.882	4.973	0.882	7.893	2.136
	(n)=K15	5.132	0.968	5.132	0.882	4.973	0.882	7.893	2.136
SFL	(n)=K1	5.435	0.124	4.267	0.017	4.108	0.017	6.431	0.674
	(n)=K15	5.431	0.124	4.267	0.017	4.108	0.017	6.431	0.674
SFLP	(n)=K1	5.132	0.970	5.132	0.882	4.973	0.882	7.893	2.136
	(n)=K15	5.132	0.970	5.132	0.882	4.973	0.882	7.893	2.136
BSFR	(n)=K1	4.771	0.091	3.767	0.017	3.508	0.017	6.736	0.674
	(n)=K96	7.890	0.091	6.368	0.017	6.157	0.017	8.297	0.674
BSFRP	(n)=K1	4.996	0.970	4.051	0.882	3.861	0.882	8.198	2.136
	(n)=K96	8.086	0.970	6.616	0.882	6.447	0.882	9.759	2.136
BSFL	(n)=K1	4.631	0.090	3.767	0.017	3.508	0.017	6.736	0.674
	(n)=K96	7.699	0.090	6.368	0.017	6.157	0.017	8.297	0.674
BSFLP	(n)=K1	4.834	0.969	4.051	0.882	3.861	0.882	8.198	2.136
	(n)=K96	7.914	0.969	6.616	0.882	6.447	0.882	9.759	2.136
SFTR	(n1)=K16, (n2)=K1	9.279	0.090	7.324	0.017	7.141	0.017	9.885	0.674
	(n1)=K16, (n2)=K15	9.282	0.090	7.324	0.017	7.141	0.017	9.885	0.674
SFTRP	(n1)=K16, (n2)=K1	9.335	0.970	7.509	0.882	7.319	0.882	9.635	2.136
	(n1)=K16, (n2)=K15	9.383	0.970	7.509	0.882	7.319	0.882	9.635	2.136
SFTL	(n1)=K16, (n2)=K1	9.023	0.090	7.324	0.017	7.141	0.017	9.885	0.674
	(n1)=K16, (n2)=K15	9.022	0.090	7.324	0.017	7.141	0.017	9.885	0.674
SFTLP	(n1)=K16, (n2)=K1	8.975	0.971	7.509	0.882	7.319	0.882	9.635	2.136
	(n1)=K16, (n2)=K15	8.970	0.971	7.509	0.882	7.319	0.882	9.635	2.136
DSFR	(n)=K1	3.766	0.090	3.067	0.017	2.925	0.017	4.967	0.674
	(n)=K96	8.022	0.090	6.901	0.017	6.743	0.017	8.881	0.674
DSFRP	(n)=K1	3.904	0.968	3.287	0.882	3.143	0.882	5.753	2.136
	(n)=K96	8.075	0.968	7.163	0.882	6.959	0.882	9.500	2.136
DSFL	(n)=K1	3.099	0.091	3.067	0.017	2.925	0.017	4.967	0.674
	(n)=K96	7.224	0.091	6.901	0.017	6.743	0.017	8.881	0.674
DSFLP	(n)=K1	3.304	0.970	3.287	0.882	3.143	0.882	5.753	2.136
	(n)=K96	7.375	0.970	7.163	0.882	6.959	0.882	9.500	2.136

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		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
WSFR	(n1)=K16, (n2)=K1	9.176	0.087	7.696	0.017	7.417	0.017	10.208	0.674
	(n1)=K16, (n2)=K15	9.154	0.087	7.696	0.017	7.417	0.017	10.208	0.674
WSFRP	(n1)=K16, (n2)=K1	9.218	0.970	7.860	0.882	7.617	0.882	10.097	2.136
	(n1)=K16, (n2)=K15	9.227	0.970	7.860	0.882	7.617	0.882	10.097	2.136
WSFL	(n1)=K16, (n2)=K1	8.822	0.087	7.696	0.017	7.417	0.017	10.208	0.674
	(n1)=K16, (n2)=K15	8.820	0.087	7.696	0.017	7.417	0.017	10.208	0.674
WSFLP	(n1)=K16, (n2)=K1	8.867	0.970	7.860	0.882	7.617	0.882	10.097	2.136
	(n1)=K16, (n2)=K15	8.875	0.970	7.860	0.882	7.617	0.882	10.097	2.136
MC	—	3.641	3.600	2.800	2.716	2.800	2.716	4.483	4.566
MCR	—	1.950	1.958	1.591	1.600	1.591	1.600	2.941	2.950
FEND	—	0.250	—	0.250	—	0.250	—	0.250	—
END	—	0.250	—	0.250	—	0.250	—	0.250	—
STOP	—	0.991	0.091	0.925	0.017	0.925	0.017	1.566	0.674
NOP	—	0.000	—	0.000	—	0.000	—	0.000	—
LD=	—	3.796	—	2.867	—	2.751	—	4.943	—
LD<>	—	3.802	—	2.867	—	2.751	—	4.943	—
LD>	—	3.763	—	2.867	—	2.751	—	4.943	—
LD<=	—	3.766	—	2.867	—	2.751	—	4.943	—
LD<	—	3.763	—	2.867	—	2.751	—	4.943	—
LD>=	—	3.764	—	2.867	—	2.751	—	4.943	—
AND=	—	3.864	—	2.867	—	2.751	—	4.943	—
AND<>	—	3.830	—	2.867	—	2.751	—	4.943	—
AND>	—	3.831	—	2.867	—	2.751	—	4.943	—
AND<=	—	3.798	—	2.867	—	2.751	—	4.943	—
AND<	—	3.800	—	2.867	—	2.751	—	4.943	—
AND>=	—	3.830	—	2.867	—	2.751	—	4.943	—
OR=	—	3.863	—	2.867	—	2.751	—	4.943	—
OR<>	—	3.831	—	2.867	—	2.751	—	4.943	—
OR>	—	3.835	—	2.867	—	2.751	—	4.943	—
OR<=	—	3.796	—	2.867	—	2.751	—	4.943	—
OR<	—	3.835	—	2.867	—	2.751	—	4.943	—
OR>=	—	3.834	—	2.867	—	2.751	—	4.943	—
CMP	—	3.931	0.090	3.300	0.017	3.108	0.017	5.405	0.674
CMPP	—	4.107	0.969	3.469	0.882	3.304	0.882	6.091	2.136
LD=_U	—	3.796	—	2.867	—	2.751	—	4.943	—
LD<>_U	—	3.796	—	2.867	—	2.751	—	4.943	—
LD>_U	—	3.830	—	2.867	—	2.751	—	4.943	—
LD<=_U	—	3.796	—	2.867	—	2.751	—	4.943	—
LD<_U	—	3.830	—	2.867	—	2.751	—	4.943	—
LD>=_U	—	3.831	—	2.867	—	2.751	—	4.943	—
AND=_U	—	3.896	—	2.867	—	2.751	—	4.943	—

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
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		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
AND<>_U	—	3.831	—	2.867	—	2.751	—	4.943	—
AND>_U	—	3.896	—	2.867	—	2.751	—	4.943	—
AND<= _U	—	3.838	—	2.867	—	2.751	—	4.943	—
AND<_U	—	3.864	—	2.867	—	2.751	—	4.943	—
AND>= _U	—	3.863	—	2.867	—	2.751	—	4.943	—
OR=_U	—	3.896	—	2.867	—	2.751	—	4.943	—
OR<>_U	—	3.870	—	2.867	—	2.751	—	4.943	—
OR>_U	—	3.864	—	2.867	—	2.751	—	4.943	—
OR<= _U	—	3.831	—	2.867	—	2.751	—	4.943	—
OR<_U	—	3.899	—	2.867	—	2.751	—	4.943	—
OR>= _U	—	3.896	—	2.867	—	2.751	—	4.943	—
CMP_U	—	3.898	0.087	3.300	0.017	3.108	0.017	5.405	0.674
CMPP_U	—	4.076	0.969	3.469	0.882	3.304	0.882	6.091	2.136
LDD=	—	4.198	—	3.244	—	3.084	—	4.929	—
LDD<>	—	4.230	—	3.244	—	3.084	—	4.929	—
LDD>	—	4.203	—	3.244	—	3.084	—	4.929	—
LDD<=	—	4.196	—	3.244	—	3.084	—	4.929	—
LDD<	—	4.203	—	3.244	—	3.084	—	4.929	—
LDD>=	—	4.199	—	3.244	—	3.084	—	4.929	—
ANDD=	—	4.296	—	3.244	—	3.084	—	4.929	—
ANDD<>	—	4.264	—	3.244	—	3.084	—	4.929	—
ANDD>	—	4.264	—	3.244	—	3.084	—	4.929	—
ANDD<=	—	4.235	—	3.244	—	3.084	—	4.929	—
ANDD<	—	4.230	—	3.244	—	3.084	—	4.929	—
ANDD>=	—	4.264	—	3.244	—	3.084	—	4.929	—
ORD=	—	4.298	—	3.244	—	3.084	—	4.929	—
ORD<>	—	4.263	—	3.244	—	3.084	—	4.929	—
ORD>	—	4.264	—	3.244	—	3.084	—	4.929	—
ORD<=	—	4.235	—	3.244	—	3.084	—	4.929	—
ORD<	—	4.264	—	3.244	—	3.084	—	4.929	—
ORD>=	—	4.264	—	3.244	—	3.084	—	4.929	—
DCMP	—	3.967	0.087	3.333	0.017	3.133	0.017	5.420	0.674
DCMPP	—	4.100	0.970	3.547	0.882	3.341	0.882	6.109	2.136
LDD=_U	—	4.230	—	3.244	—	3.084	—	4.929	—
LDD<>_U	—	4.230	—	3.244	—	3.084	—	4.929	—
LDD>_U	—	4.263	—	3.244	—	3.084	—	4.929	—
LDD<= _U	—	4.232	—	3.244	—	3.084	—	4.929	—
LDD<_U	—	4.263	—	3.244	—	3.084	—	4.929	—
LDD>= _U	—	4.270	—	3.244	—	3.084	—	4.929	—
ANDD=_U	—	4.330	—	3.244	—	3.084	—	4.929	—
ANDD<>_U	—	4.264	—	3.244	—	3.084	—	4.929	—
ANDD>_U	—	4.330	—	3.244	—	3.084	—	4.929	—
ANDD<= _U	—	4.296	—	3.244	—	3.084	—	4.929	—
ANDD<_U	—	4.298	—	3.244	—	3.084	—	4.929	—
ANDD>= _U	—	4.296	—	3.244	—	3.084	—	4.929	—
ORD=_U	—	4.330	—	3.244	—	3.084	—	4.929	—
ORD<>_U	—	4.298	—	3.244	—	3.084	—	4.929	—

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		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ORD>_U	—	4.300	—	3.244	—	3.084	—	4.929	—
ORD<=_U	—	4.298	—	3.244	—	3.084	—	4.929	—
ORD<_U	—	4.335	—	3.244	—	3.084	—	4.929	—
ORD>=_U	—	4.330	—	3.244	—	3.084	—	4.929	—
DCMP_U	—	3.931	0.087	3.333	0.017	3.133	0.017	5.420	0.674
DCMPP_U	—	4.112	0.972	3.547	0.882	3.341	0.882	6.109	2.136
ZCP	—	4.199	0.087	3.633	0.017	3.367	0.017	5.916	0.674
ZCPP	—	4.356	0.968	3.805	0.882	3.555	0.882	7.378	2.136
ZCP_U	—	4.202	0.087	3.633	0.017	3.367	0.017	5.916	0.674
ZCPP_U	—	4.366	0.970	3.805	0.882	3.555	0.882	7.378	2.136
DZCP	—	4.266	0.087	3.633	0.017	3.367	0.017	5.916	0.674
DZCOP	—	4.460	0.972	3.805	0.882	3.555	0.882	7.378	2.136
DZCP_U	—	4.231	0.087	3.633	0.017	3.367	0.017	5.916	0.674
DZCOP_U	—	4.403	0.971	3.805	0.882	3.555	0.882	7.378	2.136
BKCMP=	(n)=K1	8.419	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.559	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP<>	(n)=K1	8.450	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.566	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP>	(n)=K1	8.418	0.089	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.536	0.089	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP<=	(n)=K1	8.451	0.088	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.558	0.088	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP<	(n)=K1	8.423	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.526	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP>=	(n)=K1	8.418	0.089	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.567	0.089	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP=P	(n)=K1	8.440	0.971	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.963	0.971	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<>P	(n)=K1	8.454	0.971	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.908	0.971	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP>P	(n)=K1	8.503	0.971	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	39.271	0.971	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<=P	(n)=K1	8.455	0.970	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.908	0.970	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<P	(n)=K1	8.439	0.970	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.992	0.970	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP>=P	(n)=K1	8.504	0.972	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.915	0.972	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP=_U	(n)=K1	8.416	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.498	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP<_U	(n)=K1	8.418	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.500	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP>_U	(n)=K1	8.452	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.532	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP<=_U	(n)=K1	8.451	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.528	0.087	24.760	0.017	24.332	0.017	27.296	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
BKCMP<_U	(n)=K1	8.464	0.089	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.531	0.089	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP>=_U	(n)=K1	8.450	0.087	6.928	0.017	6.501	0.017	9.473	0.674
	(n)=K96	39.526	0.087	24.760	0.017	24.332	0.017	27.296	0.674
BKCMP=P_U	(n)=K1	8.428	0.970	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.747	0.970	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<>P_U	(n)=K1	8.431	0.971	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.851	0.971	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP>P_U	(n)=K1	8.483	0.970	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.739	0.970	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<=P_U	(n)=K1	8.447	0.973	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	39.140	0.973	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP<P_U	(n)=K1	8.456	0.972	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	38.830	0.972	24.900	0.882	24.456	0.882	28.758	2.136
BKCMP>=P_U	(n)=K1	8.450	0.971	7.112	0.882	6.687	0.882	10.935	2.136
	(n)=K96	39.032	0.971	24.900	0.882	24.456	0.882	28.758	2.136
DBKCMP=	(n)=K1	8.418	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.634	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP<>	(n)=K1	8.418	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.631	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP>	(n)=K1	8.416	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.596	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP<=	(n)=K1	8.418	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.592	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP<	(n)=K1	8.419	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.567	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP>=	(n)=K1	8.420	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	45.592	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP=P	(n)=K1	8.424	0.971	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	44.783	0.971	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<>P	(n)=K1	8.508	0.972	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	44.823	0.972	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP>P	(n)=K1	8.424	0.971	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	44.788	0.971	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<=P	(n)=K1	8.495	0.970	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	44.722	0.970	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<P	(n)=K1	8.454	0.973	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	44.755	0.973	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP>=P	(n)=K1	8.439	0.972	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.972	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP=_U	(n)=K1	8.487	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP<>_U	(n)=K1	8.487	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP>_U	(n)=K1	8.518	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DBKCMP<_U	(n)=K1	8.519	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP<_U	(n)=K1	8.519	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP>=_U	(n)=K1	8.518	0.087	6.935	0.017	6.492	0.017	9.464	0.674
	(n)=K96	29.212	0.087	29.212	0.017	28.435	0.017	31.341	0.674
DBKCMP=P_U	(n)=K1	8.492	0.970	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.970	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<>P_U	(n)=K1	8.560	0.970	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.970	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP>P_U	(n)=K1	8.538	0.971	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.971	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<=P_U	(n)=K1	8.535	0.972	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.972	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP<P_U	(n)=K1	8.543	0.971	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.971	30.077	0.882	29.897	0.882	32.803	2.136
DBKCMP>=P_U	(n)=K1	8.535	0.971	7.119	0.882	6.684	0.882	10.926	2.136
	(n)=K96	30.077	0.971	30.077	0.882	29.897	0.882	32.803	2.136
+ (2 operands)	—	4.831	0.092	3.700	0.017	3.492	0.017	5.363	0.674
+P (2 operands)	—	4.565	0.969	4.565	0.882	3.683	0.882	6.067	2.136
+ (3 operands)	—	4.832	0.120	3.700	0.017	3.492	0.017	5.363	0.674
+P (3 operands)	—	4.565	0.969	4.565	0.882	3.683	0.882	6.067	2.136
+_U (2 operands)	—	4.831	0.120	3.700	0.017	3.492	0.017	5.363	0.674
+P_U (2 operands)	—	4.565	0.972	4.565	0.882	3.683	0.882	6.067	2.136
+_U (3 operands)	—	4.836	0.122	3.700	0.017	3.492	0.017	5.363	0.674
+P_U (3 operands)	—	4.565	0.972	4.565	0.882	3.683	0.882	6.067	2.136
ADD	—	3.298	0.087	2.900	0.017	2.733	0.017	4.157	0.674
ADDP	—	3.468	0.970	3.065	0.882	2.920	0.882	4.883	2.136
ADD_U	—	3.331	0.087	2.900	0.017	2.733	0.017	4.157	0.674
ADDP_U	—	3.467	0.969	3.065	0.882	2.920	0.882	4.883	2.136
- (2 operands)	—	4.768	0.092	3.700	0.017	3.492	0.017	5.363	0.674
-P (2 operands)	—	4.565	0.970	4.565	0.882	3.683	0.882	6.067	2.136
- (3 operands)	—	4.764	0.120	3.700	0.017	3.492	0.017	5.363	0.674
-P (3 operands)	—	4.565	0.970	4.565	0.882	3.683	0.882	6.067	2.136
-_U (2 operands)	—	4.764	0.120	3.700	0.017	3.492	0.017	5.363	0.674
-P_U (2 operands)	—	4.565	0.971	4.565	0.882	3.683	0.882	6.067	2.136
-_U (3 operands)	—	4.764	0.120	3.700	0.017	3.492	0.017	5.363	0.674
-P_U (3 operands)	—	4.565	0.971	4.565	0.882	3.683	0.882	6.067	2.136
SUB	—	3.398	0.087	2.900	0.017	2.733	0.017	4.157	0.674
SUBP	—	3.600	0.970	3.065	0.882	2.920	0.882	4.883	2.136
SUB_U	—	3.364	0.087	2.900	0.017	2.733	0.017	4.157	0.674
SUBP_U	—	3.559	0.970	3.065	0.882	2.920	0.882	4.883	2.136
D+ (2 operands)	—	5.511	0.092	4.233	0.017	3.967	0.017	6.553	0.674
D+P (2 operands)	—	5.098	0.966	5.098	0.882	4.152	0.882	8.015	2.136
D+ (3 operands)	—	5.503	0.117	4.233	0.017	3.967	0.017	6.553	0.674
D+P (3 operands)	—	5.098	0.970	5.098	0.882	4.152	0.882	8.015	2.136
D+_U (2 operands)	—	5.498	0.117	4.233	0.017	3.967	0.017	6.553	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
D+P_U (2 operands)	—	5.098	0.971	5.098	0.882	4.152	0.882	8.015	2.136
D+_U (3 operands)	—	5.498	0.117	4.233	0.017	3.967	0.017	6.553	0.674
D+P_U (3 operands)	—	5.098	0.970	5.098	0.882	4.152	0.882	8.015	2.136
DADD	—	3.331	0.087	2.933	0.017	2.749	0.017	4.875	0.674
DADDP	—	3.488	0.970	3.112	0.882	2.931	0.882	6.337	2.136
DADD_U	—	3.364	0.087	2.933	0.017	2.749	0.017	4.875	0.674
DADDP_U	—	3.495	0.970	3.112	0.882	2.931	0.882	6.337	2.136
D- (2 operands)	—	5.464	0.092	4.233	0.017	3.967	0.017	6.553	0.674
D-P (2 operands)	—	5.098	0.973	5.098	0.882	4.152	0.882	8.015	2.136
D- (3 operands)	—	5.467	0.117	4.233	0.017	3.967	0.017	6.553	0.674
D-P (3 operands)	—	5.098	0.970	5.098	0.882	4.152	0.882	8.015	2.136
D-_U (2 operands)	—	5.464	0.117	4.233	0.017	3.967	0.017	6.553	0.674
D-P_U (2 operands)	—	5.098	0.972	5.098	0.882	4.152	0.882	8.015	2.136
D-_U (3 operands)	—	5.464	0.117	4.233	0.017	3.967	0.017	6.553	0.674
D-P_U (3 operands)	—	5.098	0.968	5.098	0.882	4.152	0.882	8.015	2.136
DSUB	—	3.431	0.087	2.933	0.017	2.749	0.017	4.875	0.674
DSUBP	—	3.632	0.971	3.112	0.882	2.931	0.882	6.337	2.136
DSUB_U	—	3.364	0.087	2.933	0.017	2.749	0.017	4.875	0.674
DSUBP_U	—	3.510	0.972	3.112	0.882	2.931	0.882	6.337	2.136
*	—	5.344	0.084	3.967	0.017	3.749	0.017	5.616	0.674
*P	—	4.832	0.973	4.832	0.882	3.939	0.882	6.307	2.136
*_U	—	3.967	0.084	3.967	0.017	3.749	0.017	5.616	0.674
*P_U	—	4.832	0.882	4.832	0.882	3.939	0.882	6.307	2.136
MUL	—	4.399	0.087	3.300	0.017	3.067	0.017	4.933	0.674
MULP	—	4.496	0.970	3.511	0.882	3.263	0.882	5.649	2.136
MUL_U	—	4.398	0.087	3.300	0.017	3.067	0.017	4.933	0.674
MULP_U	—	4.482	0.970	3.511	0.882	3.263	0.882	5.649	2.136
/	—	4.431	0.092	3.827	0.017	3.827	0.017	5.735	0.674
/P	—	4.692	0.972	4.692	0.882	4.692	0.882	7.197	2.136
/_U	—	4.331	0.087	3.827	0.017	3.827	0.017	5.735	0.674
/P_U	—	4.692	0.971	4.692	0.882	4.692	0.882	7.197	2.136
DIV	—	4.598	0.087	3.503	0.017	3.275	0.017	5.841	0.674
DIVP	—	4.692	0.972	3.660	0.882	3.456	0.882	7.303	2.136
DIV_U	—	4.531	0.087	3.503	0.017	3.275	0.017	5.841	0.674
DIVP_U	—	4.660	0.970	3.660	0.882	3.456	0.882	7.303	2.136
D*	—	5.098	0.092	4.333	0.017	4.333	0.017	6.236	0.674
D*P	—	5.228	0.973	5.198	0.882	5.198	0.882	7.698	2.136
D*_U	—	5.100	0.087	4.333	0.017	4.333	0.017	6.236	0.674
D*P_U	—	5.224	0.972	5.198	0.882	5.198	0.882	7.698	2.136
DMUL	—	5.198	0.087	4.067	0.017	3.741	0.017	6.320	0.674
DMULP	—	5.368	0.970	4.267	0.882	3.928	0.882	7.782	2.136
DMUL_U	—	5.198	0.087	4.067	0.017	3.741	0.017	6.320	0.674
DMULP_U	—	5.368	0.973	4.267	0.882	3.928	0.882	7.782	2.136
D/	—	4.998	0.092	4.413	0.017	4.413	0.017	6.432	0.674
D/P	—	5.278	0.969	5.278	0.882	5.278	0.882	7.894	2.136
D/_U	—	4.999	0.084	4.413	0.017	4.413	0.017	6.432	0.674
D/P_U	—	5.278	0.972	5.278	0.882	5.278	0.882	7.894	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DDIV	—	5.266	0.084	4.133	0.017	3.916	0.017	6.565	0.674
DDIVP	—	5.435	0.972	4.335	0.882	4.108	0.882	8.027	2.136
DDIV_U	—	5.131	0.084	4.133	0.017	3.916	0.017	6.565	0.674
DDIVP_U	—	5.295	0.970	4.335	0.882	4.108	0.882	8.027	2.136
B+ (2 operands)	—	5.064	0.088	4.567	0.017	4.300	0.017	6.012	0.674
B+P (2 operands)	—	5.202	0.972	4.737	0.882	4.513	0.882	7.474	2.136
B+ (3 operands)	—	4.567	0.087	4.567	0.017	4.300	0.017	6.012	0.674
B+P (3 operands)	—	4.737	0.968	4.737	0.882	4.513	0.882	7.474	2.136
B- (2 operands)	—	5.066	0.087	4.567	0.017	4.300	0.017	6.012	0.674
B-P (2 operands)	—	5.228	0.971	4.737	0.882	4.513	0.882	7.474	2.136
B- (3 operands)	—	4.567	0.087	4.567	0.017	4.300	0.017	6.012	0.674
B-P (3 operands)	—	4.737	0.969	4.737	0.882	4.513	0.882	7.474	2.136
DB+ (2 operands)	—	5.899	0.087	5.400	0.017	5.092	0.017	7.565	0.674
DB+P (2 operands)	—	6.067	0.971	5.541	0.882	5.301	0.882	9.027	2.136
DB+ (3 operands)	—	5.400	0.087	5.400	0.017	5.092	0.017	7.565	0.674
DB+P (3 operands)	—	5.541	0.970	5.541	0.882	5.301	0.882	9.027	2.136
DB- (2 operands)	—	5.903	0.087	5.400	0.017	5.092	0.017	7.565	0.674
DB-P (2 operands)	—	6.087	0.973	5.541	0.882	5.301	0.882	9.027	2.136
DB- (3 operands)	—	5.400	0.087	5.400	0.017	5.092	0.017	7.565	0.674
DB-P (3 operands)	—	5.541	0.971	5.541	0.882	5.301	0.882	9.027	2.136
B*	—	5.998	0.087	4.733	0.017	4.533	0.017	6.481	0.674
B*P	—	6.135	0.970	4.909	0.882	4.731	0.882	7.112	2.136
B/	—	6.300	0.087	4.879	0.017	4.684	0.017	7.332	0.674
B/P	—	6.479	0.968	5.071	0.882	4.889	0.882	8.794	2.136
DB*	—	6.664	0.087	5.300	0.017	4.992	0.017	7.641	0.674
DB*P	—	6.802	0.969	5.427	0.882	5.180	0.882	9.103	2.136
DB/	—	8.032	0.084	6.568	0.017	6.351	0.017	9.015	0.674
DB/P	—	8.159	0.971	6.753	0.882	6.523	0.882	10.477	2.136
BK+	(n)=K1	8.126	0.087	6.800	0.017	6.367	0.017	8.639	0.674
	(n)=K96	20.734	0.087	17.131	0.017	16.641	0.017	18.900	0.674
BK+P	(n)=K1	8.230	0.971	6.959	0.882	6.569	0.882	9.261	2.136
	(n)=K96	20.584	0.971	17.201	0.882	16.851	0.882	19.629	2.136
BK+_U	(n)=K1	8.032	0.088	6.800	0.017	6.367	0.017	8.639	0.674
	(n)=K96	20.626	0.088	17.131	0.017	16.641	0.017	18.900	0.674
BK+P_U	(n)=K1	8.135	0.970	6.959	0.882	6.569	0.882	9.261	2.136
	(n)=K96	20.440	0.970	17.201	0.882	16.851	0.882	19.629	2.136
BK-	(n)=K1	8.094	0.087	6.800	0.017	6.367	0.017	8.639	0.674
	(n)=K96	19.903	0.087	17.131	0.017	16.641	0.017	18.900	0.674
BK-P	(n)=K1	8.184	0.969	6.959	0.882	6.569	0.882	9.261	2.136
	(n)=K96	19.812	0.969	17.201	0.882	16.851	0.882	19.629	2.136
BK_-U	(n)=K1	8.055	0.088	6.800	0.017	6.367	0.017	8.639	0.674
	(n)=K96	19.830	0.088	17.131	0.017	16.641	0.017	18.900	0.674
BK-P_U	(n)=K1	8.186	0.971	6.959	0.882	6.569	0.882	9.261	2.136
	(n)=K96	19.820	0.971	17.201	0.882	16.851	0.882	19.629	2.136
DBK+	(n)=K1	8.258	0.087	6.801	0.017	6.367	0.017	9.339	0.674
	(n)=K96	25.666	0.087	21.976	0.017	21.223	0.017	24.096	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DBK+P	(n)=K1	8.360	0.972	6.985	0.882	6.571	0.882	10.801	2.136
	(n)=K96	25.555	0.972	21.996	0.882	21.348	0.882	25.558	2.136
DBK+_U	(n)=K1	8.251	0.087	6.801	0.017	6.367	0.017	9.339	0.674
	(n)=K96	25.631	0.087	21.976	0.017	21.223	0.017	24.096	0.674
DBK+P_U	(n)=K1	8.275	0.972	6.985	0.882	6.571	0.882	10.801	2.136
	(n)=K96	25.539	0.972	21.996	0.882	21.348	0.882	25.558	2.136
DBK-	(n)=K1	8.251	0.087	6.801	0.017	6.367	0.017	9.339	0.674
	(n)=K96	24.795	0.087	21.976	0.017	21.223	0.017	24.096	0.674
DBK-P	(n)=K1	8.338	0.971	6.985	0.882	6.571	0.882	10.801	2.136
	(n)=K96	24.595	0.971	21.996	0.882	21.348	0.882	25.558	2.136
DBK-_U	(n)=K1	8.124	0.087	6.801	0.017	6.367	0.017	9.339	0.674
	(n)=K96	24.732	0.087	21.976	0.017	21.223	0.017	24.096	0.674
DBK-P_U	(n)=K1	8.224	0.972	6.985	0.882	6.571	0.882	10.801	2.136
	(n)=K96	24.560	0.972	21.996	0.882	21.348	0.882	25.558	2.136
INC	—	4.631	0.110	3.533	0.017	3.400	0.017	4.875	0.674
INCP	—	4.398	0.970	4.398	0.882	3.560	0.882	5.547	2.136
INC_U	—	4.632	0.110	3.533	0.017	3.400	0.017	4.875	0.674
INCP_U	—	4.398	0.971	4.398	0.882	3.560	0.882	5.547	2.136
DEC	—	4.634	0.111	3.533	0.017	3.400	0.017	4.875	0.674
DECP	—	4.398	0.968	4.398	0.882	3.560	0.882	5.547	2.136
DEC_U	—	4.631	0.110	3.533	0.017	3.400	0.017	4.875	0.674
DECP_U	—	4.398	0.973	4.398	0.882	3.560	0.882	5.547	2.136
DINC	—	5.098	0.107	3.900	0.017	3.716	0.017	5.901	0.674
DINCP	—	4.765	0.967	4.765	0.882	3.900	0.882	7.363	2.136
DINC_U	—	5.099	0.107	3.900	0.017	3.716	0.017	5.901	0.674
DINCP_U	—	4.765	0.969	4.765	0.882	3.900	0.882	7.363	2.136
DDEC	—	5.131	0.107	3.900	0.017	3.716	0.017	5.901	0.674
DDECP	—	4.765	0.970	4.765	0.882	3.900	0.882	7.363	2.136
DDEC_U	—	5.131	0.107	3.900	0.017	3.716	0.017	5.901	0.674
DDECP_U	—	4.765	0.970	4.765	0.882	3.900	0.882	7.363	2.136
WAND (2 operands)	—	4.731	0.092	3.633	0.017	3.467	0.017	5.341	0.674
WANDP (2 operands)	—	4.498	0.970	4.498	0.882	3.659	0.882	6.075	2.136
WAND (3 operands)	—	4.731	0.121	3.633	0.017	3.467	0.017	5.341	0.674
WANDP (3 operands)	—	4.498	0.970	4.498	0.882	3.659	0.882	6.075	2.136
DAND (2 operands)	—	5.398	0.092	4.167	0.017	3.941	0.017	6.541	0.674
DANDP (2 operands)	—	5.032	0.972	5.032	0.882	4.135	0.882	8.003	2.136
DAND (3 operands)	—	5.403	0.117	4.167	0.017	3.941	0.017	6.541	0.674
DANDP (3 operands)	—	5.032	0.969	5.032	0.882	4.135	0.882	8.003	2.136
BKAND	(n)=K1	7.527	0.089	6.433	0.017	6.033	0.017	8.305	0.674
	(n)=K96	20.104	0.089	16.809	0.017	16.403	0.017	18.655	0.674
BKANDP	(n)=K1	7.618	0.973	6.612	0.882	6.228	0.882	8.932	2.136
	(n)=K96	20.135	0.973	16.897	0.882	16.617	0.882	19.172	2.136
WOR (2 operands)	—	4.731	0.120	3.633	0.017	3.467	0.017	5.341	0.674
WORP (2 operands)	—	4.498	0.971	4.498	0.882	3.659	0.882	6.075	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
WOR (3 operands)	—	4.731	0.120	3.633	0.017	3.467	0.017	5.341	0.674
WORP (3 operands)	—	4.498	0.971	4.498	0.882	3.659	0.882	6.075	2.136
DOR (2 operands)	—	5.434	0.117	4.167	0.017	3.941	0.017	6.541	0.674
DORP (2 operands)	—	5.032	0.969	5.032	0.882	4.135	0.882	8.003	2.136
DOR (3 operands)	—	5.434	0.117	4.167	0.017	3.941	0.017	6.541	0.674
DORP (3 operands)	—	5.032	0.972	5.032	0.882	4.135	0.882	8.003	2.136
BKOR	(n)=K1	7.506	0.087	6.433	0.017	5.983	0.017	8.240	0.674
	(n)=K96	20.099	0.087	16.833	0.017	16.293	0.017	18.553	0.674
BKORP	(n)=K1	7.632	0.971	6.656	0.882	6.175	0.882	8.913	2.136
	(n)=K96	19.932	0.971	16.932	0.882	16.387	0.882	19.168	2.136
WXOR (2 operands)	—	4.734	0.123	3.633	0.017	3.467	0.017	5.341	0.674
WXORP (2 operands)	—	4.498	0.970	4.498	0.882	3.659	0.882	6.075	2.136
WXOR (3 operands)	—	4.731	0.124	3.633	0.017	3.467	0.017	5.341	0.674
WXORP (3 operands)	—	4.498	0.973	4.498	0.882	3.659	0.882	6.075	2.136
DXOR (2 operands)	—	5.398	0.121	4.167	0.017	3.941	0.017	6.541	0.674
DXORP (2 operands)	—	5.032	0.972	5.032	0.882	4.135	0.882	8.003	2.136
DXOR (3 operands)	—	5.398	0.120	4.167	0.017	3.941	0.017	6.541	0.674
DXORP (3 operands)	—	5.032	0.973	5.032	0.882	4.135	0.882	8.003	2.136
BKXOR	(n)=K1	7.523	0.088	6.400	0.017	6.016	0.017	8.289	0.674
	(n)=K96	20.099	0.088	16.783	0.017	16.375	0.017	18.645	0.674
BKXORP	(n)=K1	7.627	0.970	6.616	0.882	6.213	0.882	8.936	2.136
	(n)=K96	20.135	0.970	16.915	0.882	16.467	0.882	19.352	2.136
WXNR (2 operands)	—	4.764	0.127	3.667	0.017	3.467	0.017	5.341	0.674
WXNRP (2 operands)	—	4.532	0.973	4.532	0.882	3.659	0.882	6.075	2.136
WXNR (3 operands)	—	4.764	0.127	3.667	0.017	3.467	0.017	5.341	0.674
WXNRP (3 operands)	—	4.532	0.971	4.532	0.882	3.659	0.882	6.075	2.136
DXNR (2 operands)	—	5.431	0.117	4.200	0.017	3.941	0.017	6.541	0.674
DXNRP (2 operands)	—	5.065	0.969	5.065	0.882	4.135	0.882	8.003	2.136
DXNR (3 operands)	—	5.431	0.117	4.200	0.017	3.941	0.017	6.541	0.674
DXNRP (3 operands)	—	5.065	0.971	5.065	0.882	4.135	0.882	8.003	2.136
BKXNR	(n)=K1	7.498	0.087	6.433	0.017	6.000	0.017	8.272	0.674
	(n)=K96	20.900	0.087	16.860	0.017	16.340	0.017	18.609	0.674
BKXNRP	(n)=K1	7.704	0.969	6.645	0.882	6.197	0.882	8.897	2.136
	(n)=K96	20.747	0.969	16.928	0.882	16.495	0.882	19.297	2.136
BSET	(n)=K1	5.167	0.124	4.033	0.017	3.867	0.017	6.135	0.674
	(n)=K15	5.167	0.124	4.033	0.017	3.867	0.017	6.135	0.674
BSETP	(n)=K1	4.898	0.972	4.898	0.882	4.732	0.882	7.597	2.136
	(n)=K15	4.898	0.972	4.898	0.882	4.732	0.882	7.597	2.136
BRST	(n)=K1	5.200	0.124	4.033	0.017	3.867	0.017	6.135	0.674
	(n)=K15	5.200	0.124	4.033	0.017	3.867	0.017	6.135	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
BRSTP	(n)=K1	4.898	0.972	4.898	0.882	4.732	0.882	7.597	2.136
	(n)=K15	4.898	0.972	4.898	0.882	4.732	0.882	7.597	2.136
TEST	—	4.664	0.120	3.767	0.017	3.608	0.017	5.467	0.674
TESTP	—	4.632	0.970	4.632	0.882	3.792	0.882	6.177	2.136
DTEST	—	4.898	0.124	3.767	0.017	3.608	0.017	5.467	0.674
DTESTP	—	4.632	0.970	4.632	0.882	3.792	0.882	6.177	2.136
BKRST	(n)=K1	3.230	0.087	2.567	0.017	2.408	0.017	3.749	0.674
	(n)=K96	3.330	0.087	2.700	0.017	2.525	0.017	3.867	0.674
BKRSTP	(n)=K1	3.358	0.972	2.745	0.882	2.611	0.882	4.515	2.136
	(n)=K96	3.495	0.972	2.868	0.882	2.724	0.882	4.571	2.136
BCD	—	3.231	0.092	2.700	0.017	2.556	0.017	3.799	0.674
BCDP	—	3.565	0.969	3.565	0.882	3.421	0.882	4.512	2.136
DBCD	—	3.598	0.092	3.067	0.017	2.931	0.017	4.849	0.674
DBCDP	—	3.932	0.967	3.932	0.882	3.161	0.882	6.311	2.136
BIN	—	2.998	0.092	2.567	0.017	2.381	0.017	3.641	0.674
BINP	—	3.432	0.970	3.432	0.882	2.653	0.882	4.363	2.136
DBIN	—	3.264	0.092	2.833	0.017	2.660	0.017	4.591	0.674
DBINP	—	3.698	0.971	3.698	0.882	2.907	0.882	6.053	2.136
FLT2INT	—	3.198	0.087	3.183	0.017	3.183	0.017	4.659	0.674
FLT2INTP	—	3.404	0.968	3.368	0.882	3.368	0.882	5.360	2.136
FLT2UINT	—	3.231	0.088	3.183	0.017	3.183	0.017	4.659	0.674
FLT2UINTP	—	3.395	0.969	3.368	0.882	3.368	0.882	5.360	2.136
FLT2DINT	—	3.203	0.087	3.183	0.017	3.183	0.017	4.659	0.674
FLT2DINTP	—	3.414	0.970	3.368	0.882	3.368	0.882	5.360	2.136
FLT2UDINT	—	3.264	0.087	3.183	0.017	3.183	0.017	4.659	0.674
FLT2UDINTP	—	3.452	0.970	3.368	0.882	3.368	0.882	5.360	2.136
INT2UINT	—	3.931	0.110	3.183	0.017	3.183	0.017	4.659	0.674
INT2UINTP	—	3.368	0.971	3.368	0.882	3.368	0.882	5.360	2.136
INT2DINT	—	4.264	0.113	3.183	0.017	3.183	0.017	4.659	0.674
INT2DINTP	—	3.368	0.970	3.368	0.882	3.368	0.882	5.360	2.136
INT2UDINT	—	4.232	0.114	3.183	0.017	3.183	0.017	4.659	0.674
INT2UDINTP	—	3.368	0.972	3.368	0.882	3.368	0.882	5.360	2.136
UINT2INT	—	3.998	0.110	3.183	0.017	3.183	0.017	4.659	0.674
UINT2INTP	—	3.368	0.968	3.368	0.882	3.368	0.882	5.360	2.136
UINT2DINT	—	4.264	0.113	3.183	0.017	3.183	0.017	4.659	0.674
UINT2DINTP	—	3.368	0.970	3.368	0.882	3.368	0.882	5.360	2.136
UINT2UDINT	—	4.268	0.114	3.183	0.017	3.183	0.017	4.659	0.674
UINT2UDINTP	—	3.368	0.971	3.368	0.882	3.368	0.882	5.360	2.136
DINT2INT	—	4.298	0.110	3.333	0.017	3.183	0.017	4.659	0.674
DINT2INTP	—	4.198	0.971	4.198	0.882	3.368	0.882	5.360	2.136
DINT2UINT	—	4.298	0.111	3.333	0.017	3.183	0.017	4.659	0.674
DINT2UINTP	—	4.198	0.969	4.198	0.882	3.368	0.882	5.360	2.136
DINT2UDINT	—	4.464	0.111	3.333	0.017	3.183	0.017	4.659	0.674
DINT2UDINTP	—	4.198	0.970	4.198	0.882	3.368	0.882	5.360	2.136
UDINT2INT	—	4.298	0.110	3.333	0.017	3.183	0.017	4.659	0.674
UDINT2INTP	—	4.198	0.972	4.198	0.882	3.368	0.882	5.360	2.136
UDINT2UINT	—	4.298	0.110	3.333	0.017	3.183	0.017	4.659	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
UDINT2UINTP	—	4.198	0.972	4.198	0.882	3.368	0.882	5.360	2.136
UDINT2DINT	—	4.466	0.111	3.333	0.017	3.183	0.017	4.659	0.674
UDINT2DINTP	—	4.198	0.972	4.198	0.882	3.368	0.882	5.360	2.136
GRY	—	2.702	0.087	2.300	0.017	2.167	0.017	3.357	0.674
GRYP	—	2.894	0.969	2.515	0.882	2.369	0.882	4.060	2.137
GRY_U	—	2.698	0.087	2.300	0.017	2.167	0.017	3.357	0.674
GRYP_U	—	2.902	0.970	2.515	0.882	2.369	0.882	4.060	2.137
DGRY	—	2.732	0.087	2.300	0.017	2.184	0.017	4.075	0.674
DGRYP	—	2.854	0.970	2.515	0.882	2.388	0.882	5.537	2.137
DGRY_U	—	2.731	0.087	2.300	0.017	2.184	0.017	4.075	0.674
DGRYP_U	—	2.848	0.973	2.515	0.882	2.388	0.882	5.537	2.137
GBIN	—	4.098	0.087	3.100	0.017	2.959	0.017	4.132	0.674
GBINP	—	4.268	0.969	3.276	0.882	3.163	0.882	4.867	2.136
GBIN_U	—	4.102	0.087	3.100	0.017	2.959	0.017	4.132	0.674
GBINP_U	—	4.270	0.968	3.276	0.882	3.163	0.882	4.867	2.136
DGBIN	—	4.103	0.087	3.100	0.017	2.959	0.017	4.849	0.674
DGBINP	—	4.296	0.973	3.276	0.882	3.163	0.882	6.311	2.136
DGBIN_U	—	4.098	0.087	3.100	0.017	2.959	0.017	4.849	0.674
DGBINP_U	—	4.288	0.970	3.276	0.882	3.163	0.882	6.311	2.136
DABIN	—	4.531	0.088	3.867	0.017	3.716	0.017	5.075	0.674
DABINP	—	4.690	0.970	4.040	0.882	3.907	0.882	5.812	2.136
DABIN_U	—	4.564	0.087	3.867	0.017	3.716	0.017	5.075	0.674
DABINP_U	—	4.707	0.972	4.040	0.882	3.907	0.882	5.812	2.136
DDABIN	—	5.268	0.087	4.633	0.017	4.475	0.017	6.575	0.674
DDABINP	—	5.420	0.969	4.767	0.882	4.651	0.882	8.037	2.136
DDABIN_U	—	5.364	0.087	4.633	0.017	4.475	0.017	6.575	0.674
DDABINP_U	—	5.518	0.969	4.767	0.882	4.651	0.882	8.037	2.136
HEXA	(n)=K1	19.367	0.091	19.367	0.017	19.367	0.017	21.997	0.674
	(n)=K96	34.244	0.091	25.769	0.017	20.085	0.017	22.713	0.674
HEXAP	(n)=K1	20.232	0.970	20.232	0.882	20.232	0.882	22.651	2.136
	(n)=K96	33.674	0.970	26.634	0.882	20.950	0.882	23.407	2.136
VAL	—	6.366	0.091	5.433	0.017	5.208	0.017	7.656	0.674
VALP	—	6.534	0.968	5.640	0.882	5.420	0.882	8.276	2.136
VAL_U	—	6.299	0.090	5.433	0.017	5.208	0.017	7.656	0.674
VALP_U	—	6.466	0.968	5.640	0.882	5.420	0.882	8.276	2.136
DVAL	—	6.536	0.087	5.433	0.017	5.208	0.017	7.743	0.674
DVALP	—	6.710	0.969	5.640	0.882	5.420	0.882	8.361	2.136
DVAL_U	—	6.499	0.088	5.433	0.017	5.208	0.017	7.743	0.674
DVALP_U	—	6.651	0.968	5.640	0.882	5.420	0.882	8.361	2.136
NEG	—	3.064	0.087	2.500	0.017	2.400	0.017	3.591	0.674
NEGP	—	3.231	0.965	2.675	0.882	2.629	0.882	4.320	2.136
DNEG	—	3.131	0.087	2.517	0.017	2.483	0.017	4.375	0.674
DNEGP	—	3.259	0.969	2.729	0.882	2.668	0.882	5.837	2.136
DECO	(n)=K2	4.664	0.087	4.200	0.017	3.975	0.017	6.697	0.674
	(n)=K8	4.664	0.087	4.408	0.017	4.408	0.017	7.831	0.674
DECOP	(n)=K2	4.828	0.971	4.371	0.882	4.164	0.882	7.329	2.136
	(n)=K8	4.835	0.971	4.593	0.882	4.593	0.882	9.293	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ENCO	(n)=K2	3.931	0.091	3.367	0.017	3.241	0.017	5.531	0.674
	(n)=K8	4.636	0.091	4.167	0.017	3.625	0.017	5.904	0.674
ENCOP	(n)=K2	4.232	0.972	4.232	0.882	4.106	0.882	6.993	2.136
	(n)=K8	5.032	0.972	5.032	0.882	3.828	0.882	7.366	2.136
DIS	(n)=K1	3.331	0.087	2.867	0.017	2.749	0.017	4.332	0.674
	(n)=K4	3.668	0.087	3.167	0.017	3.067	0.017	4.649	0.674
DISP	(n)=K1	3.539	0.971	3.028	0.882	2.939	0.882	5.037	2.136
	(n)=K4	3.867	0.971	3.340	0.882	3.256	0.882	5.336	2.136
UNI	(n)=K1	3.531	0.087	3.067	0.017	2.949	0.017	4.532	0.674
	(n)=K4	3.900	0.087	3.400	0.017	3.292	0.017	4.875	0.674
UNIP	(n)=K1	3.691	0.973	3.248	0.882	3.147	0.882	5.216	2.136
	(n)=K4	3.691	0.973	3.605	0.882	3.473	0.882	5.567	2.136
NDIS	—	10.643	0.087	8.828	0.017	8.583	0.017	11.224	0.674
NDISP	—	10.640	0.973	8.897	0.882	8.703	0.882	12.686	2.136
NUNI	—	10.646	0.087	8.781	0.017	8.591	0.017	11.233	0.674
NUNIP	—	10.656	0.973	8.921	0.882	8.692	0.882	12.695	2.136
WTOB	(n)=K1	6.064	0.090	5.033	0.017	4.759	0.017	7.289	0.674
	(n)=K96	17.180	0.090	11.699	0.017	11.423	0.017	13.873	0.674
WTOBP	(n)=K1	6.168	0.972	5.271	0.882	4.983	0.882	7.891	2.136
	(n)=K96	17.215	0.972	11.855	0.882	11.548	0.882	14.617	2.136
BTOW	(n)=K1	6.266	0.091	5.267	0.017	5.008	0.017	7.539	0.674
	(n)=K96	20.171	0.091	13.335	0.017	13.083	0.017	15.535	0.674
BTOWP	(n)=K1	6.398	0.971	5.473	0.882	5.225	0.882	8.133	2.136
	(n)=K96	20.080	0.971	13.439	0.882	13.196	0.882	16.319	2.136
MOV	—	3.468	0.084	2.633	0.017	2.549	0.017	4.016	0.674
MOVP	—	3.498	0.970	3.498	0.882	2.744	0.882	4.703	2.136
DMOV	—	3.734	0.084	2.800	0.017	2.683	0.017	4.833	0.674
DMOVP	—	3.665	0.969	3.665	0.882	2.877	0.882	6.295	2.136
CML	—	3.899	0.111	2.967	0.017	2.867	0.017	4.341	0.674
CMLP	—	3.832	0.973	3.832	0.882	3.057	0.882	5.012	2.136
DCML	—	4.399	0.107	3.333	0.017	3.175	0.017	5.359	0.674
DCMLP	—	4.198	0.971	4.198	0.882	3.353	0.882	6.821	2.136
SMOV	(n1)=K4, (n2)=K1, (n3)=K4	6.531	0.087	5.800	0.017	5.549	0.017	8.723	0.674
	(n1)=K4, (n2)=K4, (n3)=K4	6.531	0.087	5.800	0.017	5.549	0.017	8.723	0.674
SMOVP	(n1)=K4, (n2)=K1, (n3)=K4	6.698	0.970	5.967	0.882	5.747	0.882	9.329	2.136
	(n1)=K4, (n2)=K4, (n3)=K4	6.694	0.970	5.967	0.882	5.747	0.882	9.329	2.136
CMLB	—	4.264	0.120	3.267	0.017	3.125	0.017	4.599	0.674
CMLBP	—	4.132	0.969	4.132	0.882	3.300	0.882	5.329	2.136
BMOV	(n)=K1	5.198	0.087	4.333	0.017	4.208	0.017	5.975	0.674
	(n)=K96	7.827	0.087	6.867	0.017	6.767	0.017	8.599	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
BMOVP	(n)=K1	5.335	0.973	4.504	0.882	4.385	0.882	6.673	2.136
	(n)=K96	7.970	0.973	7.068	0.882	6.959	0.882	9.193	2.136
FMOV	(n)=K1	3.898	0.087	3.267	0.017	3.175	0.017	4.757	0.674
	(n)=K96	8.484	0.087	5.667	0.017	5.575	0.017	7.239	0.674
FMOVP	(n)=K1	4.079	0.972	3.436	0.882	3.357	0.882	5.440	2.136
	(n)=K96	8.491	0.972	5.829	0.882	5.756	0.882	7.900	2.136
DFMOV	(n)=K1	3.998	0.087	3.300	0.017	3.149	0.017	5.432	0.674
	(n)=K96	9.388	0.087	7.033	0.017	6.956	0.017	9.256	0.674
DFMOVP	(n)=K1	4.135	0.971	3.461	0.882	3.339	0.882	6.894	2.136
	(n)=K96	9.390	0.971	7.240	0.882	7.079	0.882	10.718	2.136
XCH	—	6.366	0.124	4.967	0.017	4.708	0.017	7.047	0.674
XCHP	—	5.832	0.970	5.832	0.882	4.899	0.882	7.757	2.136
DXCH	—	7.224	0.120	5.633	0.017	5.300	0.017	8.340	0.674
DXCHP	—	6.498	0.967	6.498	0.882	5.491	0.882	9.802	2.136
SWAP	—	4.631	0.110	3.533	0.017	3.383	0.017	4.857	0.674
SWAPP	—	4.398	0.969	4.398	0.882	3.547	0.882	5.539	2.136
DSWAP	—	5.098	0.110	3.867	0.017	3.692	0.017	5.867	0.674
DSWAPP	—	4.732	0.970	4.732	0.882	3.864	0.882	7.329	2.136
MOVB	—	4.264	0.111	3.233	0.017	3.092	0.017	4.567	0.674
MOVBP	—	4.098	0.970	4.098	0.882	3.263	0.882	5.256	2.136
BLKMOV _B	(n)=K1	5.331	0.087	4.400	0.017	4.108	0.017	5.859	0.674
	(n)=K96	5.664	0.087	4.833	0.017	4.549	0.017	6.331	0.674
BLKMOV _{BP}	(n)=K1	5.471	0.969	4.580	0.882	4.291	0.882	6.539	2.136
	(n)=K96	5.786	0.969	4.976	0.882	4.744	0.882	7.096	2.136
PRUN	—	7.224	0.087	5.667	0.017	5.375	0.017	6.931	0.674
PRUNP	—	7.338	0.970	5.823	0.882	5.557	0.882	7.608	2.136
DPRUN	—	8.351	0.087	6.467	0.017	6.167	0.017	8.431	0.674
DPRUNP	—	8.366	0.968	6.632	0.882	6.357	0.882	8.348	2.136
ROR	(n)=K1	3.452	0.087	3.069	0.017	2.917	0.017	4.920	0.674
	(n)=K15	3.452	0.087	3.069	0.017	2.917	0.017	4.920	0.674
RORP	(n)=K1	3.735	0.970	3.240	0.882	3.137	0.882	6.382	2.136
	(n)=K15	3.735	0.970	3.240	0.882	3.137	0.882	6.382	2.136
RCR	(n)=K1	3.502	0.087	3.125	0.017	2.975	0.017	4.988	0.674
	(n)=K15	3.508	0.087	3.125	0.017	2.975	0.017	4.988	0.674
RCRP	(n)=K1	3.766	0.971	3.303	0.882	3.204	0.882	6.450	2.136
	(n)=K15	3.830	0.971	3.303	0.882	3.204	0.882	6.450	2.136
DROR	(n)=K1	3.509	0.087	3.116	0.017	2.941	0.017	4.953	0.674
	(n)=K31	3.515	0.087	3.116	0.017	2.941	0.017	4.953	0.674
DRORP	(n)=K1	3.774	0.968	3.304	0.882	3.177	0.882	6.415	2.136
	(n)=K31	3.759	0.968	3.304	0.882	3.177	0.882	6.415	2.136
DRCR	(n)=K1	3.558	0.087	3.175	0.017	3.000	0.017	5.022	0.674
	(n)=K31	3.574	0.087	3.175	0.017	3.000	0.017	5.022	0.674
DRCRP	(n)=K1	3.796	0.972	3.379	0.882	3.221	0.882	6.484	2.136
	(n)=K31	3.823	0.972	3.379	0.882	3.221	0.882	6.484	2.136
ROL	(n)=K1	3.434	0.087	3.023	0.017	2.917	0.017	4.920	0.674
	(n)=K15	3.435	0.087	3.023	0.017	2.917	0.017	4.920	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ROLP	(n)=K1	3.775	0.973	3.247	0.882	3.136	0.882	6.382	2.136
	(n)=K15	3.770	0.973	3.247	0.882	3.136	0.882	6.382	2.136
RCL	(n)=K1	3.502	0.088	3.118	0.017	2.975	0.017	4.987	0.674
	(n)=K15	3.518	0.088	3.118	0.017	2.975	0.017	4.987	0.674
RCLP	(n)=K1	3.780	0.970	3.304	0.882	3.212	0.882	6.449	2.136
	(n)=K15	3.784	0.970	3.304	0.882	3.212	0.882	6.449	2.136
DROL	(n)=K1	3.492	0.087	3.102	0.017	2.941	0.017	4.961	0.674
	(n)=K31	3.498	0.087	3.102	0.017	2.941	0.017	4.961	0.674
DROLP	(n)=K1	3.818	0.972	3.276	0.882	3.172	0.882	6.423	2.136
	(n)=K31	3.767	0.972	3.276	0.882	3.172	0.882	6.423	2.136
DRCL	(n)=K1	3.562	0.087	3.165	0.017	3.000	0.017	5.023	0.674
	(n)=K31	3.587	0.087	3.165	0.017	3.000	0.017	5.023	0.674
DRCLP	(n)=K1	3.819	0.971	3.343	0.882	3.215	0.882	6.485	2.136
	(n)=K31	3.896	0.971	3.343	0.882	3.215	0.882	6.485	2.136
CJ	—	3.300	0.087	2.504	0.017	2.317	0.017	3.883	0.674
CJP	—	3.467	0.982	2.761	0.882	2.493	0.882	4.560	2.136
GOEND	—	0.108	0.083	0.091	0.017	0.091	0.017	0.408	0.674
DI (0 operands)	—	1.828	0.088	1.168	0.017	1.168	0.017	1.792	0.674
DI (1 operand)	—	2.840	0.090	2.107	0.017	2.107	0.017	3.747	0.674
EI	—	1.828	0.087	1.263	0.017	1.235	0.017	1.859	0.674
IMASK	—	7.570	—	6.441	—	5.183	—	6.879	—
SIMASK	—	4.099	0.088	3.000	0.017	2.833	0.017	4.308	0.674
IRET	—	0.100	—	0.050	—	0.050	—	0.358	—
WDT	—	1.498	0.087	1.141	0.017	1.141	0.017	1.832	0.674
WDTP	—	1.774	0.972	1.540	0.882	1.452	0.882	2.657	2.136
FOR	(n)=K1	2.444	—	2.023	—	2.023	—	4.109	—
NEXT	—	1.386	—	1.227	—	1.156	—	2.325	—
BREAK	—	4.172	0.104	3.203	0.017	2.971	0.017	5.249	0.674
BREAKP	—	4.450	1.037	3.515	0.882	3.215	0.882	5.661	2.136
CALL	—	4.508	0.100	3.851	0.017	3.335	0.017	5.816	0.674
CALLP	—	4.800	1.020	4.111	0.882	3.489	0.882	6.224	2.136
RET	—	2.474	—	2.279	—	1.939	—	3.448	—
SRET	—	2.470	—	2.279	—	1.939	—	3.448	—
XCALL	—	4.771	0.894	4.077	0.882	3.544	0.882	6.335	2.136
SFRD	(n)=K2	5.899	0.087	5.069	0.017	4.864	0.017	7.123	0.674
	(n)=K97	10.134	0.087	9.039	0.017	8.905	0.017	11.132	0.674
SFRDP	(n)=K2	6.215	0.977	5.344	0.882	5.097	0.882	8.585	2.136
	(n)=K97	10.486	0.977	9.299	0.882	9.111	0.882	11.568	2.136
POP	(n)=K2	5.072	0.088	4.335	0.017	4.119	0.017	6.392	0.674
	(n)=K97	5.022	0.088	4.312	0.017	4.104	0.017	6.383	0.674
POPP	(n)=K2	5.334	0.972	4.563	0.882	4.359	0.882	7.854	2.136
	(n)=K97	5.372	0.972	4.583	0.882	4.364	0.882	6.776	2.136
SFWR	(n)=K2	3.802	0.088	3.067	0.017	2.941	0.017	4.507	0.674
	(n)=K97	3.798	0.088	3.067	0.017	2.941	0.017	4.507	0.674
SFWRP	(n)=K2	3.924	0.970	3.204	0.882	3.127	0.882	5.219	2.136
	(n)=K97	3.916	0.970	3.203	0.882	3.135	0.882	5.200	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
FINS	(n)=K1	5.768	0.087	4.877	0.017	4.623	0.017	6.879	0.674
	(n)=K96	5.868	0.087	4.892	0.017	4.724	0.017	6.975	0.674
FINSP	(n)=K1	6.080	0.971	5.111	0.882	4.859	0.882	8.341	2.136
	(n)=K96	6.163	0.971	5.180	0.882	4.967	0.882	7.404	2.136
FDEL	(n)=K1	6.372	0.087	5.373	0.017	5.124	0.017	7.387	0.674
	(n)=K96	6.627	0.087	5.561	0.017	5.364	0.017	7.576	0.674
FDELP	(n)=K1	6.692	0.971	5.635	0.882	5.355	0.882	8.849	2.136
	(n)=K96	6.974	0.971	5.841	0.882	5.579	0.882	8.001	2.136
LD\$=	—	6.458	—	6.384	—	6.384	—	8.319	—
LD\$<>	—	6.384	—	6.384	—	6.384	—	8.319	—
LD\$>	—	6.384	—	6.384	—	6.384	—	8.319	—
LD\$<=	—	6.384	—	6.384	—	6.384	—	8.319	—
LD\$<	—	6.384	—	6.384	—	6.384	—	8.319	—
LD\$>=	—	6.384	—	6.384	—	6.384	—	8.319	—
AND\$=	—	6.396	—	6.384	—	6.384	—	8.319	—
AND\$<>	—	6.396	—	6.384	—	6.384	—	8.319	—
AND\$>	—	6.384	—	6.384	—	6.384	—	8.319	—
AND\$<=	—	6.396	—	6.384	—	6.384	—	8.319	—
AND\$<	—	6.398	—	6.384	—	6.384	—	8.319	—
AND\$>=	—	6.384	—	6.384	—	6.384	—	8.319	—
OR\$=	—	6.396	—	6.384	—	6.384	—	8.319	—
OR\$<>	—	6.384	—	6.384	—	6.384	—	8.319	—
OR\$>	—	6.384	—	6.384	—	6.384	—	8.319	—
OR\$<=	—	6.384	—	6.384	—	6.384	—	8.319	—
OR\$<	—	6.398	—	6.384	—	6.384	—	8.319	—
OR\$>=	—	6.384	—	6.384	—	6.384	—	8.319	—
\$+ (2 operands)	—	12.344	0.087	10.188	0.017	10.039	0.017	12.591	0.674
\$+P (2 operands)	—	12.415	0.969	10.308	0.882	10.139	0.882	14.053	2.136
\$+ (3 operands)	—	12.343	0.084	10.188	0.017	10.039	0.017	12.591	0.674
\$+P (3 operands)	—	12.376	0.972	10.308	0.882	10.139	0.882	14.053	2.136
\$MOV	—	6.632	0.091	5.433	0.017	5.300	0.017	7.605	0.674
\$MOVP	—	6.806	0.968	5.673	0.882	5.415	0.882	8.145	2.136
\$MOV_WS	0 characters	6.299	0.087	4.965	0.018	4.891	0.016	7.505	0.700
	32 characters	9.788	0.087	7.800	0.018	7.733	0.016	10.363	0.700
\$MOVP_WS	0 characters	6.139	0.973	5.225	0.882	5.121	0.016	8.177	0.700
	32 characters	9.055	0.973	8.128	0.882	7.951	0.016	10.939	0.700
BINDA	—	4.403	0.090	3.500	0.017	3.349	0.017	5.407	0.674
BINDAP	—	4.572	0.968	3.703	0.882	3.541	0.882	6.092	2.136
BINDA_U	—	4.331	0.090	3.500	0.017	3.349	0.017	5.407	0.674
BINDAP_U	—	4.508	0.970	3.703	0.882	3.541	0.882	6.092	2.136
DBINDA	—	5.002	0.087	4.867	0.017	4.692	0.017	6.827	0.674
DBINDAP	—	5.175	0.969	5.075	0.882	4.885	0.882	6.760	2.136
DBINDA_U	—	4.967	0.087	4.867	0.017	4.692	0.017	6.827	0.674
DBINDAP_U	—	5.151	0.970	5.075	0.882	4.885	0.882	6.760	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ASCI	(n)=K1 SM8161 =OFF	6.432	0.087	5.300	0.017	5.075	0.017	7.605	0.674
	(n)=K96 SM8161 =OFF	20.598	0.087	16.951	0.017	16.715	0.017	19.229	0.674
	(n)=K1 SM8161=ON	6.531	0.087	5.433	0.017	5.192	0.017	9.067	0.674
	(n)=K96 SM8161=ON	21.660	0.087	14.633	0.017	14.399	0.017	16.929	0.674
ASCP	(n)=K1 SM8161 =OFF	6.610	0.973	5.517	0.882	5.267	0.882	7.856	2.136
	(n)=K96 SM8161 =OFF	20.527	0.973	17.051	0.882	16.832	0.882	19.427	2.136
	(n)=K1 SM8161=ON	6.636	0.973	5.568	0.882	5.391	0.882	9.318	2.136
	(n)=K96 SM8161=ON	21.474	0.973	14.728	0.882	14.528	0.882	16.929	2.136
STR	—	6.431	0.087	5.377	0.017	5.175	0.017	7.620	0.674
STRP	—	6.600	0.968	5.568	0.882	5.352	0.882	9.082	2.136
STR_U	—	6.431	0.088	5.377	0.017	5.175	0.017	7.620	0.674
STRP_U	—	6.608	0.970	5.568	0.882	5.352	0.882	9.082	2.136
DSTR	—	6.364	0.084	5.377	0.017	5.175	0.017	7.620	0.674
DSTRP	—	6.486	0.970	5.568	0.882	5.352	0.882	9.082	2.136
DSTR_U	—	6.366	0.084	5.377	0.017	5.175	0.017	7.620	0.674
DSTRP_U	—	6.495	0.970	5.568	0.882	5.352	0.882	9.082	2.136
ESTR	—	19.296	0.090	16.267	0.017	16.044	0.017	18.660	0.674
ESTRP	—	19.159	0.969	16.355	0.882	16.125	0.882	20.122	2.136
DESTR	—	19.306	0.090	16.267	0.017	16.044	0.017	18.660	0.674
DESTRP	—	19.264	0.967	16.355	0.882	16.125	0.882	20.122	2.136
WS2SJIS	The number of characters = 1	11.044	0.088	8.452	0.018	8.379	0.016	11.376	0.700
	The number of characters = 96	44.459	0.088	43.433	0.018	43.025	0.016	45.961	0.700
WS2SJISP	The number of characters = 1	11.143	0.972	8.759	0.882	8.544	0.016	11.653	0.700
	The number of characters = 96	46.500	0.972	43.696	0.882	43.167	0.016	46.636	0.700
SJIS2WS	The number of characters = 1	10.042	0.087	7.799	0.018	7.717	0.016	10.355	0.700
	The number of characters = 96	52.647	0.087	48.516	0.018	48.311	0.016	52.480	0.700

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
SJIS2WSP	The number of characters = 1	10.481	0.976	8.132	0.882	7.963	0.016	11.299	0.700
	The number of characters = 96	54.096	0.976	48.749	0.882	48.531	0.016	52.840	0.700
SJIS2WSB	The number of characters = 1	10.222	0.087	7.905	0.018	7.849	0.016	10.487	0.700
	The number of characters = 96	52.714	0.087	48.515	0.018	48.317	0.016	52.521	0.700
SJIS2WSBP	The number of characters = 1	10.576	0.976	8.232	0.882	8.064	0.016	11.416	0.700
	The number of characters = 96	54.039	0.976	48.763	0.882	48.539	0.016	52.871	0.700
LEN	—	4.198	0.091	3.400	0.017	3.308	0.017	5.367	0.674
LENP	—	4.379	0.969	3.643	0.882	3.527	0.882	6.103	2.136
RIGHT	(n)=K1 The number of character strings to be extracted: 100	16.008	0.084	14.632	0.017	14.524	0.017	17.051	0.674
	(n)=K96 The number of character strings to be extracted: 100	16.707	0.084	15.332	0.017	15.216	0.017	17.749	0.674
RIGHTP	(n)=K1 The number of character strings to be extracted: 100	16.063	0.973	14.792	0.882	14.627	0.882	18.513	2.136
	(n)=K96 The number of character strings to be extracted: 100	16.695	0.973	15.471	0.882	15.332	0.882	17.733	2.136
LEFT	(n)=K1 The number of character strings to be extracted: 100	15.943	0.084	14.665	0.017	14.524	0.017	17.051	0.674
	(n)=K96 The number of character strings to be extracted: 100	16.680	0.084	15.365	0.017	15.216	0.017	17.749	0.674

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Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
LEFTP	(n)=K1 The number of character strings to be extracted: 100	16.076	0.973	14.768	0.882	14.627	0.882	18.513	2.136
	(n)=K96 The number of character strings to be extracted: 100	16.668	0.973	15.501	0.882	15.332	0.882	17.733	2.136
MIDR	—	7.098	0.090	5.800	0.017	5.608	0.017	8.332	0.674
MIDRP	—	7.252	0.969	6.017	0.882	5.799	0.882	9.794	2.136
MIDW	—	7.531	0.087	6.200	0.017	6.075	0.017	8.081	0.674
MIDWP	—	7.676	0.971	6.423	0.882	6.247	0.882	8.736	2.136
INSTR	—	6.936	0.090	5.867	0.017	5.659	0.017	8.481	0.674
INSTRP	—	7.103	0.968	6.075	0.882	5.851	0.882	9.943	2.136
STRINS	(s1)=1 character, (d)=128 characters, (s2)=K40	26.976	0.087	24.384	0.017	24.241	0.017	26.772	0.674
	(s1)=48 characters, (d)=128 characters, (s2)=K40	33.352	0.087	30.327	0.017	30.131	0.017	31.904	0.674
STRINSP	(s1)=1 character, (d)=128 characters, (s2)=K40	26.987	0.973	24.516	0.882	24.427	0.882	28.234	2.136
	(s1)=48 characters, (d)=128 characters, (s2)=K40	32.776	0.973	31.593	0.882	31.593	0.882	32.579	2.136
STRDEL	(d)=128 characters, (s)=K40, (n)=K1	21.535	0.084	19.905	0.017	19.823	0.017	22.069	0.674
	(d)=128 characters, (s)=K40, (n)=K48	20.858	0.084	19.328	0.017	19.224	0.017	21.449	0.674
STRDELP	(d)=128 characters, (s)=K40, (n)=K1	21.648	0.972	20.044	0.882	20.044	0.882	23.531	2.136
	(d)=128 characters, (s)=K40, (n)=K48	21.183	0.972	19.416	0.882	19.416	0.882	22.911	2.136
LDE=	—	2.566	—	2.247	—	2.100	—	3.980	—
LDE<>	—	2.563	—	2.247	—	2.100	—	3.980	—
LDE>	—	2.564	—	2.247	—	2.100	—	3.980	—
LDE<=	—	2.570	—	2.247	—	2.100	—	3.980	—

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
LDE<	—	2.536	—	2.247	—	2.100	—	3.980	—
LDE>=	—	2.530	—	2.247	—	2.100	—	3.980	—
ANDE=	—	2.763	—	2.247	—	2.100	—	3.980	—
ANDE<>	—	2.635	—	2.247	—	2.100	—	3.980	—
ANDE>	—	2.666	—	2.247	—	2.100	—	3.980	—
ANDE<=	—	2.602	—	2.247	—	2.100	—	3.980	—
ANDE<	—	2.596	—	2.247	—	2.100	—	3.980	—
ANDE>=	—	2.598	—	2.247	—	2.100	—	3.980	—
ORE=	—	2.663	—	2.247	—	2.100	—	3.980	—
ORE<>	—	2.630	—	2.247	—	2.100	—	3.980	—
ORE>	—	2.634	—	2.247	—	2.100	—	3.980	—
ORE<=	—	2.602	—	2.247	—	2.100	—	3.980	—
ORE<	—	2.635	—	2.247	—	2.100	—	3.980	—
ORE>=	—	2.630	—	2.247	—	2.100	—	3.980	—
DECMP	—	4.231	0.088	3.600	0.017	3.400	0.017	4.991	0.674
DECMPP	—	4.438	0.968	3.780	0.882	3.581	0.882	5.665	2.136
DEZCP	—	4.698	0.087	4.100	0.017	3.859	0.017	6.499	0.674
DEZCPP	—	4.950	0.968	4.337	0.882	4.055	0.882	7.961	2.136
E+ (2 operands)	—	4.498	0.087	3.967	0.017	3.692	0.017	5.389	0.674
E+P (2 operands)	—	4.656	0.971	4.180	0.882	3.888	0.882	6.101	2.136
E+ (3 operands)	—	3.998	0.087	3.967	0.017	3.692	0.017	5.389	0.674
E+P (3 operands)	—	4.180	0.971	4.180	0.882	3.888	0.882	6.101	2.136
DEADD	—	3.998	0.087	3.967	0.017	3.692	0.017	5.389	0.674
DEADDP	—	4.180	0.973	4.180	0.882	3.888	0.882	6.101	2.136
E- (2 operands)	—	4.532	0.087	4.000	0.017	3.700	0.017	5.391	0.674
E-P (2 operands)	—	4.668	0.972	4.172	0.882	3.901	0.882	6.076	2.136
E- (3 operands)	—	4.098	0.087	4.000	0.017	3.700	0.017	5.391	0.674
E-P (3 operands)	—	4.224	0.968	4.172	0.882	3.901	0.882	6.076	2.136
DESUB	—	4.098	0.087	4.000	0.017	3.700	0.017	5.391	0.674
DESUBP	—	4.228	0.970	4.172	0.882	3.901	0.882	6.076	2.136
E*	—	4.134	0.087	3.400	0.017	3.208	0.017	4.633	0.674
E*P	—	4.268	0.971	3.575	0.882	3.395	0.882	5.344	2.136
DEMUL	—	4.131	0.087	3.400	0.017	3.208	0.017	4.633	0.674
DEMULP	—	4.268	0.971	3.575	0.882	3.395	0.882	5.344	2.136
E/	—	3.998	0.087	3.433	0.017	3.259	0.017	4.683	0.674
E/P	—	4.135	0.971	3.603	0.882	3.439	0.882	5.395	2.136
DEDIV	—	3.998	0.087	3.433	0.017	3.259	0.017	4.683	0.674
DEDIVP	—	4.136	0.971	3.603	0.882	3.439	0.882	5.395	2.136
INT2FLT	—	4.303	0.110	3.333	0.017	3.183	0.017	4.657	0.674
INT2FLTP	—	4.645	0.970	4.645	0.882	4.645	0.882	6.119	2.136
UINT2FLT	—	4.264	0.111	3.333	0.017	3.183	0.017	4.657	0.674
UINT2FLTP	—	4.048	0.969	4.048	0.882	4.048	0.882	6.119	2.136
DINT2FLT	—	4.468	0.110	3.333	0.017	3.183	0.017	4.657	0.674
DINT2FLTP	—	4.645	0.970	4.645	0.882	4.645	0.882	6.119	2.136
UDINT2FLT	—	4.466	0.111	3.333	0.017	3.183	0.017	4.657	0.674
UDINT2FLTP	—	4.048	0.971	4.048	0.882	4.048	0.882	6.119	2.136
EVAL	—	5.298	0.090	4.533	0.017	4.433	0.017	6.572	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
EVALP	—	5.467	0.970	4.669	0.882	4.620	0.882	7.268	2.136
DEVAL	—	5.300	0.091	4.533	0.017	4.433	0.017	6.572	0.674
DEVALP	—	5.468	0.971	4.735	0.882	4.620	0.882	7.268	2.136
DEBCD	—	3.698	0.087	3.033	0.017	2.925	0.017	4.816	0.674
DEBCDP	—	3.868	0.971	3.248	0.882	3.124	0.882	6.278	2.136
DEBIN	—	3.698	0.088	3.033	0.017	2.916	0.017	4.107	0.674
DEBINP	—	3.899	0.971	3.241	0.882	3.112	0.882	4.784	2.136
ENEG	—	3.064	0.087	2.491	0.017	2.449	0.017	3.641	0.674
ENE GP	—	3.234	0.969	2.745	0.882	2.640	0.882	4.337	2.136
DENEG	—	3.067	0.087	2.491	0.017	2.449	0.017	3.641	0.674
DENEGP	—	3.236	0.972	2.745	0.882	2.640	0.882	4.337	2.136
EMOV	—	4.402	0.110	3.333	0.017	3.167	0.017	4.641	0.674
EMO VP	—	4.032	0.969	4.032	0.882	4.032	0.882	6.103	2.136
DEMOV	—	4.399	0.110	3.333	0.017	3.167	0.017	4.641	0.674
DEMO VP	—	4.032	0.973	4.032	0.882	4.032	0.882	6.103	2.136
SIN	—	4.302	0.087	3.584	0.017	3.584	0.017	5.475	0.674
SINP	—	4.510	0.968	3.788	0.882	3.788	0.882	6.937	2.136
DSIN	—	4.298	0.089	3.584	0.017	3.584	0.017	5.475	0.674
DSINP	—	4.504	0.971	3.788	0.882	3.788	0.882	6.937	2.136
COS	—	4.298	0.087	3.584	0.017	3.584	0.017	5.475	0.674
COSP	—	4.504	0.969	3.788	0.882	3.788	0.882	6.937	2.136
DCOS	—	4.298	0.087	3.584	0.017	3.584	0.017	5.475	0.674
DCOSP	—	4.499	0.973	3.788	0.882	3.788	0.882	6.937	2.136
TAN	—	4.367	0.087	3.675	0.017	3.584	0.017	5.475	0.674
TANP	—	4.534	0.968	3.879	0.882	3.788	0.882	6.937	2.136
DTAN	—	4.364	0.087	3.675	0.017	3.584	0.017	5.475	0.674
DTANP	—	4.534	0.969	3.879	0.882	3.788	0.882	6.937	2.136
ASIN	—	3.631	0.087	2.967	0.017	2.967	0.017	4.859	0.674
ASINP	—	3.772	0.970	3.179	0.882	3.179	0.882	6.321	2.136
DASIN	—	3.631	0.088	2.967	0.017	2.967	0.017	4.859	0.674
DASINP	—	3.771	0.969	3.179	0.882	3.179	0.882	6.321	2.136
ACOS	—	3.831	0.087	3.045	0.017	2.967	0.017	4.859	0.674
ACOSP	—	4.055	0.969	3.279	0.882	3.179	0.882	6.321	2.136
DACOS	—	3.831	0.088	3.045	0.017	2.967	0.017	4.859	0.674
DACOSP	—	4.034	0.970	3.279	0.882	3.179	0.882	6.321	2.136
ATAN	—	3.631	0.087	3.013	0.017	2.967	0.017	4.859	0.674
ATANP	—	3.834	0.969	3.251	0.882	3.179	0.882	6.321	2.136
DATAN	—	3.636	0.087	3.013	0.017	2.967	0.017	4.859	0.674
DATANP	—	3.816	0.971	3.251	0.882	3.179	0.882	6.321	2.136
RAD	—	3.331	0.088	2.772	0.017	2.667	0.017	4.559	0.674
RADP	—	3.470	0.965	2.980	0.882	2.873	0.882	6.021	2.136
DRAD	—	3.331	0.087	2.772	0.017	2.667	0.017	4.559	0.674
DRADP	—	3.467	0.971	2.980	0.882	2.873	0.882	6.021	2.136
DEG	—	3.300	0.087	2.780	0.017	2.675	0.017	4.567	0.674
DEGP	—	3.508	0.970	2.980	0.882	2.873	0.882	6.029	2.136
DDEG	—	3.298	0.088	2.780	0.017	2.675	0.017	4.567	0.674
DDEGP	—	3.532	0.971	2.980	0.882	2.873	0.882	6.029	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ESQRT	—	3.231	0.087	2.733	0.017	2.608	0.017	3.799	0.674
ESQRTP	—	3.430	0.972	2.920	0.882	2.787	0.882	4.500	2.136
DESQR	—	3.234	0.087	2.733	0.017	2.608	0.017	3.799	0.674
DESQRP	—	3.426	0.965	2.920	0.882	2.787	0.882	4.500	2.136
EXP	—	3.966	0.087	3.333	0.017	3.225	0.017	4.416	0.674
EXPP	—	4.091	0.970	3.509	0.882	3.408	0.882	5.132	2.136
DEXP	—	3.964	0.087	3.333	0.017	3.225	0.017	4.416	0.674
DEXPP	—	4.095	0.968	3.509	0.882	3.408	0.882	5.132	2.136
LOG	—	4.698	0.087	3.633	0.017	3.508	0.017	4.699	0.674
LOGP	—	4.824	0.968	3.837	0.882	3.693	0.882	5.381	2.136
DLOGE	—	4.698	0.088	3.633	0.017	3.508	0.017	4.699	0.674
DLOGEP	—	4.818	0.969	3.837	0.882	3.693	0.882	5.381	2.136
POW	—	4.431	0.087	3.800	0.017	3.641	0.017	5.065	0.674
POWP	—	4.603	0.973	3.975	0.882	3.827	0.882	5.771	2.136
LOG10	—	4.764	0.087	3.600	0.017	3.516	0.017	4.708	0.674
LOG10P	—	4.922	0.969	3.805	0.882	3.711	0.882	5.411	2.136
DLOG10	—	4.764	0.087	3.600	0.017	3.516	0.017	4.708	0.674
DLOG10P	—	4.928	0.973	3.805	0.882	3.711	0.882	5.411	2.136
EMAX	(n)=K1	5.466	0.087	4.500	0.017	4.200	0.017	6.731	0.674
	(n)=K96	33.187	0.087	26.552	0.017	26.219	0.017	28.735	0.674
EMAXP	(n)=K1	5.604	0.970	4.741	0.882	4.396	0.882	8.193	2.136
	(n)=K96	32.520	0.970	27.084	0.882	27.084	0.882	30.197	2.136
EMIN	(n)=K1	5.498	0.087	4.533	0.017	4.225	0.017	6.699	0.674
	(n)=K96	33.232	0.087	26.112	0.017	25.616	0.017	28.156	0.674
EMINP	(n)=K1	5.655	0.973	4.691	0.882	4.404	0.882	8.161	2.136
	(n)=K96	32.540	0.973	26.481	0.882	26.481	0.882	29.618	2.136
RND	—	2.532	0.087	2.033	0.017	1.941	0.017	2.883	0.674
RNDP	—	2.830	0.971	2.201	0.882	2.116	0.882	3.565	2.136
ZPUSH (1 operand)	—	6.843	0.087	6.843	0.017	5.735	0.017	7.648	0.674
ZPUSHP (1 operand)	—	7.103	0.972	7.103	0.882	5.953	0.882	9.110	2.136
ZPUSH (2 operands)	K2 R0	6.546	0.087	6.200	0.017	5.735	0.017	7.648	0.674
ZPUSHP (2 operands)	K2 R0	6.816	0.973	6.433	0.882	5.953	0.882	9.110	2.136
ZPOP (1 operand)	—	7.587	0.084	6.843	0.017	5.735	0.017	7.648	0.674
ZPOPP (1 operand)	—	7.880	0.973	7.103	0.882	5.953	0.882	9.110	2.136
ZPOP (2 operands)	K2 R0	7.046	0.084	6.200	0.017	5.735	0.017	7.648	0.674
ZPOPP (2 operands)	K2 R0	7.312	0.970	6.433	0.882	5.953	0.882	9.110	2.136
LIMIT	—	3.196	0.087	2.667	0.017	2.567	0.017	4.883	0.674
LIMITP	—	3.335	0.969	2.843	0.882	2.771	0.882	6.345	2.136
LIMIT_U	—	3.198	0.087	2.667	0.017	2.567	0.017	4.883	0.674
LIMITP_U	—	3.362	0.971	2.843	0.882	2.771	0.882	6.345	2.136
DLIMIT	—	3.198	0.087	2.667	0.017	2.567	0.017	4.883	0.674
DLIMITP	—	3.366	0.973	2.843	0.882	2.771	0.882	6.345	2.136
DLIMIT_U	—	3.231	0.087	2.667	0.017	2.567	0.017	4.883	0.674
DLIMITP_U	—	3.391	0.969	2.843	0.882	2.771	0.882	6.345	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
BAND	—	3.134	0.088	2.667	0.017	2.567	0.017	4.883	0.674
BANDP	—	3.292	0.972	2.845	0.882	2.772	0.882	6.345	2.136
BAND_U	—	3.164	0.087	2.667	0.017	2.567	0.017	4.883	0.674
BANDP_U	—	3.344	0.972	2.845	0.882	2.772	0.882	6.345	2.136
DBAND	—	3.198	0.087	2.667	0.017	2.567	0.017	4.883	0.674
DBANDP	—	3.408	0.972	2.845	0.882	2.772	0.882	6.345	2.136
DBAND_U	—	3.198	0.087	2.667	0.017	2.567	0.017	4.883	0.674
DBANDP_U	—	3.360	0.970	2.845	0.882	2.772	0.882	6.345	2.136
ZONE	—	3.131	0.087	2.633	0.017	2.559	0.017	4.875	0.674
ZONEP	—	3.258	0.971	2.848	0.882	2.765	0.882	6.337	2.136
ZONE_U	—	3.131	0.087	2.633	0.017	2.559	0.017	4.875	0.674
ZONEP_U	—	3.287	0.970	2.848	0.882	2.765	0.882	6.337	2.136
DZONE	—	3.198	0.087	2.633	0.017	2.559	0.017	4.875	0.674
DZONEP	—	3.359	0.971	2.848	0.882	2.765	0.882	6.337	2.136
DZONE_U	—	3.170	0.087	2.633	0.017	2.559	0.017	4.875	0.674
DZONEP_U	—	3.291	0.971	2.848	0.882	2.765	0.882	6.337	2.136
SCL	—	5.998	0.091	4.733	0.017	4.541	0.017	6.733	0.674
SCLP	—	6.172	0.971	4.939	0.882	4.752	0.882	7.517	2.136
SCL_U	—	6.031	0.090	4.733	0.017	4.541	0.017	6.733	0.674
SCLP_U	—	6.198	0.970	4.939	0.882	4.752	0.882	7.517	2.136
DSCL	—	5.931	0.087	4.733	0.017	4.541	0.017	6.733	0.674
DSCLP	—	6.026	0.971	4.939	0.882	4.752	0.882	7.517	2.136
DSCL_U	—	5.868	0.087	4.733	0.017	4.541	0.017	6.733	0.674
DSCLP_U	—	6.042	0.969	4.939	0.882	4.752	0.882	7.517	2.136
SCL2	—	6.499	0.091	5.233	0.017	5.033	0.017	7.377	0.674
SCL2P	—	6.667	0.973	5.451	0.882	5.240	0.882	7.991	2.136
SCL2_U	—	6.535	0.090	5.233	0.017	5.033	0.017	7.377	0.674
SCL2P_U	—	6.710	0.970	5.451	0.882	5.240	0.882	7.991	2.136
DSCL2	—	6.498	0.087	5.233	0.017	5.033	0.017	7.377	0.674
DSCL2P	—	6.643	0.971	5.451	0.882	5.240	0.882	7.991	2.136
DSCL2_U	—	6.498	0.087	5.233	0.017	5.033	0.017	7.377	0.674
DSCL2P_U	—	6.608	0.973	5.451	0.882	5.240	0.882	7.991	2.136
UDCNTF	—	4.171	3.578	3.267	2.568	3.067	2.568	4.716	4.334
TTMR	—	4.232	3.661	3.633	2.917	3.400	2.917	4.932	4.985
STMR	—	5.666	6.460	4.433	4.624	4.183	4.624	7.003	7.199
ROTC	—	8.134	8.033	6.800	6.431	6.667	6.431	9.600	9.274
RAMPF	(n)=K1	5.966	3.818	5.467	3.263	5.067	3.028	8.067	5.013
SPD	—	13.867	3.081	12.800	2.426	10.533	2.426	13.200	4.654
DSPD	—	13.867	3.108	12.800	2.426	10.533	2.426	13.200	4.654
PLSY	—	30.400	6.573	30.400	6.573	25.217 (24.267)	6.573	27.617 (26.667)	8.096
DPLSY	—	30.400	6.573	30.400	6.573	24.267	6.573	26.667	8.096
PWM	—	20.000	9.443	18.667	6.456	15.750 (14.800)	6.456	18.417 (17.467)	7.979
DPWM	—	20.000	9.456	18.667	6.456	14.800	6.456	17.467	7.979
ZRST	—	2.800	0.084	2.533	0.017	2.425	0.017	4.703	0.674
ZRSTP	—	3.139	0.970	2.771	0.882	2.687	0.882	6.165	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
SERMM	(n)=K1 No same data	5.799	0.087	4.933	0.017	4.541	0.017	7.331	0.674
	(n)=K1 The number of same data=1	5.834	0.087	4.967	0.017	4.567	0.017	7.357	0.674
	(n)=K96 No same data	20.102	0.087	16.283	0.017	15.984	0.017	18.785	0.674
	(n)=K96 The number of same data=96	23.327	0.087	19.577	0.017	19.173	0.017	21.887	0.674
SERMMP	(n)=K1 No same data	5.924	0.974	5.105	0.882	4.727	0.882	8.793	2.136
	(n)=K1 The number of same data=1	5.963	0.974	5.137	0.882	4.753	0.882	8.819	2.136
	(n)=K96 No same data	20.000	0.974	16.389	0.882	16.084	0.882	18.981	2.136
	(n)=K96 The number of same data=96	23.272	0.974	20.442	0.882	19.219	0.882	22.020	2.136
DSERMM	(n)=K1 No same data	5.831	0.088	4.933	0.017	4.541	0.017	7.331	0.674
	(n)=K1 The number of same data=1	5.868	0.088	4.967	0.017	4.567	0.017	7.357	0.674
	(n)=K96 No same data	20.207	0.088	16.816	0.017	16.383	0.017	19.177	0.674
	(n)=K96 The number of same data=96	23.430	0.088	20.112	0.017	19.657	0.017	22.381	0.674
DSERMMP	(n)=K1 No same data	5.995	0.975	5.105	0.882	4.727	0.882	8.793	2.136
	(n)=K1 The number of same data=1	6.040	0.975	5.137	0.882	4.753	0.882	8.819	2.136
	(n)=K96 No same data	20.063	0.975	16.869	0.882	16.489	0.882	20.639	2.136
	(n)=K96 The number of same data=96	23.422	0.975	20.105	0.882	19.691	0.882	23.843	2.136
MAX	(n)=K1	5.564	0.090	4.467	0.017	4.175	0.017	6.705	0.674
	(n)=K96	18.270	0.090	14.665	0.017	14.383	0.017	16.913	0.674
MAXP	(n)=K1	5.751	0.968	4.668	0.882	4.388	0.882	8.167	2.136
	(n)=K96	18.156	0.968	14.801	0.882	14.496	0.882	18.375	2.136
MAX_U	(n)=K1	5.464	0.091	4.467	0.017	4.175	0.017	6.705	0.674
	(n)=K96	17.247	0.091	14.665	0.017	14.383	0.017	16.913	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
MAXP_U	(n)=K1	5.603	0.971	4.668	0.882	4.388	0.882	8.167	2.136
	(n)=K96	17.280	0.971	14.801	0.882	14.496	0.882	18.375	2.136
DMAX	(n)=K1	5.568	0.090	4.467	0.017	4.175	0.017	6.705	0.674
	(n)=K96	19.932	0.090	15.549	0.017	15.193	0.017	17.717	0.674
DMAXP	(n)=K1	5.771	0.970	4.668	0.882	4.388	0.882	8.167	2.136
	(n)=K96	19.854	0.970	15.643	0.882	15.307	0.882	18.035	2.136
DMAX_U	(n)=K1	5.432	0.091	4.467	0.017	4.175	0.017	6.705	0.674
	(n)=K96	19.828	0.091	15.549	0.017	15.193	0.017	17.717	0.674
DMAXP_U	(n)=K1	5.602	0.971	4.668	0.882	4.388	0.882	8.167	2.136
	(n)=K96	19.684	0.971	15.643	0.882	15.307	0.882	18.035	2.136
MIN	(n)=K1	5.598	0.090	4.467	0.017	4.167	0.017	6.697	0.674
	(n)=K96	18.171	0.090	14.699	0.017	14.408	0.017	16.947	0.674
MINP	(n)=K1	5.778	0.971	4.639	0.882	4.371	0.882	8.159	2.136
	(n)=K96	18.163	0.971	14.803	0.882	14.613	0.882	18.409	2.136
MIN_U	(n)=K1	5.543	0.090	4.467	0.017	4.167	0.017	6.697	0.674
	(n)=K96	17.319	0.090	14.699	0.017	14.408	0.017	16.947	0.674
MINP_U	(n)=K1	5.714	0.971	4.639	0.882	4.371	0.882	8.159	2.136
	(n)=K96	17.324	0.971	14.803	0.882	14.613	0.882	18.409	2.136
DMIN	(n)=K1	5.498	0.091	4.467	0.017	4.167	0.017	6.697	0.674
	(n)=K96	19.900	0.091	15.517	0.017	15.192	0.017	17.721	0.674
DMINP	(n)=K1	5.703	0.972	4.639	0.882	4.371	0.882	8.159	2.136
	(n)=K96	19.740	0.972	15.636	0.882	15.359	0.882	18.119	2.136
DMIN_U	(n)=K1	5.435	0.091	4.467	0.017	4.167	0.017	6.697	0.674
	(n)=K96	19.800	0.091	15.517	0.017	15.192	0.017	17.721	0.674
DMINP_U	(n)=K1	5.648	0.972	4.639	0.882	4.371	0.882	8.159	2.136
	(n)=K96	19.703	0.972	15.636	0.882	15.359	0.882	18.119	2.136
SORTTBL	(n1)=K1, (n2)=K1, (n3)=K1	6.000	1.112	5.467	1.083	5.200	1.083	8.400	2.417
	(n1)=K32, (n2)=K1, (n3)=K1	6.934	1.112	6.400	1.083	6.133	1.083	9.333	2.413
	(n1)=K32, (n2)=K6, (n3)=K1	12.667	1.112	11.733	1.082	11.467	1.082	14.667	2.415
SORTTBL_U	(n1)=K1, (n2)=K1, (n3)=K1	6.000	1.111	5.467	1.083	5.200	1.083	8.400	2.417
	(n1)=K32, (n2)=K1, (n3)=K1	7.067	1.111	6.400	1.083	6.133	1.083	9.333	2.413
	(n1)=K32, (n2)=K6, (n3)=K1	12.534	1.111	11.733	1.082	11.467	1.082	14.667	2.415

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
SORTTBL2	(n1)=K1, (n2)=K1, (n3)=K1	6.534	1.135	5.733	1.083	5.467	1.083	8.667	2.418
	(n1)=K32, (n2)=K1, (n3)=K1	7.600	1.135	6.667	1.083	6.267	1.083	9.600	2.414
	(n1)=K32, (n2)=K6, (n3)=K1	13.067	1.135	12.000	1.083	11.733	1.083	14.933	2.413
SORTTBL2_U	(n1)=K1, (n2)=K1, (n3)=K1	6.667	1.136	5.733	1.083	5.467	1.083	8.667	2.418
	(n1)=K32, (n2)=K1, (n3)=K1	7.600	1.136	6.667	1.083	6.267	1.083	9.600	2.414
	(n1)=K32, (n2)=K6, (n3)=K1	13.067	1.136	12.000	1.083	11.733	1.083	14.933	2.413
DSORTTBL2	(n1)=K1, (n2)=K1, (n3)=K1	6.800	1.136	5.733	1.083	5.467	1.083	8.667	2.418
	(n1)=K32, (n2)=K1, (n3)=K1	8.267	1.136	7.333	1.083	6.933	1.083	9.867	2.414
	(n1)=K32, (n2)=K1, (n3)=K1	18.800	1.136	17.467	1.083	17.067	1.083	20.000	2.416
DSORTTBL2_U	(n1)=K1, (n2)=K1, (n3)=K1	6.800	1.136	5.733	1.083	5.467	1.083	8.667	2.418
	(n1)=K32, (n2)=K1, (n3)=K1	8.267	1.136	7.333	1.083	6.933	1.083	9.867	2.414
	(n1)=K32, (n2)=K1, (n3)=K1	18.800	1.136	17.467	1.083	17.067	1.083	20.000	2.416
SUM	—	3.599	0.090	2.800	0.017	2.683	0.017	4.575	0.674
SUMP	—	3.735	0.970	2.975	0.882	2.893	0.882	4.947	2.136
BON	(n)=K1	3.098	0.090	2.600	0.017	2.475	0.017	4.591	0.674
BONP	—	3.235	0.972	2.805	0.882	2.664	0.882	6.053	2.136
DSUM	—	4.435	0.090	3.133	0.017	3.000	0.017	4.896	0.674
DSUMP	—	4.564	0.969	3.384	0.882	3.219	0.882	5.240	2.136
DBON	—	3.131	0.090	2.600	0.017	2.475	0.017	4.591	0.674
DBONP	—	3.259	0.969	2.805	0.882	2.664	0.882	6.053	2.136
WSUM	(n)=K1	5.998	0.087	4.433	0.017	4.192	0.017	6.831	0.674
	(n)=K96	13.776	0.087	7.645	0.017	7.367	0.017	10.023	0.674
WSUMP	(n)=K1	6.136	0.977	4.639	0.882	4.387	0.882	8.293	2.136
	(n)=K96	13.870	0.977	7.845	0.882	7.564	0.882	11.485	2.136
WSUM_U	(n)=K1	5.998	0.087	4.433	0.017	4.192	0.017	6.831	0.674
	(n)=K96	13.808	0.087	7.645	0.017	7.367	0.017	10.023	0.674
WSUMP_U	(n)=K1	6.211	0.977	4.639	0.882	4.387	0.882	8.293	2.136
	(n)=K96	13.854	0.977	7.845	0.882	7.564	0.882	11.485	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DWSUM	(n)=K1	6.799	0.087	5.000	0.017	4.616	0.017	7.256	0.674
	(n)=K96	18.900	0.087	10.851	0.017	10.473	0.017	13.048	0.674
DWSUMP	(n)=K1	6.938	0.975	5.203	0.882	4.813	0.882	8.718	2.136
	(n)=K96	18.787	0.975	10.939	0.882	10.592	0.882	14.510	2.136
DWSUM_U	(n)=K1	6.799	0.087	5.000	0.017	4.616	0.017	7.256	0.674
	(n)=K96	18.067	0.087	10.851	0.017	10.473	0.017	13.048	0.674
DWSUMP_U	(n)=K1	7.024	0.974	5.203	0.882	4.813	0.882	8.718	2.136
	(n)=K96	18.044	0.974	10.939	0.882	10.592	0.882	14.510	2.136
MEAN	(n)=K1	4.731	0.088	3.815	0.017	3.675	0.017	5.975	0.674
	(n)=K96	12.544	0.088	7.015	0.017	6.892	0.017	9.273	0.674
MEANP	(n)=K1	4.896	0.971	4.012	0.882	3.871	0.882	7.437	2.136
	(n)=K96	12.659	0.971	7.217	0.882	7.104	0.882	10.735	2.136
MEAN_U	(n)=K1	4.764	0.087	3.815	0.017	3.675	0.017	5.975	0.674
	(n)=K96	12.579	0.087	7.015	0.017	6.892	0.017	9.273	0.674
MEANP_U	(n)=K1	4.912	0.971	4.012	0.882	3.871	0.882	7.437	2.136
	(n)=K96	12.642	0.971	7.217	0.882	7.104	0.882	10.735	2.136
DMEAN	(n)=K1	5.264	0.084	4.167	0.017	4.049	0.017	6.415	0.674
	(n)=K96	13.976	0.084	9.999	0.017	9.865	0.017	12.164	0.674
DMEANP	(n)=K1	5.448	0.973	4.392	0.882	4.252	0.882	7.877	2.136
	(n)=K96	14.039	0.973	10.139	0.882	10.009	0.882	13.626	2.136
DMEAN_U	(n)=K1	5.164	0.084	4.167	0.017	4.049	0.017	6.415	0.674
	(n)=K96	13.914	0.084	9.999	0.017	9.865	0.017	12.164	0.674
DMEANP_U	(n)=K1	5.339	0.971	4.392	0.882	4.252	0.882	7.877	2.136
	(n)=K96	14.000	0.971	10.139	0.882	10.009	0.882	13.626	2.136
SQRT	—	2.964	0.090	2.533	0.017	2.425	0.017	4.316	0.674
SQRTP	—	3.114	0.971	2.741	0.882	2.648	0.882	5.027	2.136
DSQRT	—	3.467	0.087	3.067	0.017	2.933	0.017	4.824	0.674
DSQRTP	—	3.679	0.966	3.237	0.882	3.157	0.882	6.286	2.136
CCD	(n)=K1 SM8161=OFF	5.631	0.087	4.633	0.017	4.392	0.017	6.923	0.674
	(n)=K96 SM8161=OFF	14.346	0.087	9.311	0.017	9.024	0.017	11.491	0.674
	(n)=K1 SM8161=ON	5.542	0.087	4.633	0.017	4.392	0.017	6.923	0.674
	(n)=K96 SM8161=ON	13.275	0.087	8.073	0.017	7.817	0.017	10.365	0.674
CCDP	(n)=K1 SM8161=OFF	5.764	0.972	5.257	0.882	5.257	0.882	8.385	2.136
	(n)=K96 SM8161=OFF	14.580	0.972	9.375	0.882	9.169	0.882	11.921	2.136
	(n)=K1 SM8161=ON	5.656	0.972	5.257	0.882	5.257	0.882	8.385	2.136
	(n)=K96 SM8161=ON	13.366	0.972	8.281	0.882	7.984	0.882	10.648	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
CRC	(n)=K1 SM8161=OFF	5.903	0.087	4.967	0.017	4.659	0.017	7.189	0.674
	(n)=K96 SM8161=OFF	28.151	0.087	15.028	0.017	14.724	0.017	17.260	0.674
	(n)=K1 SM8161=ON	5.864	0.087	4.967	0.017	4.659	0.017	7.189	0.674
	(n)=K96 SM8161=ON	19.299	0.087	14.399	0.017	14.107	0.017	16.651	0.674
CRCP	(n)=K1 SM8161=OFF	6.070	0.975	5.177	0.882	4.844	0.882	8.651	2.136
	(n)=K96 SM8161=OFF	28.178	0.975	15.216	0.882	14.829	0.882	17.440	2.136
	(n)=K1 SM8161=ON	6.024	0.975	5.177	0.882	4.844	0.882	8.651	2.136
	(n)=K96 SM8161=ON	19.236	0.975	14.493	0.882	14.195	0.882	18.113	2.136
ADRSET	—	2.598	0.090	2.300	0.017	2.208	0.017	4.099	0.674
ADRSETP	—	2.860	0.973	2.509	0.882	2.436	0.882	4.801	2.136
TRD	—	3.164	0.088	2.600	0.017	2.549	0.017	3.657	0.674
TRDP	—	3.404	0.971	2.756	0.882	2.741	0.882	4.351	2.136
TWR	—	7.270	0.087	6.143	0.017	5.149	0.017	7.037	0.674
TWRP	—	7.394	0.972	6.272	0.882	5.321	0.882	8.499	2.136
TADD	—	5.534	0.090	4.900	0.017	4.616	0.017	7.329	0.674
TADDP	—	5.667	0.972	5.061	0.882	4.819	0.882	8.791	2.136
TSUB	—	5.431	0.090	4.867	0.017	4.608	0.017	7.316	0.674
TSUBP	—	5.592	0.972	5.083	0.882	4.799	0.882	8.778	2.136
HTOS	—	3.498	0.084	3.012	0.017	2.925	0.017	4.984	0.674
HTOSP	—	3.682	0.974	3.245	0.882	3.111	0.882	6.446	2.136
DHTOS	—	3.531	0.087	3.012	0.017	2.925	0.017	4.984	0.674
DHTOSP	—	3.752	0.976	3.245	0.882	3.111	0.882	6.446	2.136
STOH	—	3.331	0.090	2.867	0.017	2.741	0.017	4.800	0.674
STOHP	—	3.551	0.972	3.064	0.882	2.945	0.882	5.497	2.136
DSTOH	—	3.368	0.088	2.867	0.017	2.741	0.017	4.800	0.674
DSTOHP	—	3.519	0.971	3.064	0.882	2.945	0.882	5.497	2.136
LDDT=	—	5.631	—	4.867	—	4.549	—	7.107	—
LDDT<>	—	4.867	—	4.867	—	4.549	—	7.107	—
LDDT>	—	4.867	—	4.867	—	4.549	—	7.107	—
LDDT<=	—	4.867	—	4.867	—	4.549	—	7.107	—
LDDT<	—	4.867	—	4.867	—	4.549	—	7.107	—
LDDT>=	—	4.867	—	4.867	—	4.549	—	7.107	—
ANDDT=	—	5.731	—	4.867	—	4.549	—	7.107	—
ANDDT<>	—	4.867	—	4.867	—	4.549	—	7.107	—
ANDDT>	—	4.867	—	4.867	—	4.549	—	7.107	—
ANDDT<=	—	4.867	—	4.867	—	4.549	—	7.107	—
ANDDT<	—	4.867	—	4.867	—	4.549	—	7.107	—
ANDDT>=	—	4.867	—	4.867	—	4.549	—	7.107	—

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
ORDT=	—	5.731	—	4.867	—	4.549	—	7.107	—
ORDT<>	—	4.867	—	4.867	—	4.549	—	7.107	—
ORDT>	—	4.867	—	4.867	—	4.549	—	7.107	—
ORDT<=	—	4.867	—	4.867	—	4.549	—	7.107	—
ORDT<	—	4.867	—	4.867	—	4.549	—	7.107	—
ORDT>=	—	4.867	—	4.867	—	4.549	—	7.107	—
LDTM=	—	5.235	—	4.615	—	4.317	—	6.843	—
LDTM<>	—	5.263	—	4.615	—	4.317	—	6.843	—
LDTM>	—	5.267	—	4.615	—	4.317	—	6.843	—
LDTM<=	—	5.363	—	4.615	—	4.317	—	6.843	—
LDTM<	—	5.298	—	4.615	—	4.317	—	6.843	—
LDTM>=	—	5.296	—	4.615	—	4.317	—	6.843	—
ANDTM=	—	5.296	—	4.615	—	4.317	—	6.843	—
ANDTM<>	—	5.368	—	4.615	—	4.317	—	6.843	—
ANDTM>	—	5.430	—	4.615	—	4.317	—	6.843	—
ANDTM<=	—	5.464	—	4.615	—	4.317	—	6.843	—
ANDTM<	—	5.430	—	4.615	—	4.317	—	6.843	—
ANDTM>=	—	5.435	—	4.615	—	4.317	—	6.843	—
ORTM=	—	5.330	—	4.615	—	4.317	—	6.843	—
ORTM<>	—	5.396	—	4.615	—	4.317	—	6.843	—
ORTM>	—	5.399	—	4.615	—	4.317	—	6.843	—
ORTM<=	—	5.463	—	4.615	—	4.317	—	6.843	—
ORTM<	—	5.463	—	4.615	—	4.317	—	6.843	—
ORTM>=	—	5.432	—	4.615	—	4.317	—	6.843	—
TCMP	—	6.031	0.084	5.200	0.017	4.792	0.017	7.833	0.674
TCMPP	—	6.136	0.973	5.393	0.882	4.975	0.882	9.295	2.136
TZCP	—	7.631	0.087	6.767	0.017	6.225	0.017	9.381	0.674
TZCPP	—	7.736	0.975	6.939	0.882	6.409	0.882	10.843	2.136
DUTY	(n1)=K1, (n2)=K1	2.700	2.683	2.300	2.235	2.225	2.235	4.488	4.292
HOURM	—	4.998	0.087	4.000	0.017	3.841	0.017	6.000	0.674
DHOURM	—	4.799	0.087	4.000	0.017	3.841	0.017	6.000	0.674
RFS	(s)=Y, (n)=K1	4.332	0.087	3.500	0.017	3.400	0.017	4.741	0.674
	(s)=Y, (n)=K64	6.732	0.087	5.633	0.017	5.525	0.017	6.947	0.674
	(s)=X, (n)=K1	4.434	0.087	3.567	0.017	3.425	0.017	4.765	0.674
	(s)=X, (n)=K64	7.856	0.087	6.400	0.017	6.192	0.017	7.619	0.674
RFSP	(s)=Y, (n)=K1	4.522	0.972	3.707	0.882	3.611	0.882	5.460	2.136
	(s)=Y, (n)=K64	6.860	0.972	5.813	0.882	5.777	0.882	7.739	2.136
	(s)=X, (n)=K1	4.558	0.972	3.743	0.882	3.628	0.882	5.521	2.136
	(s)=X, (n)=K64	7.958	0.972	6.595	0.882	6.375	0.882	8.259	2.136
REF	(s)=Y, (n)=K1	4.336	0.088	3.500	0.017	3.400	0.017	4.741	0.674
	(s)=Y, (n)=K64	6.731	0.088	5.633	0.017	5.525	0.017	6.947	0.674
	(s)=X, (n)=K1	4.432	0.088	3.567	0.017	3.425	0.017	4.765	0.674
	(s)=X, (n)=K64	7.831	0.088	6.400	0.017	6.192	0.017	7.619	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
REFP	(s)=Y, (n)=K1	4.531	0.972	3.707	0.882	3.611	0.882	5.460	2.136
	(s)=Y, (n)=K64	6.862	0.972	5.813	0.882	5.777	0.882	7.739	2.136
	(s)=X, (n)=K1	4.564	0.972	3.743	0.882	3.628	0.882	5.521	2.136
	(s)=X, (n)=K64	7.959	0.972	6.595	0.882	6.375	0.882	8.259	2.136
FROM	(n)=K1	—	—	12.020	0.017	11.600	0.017	13.401	0.674
	(n)=K1000	—	—	662.387	0.017	617.560	0.017	619.787	0.674
FROMP	(n)=K1	—	—	12.591	0.882	11.697	0.882	14.055	2.136
	(n)=K1000	—	—	663.252	0.882	619.022	0.882	621.249	2.136
DFROM	(n)=K1	—	—	13.023	0.017	11.792	0.017	14.335	0.674
	(n)=K500	—	—	662.387	0.017	617.560	0.017	619.787	0.674
DFROMP	(n)=K1	—	—	13.888	0.882	11.865	0.882	15.797	2.136
	(n)=K500	—	—	663.252	0.882	619.022	0.882	621.249	2.136
TO	(n)=K1	—	—	11.801	0.017	11.476	0.017	13.395	0.674
	(n)=K1000	—	—	716.827	0.017	632.973	0.017	634.893	0.674
TOP	(n)=K1	—	—	11.928	0.882	11.673	0.882	14.012	2.136
	(n)=K1000	—	—	717.692	0.882	633.838	0.882	636.355	2.136
DTO	(n)=K1	—	—	12.715	0.017	11.637	0.017	13.484	0.674
	(n)=K500	—	—	716.827	0.017	632.973	0.017	634.893	0.674
DTOP	(n)=K1	—	—	12.872	0.882	11.717	0.882	14.055	2.136
	(n)=K500	—	—	717.692	0.882	633.838	0.882	636.355	2.136
FROMD	(n)=K1	—	—	12.240	0.017	11.564	0.017	13.372	0.674
	(n)=K1000	—	—	662.387	0.017	618.080	0.017	619.893	0.674
FROMDP	(n)=K1	—	—	12.559	0.882	11.653	0.882	13.948	2.136
	(n)=K1000	—	—	663.252	0.882	618.945	0.882	621.355	2.136
DFROMD	(n)=K1	—	—	13.081	0.017	11.748	0.017	13.512	0.674
	(n)=K500	—	—	662.387	0.017	618.080	0.017	619.893	0.674
DFROMDP	(n)=K1	—	—	13.946	0.882	11.833	0.882	14.139	2.136
	(n)=K500	—	—	663.252	0.882	618.945	0.882	621.355	2.136
TOD	(n)=K1	—	—	11.753	0.017	11.548	0.017	13.368	0.674
	(n)=K1000	—	—	716.307	0.017	633.040	0.017	634.893	0.674
TODP	(n)=K1	—	—	12.687	0.882	11.631	0.882	13.953	2.136
	(n)=K1000	—	—	717.172	0.882	633.905	0.882	636.355	2.136
DTOD	(n)=K1	—	—	12.687	0.017	11.631	0.017	13.425	0.674
	(n)=K500	—	—	716.307	0.017	633.040	0.017	634.893	0.674
DTODP	(n)=K1	—	—	12.845	0.882	11.696	0.882	14.011	2.136
	(n)=K500	—	—	717.172	0.882	633.905	0.882	636.355	2.136
RBFM	(s)=K0, (n1)=K1	—	—	—	—	11.496	1.359	13.421	2.853
	(s)=K0, (n1)=K1000	—	—	—	—	34.631	1.360	35.936	2.851
WBFM	(s)=K0, (n1)=K1	—	—	—	—	10.583	1.341	12.337	2.835
	(s)=K0, (n1)=K1000	—	—	—	—	9.599	1.342	11.319	2.836
LOGTRG	—	27.467	0.971	27.467	0.882	23.867	0.882	25.333	2.136
LOGTRGR	—	6.400	0.976	6.400	0.882	6.400	0.882	8.000	2.136

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
SP.SOCOPEN	—	11.867	1.083	10.400	0.882	8.817 (7.867)	0.882	11.750 (10.800)	2.136
SP.SOCCLOSE	—	9.067	1.082	7.867	0.882	7.083 (6.133)	0.882	9.617 (8.667)	2.526
SP.SOCRCV	TCP Receive data length = 1 byte	9.734	1.082	8.400	0.882	7.350 (6.400)	0.882	10.283 (9.333)	2.136
	TCP Receive data length = 2046 bytes	9.734	1.082	8.400	0.882	7.483 (6.533)	0.882	10.683 (9.733)	2.136
	UDP Receive data length = 1 byte	9.734	1.082	8.400	0.882	7.483 (6.533)	0.882	10.683 (9.733)	2.136
	UDP Receive data length = 2046 bytes	9.734	1.082	8.400	0.882	7.483 (6.533)	0.882	10.683 (9.733)	2.136
SP.FTPPUT	File name + folder path of the FTP server = 32 characters	60.800	1.067	59.867	0.882	44.267	0.882	53.733	2.136
	File name + folder path of the FTP server = 64 characters	114.400	1.067	102.533	0.882	74.000	0.882	90.267	2.136
SP.FTPGET	File name + folder path of the FTP server = 32 characters	62.534	1.067	61.333	0.882	46.133	0.882	55.333	2.136
	File name + folder path of the FTP server = 64 characters	110.534	1.067	110.800	0.882	80.800	0.882	97.333	2.136
SP.SOC SND	TCP Receive data length = 1 byte	14.267	1.082	11.733	0.882	10.817 (9.867)	0.882	13.617 (12.667)	2.136
	TCP Receive data length = 2046 bytes	133.734	1.082	131.600	0.882	70.683 (69.733)	0.882	74.017 (73.067)	2.136
	UDP Receive data length = 1 byte	13.334	1.082	11.333	0.882	10.950 (10.000)	0.882	14.283 (13.333)	2.136
	UDP Receive data length = 2046 bytes	133.600	1.082	131.600	0.882	70.683 (69.733)	0.882	74.017 (73.067)	2.136
SP.SOCCINF	—	7.467	0.972	6.267	0.882	5.467	0.882	7.867	2.205

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
S.SOCRDATA	(n)=K1	5.440	0.087	4.456	0.017	4.009	0.017	6.761	0.674
	(n)=K1024	146.036	0.087	144.305	0.017	104.037	0.017	106.584	0.674
SP.SOCRDATA	(n)=K1	5.782	0.978	4.721	0.882	4.281	0.882	7.192	2.136
	(n)=K1024	146.328	0.978	144.337	0.882	104.147	0.882	107.397	2.136
SP.ECPRTCL	—	16.000	0.971	14.667	0.882	10.400	0.882	13.333	2.205
PID	—	6.066	6.197	5.115	4.996	4.925	4.996	6.785	7.016
LD [S□]	Continuity/ Non-continuity	—	—	—	—	1.400	1.400	1.700	1.700
LD [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.700	2.700
LD [BL□]	Continuity/ Non-continuity	—	—	—	—	1.100	1.100	1.500	1.500
LDI [S□]	Continuity/ Non-continuity	—	—	—	—	1.100	1.100	1.700	1.700
LDI [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.700	2.700
LDI [BL□]	Continuity/ Non-continuity	—	—	—	—	1.100	1.100	1.500	1.500
AND [S□]	Continuity/ Non-continuity	—	—	—	—	1.400	1.400	1.800	1.800
AND [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.700	2.700
AND [BL□]	Continuity/ Non-continuity	—	—	—	—	1.200	1.200	1.500	1.500
ANI [S□]	Continuity/ Non-continuity	—	—	—	—	1.400	1.400	1.700	1.700
ANI [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.600	2.600
ANI [BL□]	Continuity/ Non-continuity	—	—	—	—	1.200	1.200	1.500	1.500
OR [S□]	Continuity/ Non-continuity	—	—	—	—	1.400	1.400	1.700	1.700
OR [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.700	2.700
OR [BL□]	Continuity/ Non-continuity	—	—	—	—	1.100	1.100	1.500	1.500
ORI [S□]	Continuity/ Non-continuity	—	—	—	—	1.400	1.400	1.700	1.700
ORI [BL□\S□]	Continuity/ Non-continuity	—	—	—	—	2.300	2.300	2.700	2.700
ORI [BL□]	Continuity/ Non-continuity	—	—	—	—	1.200	1.200	1.500	1.500
MOV [KnS□]	—	—	—	—	—	3.900	3.900	5.000	5.000
MOV [BL□\KnS□]	—	—	—	—	—	4.200	4.200	5.300	5.300
MOVP [KnS□]	—	—	—	—	—	3.900	3.900	5.000	5.000
MOVP [BL□\KnS□]	—	—	—	—	—	4.200	4.200	5.300	5.300
DMOV [KnS□]	—	—	—	—	—	4.100	4.100	5.100	5.100
DMOV [BL□\KnS□]	—	—	—	—	—	4.400	4.400	5.400	5.400
DMOVP [KnS□]	—	—	—	—	—	4.100	4.100	5.100	5.100
DMOVP [BL□\KnS□]	—	—	—	—	—	4.400	4.400	5.400	5.400

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
BMOV [KnS□]	(n)=K1	—	—	—	—	5.400	5.400	6.900	6.900
	(n)=K96	—	—	—	—	13.500	13.500	15.000	15.000
BMOV [BL□\KnS□]	(n)=K1	—	—	—	—	5.500	5.500	7.000	7.000
	(n)=K96	—	—	—	—	13.500	13.500	15.000	15.000
BMOVP [KnS□]	(n)=K1	—	—	—	—	5.400	5.400	6.900	6.900
	(n)=K96	—	—	—	—	13.500	13.500	15.000	15.000
BMOVP [BL□\KnS□]	(n)=K1	—	—	—	—	5.500	5.500	7.000	7.000
	(n)=K96	—	—	—	—	13.500	13.500	15.000	15.000
SET [S□]	No change time	—	—	—	—	4.300	4.300	4.800	4.800
	Change time	—	—	—	—	7.500	7.500	8.000	8.000
SET [BL□\S□]	No change time	—	—	—	—	4.100	4.100	4.700	4.700
	Change time	—	—	—	—	7.300	7.300	7.800	7.800
SET [BL□]	No change time	—	—	—	—	1.300	1.300	1.800	1.800
	Change time	—	—	—	—	1.300	1.300	1.800	1.800
RST [S□]	No change time	—	—	—	—	2.900	2.900	3.500	3.500
	Change time	—	—	—	—	3.900	3.900	4.400	4.400
RST [BL□\S□]	No change time	—	—	—	—	2.800	2.800	3.300	3.300
	Change time	—	—	—	—	3.800	3.800	4.300	4.300
RST [BL□]	No change time	—	—	—	—	1.700	1.700	2.200	2.200
	Change time	—	—	—	—	1.700	1.700	2.200	2.200
OUT [S□]	No change time	—	—	—	—	4.200	4.200	4.900	4.900
	Change time	—	—	—	—	7.400	7.400	8.100	8.100
OUT [BL□\S□]	No change time	—	—	—	—	4.400	4.400	4.700	4.700
	Change time	—	—	—	—	7.500	7.500	7.900	7.900
ZRST [S□]	1 point	—	—	—	—	7.100	7.100	8.200	8.200
	128 points	—	—	—	—	2943.400	2955.500	2954.700	2959.400
	4096 points	—	—	—	—	2946.400	2959.400	2946.100	2961.100
ZRST [BL□\S□]	1 point	—	—	—	—	6.600	6.600	7.700	7.700
	128 points	—	—	—	—	461.300	462.900	452.100	464.600
	4096 points	—	—	—	—	9600.100	9608.000	9598.300	9609.600
ZRSTP [S□]	1 point	—	—	—	—	7.100	7.100	8.200	8.200
	128 points	—	—	—	—	2943.400	2955.500	2954.700	2959.400
	4096 points	—	—	—	—	2946.400	2959.400	2946.100	2961.100
ZRSTP [BL□\S□]	1 point	—	—	—	—	6.600	6.600	7.700	7.700
	128 points	—	—	—	—	461.300	462.900	452.100	464.600
	4096 points	—	—	—	—	9600.100	9608.000	9598.300	9609.600
ABSD	(n)=K1	6.966	0.084	5.551	0.017	5.551	0.017	8.104	0.674
DABSD	(n)=K1	7.894	0.087	6.000	0.017	5.551	0.017	8.104	0.674
INCD	(n)=K1	8.886	1.187	7.216	1.149	6.725	1.149	9.947	2.475
IST	—	5.936	1.082	5.076	1.056	4.167	1.056	5.897	2.380
DHSCS	—	8.547	0.088	7.400	0.017	5.667	0.017	8.613	0.674

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DHSCR	—	9.814	0.088	8.640	0.017	6.813	0.017	10.480	0.674
DHSZ	—	9.840	0.088	8.600	0.017	6.853	0.017	10.280	0.674
HCMOV	(n)=K0	5.398	0.087	4.567	0.017	4.375	0.017	6.737	0.674
HCMOVP	—	5.563	0.969	4.771	0.882	4.573	0.882	7.271	2.136
DHCMOV	(n)=K0	5.464	0.087	4.567	0.017	4.375	0.017	6.737	0.674
DHCMOVP	—	5.630	0.968	4.771	0.882	4.573	0.882	7.271	2.136
RS2	—	17.200	1.161	14.533	1.091	13.083 (12.133)	1.091	16.017 (15.067)	2.350
IVCK	—	13.867	1.469	12.933	1.371	12.283 (11.333)	1.371	14.950 (14.000)	2.706
IVDR	—	14.934	1.498	13.867	1.412	13.083 (12.133)	1.412	15.750 (14.800)	2.746
IVRD	—	17.467	1.507	16.667	1.395	15.750 (14.800)	1.395	18.417 (17.467)	2.732
IVWR	—	18.267	1.512	17.467	1.396	16.550 (15.600)	1.396	19.217 (18.267)	2.733
IVBWR	—	20.400	1.462	19.467	1.362	18.283 (17.333)	1.362	21.350 (20.400)	2.696
IVMC	—	18.400	1.505	16.667	1.412	15.883 (14.933)	1.412	19.750 (18.800)	2.748
ADPRW	—	25.600	1.210	23.467	1.149	18.150 (17.200)	1.149	23.083 (22.133)	2.402
S.CPRTCL	—	12.000	0.084	10.533	0.017	7.867	0.017	10.667	0.674
SP.CPRTCL	—	12.267	0.974	10.800	0.882	8.133	0.882	11.067	2.136
MTR	—	2.404	1.147	2.303	1.098	1.935	1.098	3.240	2.422
DSW	—	7.132	1.177	6.473	1.148	5.267	1.148	8.220	2.474
SEGD	—	3.899	0.087	3.400	0.017	3.292	0.017	4.816	0.674
SEGDP	—	4.006	0.972	3.531	0.882	3.468	0.882	5.515	2.136
SEGL	(n)=K1	7.102	1.184	5.940	1.148	5.251	1.148	7.929	2.471
DSZR	(s1)=K1, (s2)=K1	145.067	6.609	145.067	6.609	134.683 (133.733)	6.609	138.283 (137.333)	8.166
DDSZR	(s1)=K1, (s2)=K1	145.067	6.609	145.067	6.609	134.683 (133.733)	6.609	138.283 (137.333)	8.166
DVIT	(s1)=K100, (s2)=K1	142.533	6.585	142.533	6.585	133.217 (132.267)	6.585	136.017 (135.067)	8.141
DDVIT	(s1)=K100, (s2)=K1	142.533	6.585	142.533	6.585	132.267	6.585	135.067	8.141
TBL	(d)=K1, (n)=K1	142.667	6.575	142.667	6.575	133.883 (132.933)	6.575	136.417 (135.467)	8.097
DRV_TBL	—	145.733	6.581	145.733	6.581	136.417 (135.467)	6.581	139.350 (138.400)	8.104
DABS	—	3.200	1.292	2.933	1.216	2.533	1.216	4.000	2.718
PLSV	(s)=K1	135.200	6.590	135.200	6.590	127.483 (126.533)	6.590	130.017 (129.067)	8.113
DPLSV	(s)=K1	135.200	6.590	135.200	6.590	126.533	6.590	129.067	8.113
DRV_I	(s1)=K1, (s2)=K1	138.133	6.585	138.133	6.585	130.417 (129.467)	6.585	133.217 (132.267)	8.141
DDRVI	(s1)=K1, (s2)=K1	138.133	6.585	138.133	6.585	129.467	6.585	132.267	8.141
DRVA	(s1)=K500, (s2)=K1	138.267	6.584	138.267	6.584	130.417 (129.467)	6.584	133.217 (132.267)	8.141

Instruction name	Condition	FX5S CPU module		FX5UJ CPU module		FX5U/FX5UC CPU module			
		Program capacity setting: 48000 steps		Program capacity setting: 48000 steps		Program capacity setting: 64000 steps		Program capacity setting: 128000 steps	
		Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs)	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)	Execution time in ON status (μs) ^{*1}	Execution time in OFF status (μs)
DDRVA	(s1)=K500, (s2)=K1	138.267	6.584	138.267	6.584	129.467	6.584	132.267	8.141
DRVMUL	—	578.134	6.842	528.133	6.842	529.083 (528.133)	6.842	532.683 (531.733)	8.287
HIOEN	—	15.754	0.090	14.897	0.017	12.416	0.017	14.631	0.674
HIOENP	—	16.060	0.975	15.085	0.882	12.620	0.882	14.836	2.136
DHIOEN	—	15.762	0.087	14.881	0.017	12.385	0.017	14.428	0.674
DHIOENP	—	16.019	0.976	15.079	0.882	12.627	0.882	14.801	2.136
STL	—	4.667	—	4.000	—	3.333	—	4.800	—
RETSTL	—	2.000	—	1.867	—	1.467	—	2.667	—
SP.SLMP SND	—	13.867	0.971	12.533	0.882	10.017 (9.067)	0.882	12.950 (12.000)	2.136
ERREAD	(n)=K1	11.600	2.483	10.133	2.168	9.217 (8.267)	2.168	12.150 (11.200)	3.834
	(n)=K0 (all points)	11.600	2.483	10.133	2.168	9.350 (8.400)	2.168	12.150 (11.200)	3.832
ERWRITE	(n)=K1	11.734	2.480	10.267	2.140	9.483 (8.533)	2.140	12.150 (11.200)	3.806
	(n)=K0 (all points)	11.734	2.479	10.133	2.168	9.350 (8.400)	2.168	12.150 (11.200)	3.832
ERINIT	—	10.534	2.392	9.067	2.036	8.283 (7.333)	2.036	10.417 (9.467)	3.691
RTM	—	1.334	0.091	0.933	0.017	0.800	0.017	1.867	0.674
S.DEVLD	(n)=K1	4.500	0.094	3.467	0.017	3.259	0.017	5.833	0.674
	(n)=K64	13.170	0.094	12.063	0.017	9.491	0.017	12.051	0.674
SP.DEVLD	(n)=K1	4.667	1.086	3.600	0.882	3.333	0.882	7.295	2.136
	(n)=K64	13.200	1.086	12.133	0.882	10.356	0.882	13.513	2.136
SP.DEVST	(n)=K1	20.400	1.092	16.133	0.882	12.667	0.882	15.600	2.136
	(n)=K64	25.067	1.092	23.467	0.882	16.400	0.882	19.333	2.136
SP.FREAD	—	21.867	1.067	20.133	0.882	17.733	0.882	21.333	2.136
SP.FWRITE	—	22.134	1.067	20.267	0.882	17.733	0.882	21.333	2.136
SP.FDELETE	—	47.867	1.067	46.000	0.882	28.933	0.882	34.267	2.136
SP.FCOPY	—	85.334	1.067	83.067	0.882	50.133	0.882	57.067	2.136
SP.FMOVE	—	85.467	1.067	83.067	0.882	50.000	0.882	56.800	2.136
SP.FRENAME	—	86.934	1.200	84.533	0.882	51.467	0.882	58.933	2.136
SP.FSTATUS	—	50.667	1.200	48.133	0.882	30.667	0.882	36.533	2.136

*1 The value in the parentheses is the value before supporting the SFC program.