

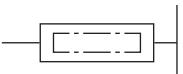
6.7 Stop Instruction

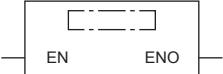
Stopping the sequence program

STOP

FX5S FX5UJ FX5U FX5UC

This instruction resets outputs (Y) and stops operation of the CPU module when the execution command turns ON. (This operation is the same as setting the switch to STOP.)

Ladder diagram	Structured text
	ENO:=STOP(EN);

FBD/LD


Processing details

- This instruction resets outputs (Y) and stops operation of the CPU module when the execution command turns ON. (This operation is the same as setting the switch to STOP.)
- To restart operation of the CPU module after this instruction is executed, return the switch from RUN→STOP and set it to RUN again.

Operation error

Error code (SD0/SD8067)	Description
3340H	The STOP instruction is executed before the NEXT instruction is executed after the FOR instruction is executed.
3381H	The STOP instruction is executed before the RET instruction is executed after the CALL(P) or XCALL(P) instruction is executed.
3582H	The STOP instruction is executed before the IRET instruction is executed in the interruption program.