

# Two's complement of 32-bit binary data (sign inversion)

## DNEG(P)

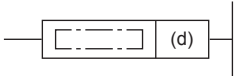
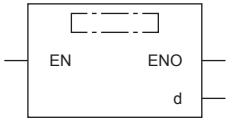
FX5S

FX5UJ

FX5U

FX5UC

These instructions invert the sign of the 32-bit binary data in the device specified by (d), and store the resultant data in the device specified by (d).

Ladder diagram	Structured text
	ENO:=DNEG(EN,d); ENO:=DNEGP(EN,d);
FBD/LD	
	

## Setting data

### ■Descriptions, ranges, and data types

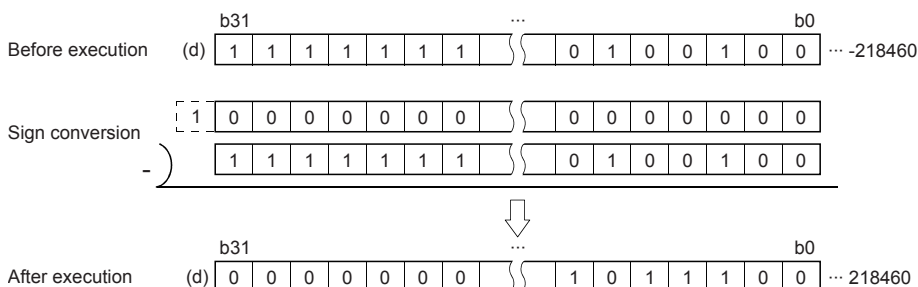
Operand	Description	Range	Data type	Data type (label)
(d)	Head device for storing the data that performs two's complement	-2147483648 to +2147483647	32-bit signed binary	ANY32
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

### ■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	\$	
(d)	○	○	○	○	○	○	○	—	—	—	—

## Processing details

- These instructions invert the sign of the 32-bit binary data in the device specified by (d), and store the resultant data in the device specified by (d).
- They are used when a positive or negative sign is to be inverted.



## Precautions

Note that data is inverted in every operation cycle in a continuous operation type (DNEG) instruction.

## Operation error

There is no operation error.