Pausing a block

PAUSE [BL□]
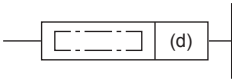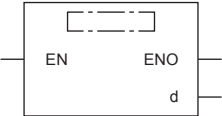
FX5S **FX5UJ** **FX5U** **FX5UC**

This instruction temporarily stops the step sequence in the specified block.

Ladder diagram	Structured text
(d)	ENO:=PAUSE(EN,d);

FBD/LD
EN ENO d

■Execution condition

Instruction	Execution condition
PAUSE	⎍

Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Block No. where the sequence is temporarily stopped	—	Bit	ANY_BOOL
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	$	(BL□)
(d)	—	—	—	—	—	—	—	—	—	—	○

Processing details

- This instruction temporarily stops the step sequence in the specified block.
- All the steps become inactive immediately after execution of the instruction.
- The coil output status caused by the OUT instruction at the temporary stop depends on the status of SM325 (output mode at pausing a block).

Status of SM325	Coil output status by using the OUT instruction
On	During the stop, the coil output remains the state immediately before stop.
Off	The coil output turns off at stop.

Precautions

If the sequence is stopped while SM325 is off, coil HOLD steps become inactive. The sequence cannot be restarted with the hold status. If the sequence is stopped while SM325 is on, the sequence can be restarted with the hold status.

Operation error

Error code (SD0)	Description
31A1H	The specified block does not exist.
	The instruction is executed when no SFC program (scan execution type) exists or SM321 (start/stop SFC program) is off.
31A2H	The specified block No. is out of the range.
3582H	The SFC control instruction is used in the interrupt routine program.

Point

Operation of the PAUSE and RESTART instructions depends on the combination of the SM325 status and step hold status. For details, refer to the following.

MELSEC iQ-F FX5 Programming Manual (Program Design)