

Inverting the bit device output

ALT(P)

FX5S FX5UJ FX5U FX5UC

These instructions reverse (ON ↔ OFF) bit devices when input turns ON.

Ladder diagram	Structured text
	ENO:=ALT(EN,d); ENO:=ALTP(EN,d);
FBD/LD	

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Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Bit device number whose output is alternated	—	Bit	ANY_BOOL
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
		X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC		K, H	E	\$	
(d)	○	○ ^{*1}	○ ^{*2}	—	—	—	—	—	—	—	—

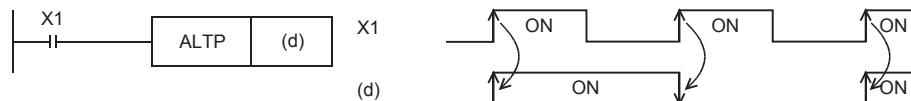
*1 T, ST, and C cannot be used.

*2 Only the FX5 intelligent function module can be used.

Processing details

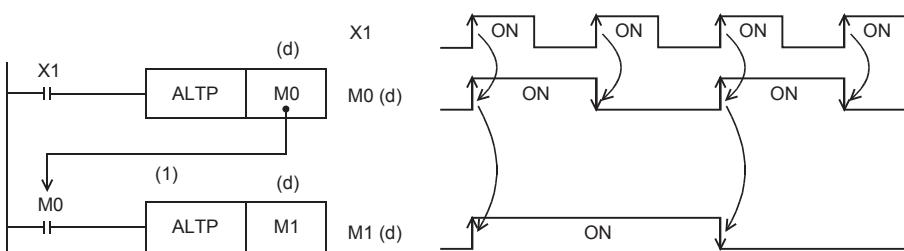
■Alternating output (1-step)

The bit device specified by (d) is reversed ON ↔ OFF each time the command input changes from OFF to ON.



■Division output (according to alternating output (2-step))

The ALTP instruction can be used in multiple combinations to perform division output.



Precautions

When the CPU module is programmed with the ALT instruction, reversal operation is performed at every operation cycle. To perform reversal operation by command ON/OFF, either use the ALTP instruction (pulse execution type) or set a command contact as LDP etc. (pulse execution type).

Operation error

There is no operation error.