

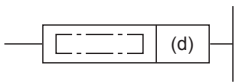
Activating a step

SET [S□/BL□\S□]

- FX5S
- FX5UJ
- FX5U
- FX5UC

This instruction activates the specified step.

Ladder diagram



Structured text

ENO:=SET(EN,d);

FBD/LD



Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Step No. to be activated (Target bit device number to be set (on))	—	Bit	ANY_BOOL
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others (BL□\S□)
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	\$	
(d)	○*1*2	—	—	—	—	—	—	—	—	—	○*2

*1 Only S can be used.
*2 Indexing is not available.

Processing details

- This instruction activates the specified step in the specified block. The operation of the specified block will be as follows depending on the status (active or inactive) of the specified block.

Status of the specified block	Operation
Inactive	The specified block is activated at execution of the instruction, and starts the processing from the specified step.
Active	The specified step is newly activated while the step which has already been active continues processing.

- If the instruction is executed to an active step, the instruction is ignored and processing will continue. Note that if the specified step is holding the operating status, the hold status is reset. The step becomes a normal step, and executes the action and transition.
- If no block is specified, the following block is targeted depending on the execution program type.
 - Sequence program: Block 0
 - SFC program (within the action): Block where the instruction is executed (current block)

Precautions

- In a simultaneous sequence, specify and activate all the steps by using the SET instruction (Activating a step). If there is any inactive step left, a convergence is not performed. Likewise, if the RST instruction (Deactivating a step) is executed to a single step in a simultaneous sequence, a convergence condition is not satisfied.
- The own step cannot be specified as the step No. within the action of the SFC program. If specified, an error occurs.

Operation error

Error code (SD0/SD8067)	Description
31A1H	The specified block does not exist.
	The control target block does not exist. (SET/RST/OUT S□)
31A2H	When a block No. is specified, the specified block No. is out of the range of 0 to 31.
31B1H	The instruction is executed when no SFC program (Start/stop SFC program) exists or SM321 is off.
	No specified step exists in the specified block. (SET/RST/OUT BL□\S□/S□)
31B2H	The specified step No. is out of the range (0 to 511).
31B5H	The current step is specified within the action.
3582H	The SFC control instruction is used in the interrupt routine program.