

# 18 POSITIONING INSTRUCTION

## 18.1 Dedicated Instruction (Positioning Function)

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### Instruction correspondence table for CPU module built-in positioning and high-speed pulse input/output module

The table for instructions corresponding to the CPU module and the high-speed pulse input/output module are shown below.

○: Supported, —: Not supported

Instruction	CPU module	High-speed pulse input/output module
DSZR [For the FX3 compatible operand specification]	○	—
DSZR [For the FX5 operand specification]	○	○
DDSZR	○	○
DVIT [For the FX3 compatible operand specification]	○	—
DVIT [For the FX5 operand specification]	○	○
DDVIT [For the FX3 compatible operand specification]	○	—
DDVIT [For the FX5 operand specification]	○	○
TBL [For the FX3 compatible operand specification]	○	—
TBL [For the FX5 operand specification]	○	—
DRVTBL	○	○
DRVMUL	○	○
DABS	○	○
PLSV [For the FX3 compatible operand specification]	○	—
PLSV [For the FX5 operand specification]	○	○
DPLSV [For the FX3 compatible operand specification]	○	—
DPLSV [For the FX5 operand specification]	○	○
DRVI [For the FX3 compatible operand specification]	○	—
DRVI [For the FX5 operand specification]	○	○
DDRVI [For the FX3 compatible operand specification]	○	—
DDRVI [For the FX5 operand specification]	○	○
DRVA [For the FX3 compatible operand specification]	○	—
DRVA [For the FX5 operand specification]	○	○
DDRVA [For the FX3 compatible operand specification]	○	—
DDRVA [For the FX5 operand specification]	○	○