

17.2 Inverter Communication Instruction

Inverter operation monitoring (Status check)

IVCK

FX5S

FX5UJ

FX5U

FX5UC

This instruction reads the operation status of an inverter to the CPU module.

Ladder diagram	Structured text
	<pre>ENO:=IVCK(EN,s1,s2,n,d1,d2);</pre>

FBD/LD

Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(s1)	Inverter station number	K0 to 31	16-bit signed binary	ANY16
(s2)	Inverter instruction codes	*1	16-bit signed binary	ANY16
(d1)	Device number storing the read value	—	16-bit signed binary	ANY16
(n)	Communication channel	■FX5S/FX5UJ CPU module K2 to 4 ■FX5U CPU module K1 to 4 ■FX5UC CPU module K1, K3 to 4	16-bit unsigned binary	ANY16_U
(d2)	Head bit device to which the execution status of the instruction is output	—	Bit	ANYBIT_ARRAY (Number of elements: 3)
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL


*1 Refer to MELSEC iQ-F FX5 User's Manual (Communication) or respective inverter manual.

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	\$	
(s1)	—	○*1	○	○	—	—	○	○	—	—	—
(s2)	—	○*1	○	○	—	—	○	○	—	—	—
(d1)	○	○	○	—	—	—	○	—	—	—	—
(n)	—	—	—	—	—	—	—	○	—	—	—
(d2)	○	○*1	—	—	—	—	—	—	—	—	—

*1 T, ST, and C cannot be used.

Processing details

The operation status corresponding to the instruction code specified in (s2) of an inverter connected to the communication channel (n) whose station number is specified in (s1) is read and transferred to (d1). For details, refer to  MELSEC iQ-F FX5 User's Manual (Communication). (For the instruction codes, refer to the each inverter manual.)

Precautions

Three devices are occupied from the device specified in (d2). Make sure that these devices are not used in other controls.

Operation error

Error code (SD0/SD8067)	Description
1810H	Channel number specified by (d) is used by another instruction.
2820H	The specified device exceeds the range of the corresponding device.
3405H	The value specified by (s1) is other than any of K0 to 31. When the value specified for (n) is other than the following channel numbers. <ul style="list-style-type: none">• FX5S/FX5UJ CPU module K2 to 4• FX5U/FX5UC CPU module K1 to 4
3600H	Channel number specified by (d) is not set by parameters.

For communication errors, refer to  MELSEC iQ-F FX5 User's Manual (Communication).