

## 4.5 High-speed Counter Instruction

### High-speed processing instruction

#### ■Setting 32-bit data comparison

| Instruction symbol | Description  | Reference |
|--------------------|--|-----------|
| DHSCS              | Turns ON the bit device of (d) when the current value of the high-speed counter of CH specified by (s2) is changed to the value specified by (s1). | Page 1091 |

#### ■Reset 32-bit data comparison

| Instruction symbol | Description   | Reference |
|--------------------|---|-----------|
| DHSCR              | Turns OFF the bit device of (d) when the current value of the high-speed counter of CH specified by (s2) is changed to the value specified by (s1). | Page 1094 |

#### ■Comparison of 32-bit data band

| Instruction symbol | Description  | Reference |
|--------------------|--|-----------|
| DHSZ               | Compares whether the current value of the high-speed counter is within or outside the value range specified by (s1) or (s2). | Page 1097 |

#### ■Start/stop of the 16-bit/32-bit data high-speed I/O function

| Instruction symbol | Description  | Reference |
|--------------------|--|-----------|
| HIOEN              | Start or stop high-speed I/O for the specified CH. | Page 1100 |
| HIOENP             |  |           |
| DHIOEN             |  | Page 1104 |
| DHIOENP            |  |           |

### High-speed current value transfer instruction

#### ■High-speed current value transfer of 16-bit/32-bit data

| Instruction symbol | Description  | Reference |
|--------------------|--|-----------|
| HCMOV              | Transfers the current value of the high-speed I/O. | Page 1108 |
| HCMOVP             |  |           |
| DHCMOV             |  | Page 1110 |
| DHCMOVP            |  |           |