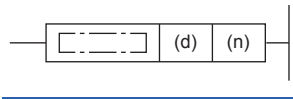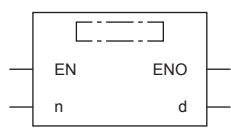# Resetting a bit in the word device

BRST(P)

| FX5S | FX5UJ | FX5U | FX5UC |

These instructions reset (to 0) (n)th bit of word device specified by (d).

Ladder diagram	Structured text
─┤ ┌─:─┐ ├─ (d) (n) ─┤	ENO:=BRST(EN,n,d); ENO:=BRSTP(EN,n,d);

FBD/LD
┌─:─┐ EN   ENO n   d

Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Head device for which bit is to be reset	—	16-bit signed binary	ANY16
(n)	Number of bit(s) to be reset	0 to 15	16-bit unsigned binary	ANY16
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	$	
(d)	○	○	○	○	—	—	○	—	—	—	—
(n)	○	○	○	○	—	—	○	○	—	—	—

Processing details

- These instructions reset (to 0) (n)th bit of word device specified by (d).
- If (n) exceeds 15, the processing will be done based on the lower 4 bits of (n).
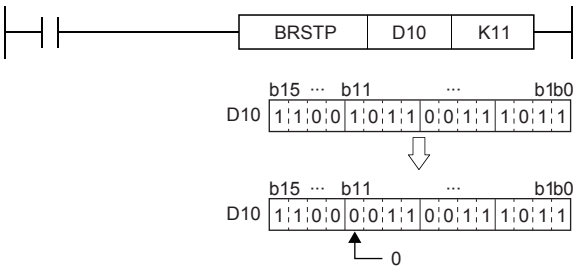
Operation error

There is no operation error.