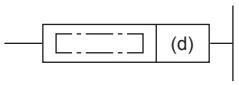
Ending a block

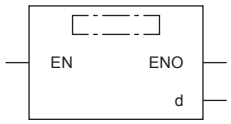
RST [BL□]

FX5S FX5UJ FX5U FX5UC

This instruction deactivates the specified block.

Ladder diagram	Structured text
	ENO:=RST(EN,d);

FBD/LD



Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Block No. to be deactivated (Target bit device number to be reset (off))	—	Bit	ANY_ELEMENTARY
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others (BL□)
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K,H	E	$	
(d)	—	—	—	—	—	—	—	—	—	—	○

Processing details

- This instruction deactivates the specified block.
- All the active steps are deactivated and coil outputs are turned off.
- If the instruction is executed to an inactive block, the instruction is ignored and processing will continue.

Operation error

Error code (SD0/SD8067)	Description
31A1H	The specified block does not exist.
	The instruction is executed when no SFC program (scan execution type) exists or SM321 is off.
31A2H	When the specified block No. is out of the range of 0 to 31.
3582H	The SFC control instruction is used in the interrupt routine program.