


Ending the sequence program

END

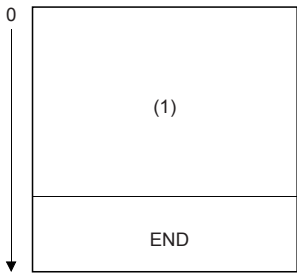
- FX5S
- FX5UJ
- FX5U
- FX5UC

This instruction indicates the end of a program.

Ladder diagram	Structured text
	Not supported.
FBD/LD	
Not supported.	

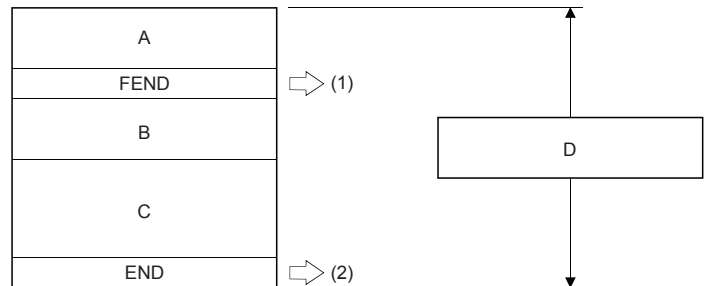
Processing details

- This instruction indicates the end of all programs including the main routine program, subroutine program, and interrupt program. When this instruction is executed, the CPU module ends execution of the currently executing program.



(1): Sequence program

- The first time the RUN is started, execution begins from this instruction.
- This instruction cannot be programmed midway during the main sequence program. When this processing is required midway during the program, use the FEND instruction.
- When programming is performed using the engineering tool in ladder edit mode, the END instruction is automatically input and cannot be edited.
- The following illustrates how the END and FEND instructions are used properly when a program contains a main routine program, subroutine program, and interrupt program.



- A: Main routine program
B: Subroutine program
C: Interrupt Program
D: Main sequence program area
(1): FEND instruction is required.
(2): END instruction is required.

The END instruction executed while a program is divided into multiple program blocks indicates the end of a program block.

The END instruction executed for END processing is executed at the end of the last executed program registered in the program settings.

Operation error

Error code (SD0/SD8067)	Description
3340H	The END instruction is executed before the NEXT instruction after the FOR instruction is executed.
3381H	The END instruction is executed before the RET instruction after the CALL(P) instruction is executed.
33E3H	The END instruction is programmed between FOR-NEXT.
33E4H	The END instruction is programmed between MC-MCR.
33E5H	The END instruction is programmed between STL-RETSTL.
33E7H	The END instruction is programmed between I-IRET.