

Exchanging 32-bit data

DXCH(P)

FX5S

FX5UJ

FX5U

FX5UC

These instructions exchange 32-bit binary data of (d1) and (d2).

Ladder diagram	Structured text
	<pre>ENO:=DXCH(EN,d1,d2); ENO:=DXCHP(EN,d1,d2);</pre>
FBD/LD	

Setting data

■Descriptions, ranges, and data types

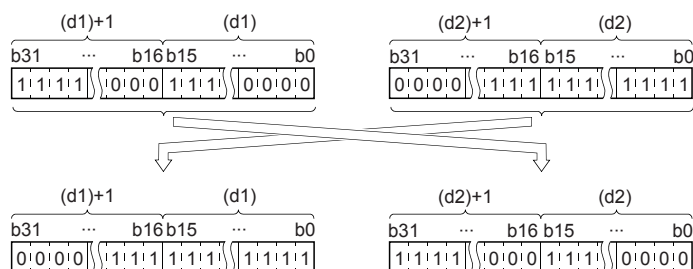
Operand	Description	Range	Data type	Data type (label)
(d1)	Head device for storing the data to be exchanged	—	32-bit signed binary	ANY32
(d2)	Head device for storing the data to be exchanged	—	32-bit signed binary	ANY32
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	\$	
(d1)	○	○	○	○	○	○	○	—	—	—	—
(d2)	○	○	○	○	○	○	○	—	—	—	—

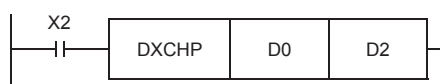
Processing details

These instructions exchange 32-bit binary data of (d1), (d1)+1 and (d2), (d2)+1



Program example

This program exchanges the contents of D0, D1 and D2, D3 when X2 is set to ON.



Operation error

There is no operation error.