


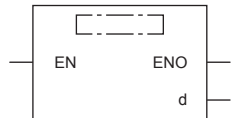
Exchanging the upper and lower bytes of 32-bit data

DSWAP(P)

FX5S FX5UJ FX5U FX5UC

These instructions swap the value of 8 bits of the upper and lower bytes of the devices specified by (d) and (d)+1.

Ladder diagram	Structured text
	ENO:=DSWAP(EN,d); ENO:=DSWAPP(EN,d);

FBD/LD


Setting data

■Descriptions, ranges, and data types

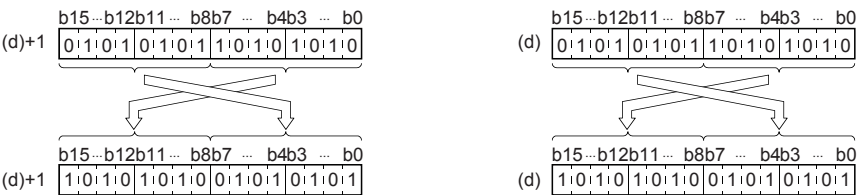
Operand	Description	Range	Data type	Data type (label)
(d)	Head device for storing the data to be exchanged	—	32-bit signed binary	ANY32
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word			Double word		Indirect specification	Constant			Others
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	\$	
(d)	○	○	○	○	○	○	○	—	—	—	—

Processing details

These instructions swap the value of each of the upper and lower 8 bits of the device specified by (d) and (d)+1.



Precautions

If a continuous operation type instruction is used, swap is done in each operation cycle.

Operation error

There is no operation error.