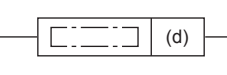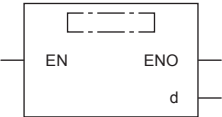# Deactivating a step

RST [S□/BL□\S□]

| FX5S | FX5UJ | FX5U | FX5UC |

This instruction deactivates the specified step.

Ladder diagram	Structured text
(d)	ENO:=RST(EN,d);

FBD/LD
EN ENO d

Setting data

■Descriptions, ranges, and data types

Operand	Description	Range	Data type	Data type (label)
(d)	Step No. to be deactivated (Target bit device number to be reset (off))	—	Bit	ANY_BOOL
EN	Execution condition	—	Bit	BOOL
ENO	Execution result	—	Bit	BOOL

■Applicable devices

Operand	Bit	Word		Double word		Indirect specification	Constant			Others (BL□\S□)
	X, Y, M, L, SM, F, B, SB, S	T, ST, C, D, W, SD, SW, R	U□\G□	Z	LC	LZ		K, H	E	$	
(d)	○*1*2	—	—	—	—	—	—	—	—	—	○*2

*1 Only S can be used.

*2 Indexing is not available.

Processing details

- This instruction deactivates the specified step in the specified block.
- When the number of active steps in the specified becomes 0, the specified block will be inactive.
- If the instruction is executed to an inactive step, the instruction is ignored and processing will continue.
- If no block is specified, the following block will be deactivated.
  - Sequence program: Block 0
  - SFC program (within the action): Block where the instruction is executed (current block)

Precautions

- If the step termination instruction is executed for a step in a parallel branch, the parallel convergence condition will not be met.
- The own step cannot be specified as the step No. within the action of the SFC program. If the own step No. is specified as the specified step No., an error will occur.

Operation error

Error code (SD0/SD8067)	Description
31A1H	The specified block does not exist.
	The control target block does not exist. (SET/RST/OUT S□)
31A2H	When a block No. is specified, the specified block No. is out of the range of 0 to 31.
31B1H	The instruction is executed when no SFC program (Start/stop SFC program) exists or SM321 is off.
	No specified step exists in the specified block. (SET/RST/OUT BL□\S□/S□)
31B2H	The specified step No. is out of the range (0 to 511).
31B5H	The current step is specified within the action.
3582H	The SFC control instruction is used in the interrupt routine program.