

**The HARTNDb::reSynthReInfer() function is crashing and causing a problem with Vivado synthesis.**

I'm experiencing a crashing issue with Vivado while synthesizing design files. My system runs on Linux, and I'm using Vivado version 2024.1. The relevant commands I'm using in the Ubuntu terminal are as follows:

```
vivado -mode tcl
```

```
read_verilog design.v
```

```
synth_design -top top
```

During the synthesis process, there's a crash, and a log is generated with information about the cause of the crash.

```
WARNING: [Synth 8-7080] Parallel synthesis criteria is not met
Abnormal program termination (11)
Please check '/home/user/vivado_project/hs_err_pid3648708.log' for details
segfault in /home/user/software/vivado2024/Vivado/2024.1/bin/unwrapped/lnx64.o/v
ivado -exec vivado -mode tcl, exiting...
user@user-System-Product-Name:~/vivado_project$ Parent process (pid 3648708) has
died. This helper process will now exit
```