

A critical error during Vivado synthesis is surfacing in the DFPin::range() function

I'm experiencing a crashing issue with Vivado while synthesizing design files. My system runs on Linux, and I'm using Vivado version 2023.2. The relevant commands I'm using in the Ubuntu terminal are as follows:

```
vivado -mode tcl  
read_verilog rtl.v  
synth_design -top top
```

During the synthesis process, there's a crash, and a log is generated with information about the cause of the crash.

```
Abnormal program termination (11)  
Please check '/home/user/vivado_project/hs_err_pid3165019.log' for details  
segfault in /home/user/software/vivado2023/Vivado/2023.2/bin/unwrapped/linux64.o/v  
ivado -exec vivado -mode tcl, exiting...  
user@user-System-Product-Name:~/vivado_project$ Parent process (pid 3165019) has  
died. This helper process will now exit
```