

Register specifier	microMIPS64	RV64GC	Thumb-2
3-bit	2-7,16, 17	8-15	0-7
Stack pointer register	29	2	0 (when used in load/store)
Global pointer register	28		
Return address register	31	1	14
Using special register	Stack pointer or global pointer; 5-bit offset	Stack pointer; 5-bit offset	Stack pointer; 8-bit offset

**FIGURE E.4** Register encodings for the 16-bit subsets of microMIPS64, RV64GC, and Thumb-2, including the core general purpose registers, and special-purpose registers accessible by some instructions.