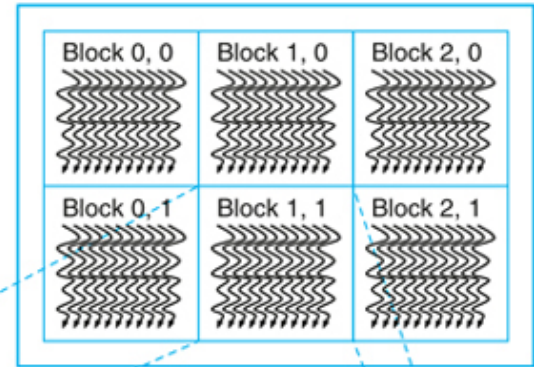


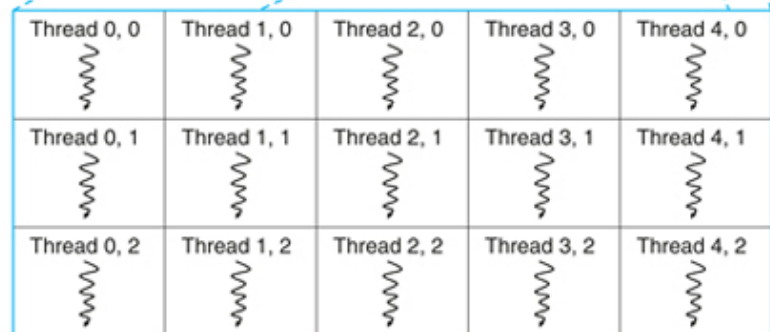
Sequence

kernelF 2D Grid is 3×2 thread blocks; each block is 5×3 threads

kernelF<<<(3, 2), (5, 3)>>>(params);



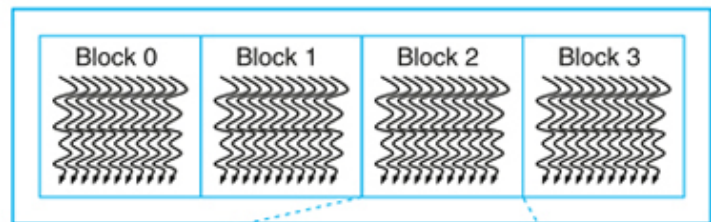
Block 1, 1



----- Interkernel Synchronization Barrier -----

kernelG 1D Grid is 4 thread blocks; each block is 6 threads

kernelG<<<4, 6>>>(params);



Block 2

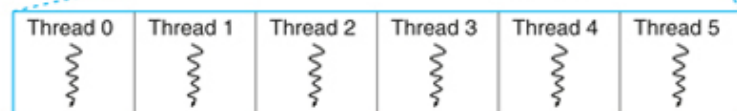


FIGURE C.3.6 Sequence of kernel *F* instantiated on a 2D grid of 2D thread blocks, an interkernel synchronization barrier, followed by kernel *G* on a 1D grid of 1D thread blocks.