Inputs

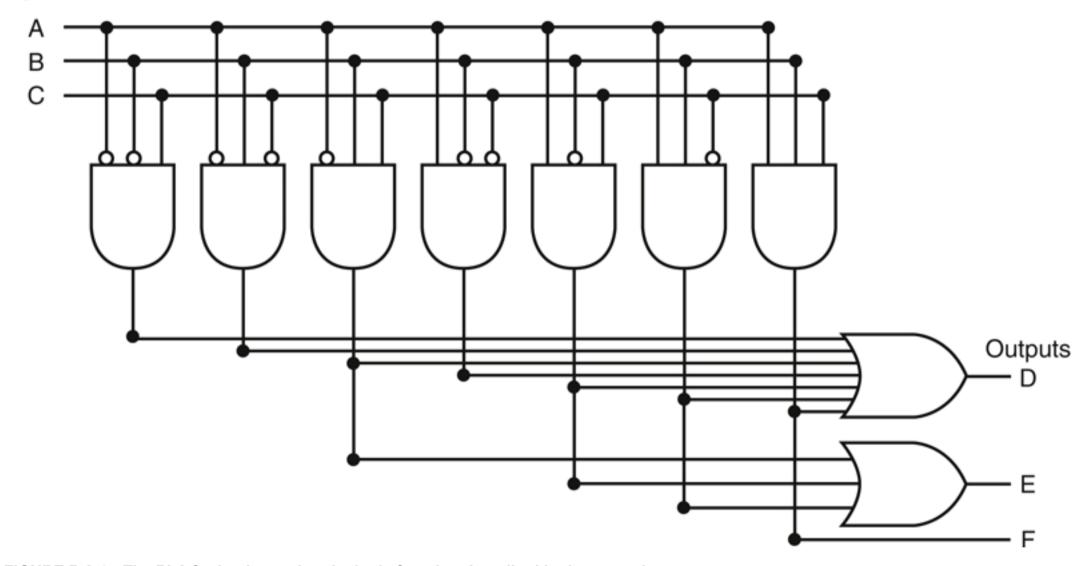


FIGURE B.3.4 The PLA for implementing the logic function described in the example.

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