```
TLBmiss:
  mfc0   $k1,Context      # copy address of PTE into temp $k1
  lw     $k1,0($k1)       # put PTE into temp $k1
  mtc0   $k1,EntryLo      # put PTE into special register EntryLo
  tlbwr                   # put EntryLo into TLB entry at Random
  eret                    # return from TLB miss exception
```

Unn Fig. 5-3.