```
# Load 8 elements of C into %zmm4
                     (%r11),%zmm4
 1
       vmovapd
                                                 # register %rcx = %rbx
                     %rbx,%rcx
 2
       mov
                                                 # register %eax = 0
 3
                     %eax, %eax
       xor
                                                 # Load 8 elements of C into %zmm3
                     0x20(%r11),%zmm3
 4
       vmovapd
                                                 # Load 8 elements of C into %zmm2
 5
                     0x40(%r11),%zmm2
       vmovapd
                                                 # Load 8 elements of C into %zmm1
 6
                     0x60(%r11),%zmm1
       vmovapd
                                                 # Make 8 copies of B element in %zmm0
       vbroadcastsd (%rax, %r8,8), %zmm0
 7
                                                 # register %rax = %rax + 8
 8
       add
                     $0x8,%rax
                                                 # Parallel mul & add %zmm0, %zmm4
       vfmadd231pd
                    (%rcx),%zmm0,%zmm4
 9
                                                 # Parallel mul & add %zmm0, %zmm3
       vfmadd231pd 0x20(%rcx),%zmm0,%zmm3
10
                                                 # Parallel mul & add %zmm0, %zmm2
                    0x40(%rcx),%zmm0,%zmm2
11
       vfmadd231pd
                                                 # Parallel mul & add %zmm0, %zmm1
                    0x60(%rcx),%zmm0,%zmm1
12
       vfmadd231pd
                                                 # register %rcx = %rcx
13
       add
                     %r9,%rcx
                                                 # compare %r10 to %rax
14
                     %r10,%rax
       cmp
                                                 # jump if not %r10 != %rax
                     50 <dgemm+0x50>
15
       jne
                                                 # register % esi = % esi + 1
16
       add
                     $0x1, %esi
                     %zmm4, (%r11)
17
                                                 # Store %zmm4 into 8 C elements
       vmovapd
18
                     %zmm3, 0x20(%r11)
                                                 # Store %zmm3 into 8 C elements
       vmovapd
                     %zmm2, 0x40(%r11)
19
                                                 # Store %zmm2 into 8 C elements
       vmovapd
20
       vmovapd
                     %zmm1, 0x60(%r11)
                                                 # Store %zmm1 into 8 C elements
```

FIGURE 4.82 The x86 assembly language for the body of the nested loops generated by compiling the unrolled C code in Figure 4.81.

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