```
Procedure body
swap: sll
                $t1, $a1, 2
                                           \# \text{ reg } \$t1 = k * 4
       add
                $t1, $a0, $t1
                                           \# \text{ reg } \$t1 = v + (k * 4)
                                           #reg $t1 has the address of v[k]
                $t0,0($t1)
                                           \# reg t0 (temp) = v[k]
       1 w
                                           \# \text{ reg $t2 = v[k+1]}
                $t2, 4($t1)
       ٦w
                                           #refers to next element of v
                                           # v[k] = reg $t2
                $t2,0($t1)
       SW
                $t0,4($t1)
                                           \# v[k+1] = reg $t0 (temp)
       SW
```

| Procedure return | | |
|------------------|------|-----------------------------|
| jr | \$ra | # return to calling routine |

FIGURE 2.25 MIPS assembly code of the procedure swap in Figure 2.24.

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