```
mfc0
      $k0, $12 # Fix Status register
      $k0, Oxfffd # Clear EXL bit
andi
                  # Enable interrupts
ori
      $k0, 0x1
mtc0
      $k0, $12
1 W
      $a0, save0 # Restore registers
l w
      $a1, save1
      $at, $k1
mov
                  # Return to EPC
eret
```

.kdata

save0: .word 0

save1: .word 0

Unn Fig. App1-25.

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