From state 1 (Op = 'J')Jump completion **PCWrite** PCSource = 10 To state 0 (Figure e4.5.8)

FIGURE e4.5.12 The jump instruction requires a single state. that asserts two control signals to write the PC with the lower 26 bits of the Instruction register shifted left 2 bits and concatenated to the upper 4 bits of the PC of this instruction.