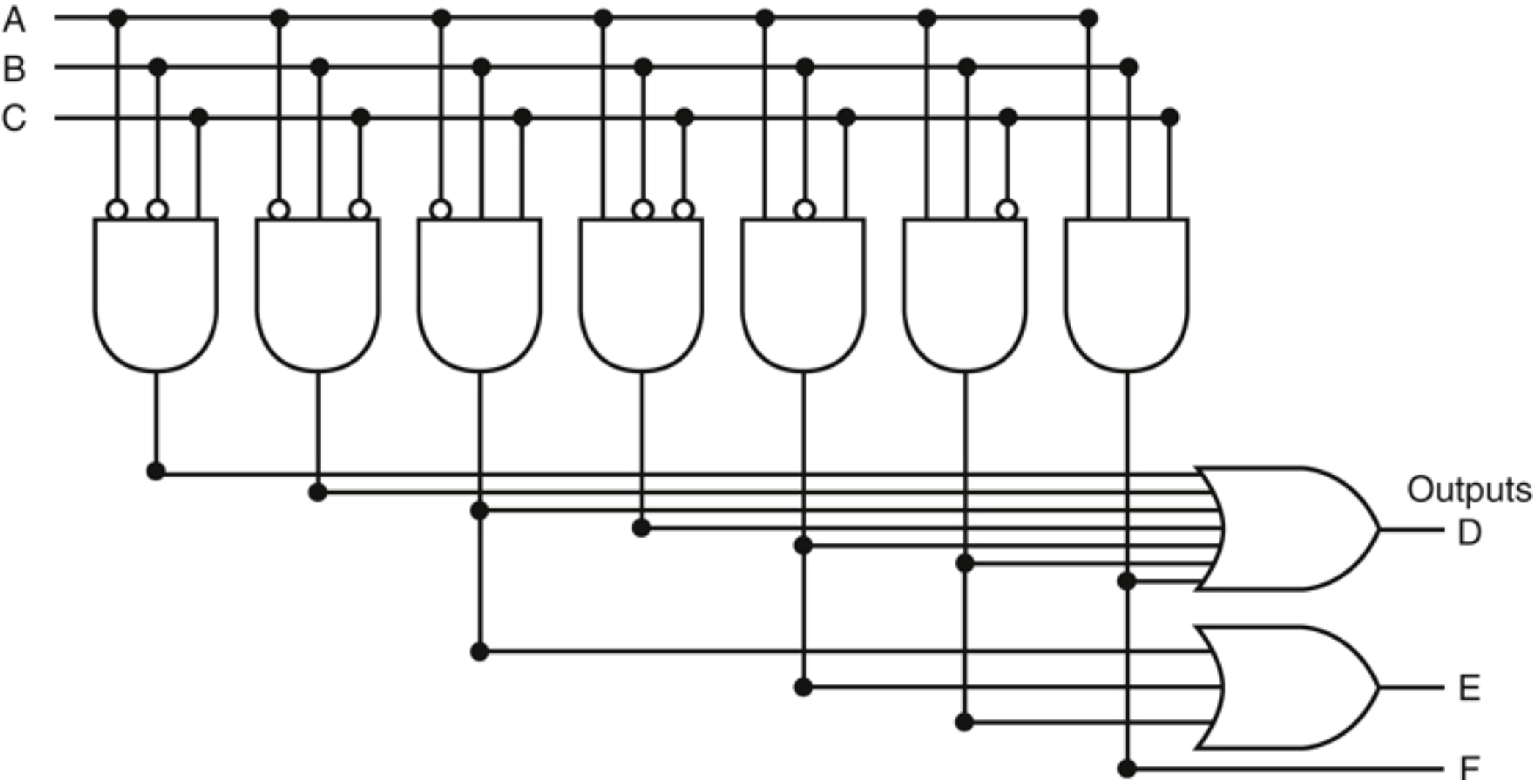**FIGURE B.3.4 The PLA for implementing the logic function described in the example.**