

```

mfc0    $k0, $12    # Fix Status register
andi    $k0, 0xffffd # Clear EXL bit
ori     $k0, 0x1     # Enable interrupts
mtc0    $k0, $12

lw      $a0, save0   # Restore registers
lw      $a1, save1
mov     $at, $k1

eret                                # Return to EPC

```

```

.kdata

```

```

save0:  .word 0
save1:  .word 0

```