

Extension-HW Fabrication Document

Layer Stack Legend

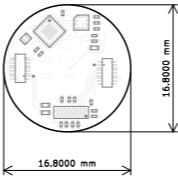
	Material	Layer	Thickness	Dielectric	Type	Gerber
		F.Paste			Paste Mask	
		F.Silkscreen			Legend	GBR
		F.Mask	0,01mm		Solder Mask	GBR
	Copper	F.Cu	0,035mm (1,00oz)		Signal	GBR
	Core		0,196mm	FR4	Dielectric	
	Copper	Supply	0,035mm (1,00oz)		Plane	GBR
	Prepreg		0,43mm	FR4	Dielectric	
	Copper	GND	0,035mm (1,00oz)		Plane	GBR
	Core		0,196mm	FR4	Dielectric	
	Copper	B.Cu	0,035mm (1,00oz)		Signal	GBR
		B.Mask	0,01mm		Solder Mask	GBR
		B.Silkscreen			Legend	GBR
		B.Paste			Paste Mask	

Total thickness: 0,982mm
Note: external layer thicknesses are specified after plating

Impedance Table

Transmission Line	Impedance [ohms]	Tolerance [%]	Layer	Trace Width [mm]	Gap [mm]	Ref. Layers
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Top Fabrication (Scale 1:1)



FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

- FABRICATE PER IPC-6012A CLASS 2.
- OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge_Cuts.GBR" SUFFIX.

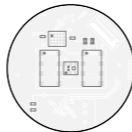
DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DRAWING FOR REFERENCE ONLY.
- SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS.

SELECTED HOLE LOCATIONS SHOWN ON THIS DRAWING FOR REFERENCE ONLY.
- SURFACE FINISH: HAL SNPB
- SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE LPI, COLOR GREEN.
- SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING BLACK NON-CONDUCTIVE EPOXY INK.
- ALL VIAS ARE TENTED ON BOTH SIDES UNLESS SOLDERMASK OPENED IN GERBER.
- VENDOR SHOULD FOLLOW ROHS COMPLIANT PROCESS AND Pb FREE FOR MANUFACTURING
- PCB MATERIAL REQUIREMENTS:

A. FLAMMABILITY RATING MUST MEET OR EXCEED UL94V-0 REQUIREMENTS.
B. Tg 150 C OR EQUIVALENT.
C. EQUIVALENT MATERIAL SHALL BE RoHS COMPLIANT, HALOGEN FREE AND APPROVED BY ZSWATCH.
- DESIGN GEOMETRY MINIMUM FEATURE SIZES:

BOARD SIZE 16.800 x 16.800 mm
BOARD THICKNESS 0.982 mm
TRACE WIDTH 0.150 mm
TRACE TO TRACE 0.100 mm
MIN. HOLE (PTH) 0.150 mm
MIN. HOLE (NPTH) N/A mm
ANNULAR RING 0.076 mm
COPPER TO HOLE 0.100 mm
COPPER TO EDGE 0.100 mm
HOLE TO HOLE 0.100 mm

	Comments: ZSWatch	Company: ZSWatch		Variant: PRELIMINARY	Git Hash: 872ab91
		Board Name: Extension-HW		Project Name: ZSWatch Extension	
	Sheet Title: Top Fabrication (Scale 1:1)	File Name: ZSWatch-Extension.kicad_pcb	Designer: Daniel Kampert	Date: 2025-07-13	Revision: + (Unreleased)
	Sheet Path:		Reviewer:	Size: A3	Sheet: 1 of 9

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Bottom Fabrication (Scale 1:1)							
							
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	Board Name:	Project Name:					
	Extension-HW		ZSWatch Extension				
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Bottom Fabrication (Scale 1:1)	ZSWatch-Extension.kicad_pcb	Daniel Kampert	2025-07-13	+ (Unreleased)			
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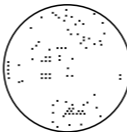
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Extension-HW Fabrication Document

Drill Drawing L1 - L4 (Scale 1:1)

Drill Table

Symbol	Count	Hole Size	Plated	Hole Shape	Drill Layer Pair	Hole Type
X	88	0,15mm (5,91mils)	PTH	Round	F,Cu - B,Cu	Via
	Total 88					



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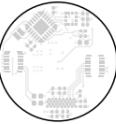
A									
B									
C									
D									
E									
F									

Extension-HW Fabrication Document

Top Test Points (Scale 1:1)

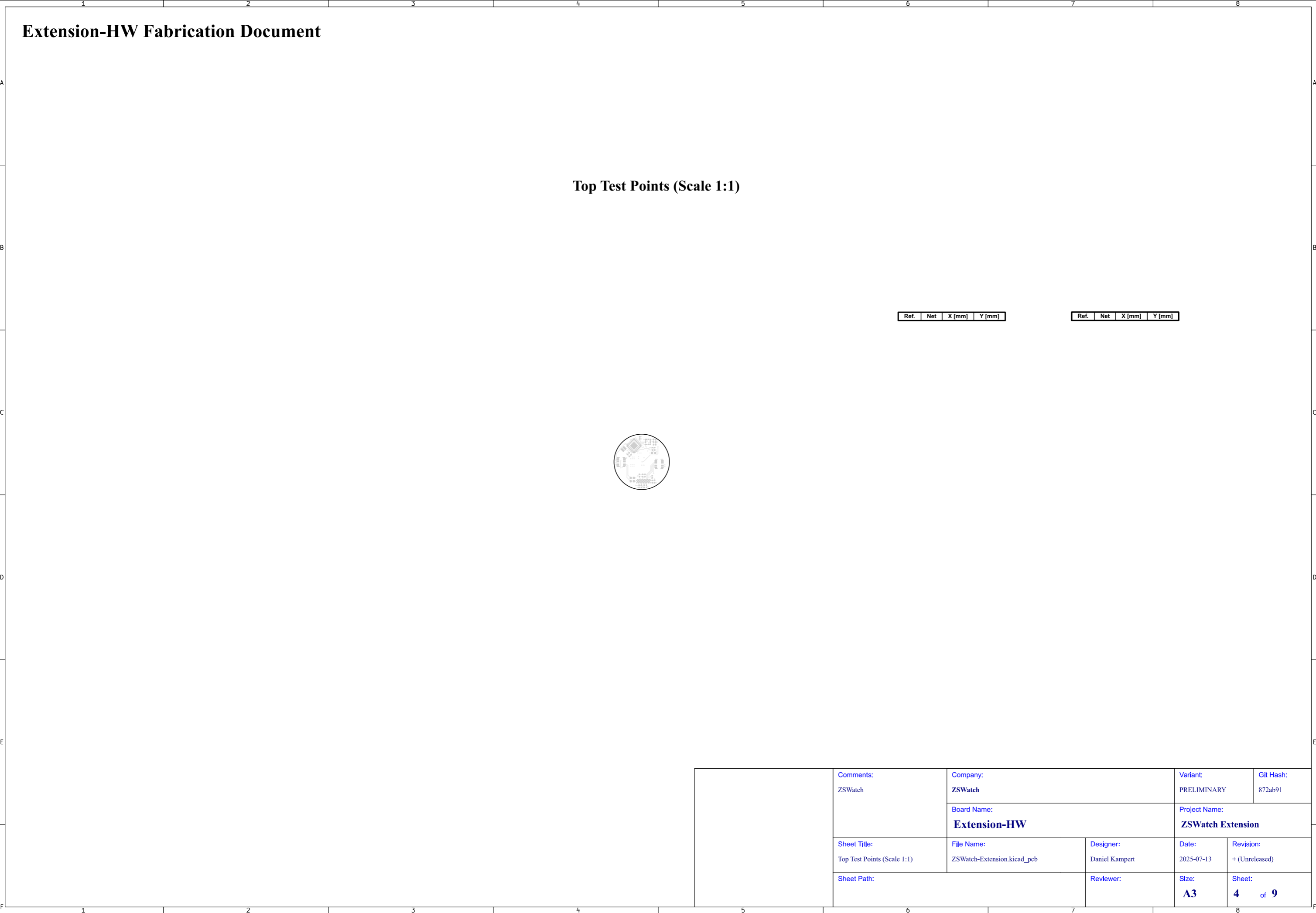
Ref.	Net	X [mm]	Y [mm]
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Ref.	Net	X [mm]	Y [mm]
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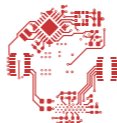


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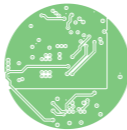
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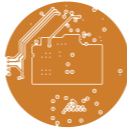
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Bottom Test Points (Scale 1:1)							
							

Extension-HW Fabrication Document													
F.Cu (Scale 1:1)													
													
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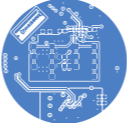
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Supply (Scale 1:1)										
										
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GND (Scale 1:1)										
										
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A	Extension-HW Fabrication Document								A
B	B.Cu (Scale 1:1)								B
C									C
D									D
E									E
F	1	2	3	4	5	6	7	8	F

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