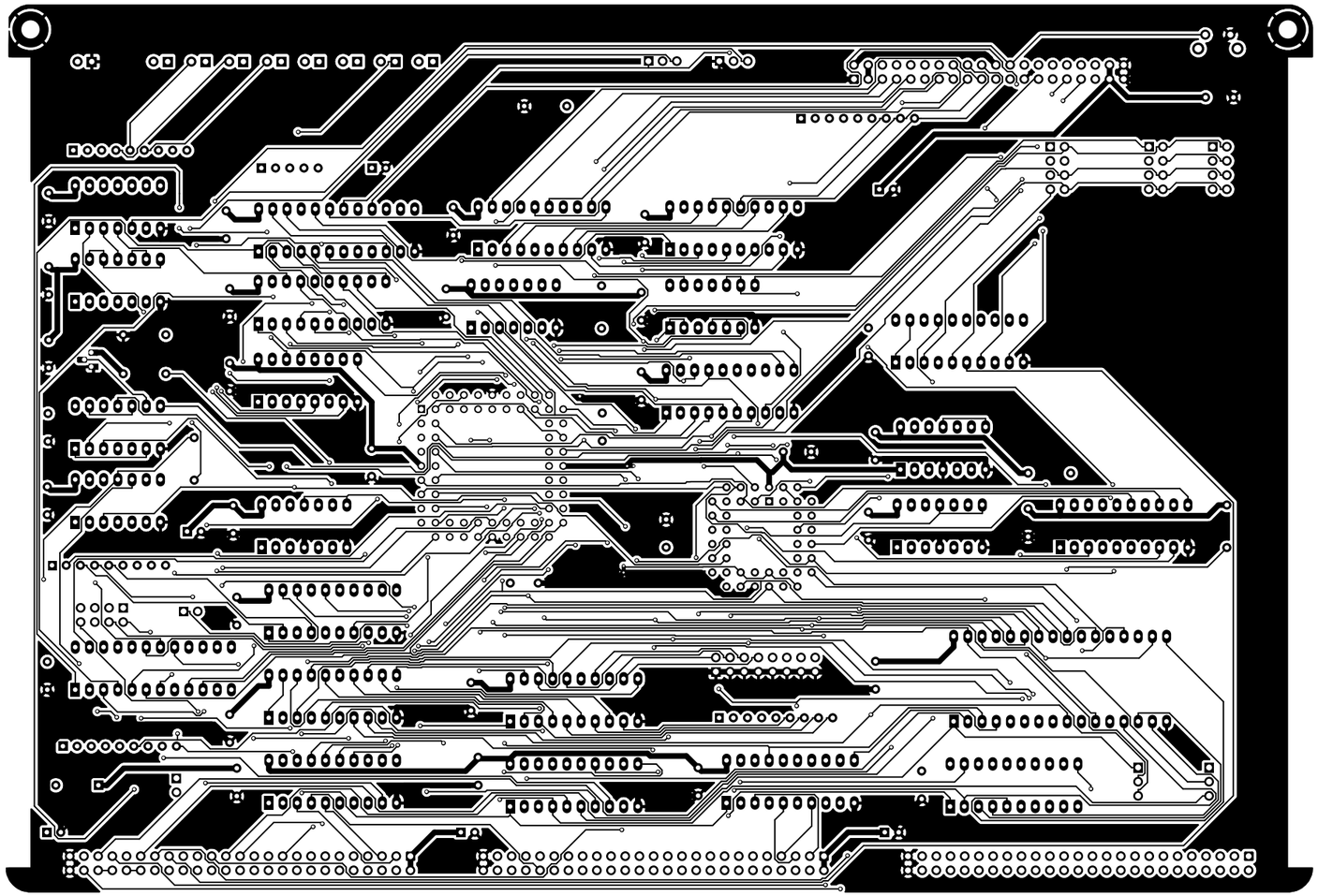

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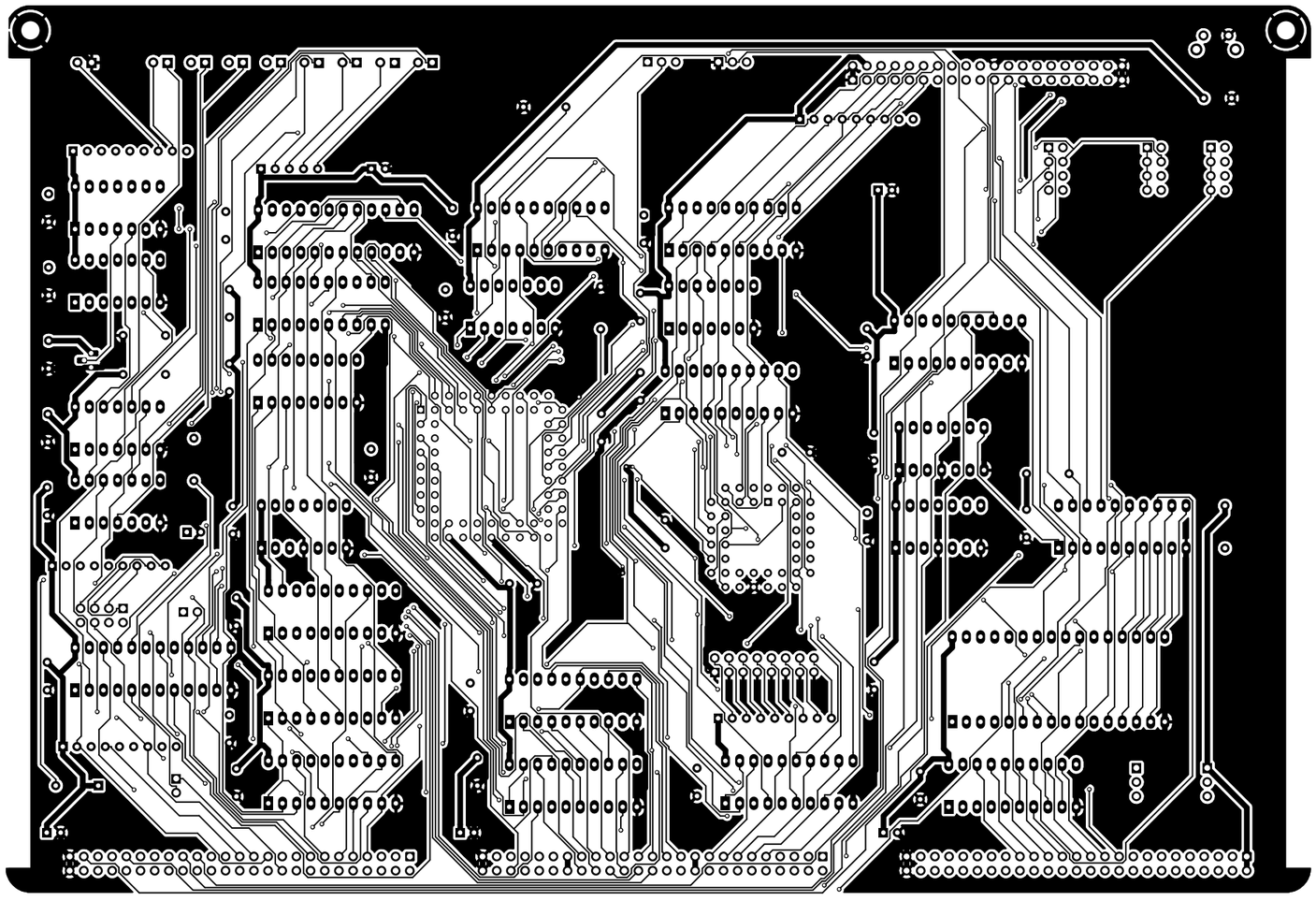
File: processor.80C188.kicad_pcb

Title: *DUODYNE 80c188 PROCESSOR*

Size: B	Date: 2024-11-24	**Rev: v0.9**
KiCad E.D.A. kicad (6.0.11)		Id: 1/1



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Title: DUODYNE 80c188 PROCESSOR

Size: B	Date: 2024-11-24	**Rev: v0.9**
KiCad E.D.A. kicad (6.0.11)		Id: 1/1

H1
MountingHole
LED
D7
POWER
80C188
D4
EN
LED
D2
RUN
LED
D5
HALT
LED
D9
UART
D8
I/01
D10
I/02
D11
I/03
D12
I/04
J2
80C188 RESET
J3
80C188 ONLY
J11
FPANEL
FRONT PANEL
GND
RTS
VCC
RX
TX
CTS
SW1
RESET
H2
MountingHole
C35
0.1u
I2C IRQ Selection
J5
J7
J8
EIRQ0
EIRQ1
EIRQ2
EIRQ3
BUS IRQ MAPPING
EIRQ0
EIRQ1
EIRQ2
EIRQ3
UART IRQ Selection
R4
1K
RN5
4700
RN3
470
RN6
470
PERIF188
C46
47uF
C29
22u
U21
74ALS74
C34
U25
GAL22V10
C33
U33
74ALS374
C31
C36
U31
74ALS244
U16
74LS08
C11
U3
74ALS373
C39
U2
74LS32
C38
R7
1K
U23
74LS00
C14
1KΩ
R9
R1
10K
U13
DS1235
C3
U24
74LS259
C22
U19
74ALS373
C15
U7
74LS240
C17
U1
74LS04
C18
R5
10
C32
C2
NT3
NetTie_2
U20
0.1u
C4
U28
PCF CLOCK
C24
10K
R2
U4
74LS07
C6
C26
10u
C12
U9
CPU CLOCK
80C188
U12
C23
0.1u
U18
UART CLOCK
C13
U27
PCF8584
C40
R3
10K
RN1 4700
GENERATE CLOCK
C9
0.1u
NT4
NetTie_2
TL16C550CFN
BUS CPU
JP4
JP1
U17
74LS244
C21
IO Address Select
$96
J1
U26
29F040
U15
GAL22V10
C19
U6
74LS244
STAT188
C37
U22
74LS688
C30
RN2
10KΩ
RN4
4700
C5
1N4148
D6
K
J4
RES SEL
U14
74LS244
C10
C27
10u
C25
C20
U10
74LS244
C8
U8
74LS244
C16
U11
74HC245
J14
J13
FLASH OR CMOS
FLASH OR CMOS
CONN_02X25
P1
P2
P3
C28
10u
PROCESSOR.80C188, VERSION 0.9, AS OF 24 NOVEMBER 2024
PATCH LEVEL 0_ 1_ 2_ 3_ 4_ 5_ 6_ 7_ 8_ 9_
BOARD GUIDES EXCLUSION ZONE
BOARD GUIDES EXCLUSION ZONE

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Size: B	Date: 2024-11-24	**Rev: v0.9**
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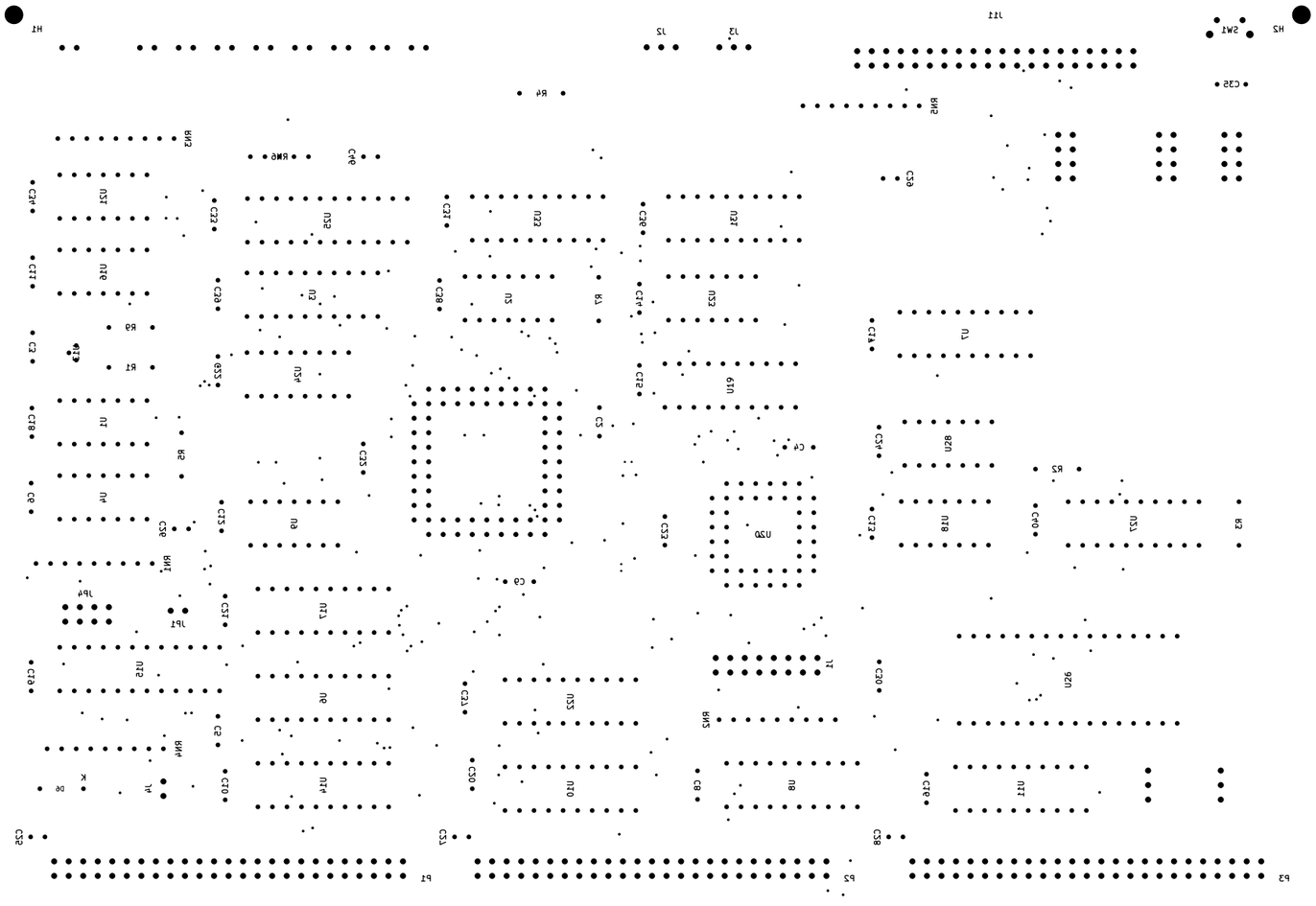

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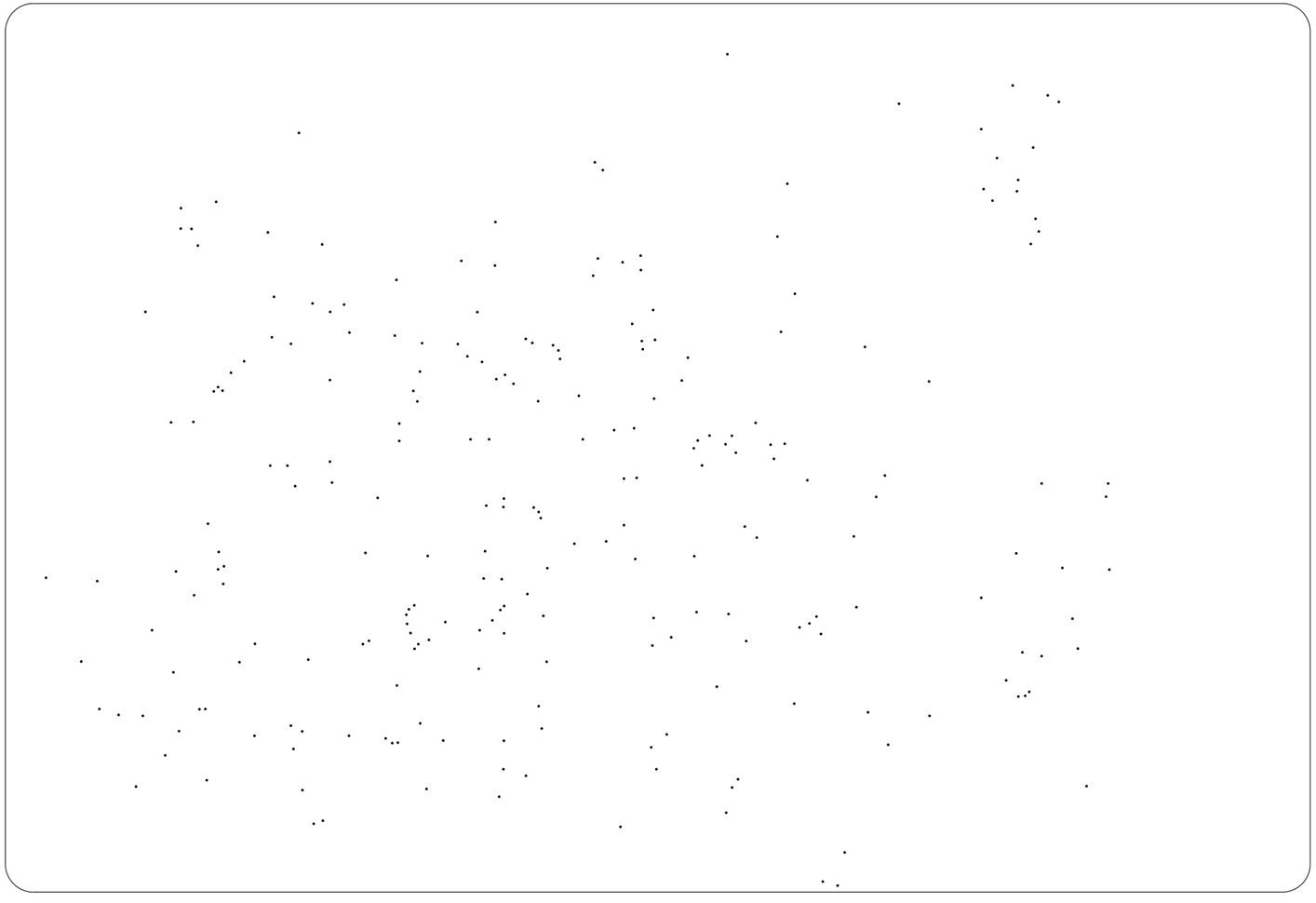

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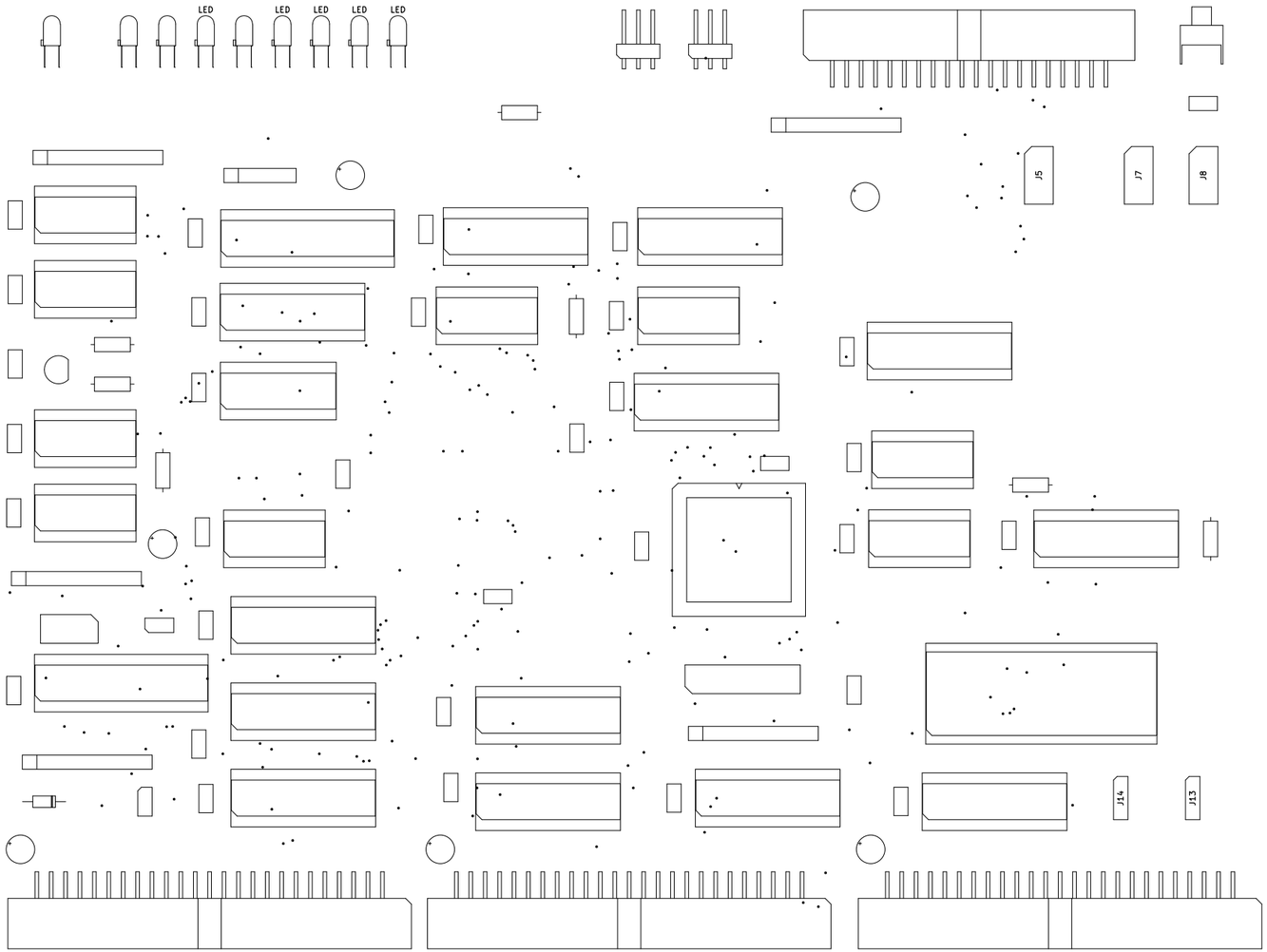

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