

12345678

A

bus

power

buffers

B

File: bus.kicad_sch
TMS9995CPU

File: power.kicad_sch
GALS

File: buffers.kicad_sch
bus sharing

IO

C

File: TMS9995CPU.kicad_sch

File: GALS.kicad_sch

File: bussharing.kicad_sch

File: io.kicad_sch

D

E

12345678

Sheet: /
File: processor.tms9995.kicad_sch

Title: **Duodyne TMS9995 CPU board**

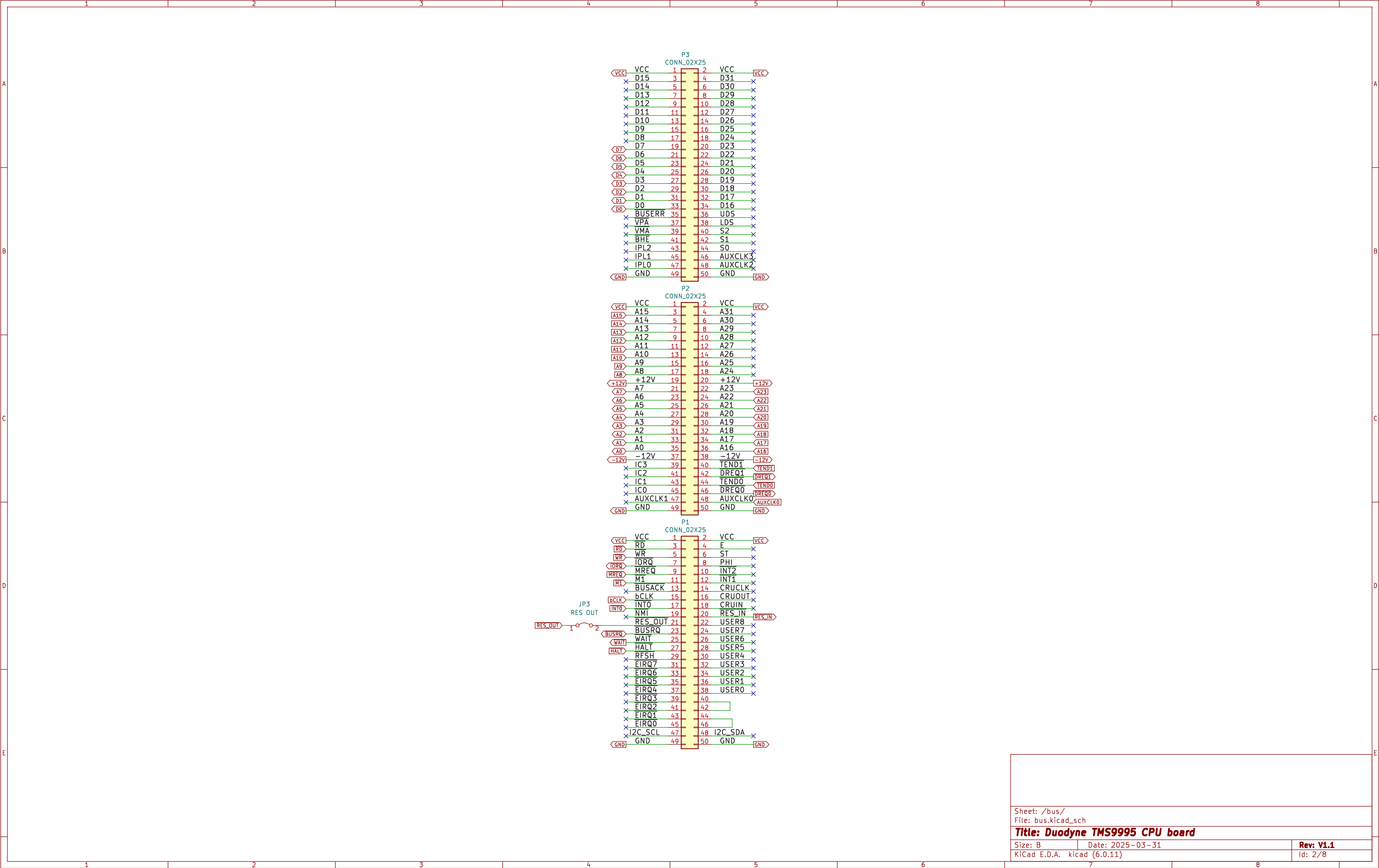
Size: B

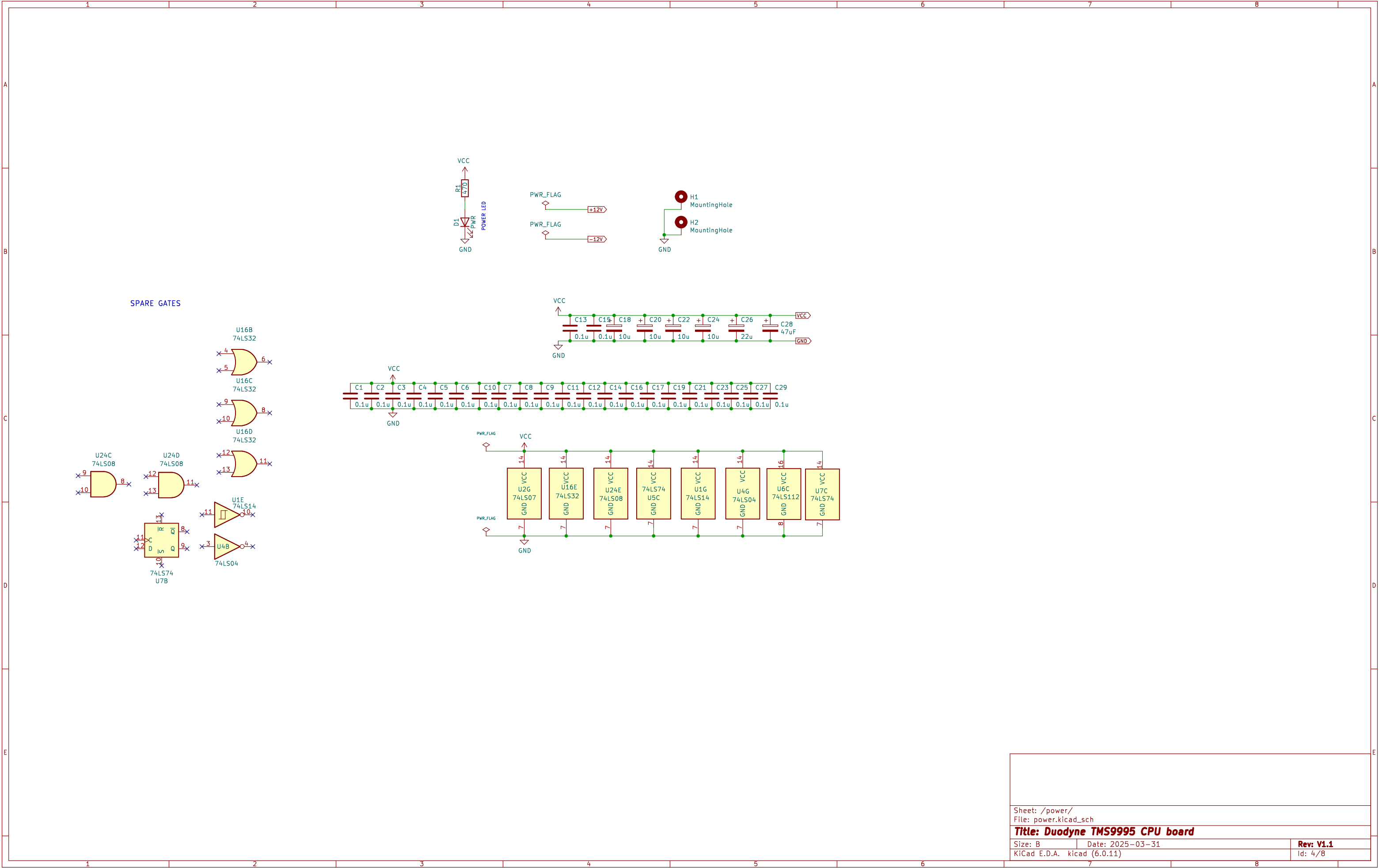
Date: 2025-03-31

Rev: **V1.1**

KiCad E.D.A. kicad (6.0.11)

Id: 1/8





Equation ('+' is 'OR', '*' is 'AND')

The diagram shows a GAL22V10 U8 with the following connections and equations:

- VCC** (Pin 24) and **GND** (Pin 12) are connected to the power supply.
- Pin 1** (MEMEN) is connected to **Pin 10/CLK**.
- Pin 2** (WE) is connected to **Pin 11**.
- Pin 3** (ISFE) is connected to **Pin 12**.
- Pin 4** (RA15) is connected to **Pin 13**.
- Pin 5** (CPU-A7) is connected to **Pin 14**.
- Pin 6** (CPU-A6) is connected to **Pin 15**.
- Pin 7** (ROMEN) is connected to **Pin 16**.
- Pin 8** (CRUCLK) is connected to **Pin 17**.
- Pin 9** (USER) is connected to **Pin 18**.
- Pin 10** (PROTECT) is connected to **Pin 19**.
- Pin 11** (PROTECT) is connected to **Pin 20**.
- Pin 12** (PROTECT) is connected to **Pin 21**.
- Pin 13** (PROTECT) is connected to **Pin 22**.
- Pin 14** (PROTECT) is connected to **Pin 23**.
- Pin 15** (PROTECT) is connected to **Pin 24**.
- Pin 16** (PROTECT) is connected to **Pin 25**.
- Pin 17** (PROTECT) is connected to **Pin 26**.
- Pin 18** (PROTECT) is connected to **Pin 27**.
- Pin 19** (PROTECT) is connected to **Pin 28**.
- Pin 20** (PROTECT) is connected to **Pin 29**.
- Pin 21** (PROTECT) is connected to **Pin 30**.
- Pin 22** (PROTECT) is connected to **Pin 31**.
- Pin 23** (PROTECT) is connected to **Pin 32**.
- Pin 24** (PROTECT) is connected to **Pin 33**.
- Pin 25** (PROTECT) is connected to **Pin 34**.
- Pin 26** (PROTECT) is connected to **Pin 35**.
- Pin 27** (PROTECT) is connected to **Pin 36**.
- Pin 28** (PROTECT) is connected to **Pin 37**.
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- Pin 57** (PROTECT) is connected to **Pin 66**.
- Pin 58** (PROTECT) is connected to **Pin 67**.
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- Pin 120** (PROTECT) is connected to **Pin 129**.
- Pin 121** (PROTECT) is connected to **Pin 130**.
- Pin 122** (PROTECT) is connected to **Pin 131**.
- Pin 123** (PROTECT) is connected to **Pin 132**.
- Pin 124** (PROTECT) is connected to **Pin 133**.
- Pin 125** (PROTECT) is connected to **Pin 134**.
- Pin 126**

Memory Address	Mapped To
>0000 -->7FFF	ROM when enabled, otherwise RAM
>8000 -->EFFF	RAM
>F000 -->F0FB	TMS 9995 internal RAM
>F0FC -->FDFE	RAM
>FE00 -->FE03	CF card ATA registers
>FE04 -->FE13	CPU RESET
>FE14 -->FE4F	Memory mapper registers 0-15
>FE80 -->FEBF	Offboard I/O (ports \$B0-\$FF)
>FFF0 -->FFFF	RAM
>FFFF -->FFFF	TMS 9995 internal RAM

TMS9995 IO GAL22V10

