

12345678

bus

power

buffers

fpanel

File: bus.kicad\_sch  
80C188CPU

File: power.kicad\_sch  
Memory

File: buffers.kicad\_sch  
bus sharing

File: fpanel.kicad\_sch  
mapper

File: CPU\_80C188.kicad\_sch

File: Memory.kicad\_sch

File: bussharing.kicad\_sch

File: mapper.kicad\_sch

A

B

C

D

E

12345678

RetroBrew Computer Group  
Based on a design by John R Coffman.

Sheet: /  
File: processor.80C188.kicad\_sch

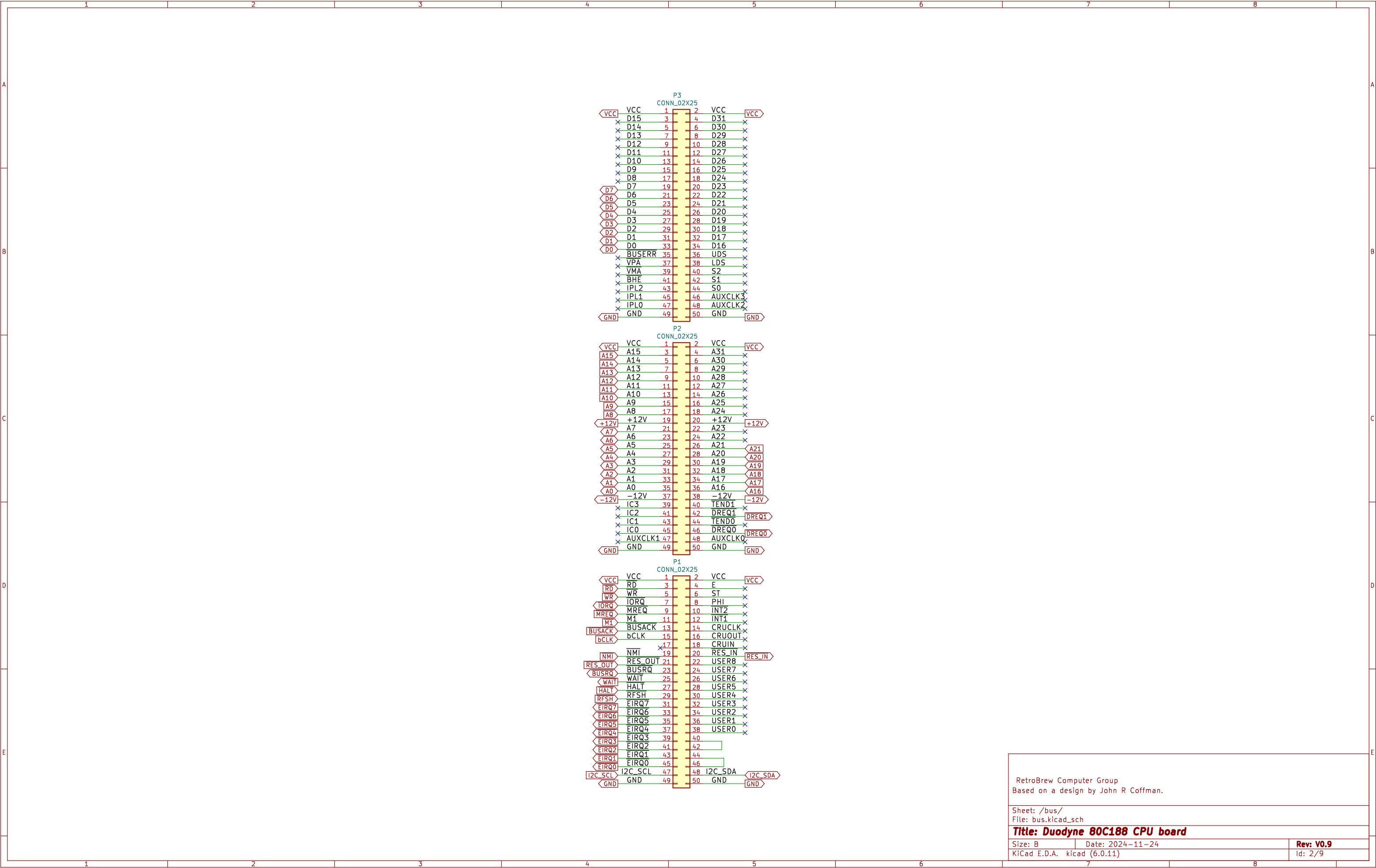
**Title: Duodyne 80C188 CPU board**

Size: BDate: 2024-11-24

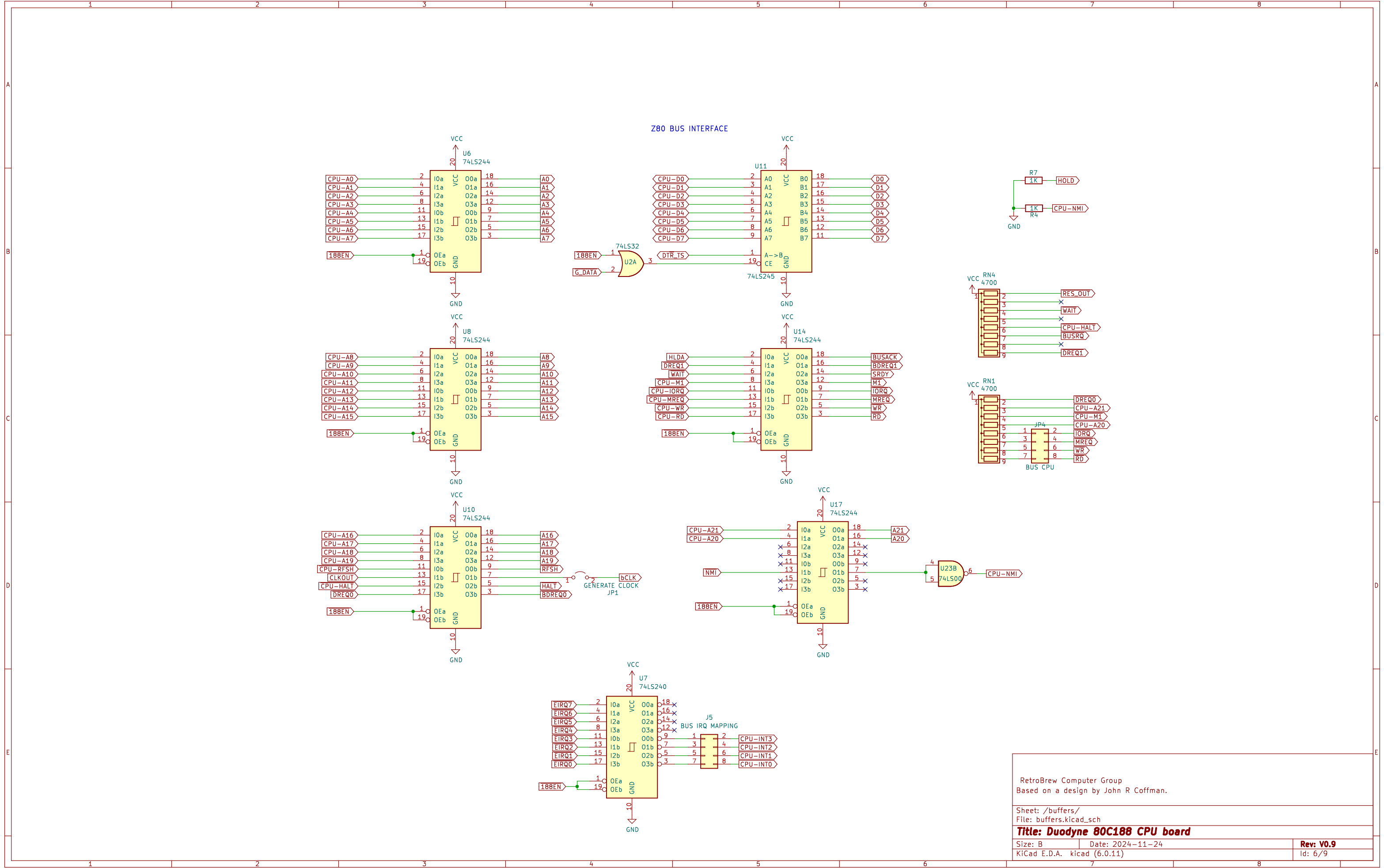
KiCad E.D.A. kicad (6.0.11)

Rev: V0.9

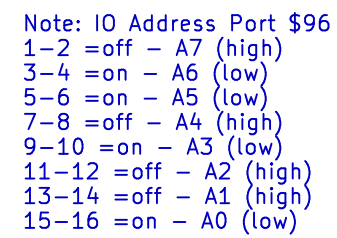
Id: 1/9

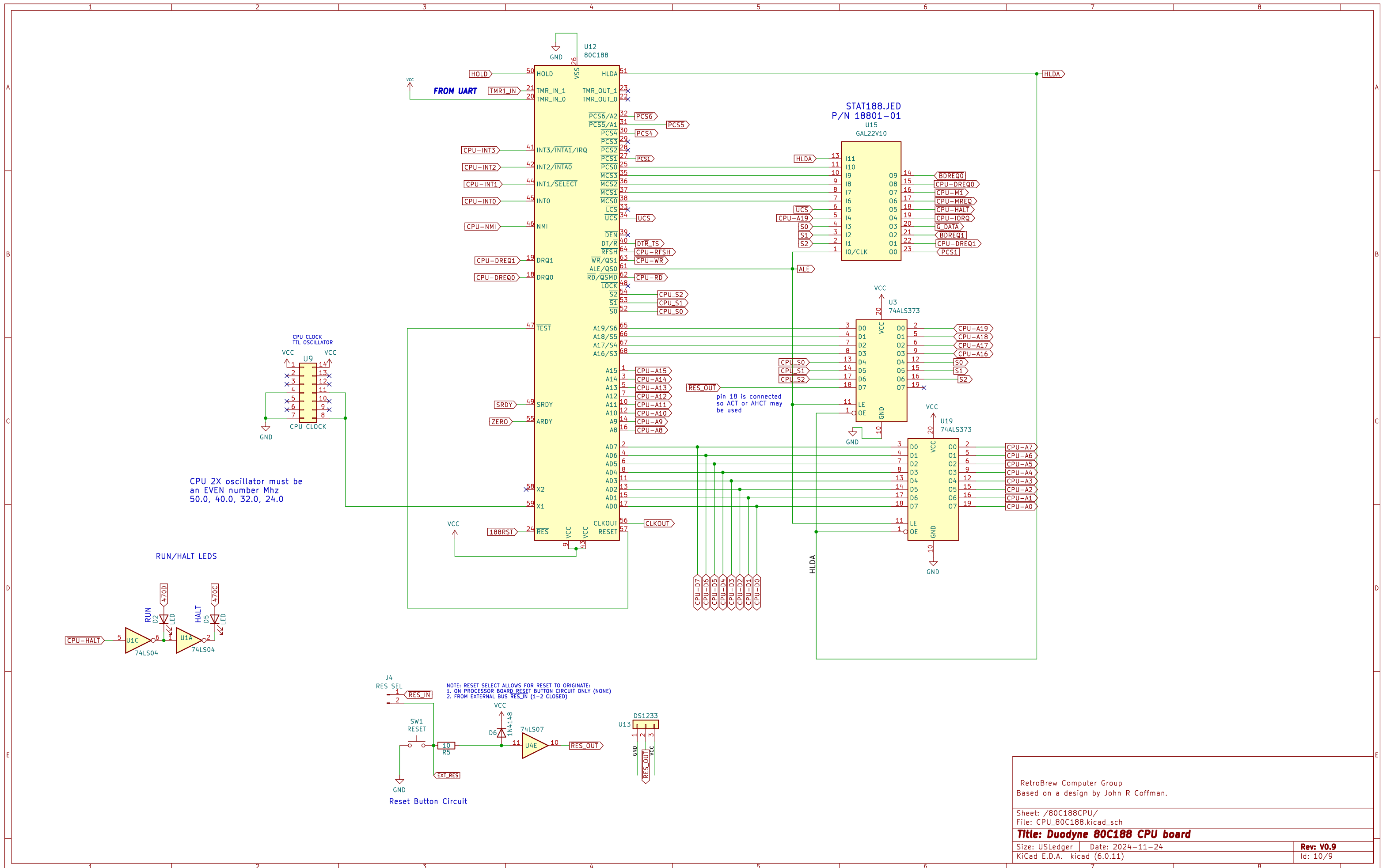


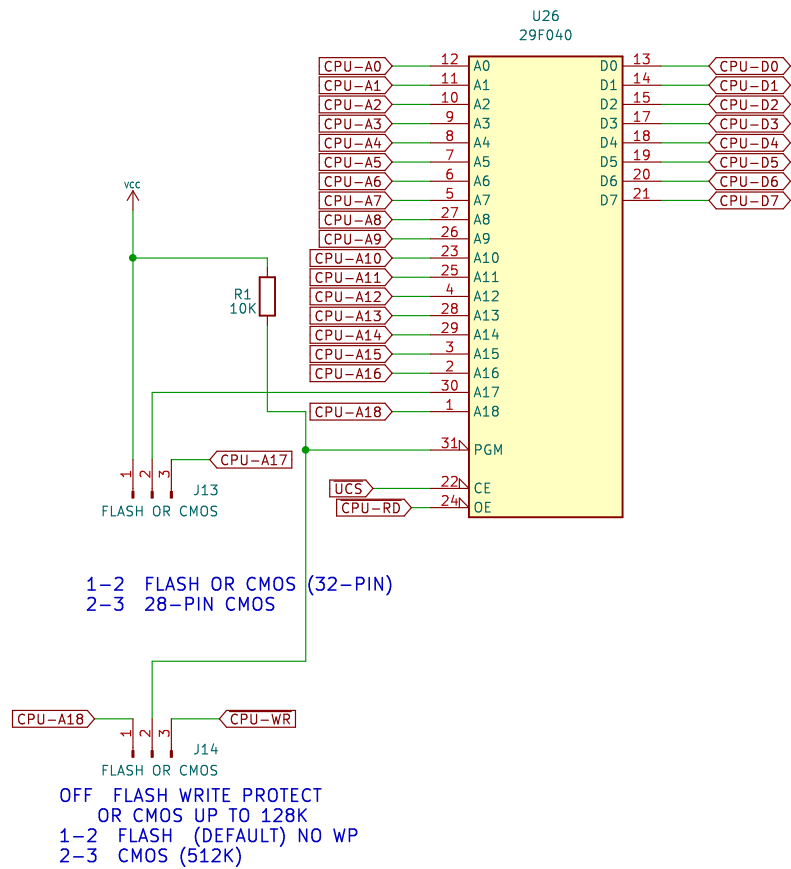












## ROM

FLASH 39SF010 (128K)  
ALTERNATES: 39SF020, 29F020, 29F010  
39SF040, 29F040 (512K) NOT RECOMMENDED

CMOS 27C010 (128K, 32pin)  
ALTERNATE 27C020  
28pin: 27C512(64K), 27C256(32K)

DEFAULT CHIP: 39SF010 (128K FLASH)  
DEFAULT JUMPERS: 1-2, 1-2 (BOTH)  
29F040 SHOWS ALL PIN ASSIGNMENTS

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Sheet: /Memory/  
File: Memory.kicad\_sch

**Title: Duodyne 80C188 CPU board**

Size: USLedger | Date: 2024-11-24  
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Rev: V0.9  
Id: 10/9



