

12345678

A

bus

power

buffers

fpanel

File: bus.kicad\_sch  
6502CPU

File: power.kicad\_sch  
GALS

File: buffers.kicad\_sch  
bus sharing

File: fpanel.kicad\_sch  
mapper

File: 6502CPU.kicad\_sch

File: GALS.kicad\_sch

File: bussharing.kicad\_sch

File: mapper.kicad\_sch

B

C

D

E

12345678

Sheet: /  
File: processor.6502.kicad\_sch

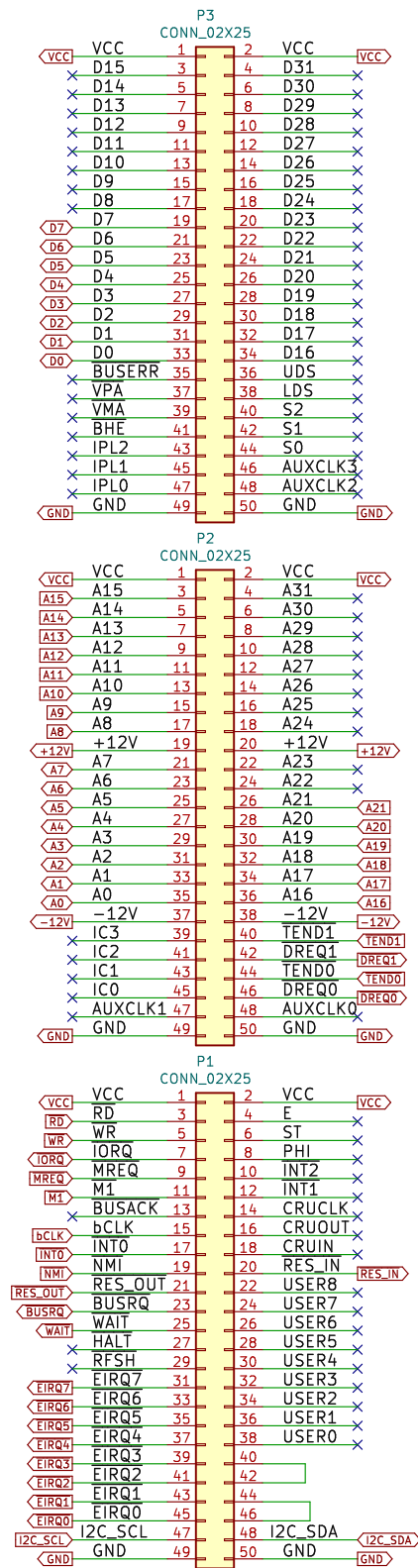
Title: Duodyne 6502 CPU board

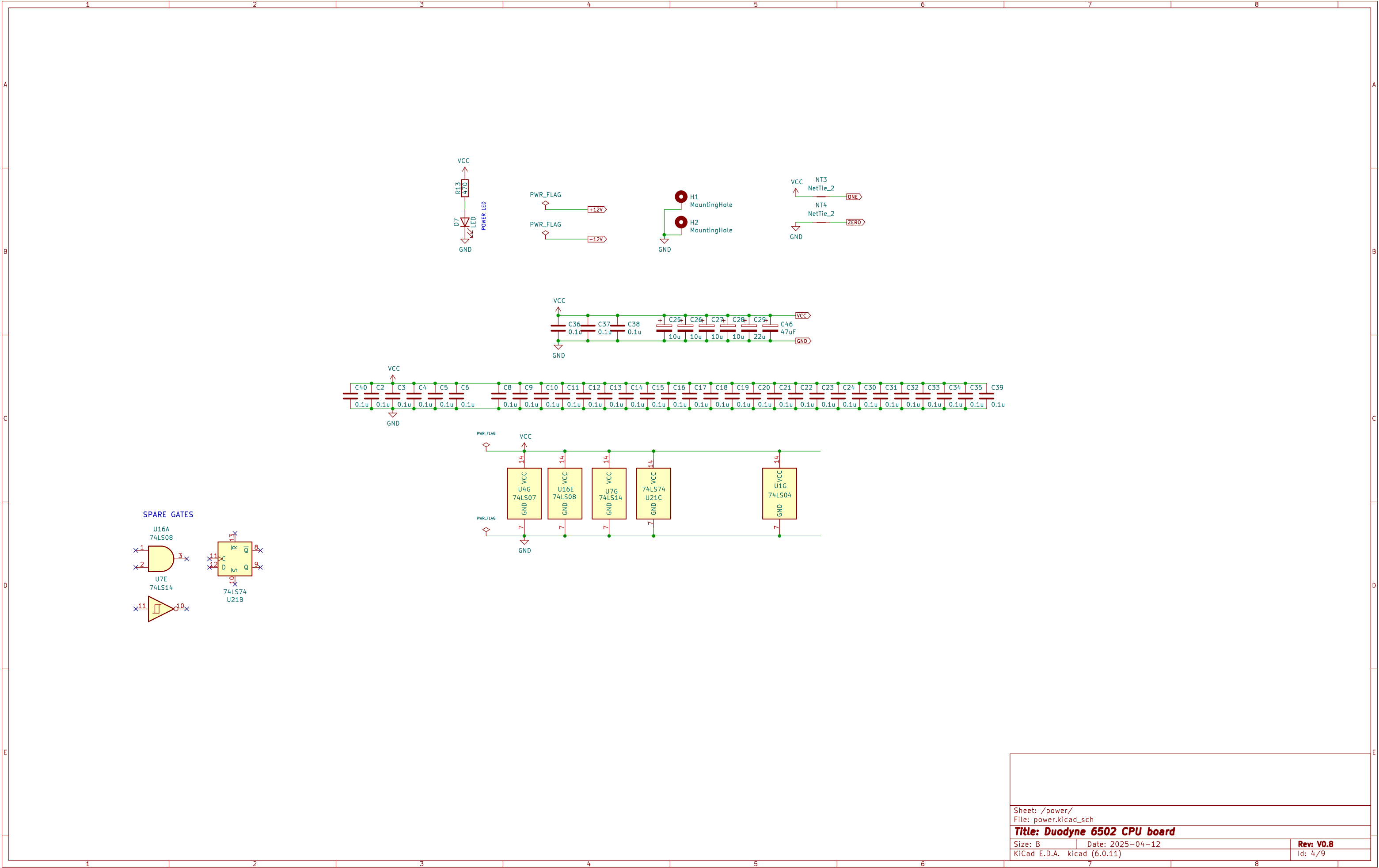
Size: BDate: 2025-04-12

KiCad E.D.A. kicad (6.0.11)

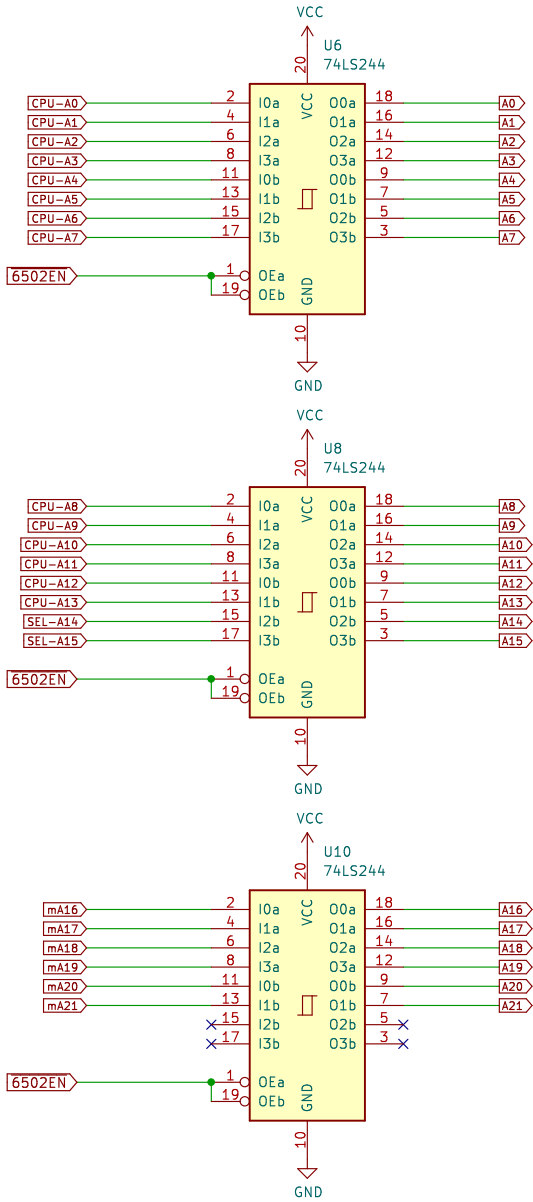
Rev: V0.8

Id: 1/9

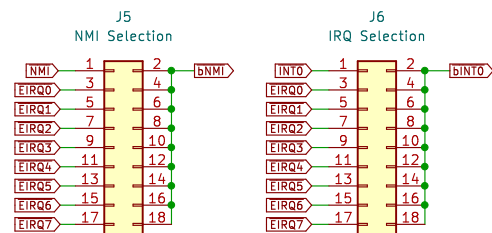
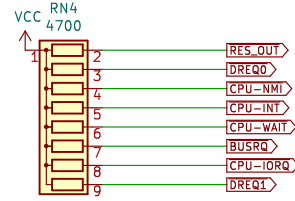
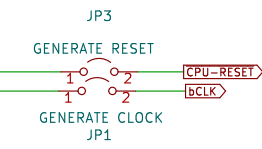
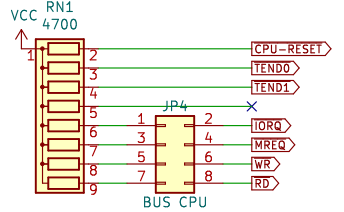
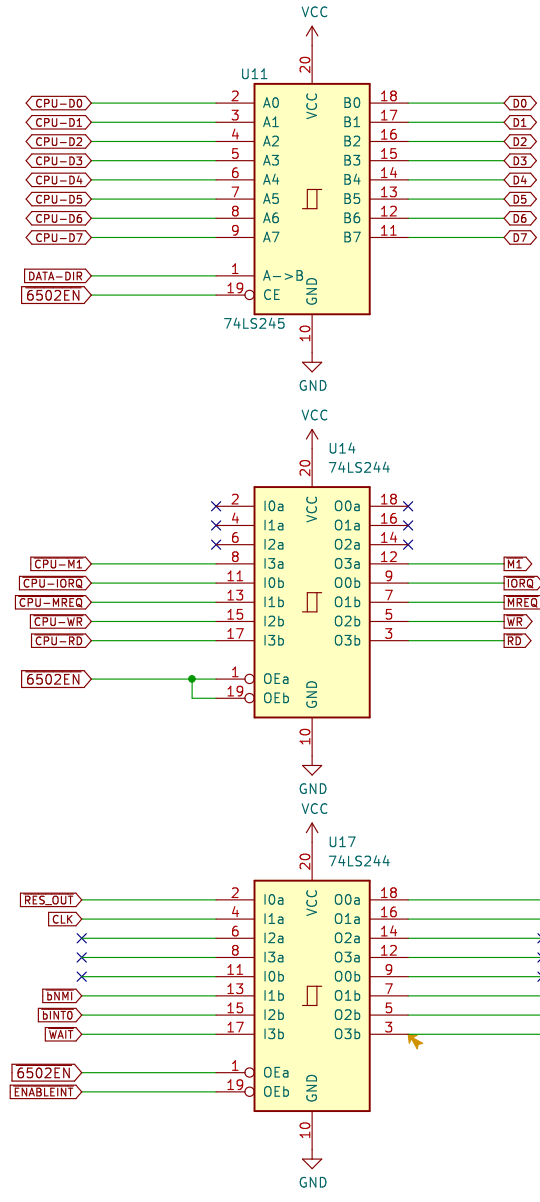






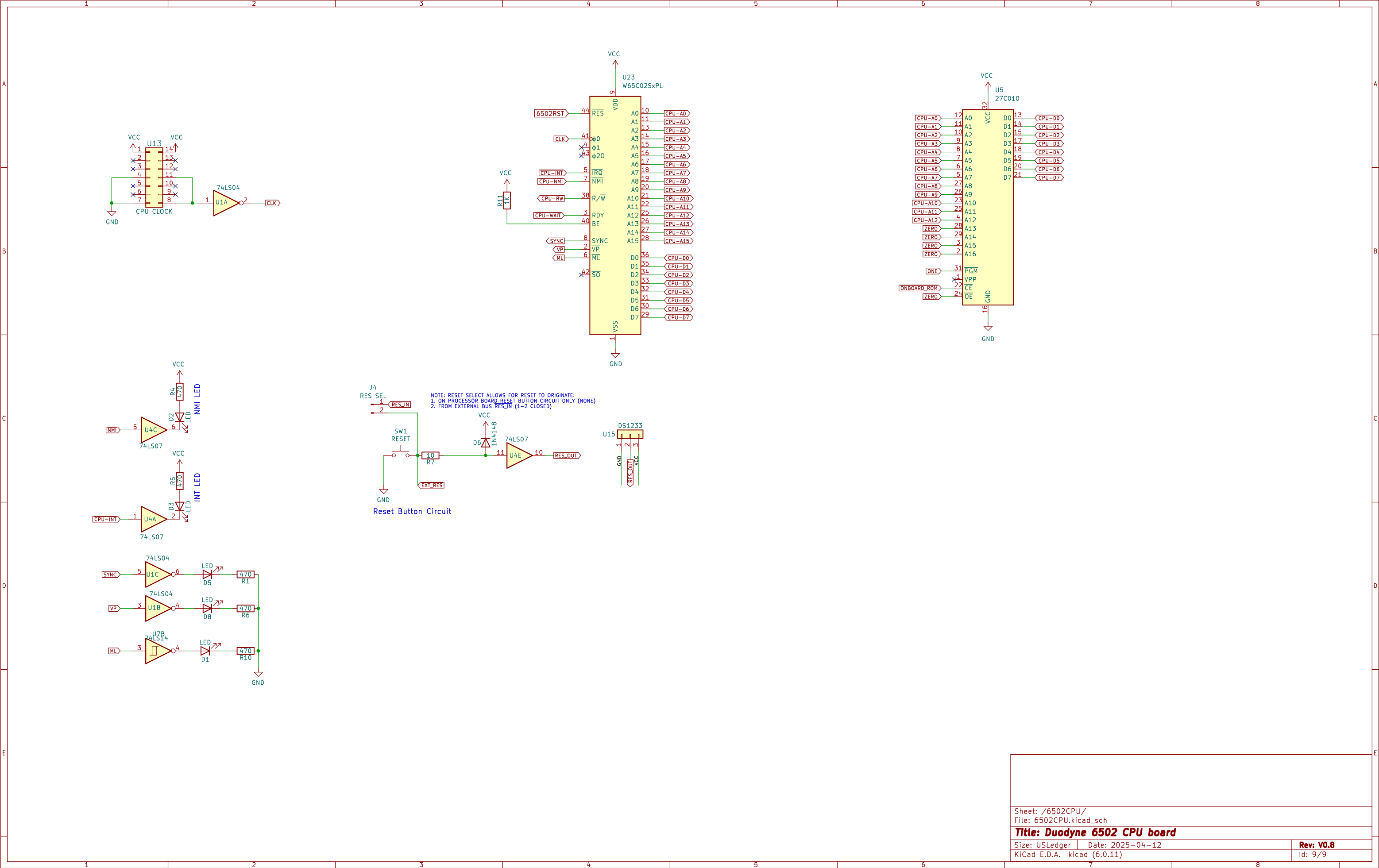


Z80 BUS INTERFACE





Sheet: /fpanel/  
 File: fpanel.kicad\_sch  
**Title: Duodyne 6502 CPU board**  
 Size: B      Date: 2025-04-12      Rev: **V0.8**  
 KiCad E.D.A. kicad (6.0.11)      Id: 8/9



Note: IO Address Port \$96  
1-2 =off - A7 (high)  
3-4 =on - A6 (low)  
5-6 =on - A5 (low)  
7-8 =off - A4 (high)  
9-10 =on - A3 (low)  
11-12 =off - A2 (high)  
13-14 =off - A1 (high)  
15-16 =on - A0 (low)

