Cody memory page map

overall memory map:

Overall II	overali memory map.																	
high\low	0xx	1xx	2xx	3xx	4xx	5xx	6xx	7xx	8xx	9xx	axx	bxx	CXX	dxx	exx	fxx		
0	0pg	stack																
1																		
2																		
3								BASIC program memory										
4																		
5																		
6						free	Α	В	С	D	Е	F	G	Н	ı	J		
7	K	L	М	N	0	Р	Q	R	S	Т	U	V	W	Χ	Υ	Z		
8	A\$	B\$	C\$	D\$	E\$	F\$	G\$	H\$	I\$	J\$	K\$	L\$	M\$	N\$	O\$	P\$		
9	Q\$	R\$	S\$	T\$	U\$	V\$	W\$	X\$	Y\$	Z\$	BASIC runtime state VI					VIA		
а							froo	VID of	acrod I									
b		free VID-shared RAM																
С	default screen RAM							RAM	default character RAM									
d	VID	VID free S/UA					free default color RAM free VID-share						nared I	RAM				
е	character ROM																	
f	BASIC ROM machine code																	

VID screen and color RAM region numbers:

high\low	0xx	1xx	2xx	3xx	4xx	5xx	6xx	7xx	8xx	9xx	axx	bxx	cxx	dxx	exx	fxx
a	\$0				\$1					\$	2		\$3			
b	\$4				\$5					\$	6		\$7			
С	\$8			\$9 (de	efault s	creen	RAM)	\$a (d	efault	char. F	RAM)	\$b (default char. RAM)				
d	\$c (blocked: VID)			\$d (blocked: SID/UART)			\$e (default color RAM)				\$f					

VID character RAM region numbers:

high\low	0xx 1xx 2xx 3xx 4xx 5xx 6xx 7xx							7xx	8xx	9xx	axx	bxx	CXX	dxx	exx	fxx		
a	\$0									\$1								
b	\$2									\$3								
С	\$4 (default screen RAM)								\$5 (default character RAM)									
d	\$6 (blocked: VID/SID/UART)								\$7 (default color RAM)									

legend:

category	label	usable?	
65C02	0pg	\$a0 and up	zero page – mostly occupied by BASIC and default ISR
special	stack	risky	processor stack – always partially used by machine code
	program memory	partially??	used depending on length of BASIC source code
BASIC	free	yes	in BASIC RAM range, but not currently used
BASIC	A, B,	depends	usable only if the respective BASIC variable is unused
	runtime state	outside BASIC	internal BASIC state, only usable if BASIC is not running
la a sal	VIA	no!!!	VIA registers
hard-	VID	no!!!	VID registers and banks
ware regs	S/UA	no!!!	SID and UART registers
regs	free	risky?	no registers currently mapped here, acts as RAM
VID-	free shared RAM	yes	
shared	default VID RAM	if remapped	
ROM	ROM	no	not writable