High frequency inverter external communication protocol

The external communication adopts UART communication, and the communication settings are: based rate 2400pps, 8 data bits, 1 stop bit, no party check, no flow control. The communication method adopts the half-duplex communication method. At the same time, only one of the manter and the slaw can send data, and the other can receive data. The external communication is initiated by the external controller (upper computer), and the inventer controller responds (does not actively

slave address field	1-31 (decimal) (31 is broadcast address)					
	0x03: Read multiple parameters					
	0x06: Write a single parameter					
Sentional domain	0x10: Write multiple parameters					
	0x17: Master-slave synchronization					
	data 0x41: Firmware upgrade					
data field	The data field includes the address field and the data payload field					
CRC field	16bit CRC check value					

CRC check range is frame address~CRC field (excluding CRC field).

2.1.1, 0x03 read multiple registers

The function code (command) is used to read the contents of a continuous block in the register. The request protocol data unit specifies the starting register address and the number of registers, in the corresponding register data each register data contains two bytes (the binary number is right-eligned in each byte). For each register, the first byte is high and the second byte is low. For example, request to read register 0x0001-0x0002:

ask	ÿHexÿ	answer	ÿHexÿ
slave address	01	slave address	01
Order	03	Order	03
Register start address high	00	number of bytes	04
Register start address low	01	Register value high bit (01)	0F
High register number	00	Register value low (01)	A0
Low register number	02	Register value high bit (02)	01
CRC low bit	CRC low bit -		C2
CRC high	-	CRC low bit	-
		CRC high	-

2.1.2, 0x06 write a single register

ask	ÿHexÿ	answer	ÿHexÿ
slave address	01	01	
Order	06	Order	06
Register start address high	00	Register start address high	00
Register start address low	08	Register start address low	08
register value high	AA	register value high	AA
register value low	AA	register value low	AA
CRC low bit	-	CRC low bit	-
CRC high	-	CRC high	-

2.1.3, 0x10 write multiple registers

This function code (command) is used to write a segment (sequence) of continuous address values into registers. The value to be written is the Return the function code, start address and register write quantity. For example, the data written to register 6x001 address is data 0x1194, and the data written to register 0x002 address is 0x01CC.

ask	ÿHexÿ	answer	ÿHexÿ
slave address	01	slave address	01
Order	10	Order	10
Register start address high	00	Register start address high	00
Register start address low	01	Register start address low	01
High register number	00	High register number	00
Low register number	02	Low register number	02
number of bytes	04	CRC low bit	-
Register value high (01	11	CRC high	-
Register value low (01)	94		
Register value high bit (02)	11		
Low register value (02)	cc	·	
CRC low bit	_		
CRC high	-		

3.1, Information Data register definition

Address(Hex) SIZE(Word)		register name	Data Type Ove	rride Unit	Attribute	Register E	escription	Remark
0xF800	1	Type	INT16U 0			R Des	ceType	Equipment Calagory 0x50: High Frequency Inverter

0xF801	.	SubType	INT16U 0		R	Subclass	Device subclass 0x0204: 3024 (3000VA/24V) 0x0408: 5048 (5000VA/48V)
0xF804		Serial number	INT16U 0	-	R	number/SN addition, the machine model passe identification	The DN code is a 14-dgp pure number, such as: SN-d193-6402050001, there: SN-d193-6402050001, shere: SN(4)-6000, in such as a s
0xF80B		CPU1 F/W Version	INT16U -2	-)	R CF	U1 F/W Version Invalid value: 0	ØFFF
0xF80C		CPU2 F/W Version	INT16U -2	-	R CF	U2 F/W Version Invalid value: 0	GFFF .

3.2, Realtime Data register definition

Address(Hex) S	750W - B		D T 0				- Paradistra	Remark
Ox1100	ZE(Word)	register name	INT16U 0	erride Ui	it Attribu		er Description	
0x1100		SettingDataSn Working mode	INT16U 0			R The	ata in the serial number setting area of t de/Mode	watering parameter was has changed = 1 O-power-on mode (Power OnMode 1-estandby mode (StandbyMode 2-bypass mode(BypassMode 3-battery mode/ BatteryMode 4-fault mode(FaultMode
								5-mains mode/LineMode 6-charging mode/PVChargeMode 0-no charging / No charge 1-Constant current
0x1102		Battery charging stage INT16U 0			-	R sta	te/State	chargerBulk charge 2-Constant voltage charge/Absorption charge 3-Float charge/Float charge
0x1103		Fault Code	INT16U 0		-	R For	fault code/Fault ID , see the high-	requency inverter fault alarm table for details
0x1104		PowerFowMag	INT16U O		-		y face olemation	15: 0. Bastery disconnected, 1: Battery connected bit4: 0. Line shormal, 1: Line normal bit3: 0. PV input abnormal, 1: PV input normal bit2: 0. Load connect unaflowed 1: Load connect disconded bit5: 0. No power flow, 0: Battery changing 10: Battery daschanging bib8: 00: No power flow, 0: Ditaine power from Line 10: Feed power flow, 1: PV MPPT working bit5: 0. No power flow, 1: PV MPPT working bit5: 0. No power flow, 1: Load connected bit5: 0. Power flow version unsupported 1: Power flow version unsupported 1: Power flow version unsupported
0x1108		Battery voltage	INT16U -2 V	- /	voltage/\	oltage		
0x1109	1	Battery current	INT16S 0 A R The	battery curren	Current has	positive and r	egative values, the negative value is the posi-	ve and negative value of the discharge current, and
0x110A 0x1111	1	Battery power	INT16S 0 W	R Battery	power			the negative value represents the discharge power
	,	AC output voltage	INT16U -1 V	R AC outp			AC input voltage	
0x1117		AC intput voltage	INT16U -1		IN	R	/Voltage AC input frequency	
0x1119	•	AC intput frequency	INT16U -2 Hz	R			/Frequency	
0x111E		AC output active power INT16S 0 WR	Output active po	wer/Watt				
0x111F		AC output apparent power	INT16U 0 VA	R Output	apparent	power/VA		
0x1120		Load percentage	INT16U 0 %	Duty rat	o/Pecent			
0x1126		PV input voltage	INT16U -1 V	R PV volt	ge/Voltag	e		
0x112A		PV input power	INT16S 0 W	R PV pow	er/Watt			

3.3, Setting Data register definition

Address(Hex) S	ZE(Word)	register name	Data Type O	erride Ur	it Attribu	te Regist	er Description	Remark	Defaults	Setting Range S Minimum Maxi	tting Range num Maximum	Setting Range Remarks
0x211F	1	Battery cut-off voltage INT16U -1 V R/	N				Discharge cut-off voltage Discharge cut-off voltage	For model 3024, pcs=2 For model 5048, pcs=4	420	105/pcs 135	pcs	3024ÿ21.0V~27.0V 5048: 42.0V~54.0V
0x2122		Battery C.V charging voltage	INT16U -1 V	R/W			Constant voltage charging voltage Absorption voltage		576	120/pcs 150	pcs	3024ÿ24.0V~30.0V 5048: 48.0V~60.0V
0x2123		Battery floating charging voltage	INT16S -1 V	R/W			Float charge voltage Float voltage		544	120/pcs 150	pcs	3024ÿ24.0V~30.0V 5048: 48.0V~60.0V
0x2129	1	AC output frequency	INT8U	0	- R/V	frequent	y Frequency	0=50Hz/1=60Hz 0=Main	0	۰	1	
0x212A	1	Output source priority INT8U		0	- R/V		Output Priority Output Priority	power priority Utility Frist 1=PV priority Solar Frist 2=PV battery main power SolarBatUtility	0	0	2	
0x212B	1	Application Mode	INT8U	0	- R/V		application mode Application Mode	0=APL/1=UPS	0x00	0	,	
0x212C		Charging source priority	INT8U	0	- R/V		charging priority Charge Priority	1=PV priority Solar Frist 2=PV and mains priority SolarAndUtilityFrist 3=PV SolarOnly only			3	
0x212D	1	Battery type	INT8U	0	- R/V	battery t	pe type	0-Gel battery AGM 1=Flood battery 2-User defined 3=Lithium battery LIFePo4	0	0	3	
0x212E		Max. charging current INT8U		OAR	w		Maximum charging current Total charge current	One grid per 1A	60	10	100	10A~100A
0x2130		Max. AC charging current	INT8U	0 A R	w		Maximum mains current AC charge current	One grid per 1A	30	10	100	10A~100A
0x2131		Buzzer enable	INT8U	0	- R/V	Buzzer/l	uzzer	0=Disable/1=Enable	0x01	۰	,	
0x2133	,	OverLoad restart enable	INT8U	0	- R/V	Overloa	restart enable bit 0=disable/1=	enable	0x00	۰	,	
0x2134		Over temperature restart enable	INT8U	0	- R/V	Over ter	perature restart enable bit 0=di	sable/1=enable	0x00	0	,	
0x2135		LCD backlight enable	INT8U	0	- R/V	Backligh	/Backlight 0=Disable/1=Enable		0x01	۰	1	
0x2137	1	OverLoad to bypass	INT8U	0	- R/V		Overload transfer to bypass 0=disabl Overload to bypass	/1-enable	0x00	۰	,	
0x2156	,	Battery back to charge voltage	INT16U -1 V	R/W			Battery low to charge Battery low to charge Battery		460	110/pcs 135	pcs	3024ÿ22.0V~27.0V 5048: 44.0V~54.0V
0x2159	-	Battery back to discharge voltage	INT16U -1 V	R/W			high voltage to discharge Battery high to discharge	If it exceeds the maximum value, it will display FULL. For example, 5048 model, 601 is FULL	540	pcs +1	150/pcs 120	3024: 24.0V~30.1V (30.1V means FULL) 5048: 48.0V~60.1V (60.1V means FULL)