CUB—CUDA UNBOUND

5 JUNE 2024 | JAN H. MEINKE



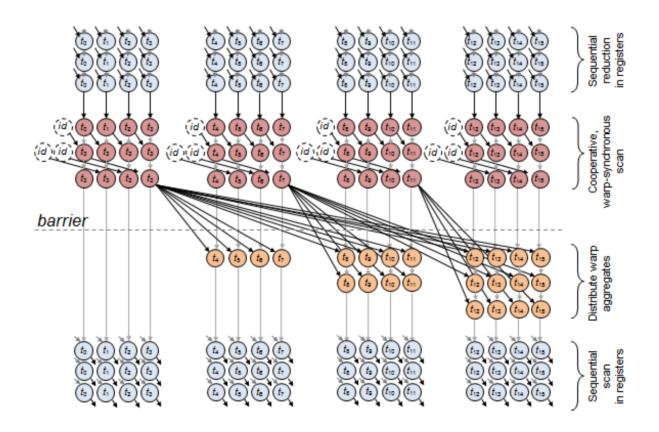
WHAT IS CUB?

- A design model for collective kernel-level primitives
 - How do I write collective primitives?
 - How do I deal with memory?
 - How do I make them tunable?
- A library of collective primitives
 - BlockLoad, BlockReduce, BlockRadixSort, ...
- A library of global primitives
 - DeviceReduce, DeviceHistogram, DeviceRadixSort, ...

(c.f. Duane Merrill's talk at GTC)

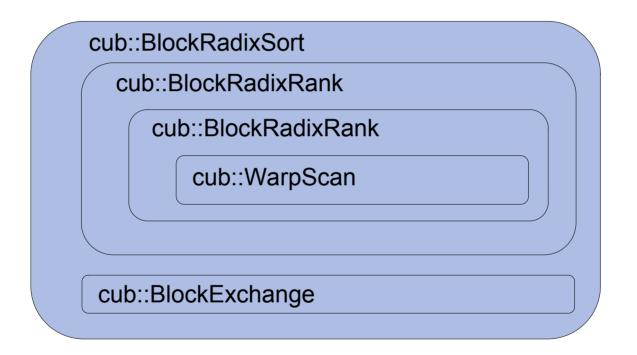


COLLECTIVE PARALLEL PROGRAMMING IS HARD





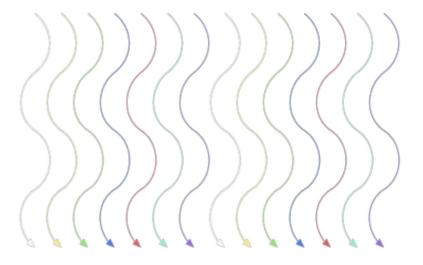
REDUCE! REUSE! RECYCLE!





MAKE IT TUNABLE

- Adjust parallelism
- Adjust grain size





MAKE IT TUNABLE

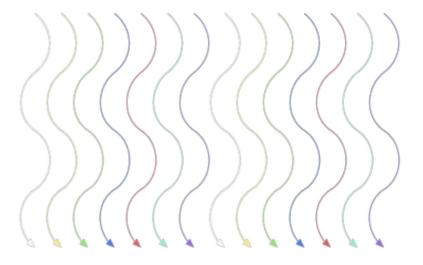
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MAKE IT TUNABLE

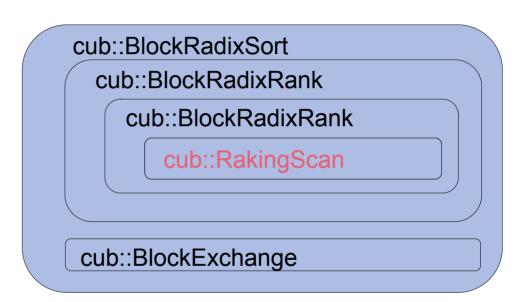
- Adjust parallelism
- Adjust grain size





SWAP OUT COMPONENTS

- Replace inner algorithm easily
- Performance depends on GPU
- Performance depends on the rest of the kernel





```
#include <cub/cub.cuh>
__global__ void ExampleKernel(...){
  // Specialize the template for double precision and 128 threads
  typedef cub::BlockReduce<double, 128> BlockReduceT;
```



```
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__global__ void ExampleKernel(...){
  // Specialize the template for double precision and 128 threads
  typedef cub::BlockReduce<double, 128> BlockReduceT;
  // Declare shared storage
  __shared__ typename BlockReduceT::TempStorage temp_storage;
  double items[4];
```



```
#include <cub/cub.cuh>
__global__ void ExampleKernel(...){
  // Specialize the template for double precision and 128 threads
  typedef cub::BlockReduce<double, 128> BlockReduceT;
  // Declare shared storage
  __shared__ typename BlockReduceT::TempStorage temp_storage;
  double items[4];
  // Instantiate an instance of BlockReduceT
  double result = BlockReduceT(temp storage).Sum(items);
```



```
global void ExampleKernel(const double* in, double* out){
 // Specialize the template for double precision and 128 threads w/ 4 items per thread
 typedef cub::BlockLoad<double, 128, 4> BlockLoadT;
 // Specialize the template for double precision and 128 threads
 typedef cub::BlockReduce<double, 128> BlockReduceT;
 // Declare shared storage
 __shared__ union {
   typename BlockLoadT::TempStorage load;
   typename BlockReduceT::TempStorage reduce:
 } temp storage;
                                                                             int main(){
 double items[4];
                                                                                ExampleKernel <<< 1, 128>>> (d gpu, result gpu);
 BlockLoadT(temp_storage.load).Load(in, items);
 __syncthreads();
 // Instantiate an instance of BlockReduceT
 double result = BlockReduceT(temp storage.reduce).Sum(items);
 if (threadIdx.x == 0) *out = result;
```



```
template <typename T>
__global__ void ExampleKernel(const T* in, T* out){
 // Specialize the template for double precision and 128 threads w/ 4 items per thread
  typedef cub::BlockLoad<T, 1024, 4> BlockLoadT;
 // Specialize the template for double precision and 128 threads
  typedef cub::BlockReduce<T, 1024> BlockReduceT;
 // Declare shared storage
  __shared__union {
    typename BlockLoadT::TempStorage load;
    typename BlockReduceT::TempStorage reduce;
                                                                              int main(){
  } temp_storage;
                                                                                ExampleKernel <<<1, 1024>>>(d gpu, result gpu);
  Titems[4];
  BlockLoadT(temp storage.load).Load(in, items);
  __syncthreads();
 // Instantiate an instance of BlockReduceT
  T result = BlockReduceT(temp_storage.reduce).Sum(items);
  if (threadIdx.x == 0) *out = result;
```



```
template <int BLOCK THREADS, int ITEMS PER THREAD, typename T>
global void ExampleKernel(const T* in, T* out){
 // Specialize the template for double precision and BLOCK THREADS threads w/ ITEMS PER THREAD items per thread
 typedef cub::BlockLoad<T, BLOCK THREADS, ITEMS PER THREAD> BlockLoadT;
 // Specialize the template for double precision and BLOCK THREADS threads
 typedef cub::BlockReduce<T, BLOCK THREADS> BlockReduceT;
 // Declare shared storage
  __shared__union {
    typename BlockLoadT::TempStorage load;
    typename BlockReduceT::TempStorage reduce;
                                                                          int main(){
 } temp_storage;
                                                                          ExampleKernel<1024, 4><<1, 1024>>>(d apu.
 Titems[ITEMS_PER_THREAD];
                                                                          result apu);
  BlockLoadT(temp storage.load).Load(in, items);
  __syncthreads();
 // Instantiate an instance of BlockReduceT
 T result = BlockReduceT(temp_storage.reduce).Sum(items);
 if (threadIdx.x == 0) *out = result;
```



```
template <int BLOCK THREADS, int ITEMS PER THREAD, cub::BlockLoadAlgorithm LOAD ALGO,
    cub::BlockReduceAlgorithm REDUCE ALGO, typename T>
global void ExampleKernel(const T* in, T* out){
 // Specialize the template for double precision and BLOCK THREADS threads w/ ITEMS PER THREAD items per thread
 typedef cub::BlockLoad<T, BLOCK THREADS, ITEMS PER THREAD, LOAD ALGO> BlockLoadT;
 // Specialize the template for double precision and BLOCK THREADS threads
 typedef cub::BlockReduce<T, BLOCK THREADS> BlockReduceT;
 // Declare shared storage
  shared union {
    typename BlockLoadT::TempStorage load;
    typename BlockReduceT::TempStorage reduce;
                                                                        int main(){
 } temp storage;
                                                                          ExampleKernel<1024, 4,
 Titems[ITEMS PER THREAD];
                                                                                    cub::BLOCK LOAD TRANSPOSE.
                                                                                    cub::BLOCK REDUCE RAKING>
  BlockLoadT(temp_storage.load).Load(in, items);
                                                                                      <<1, 1024>>>(d gpu, result gpu);
  syncthreads();
 // Instantiate an instance of BlockReduceT
 T result = BlockReduceT(temp storage.reduce).Sum(items);
  if (threadIdx.x == 0) *out = result;
```



RESOURCES

- Duane Merrill's talk at GTC
 https://on-demand.gputechconf.com/gtc/2014/presentations/S4566-cub-c ollective-software-primitives.pdf
- The CUB web page at http://nvlabs.github.io/cub/.
- CUB is part of the CUDA C++ Core Libraries.

