University of Toronto Faculty of Applied Science and Engineering

Term Test I

Feb. 9, 2015 14:15 – 15:45 Duration: 90 minutes

ECE159 - Electric Circuit Fundamentals Examiners: Prof. Li Qian

ANSWER QUESTIONS ON THE FRONT PAGES, USE THE BACK PAGES FOR ROUGH WORK ONLY. THERE ARE EXTRA PAGES IN BETWEEN QUESTIONS. WRITE IN PEN ONLY (NO PENCIL)!

- 1. Calculator type is restricted (no programmable calculators).
- 2. Weight for each question is indicated in [].

Questions Mark LAST NAME: Solutions Q1 /6 FIRST NAME: _____ Q2 /4 STUDENT NUMBER: _____ Q3 /7 TUTORIAL SECTION: Q4 /7 Q5 /6

Q1. [6 marks] Answer the following questions.

- (a) [2 marks] Circle TRUE or FALSE for the following statements:
 - 1. When the passive sign convention is followed, if the product of current and voltage associated with a circuit element is positive, the electric field does positive work.

TRUE / FALSE

2. Electric current can flow only from a high voltage node to a low voltage node in a circuit.



3. Kirchhoff's voltage law is a result of energy conservation.



4. For a linear circuit with multiple ideal independent voltage sources, if the voltage output of one of these sources is doubled, the current going through any circuit element is doubled.



- (b) [2 marks] Write your answers in the boxes provided:
 - 1. The resistance value of the resistor shown below is

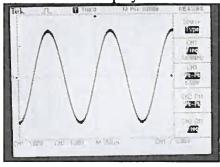
560 KQ



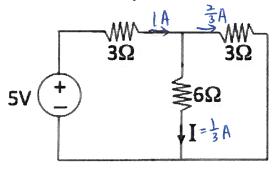
COLOR	VALUE OF RESISTANCE			COLOR	TOLERANCE
Black		0	$10^0 = 1$	Red	2 %
Brown	1	1	$10^1 = 10$	Gold	5 %
Red	2	2	$10^2 = 100$	Silver	10 %
Orange	3	3	$10^3 = 1,000$	None	20 %
Yellow	4	4	$10^4 = 10,000$		
Green	5	5	$10^5 = 100,000$	İ	
Blue	6	6	$10^6 = 1,000,000$		
Violet	7	7	$10^7 = 10,000,000$	i	
Gray	8	8	$10^8 = 100,000,000$		
White	9	9	$10^9 = 1,000,000,000$		
	1st band	2nd band	3rd band	4th band	

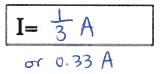
2. The picture below shows the display of an instrument called

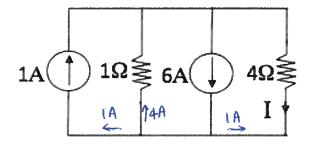
an oscilloscope

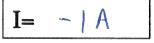


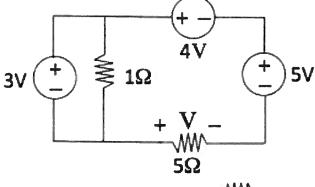
(c) [2 marks] Write your answers in the boxes provided

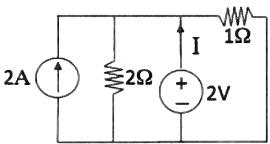






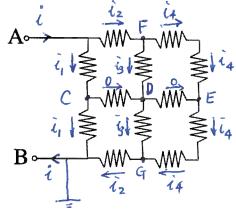






Q2. [4 marks] All resistors in the diagram below have the same resistance R. Find the equivalent resistance (in terms of R) evaluated at terminals A and B:

Use symmetry, we know that $V_C = V_D = V_E = \frac{1}{2} V_A$ Therefore, $i_{e_0} = 0$ for the branch between $C \neq D$, and $i_{b_0} = 0$ for the branch between $D \neq E$.



The rest of the currents are labelled as shown. Note the symmetrical labelling.

$$V_{FO} = V_{FE} \implies i_3 R = i_4 (R+R) \implies i_3 = 2i_4$$

$$V_{AC} = V_{AD} = V_{AF} + V_{FO} \Rightarrow \hat{c_1}R = \hat{c_2}R + \hat{c_3}R \Rightarrow \hat{c_1} = 5\hat{c_4}$$

Finally,
$$Reg = \frac{V_A}{i} = \frac{V_{AC} + V_C}{i} = \frac{i_i R + i_i R}{i} = \frac{10i_4}{8i_4} R$$

A
$$i$$
 j $Reg = \frac{5}{4}R$

B i j $Reg = \frac{5}{4}R$

Q2. [4 marks] All resistors in the diagram below have the same resistance R. Find the equivalent resistance (in terms of R) evaluated at terminals A and B:

Alternative solution: Use symmetry, we know that $V_C = V_0 = V_E = \frac{1}{2} V_A$ Therefore $\hat{c}_{co} = 0$ for the branch between C & D; and $\hat{c}_{DE} = 0$ for

the branch between D&E

A B W W B

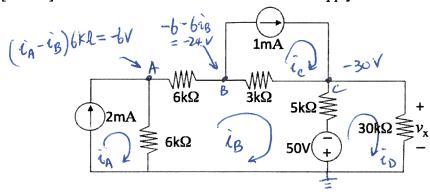
The circuit is then simplified to:

A of the simplified to:

B of the s

Page 4

- Q3. [7 marks] For the circuit shown below,
 - (a) [4 marks] Use *mesh* analysis to find v_x . Indicate mesh currents on the diagram, write down KVL equations, and carry out the calculations.
 - (b) [2 marks] Find the power supplied or consumed by the 1mA current source.
 - (c) [1 mark] Indicate whether the 1mA current source supply or consume power.



(a)
$$i_A = 2mA$$
 $i_c = 1mA$
 VL for much $B: 6i_B + 3(i_B - i_c) + 5(i_B - i_0) - 50 + 6(i_B - i_A) = 0$
 $\Rightarrow 20i_B - 5i_0 = 50 + 3i_c + 6i_A = 65 mA$ ①

KVL for mush
$$0: 30 i_0 + 50 + 5(i_0 - i_B) = 0$$

$$=) -5i_B + 35i_0 = -50 \text{ mA}$$

$$\begin{cases} 20i_{3}-5i_{0}=65 \\ -5i_{6}+35i_{0}=-50 \end{cases} \Rightarrow \begin{cases} i_{8}=3 \text{ mA} \\ i_{0}=-1 \text{ mA} \end{cases}$$

$$V_{X} = 30k\Omega \cdot i_{0} = -30V$$

(b)
$$V_A = (i_A - i_B) \cdot 6k\Omega = (2-3) \cdot 6 = -6V$$

 $V_B = V_A - 6k\Omega \cdot i_B = -6 - 6 \cdot 3 = -24V$

$$V_{c} = -30V$$

$$P_{\theta} = (1 \text{ mA})(-24 - (-30))V = 6 \text{ mW} > 0$$

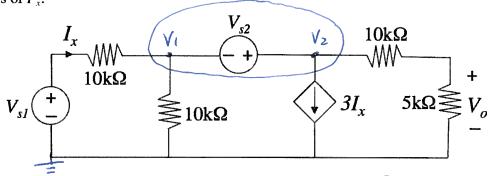
$$T_{\theta} = (1 \text{ mA})(-24 - (-30))V = 6 \text{ mW} > 0$$

(c) the IMA current source consumes 6 mw power.

Page 5

Q4. [7 marks] For the circuit below,

- (a) [4 marks] Use *nodal* analysis to find an expression for V_o as a function of V_{sl} and V_{s2} .
- (b) [1 mark] Find the power consumption by the $5k\Omega$ resistor if $V_{sl} = 2V$, $V_{s2} = 6V$
- (c) [2 marks] If the power supplied by the dependent source is P_x , what will the power supplied by the dependent source be if both V_{s1} and V_{s2} are doubled? Write your answer in terms of P_{ν} .



(a) Use supernode as shown:
$$V_2 - V_1 = V_{S_2}$$
 \bigcirc

$$KCL @ the supernode: \frac{V_{S_1} - V_1}{IOK\Omega} - \frac{V_1}{IOK\Omega} - \frac{V_2}{IOK\Omega} - \frac{V_2}{IOK\Omega} = 0 \bigcirc$$

KCL @ the supernode:
$$\frac{V_{SI}-V_{I}}{10K\Omega} - \frac{V_{I}}{10K\Omega} - \frac{3}{10K\Omega} \frac{V_{SI}-V_{I}}{10K\Omega} - \frac{V_{Z}}{10K\Omega} = 0$$
 $= 3I_{X}$

$$\Rightarrow (-\frac{1}{10} - \frac{1}{10} + \frac{3}{10})V_1 - (\frac{1}{15})V_2 = (\frac{3}{10} - \frac{1}{10})V_{s_1}$$

$$\Rightarrow \frac{V_1}{10} - \frac{V_2}{15} = \frac{V_{SI}}{5}$$
 3

Substitute (1) into (3)
$$\Rightarrow \frac{V_2 - V_{52}}{10} - \frac{V_2}{15} = \frac{V_{51}}{5}$$

$$\Rightarrow V_2 = 6V_{Si} + 3V_{S2}$$

Use voltage division: $V_0 = \frac{5}{5+10} V_2 = 2 V_{s_1} + V_{s_2}$

(b)
$$V_0 = 2V_{S_1} + V_{S_2} = 2 - 2 + 6 = 10V$$

 $P_{5k2} = \frac{V_0^2}{5k2} = \frac{100 V^2}{5k2} = \frac{20 \text{ mW}}{5}$

EXTRA BLANK PAGE

pour quadruples.

(C)
$$P_X = (-V_2)(3I_X)$$

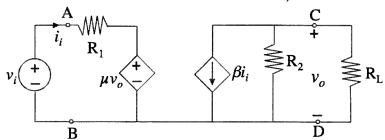
Use linearity, if both V_{S1} & V_{S2} double, then V_Z doubles.

if """, then $3I_X$ doubles.

Therefore, if both V_{S1} & V_{S2} double, then $P_X' = 4P_X$

Q5. [6 marks] For the circuit shown below,

- (a) [4 marks] Find the Thévenin equivalent circuit seen by the load R_L (i.e., looking into terminals C and D, with source connected, and the load disconnected).
- (b) [2 marks] Find the equivalent circuit seen by the source v_i (i.e., looking into terminals A and B, with load connected and the source disconnected.)



(a) Find Voc

$$V_i = \frac{R_i}{\mu V_o}$$
 $V_o = -\beta i_i R_2 = -\beta R_2 \left(\frac{V_i - \mu V_{oc}}{R_i} \right)$

$$= \frac{1 - \mu \beta R_2}{R_i} V_{oc} = -\beta \frac{R_i}{R_i} V_i \Rightarrow V_{oc} = \frac{-\beta R_2}{R_i - \mu \beta R_2} V_i$$

$$v_{i} \stackrel{i_{i}}{=} \stackrel{R_{i}}{=} \frac{1}{R_{i}} \stackrel{i_{i}}{=} \frac{1}{R_{i}} \frac{1}{R_{i}} = -\frac{B}{R_{i}} v_{i}$$

$$i_{i} \stackrel{i_{i}}{=} \frac{1}{R_{i}} = -\frac{B}{R_{i}} v_{i}$$

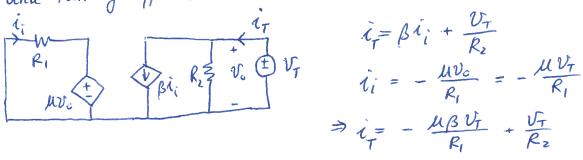
$$\vdots \quad l_{Sc} = -\beta l_{i} = -\frac{B}{R_{i}} v_{i}$$

Page 9

EXTRA BLANK PAGE

$$R_{eg} = \frac{V_{oc}}{i_{sc}} = \frac{-\beta R_2 V_i}{R_1 - \mu \beta R_2} \left[\left(-\frac{R}{R_1} \right) V_i \right] = \frac{R_1 R_2}{R_1 - \mu \beta R_2}$$

A Hematively, Reg can also be found directly using a test voltage; and turning off Vi :



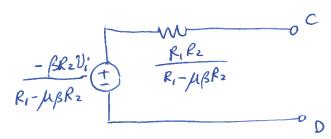
$$i_{T} = \beta i_{1} + \frac{v_{T}}{R_{2}}$$

$$i_{1} = -\frac{\mu v_{0}}{R_{1}} = -\frac{\mu v_{T}}{R_{1}}$$

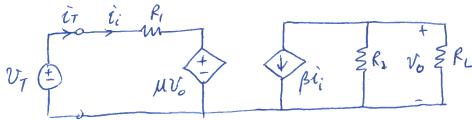
$$i_{T} = -\frac{\mu \beta v_{T}}{R_{1}} + \frac{v_{T}}{R_{2}}$$

$$-\frac{1}{2} Reg = \frac{V_T}{i_T} = \frac{V_T}{-\frac{\mu\beta V_T}{R_1} + \frac{V_T}{R_2}} = \frac{R_1 R_2}{R_1 - \mu\beta R_2}$$

i. Equivalent circuit seen by the load (Thévenin form) is:



(b) Use test voltage source to find Reg seen by the source.



EXTRA BLANK PAGE

$$V_{0} = -\beta i_{1} (R_{2} | | R_{L}) = -\beta i_{1} \cdot \frac{R_{2} R_{L}}{R_{2} + R_{L}}$$

$$i_{1} = i_{T} = \frac{V_{T} - \mu V_{0}}{R_{1}} = \frac{V_{T}}{R_{1}} + \frac{\mu \beta i_{1} (R_{2} R_{L})}{R_{1} (R_{2} + R_{L})}$$

$$\Rightarrow \left(1 - \frac{\mu \beta R_{2} R_{L}}{R_{1} (R_{2} + R_{L})}\right) i_{1}^{2} = \frac{V_{T}}{R_{1}}$$

$$\Rightarrow i_{1}^{2} = \left[R_{1} \left(1 - \frac{\mu \beta R_{2} R_{L}}{R_{1} (R_{2} + R_{L})}\right)\right]^{-1} V_{T}$$

$$\therefore R_{0}^{2} = \frac{V_{T}}{i_{T}} = \frac{V_{T}}{i_{T}} = R_{1} \left(1 - \frac{\mu \beta R_{2} R_{L}}{R_{1} (R_{2} + R_{L})}\right)$$

$$= R_{1} - \frac{\mu \beta R_{2} R_{L}}{R_{2} + R_{L}}$$

.. The equivalent circuit seen by the source is