University of Toronto Faculty of Applied Science and Engineering

Term Test I

Feb. 6, 2014 Duration: 90 minutes

ECE159 - Electric Circuit Fundamentals Examiners: Ali Sheikholeslami and Li Qian

ANSWER QUESTIONS ON THESE SHEETS, USING THE BACKS IF NECESSARY.

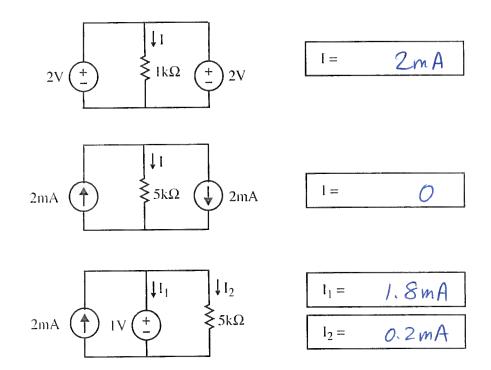
WRITE IN PEN ONLY (NO PENCIL)!

- 1. Calculator type is restricted (no programmable calculators).
- 2. Weight for each question is indicated in []. Attempt all questions, since a blank sheet will certainly get a zero.

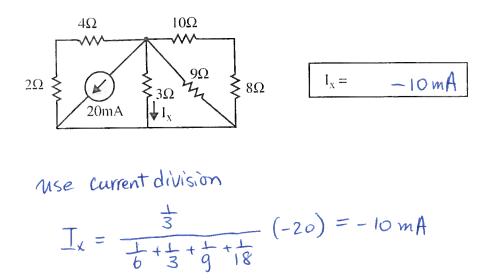
Solution	maxim	maximum grade = 30	
	Questic	on Mark	
Last Name:	I	/6	
	2	/6	
First Name:	3	/6	
Student Number:	4	/8	
Tutorial Section:	5	/4	
	Total	/30	

Q1. [6 marks]

(a) [4 marks] Fill out the current values in the boxes below:

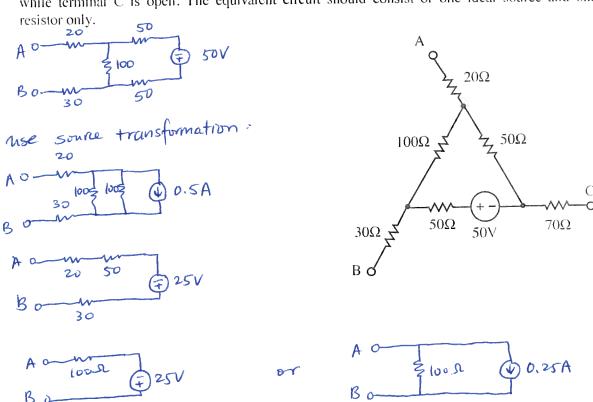


(b) |2| marksFind current l_x as indicated below.

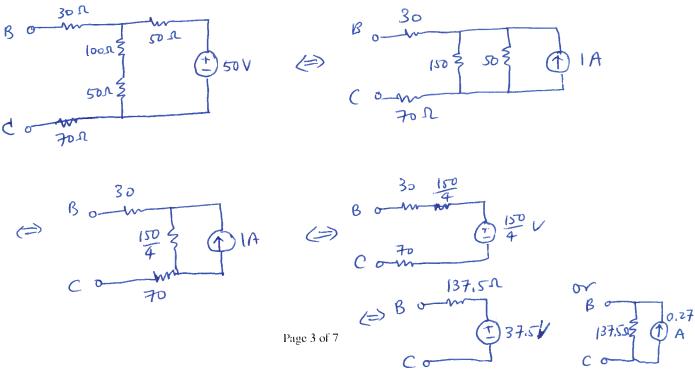


Q2 [6 marks]

(a) [3 marks] For the circuit shown below, find an equivalent circuit between terminals A and B while terminal C is open. The equivalent circuit should consist of one ideal source and one resistor only.



(b) [3 marks] For the circuit shown above, find an equivalent circuit between terminals B and C while terminal A is open. The equivalent circuit should consist of one ideal source and one resistor only.

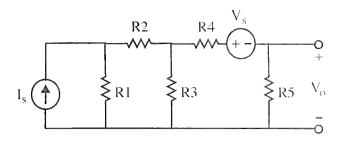


Q3 [6 marks]

(a) [3 marks] In the circuit shown below, the following measurements are obtained:

For
$$I_s = 2mA \& V_s = 3V, V_o = 7V$$

For $I_s = 4mA \& V_s = 9V, V_o = 20V$



Find V_0 when $I_s = 1$ mA and $V_s = 6$ V

Using linearity:
$$V_0 = \alpha I_s + \beta V_s$$

$$\Rightarrow \begin{cases} \alpha \cdot 2mA + \beta \cdot 3V = 7V \\ \alpha \cdot 4mA + \beta \cdot 9V = 20V \end{cases}$$

$$\Rightarrow \begin{cases} \alpha = 0.5 \text{ kg} \\ \beta = 2 \end{cases}$$

$$V_0 = 12.5V$$

(b) [3 marks] As in part (a), assume with $I_s = 2mA$ and $V_s = 3V$, we measure $V_o = 7V$. If we now short the V_o terminals together, we will measure a short-circuit current of 10mA. Assuming all resistors (R1 to R5) are equal, i.e. R1 = R2 = R3 = R4 = R5 = R, calculate R.

Solution # 1: Given open-circuit voltage
$$V_{oc} = 7V$$
 } $R_{eg} = \frac{V_{oc}}{I_{sc}} = 0.7KD$

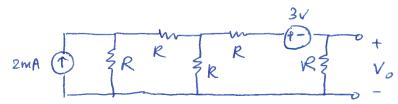
with Is & Vs turned off.

$$R_{\frac{3}{8}}^{R} = R_{\frac{3}{8}}^{R} = \frac{5}{3}RIIR = \frac{5}{8}R$$

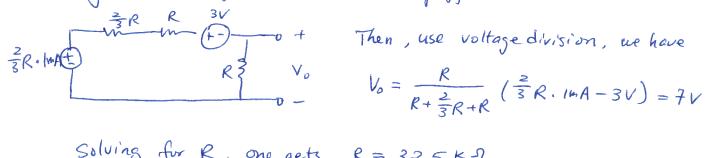
$$R_{\frac{3}{8}}^{R} = 0.7K\Omega \implies R = \frac{5.6}{5}K\Omega = 1.12K\Omega$$

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Solution#2. If one assumes all sesistors have equal value R, and Is = 2 mA, Vs = 3V, Let's find R in order to produce Vo=7V



Using source transformation, one can simplify the circuit to:

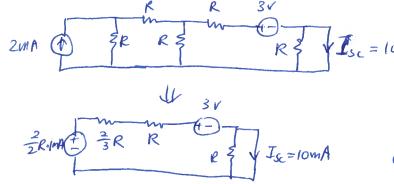


$$V_0 = \frac{R}{R + \frac{2}{3}R + R} \left(\frac{2}{3}R \cdot ImA - 3V \right) = 7V$$

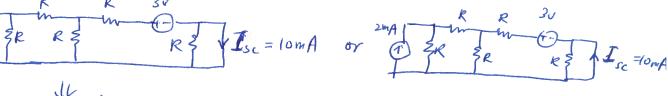
Solving for R, one gets R = 32.5 K2

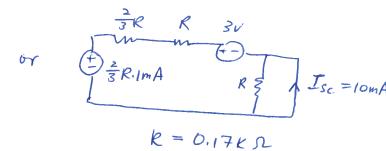
(Note here the Isc is not 10mA)

Solution #3: If one assumes all R's equal, and Is = 2mA, Vs=3V, let's find R in order to procluce Isc = 10 mA



RCO imposible

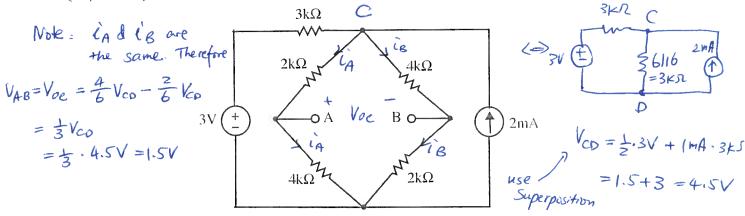




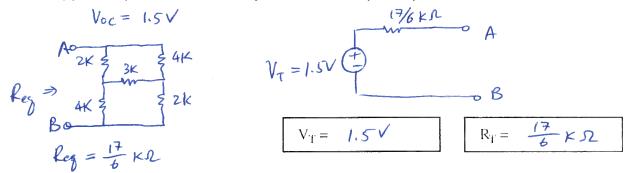
(Note here the Voc cannot be 7v)

Any one of the above solutions will get full marks for the question

Q4. [8 marks] In the circuit shown below:



(a) [4 marks] Find the Thevenin equivalent circuit (V_T and R_T) between terminals A and B.

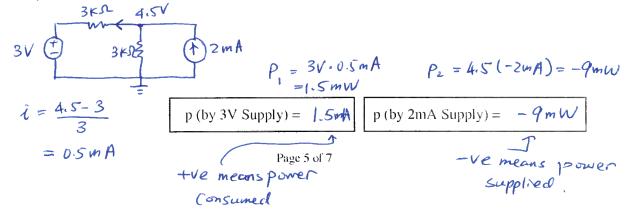


(b) [2 marks] If a variable load resistor is connected between terminals A and B, what is the maximum power that can be transferred to this load?

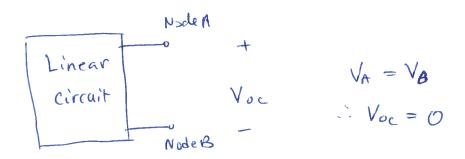
$$P_{\text{max}} = \frac{V_T^2}{4R_T} = \frac{1.5^2}{4.\frac{17}{6}} = 0.199 \text{ mW} \approx 0.2 \text{ mW}$$

$$p_{\text{max}} = 0.2 \, \text{mW}$$

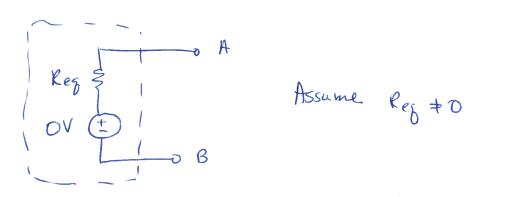
(c) [2 marks] How much power is generated by each source when there is no load (i.e. when there is open circuit between A and B)?



Q5 [4 marks] In a linear circuit that includes resistors and independent voltage and current sources only, we measure the voltages of two distinct nodes (A & B) to be identical. If we connect these two nodes using a short circuit, prove that the current in the short circuit will be zero. (Hint: Use Thevenin Equivalent Circuit Theorem).



The equivalent circuit for the above is



Therefore, no current will flow if AdB are short-circuited because VT=OV

