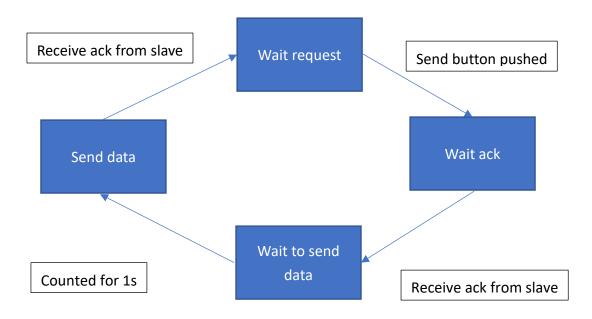
# Lab 6: Peripherals

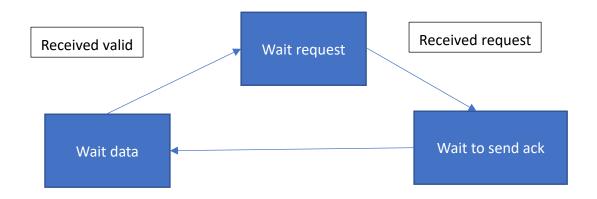
## **Design Explanation**

#### 1. Chip 2 Chip

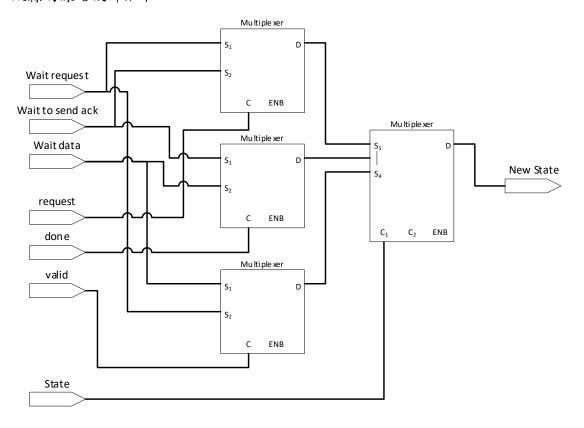
根據助教於課堂上的解釋,主人的行為可以用以下自動機表示。

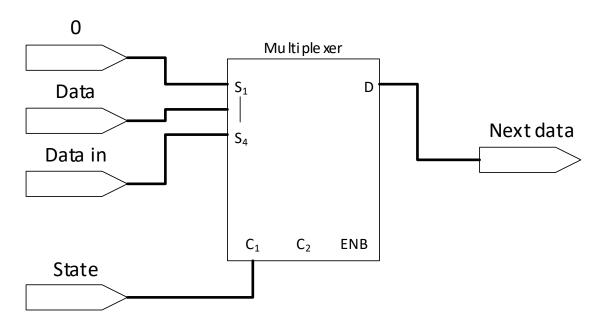


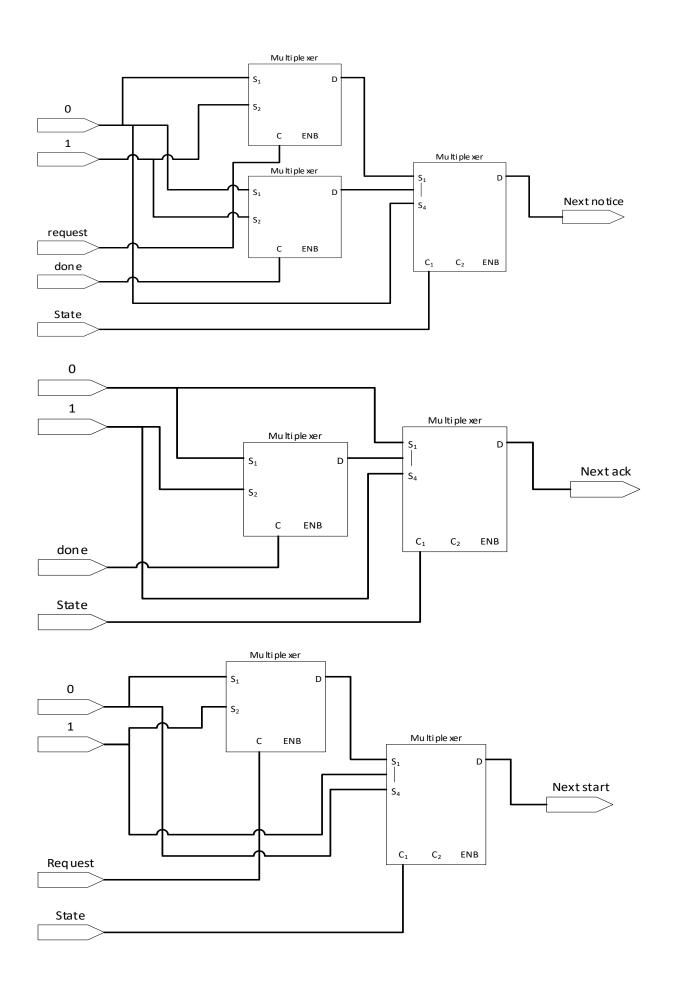
而奴隸的行為可以用以下自動機表示。



#### 奴隸的線路設計如下。







## 2. 拉霸機

The details are down below:

- (1) Design Specification:
- (2) Truth Table:
- (3) Gate Level Circuits:

## Design Verification

## 1. Chip 2 Chip

Enumerate all possible inputs, and verify the outputs manually. The pictures below are the representation with verification.

## 2. 拉霸機

???????

## Contribution

- 1. Lawrence Wu Chip 2 Chip 之設計。
- 2. Ariel Chang

## What have we learned?

- 1. 記得要填註解上去,不然接手的人會討厭你
- 2. 模組切的好,今晚睡得好;模組沒切好,糾錯糾到老