

Access provided by: **Columbia University** Sign Out



Browse Journals & Magazines > Solid-State Circuits, IEEE Jou ...

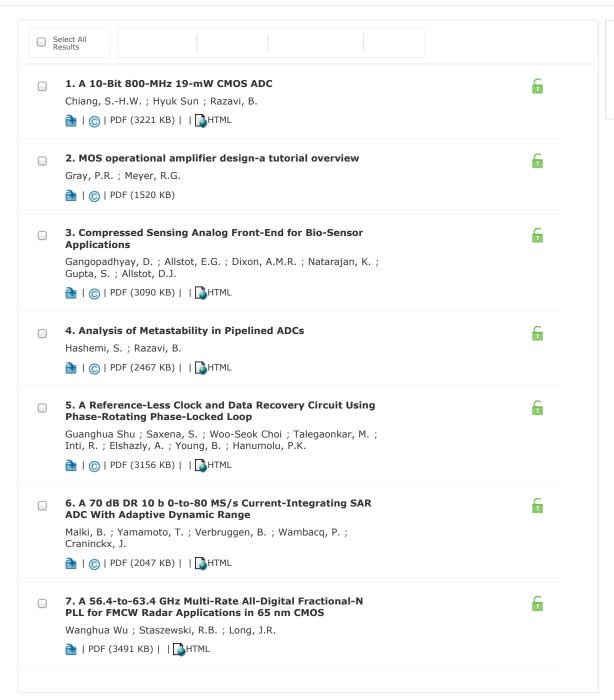
## Solid-State Circuits, IEEE Journal of



Popular Early Access Current Issue Past Issues About Journal Submit Your Manuscript

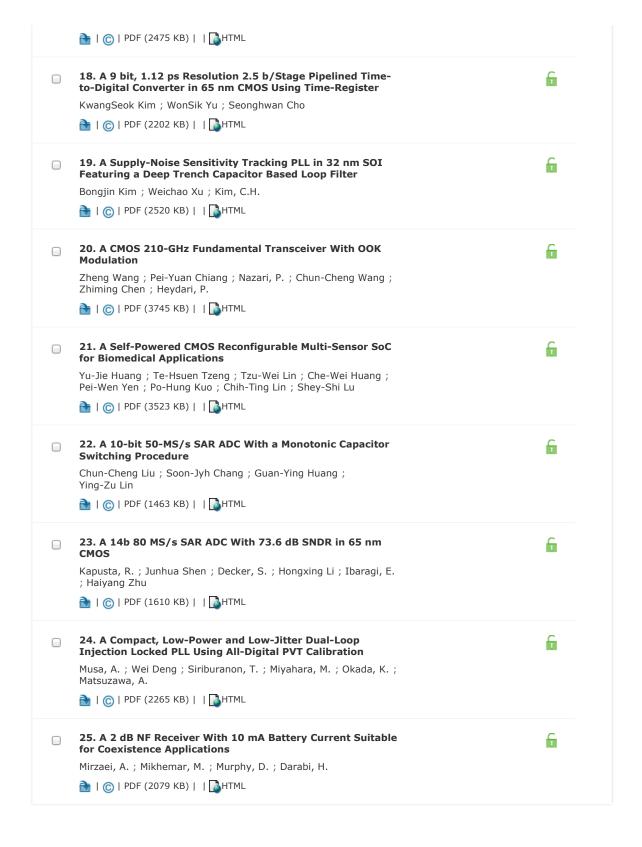
## **Popular Articles (April 2014)**

Includes the top 25 most frequently downloaded documents for this publication according to the most recent monthly usage statistics.





8. External Capacitor-Less Low Drop-Out Regulator With 25 dB Superior Power Supply Rejection in the 0.4–4 MHz Range  Chang-Joon Park; Onabajo, M.; Silva-Martinez, J.	
9. A 0.5-to-3 GHz Software-Defined Radio Receiver Using Discrete-Time RF Signal Processing Run Chen; Hashemi, H.    C   PDF (4058 KB)     HTML	6
10. A Noise-Shaping Time-to-Digital Converter Using Switched-Ring Oscillators—Analysis, Design, and Measurement Techniques  Elshazly, A.; Rao, S.; Young, B.; Hanumolu, P.K.	
11. A Deterministic Digital Background Calibration Technique for VCO-Based ADCs Sachin Rao; Reddy, K.; Young, B.; Hanumolu, P.K.	
12. Multi-Drive Stacked-FET Power Amplifiers at 90 GHz in 45 nm SOI CMOS  Agah, A.; Jayamon, J.A.; Asbeck, P.M.; Larson, L.E.; Buckwalter, J.F.    ©   PDF (2364 KB)     HTML	
13. A 3 GHz Dual Core Processor ARM Cortex TM -A9 in 28 nm UTBB FD-SOI CMOS With Ultra-Wide Voltage Range and Energy Efficiency Optimization  Jacquet, D.; Hasbani, F.; Flatresse, P.; Wilson, R.; Arnaud, F.; Cesana, G.; Di Gilio, T.; Lecocq, C.; Roy, T.; Chhabra, A.; Grover, C.; Minez, O.; Uginet, J.; Durieu, G.; Adobati, C.; Casalotto, D.; Nyer, F.; Menut, P.; Cathelin, A.; Vongsavady, I.; Magarshack, P.	
14. A Time-Based Pipelined ADC Using Both Voltage and Time Domain Information  Oh, T.; Venkatram, H.; Moon, UK.	6
15. Cancellation of OpAmp Virtual Ground Imperfections by a Negative Conductance Applied to Improve RF Receiver Linearity  Mahrof, D.H.; Klumperink, E.A.M.; Zhiyu Ru; Oude Alink, M.S.; Nauta, B.   □   ○   PDF (3473 KB)   □ HTML	
16. Current Reference Pre-Charging Techniques for Low-Power Zero-Crossing Pipeline-SAR ADCs  Kuppambatti, J.; Kinget, P.R.	<b>6</b>
17. A -90 dBm Sensitivity Wireless Transceiver Using VCO-PA-LNA-Switch-Modulator Co-Design for Low Power Insect-Based Wireless Sensor Networks  Sayilir, S.; Loke, WF.; Lee, J.; Diamond, H.; Epstein, B.; Rhodes, D.L.; Byunghoo Jung	<b>6</b>



## Aims & Scope Meet Our Editors Further Links

The IEEE Journal of Solid-State Circuits publishes papers each month in the broad area of solid-state circuits with particular emphasis on transistor-level design of integrated circuits.

Editor-in-Chief Michael Flynn University of Michigan

Sign In | Create Account

**IEEE Account** 

**Purchase Details** 

**Profile Information** 

Need Help?

Change Username/Password

Payment Options
Order History

Communications Preferences

**US & Canada:** +1 800 678 4333 **Worldwide:** +1 732 981 0060

Update Address

Access Purchased Documents

Profession and Education
Technical Interests

Contact & Support

 $\textbf{About IEEE} \ \textit{Xplore} \ | \ \textbf{Contact} \ | \ \textbf{Help} \ | \ \textbf{Terms} \ \textbf{of Use} \ | \ \textbf{Nondiscrimination Policy} \ | \ \textbf{Site Map} \ | \ \textbf{Privacy \& Opting Out of Cookies}$ 

A not-for-profit organization, IEEE is the world's largest professional association for the advancement of technology. © Copyright 2014 IEEE - All rights reserved. Use of this web site signifies your agreement to the terms and conditions.