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Article Information

1.   **HTS/ferroelectric devices for microwave applications**
     Gevorgian, S.S.; Carlsson, E.F.; Rudner, S.; Helmersson, U.; Kollberg, E.L.; Wikborg, E.; Vendik, O.G.
     Applied Superconductivity, IEEE Transactions on
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     Digital Object Identifier 10.1109/77.621737

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2.   **Conformal mapping of the field and charge distributions inmultilayered substrate CPWs**
     Carlsson, E.; Gevorgian, S.
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3.   **CAD models for multilayered substrate interdigital capacitors**
     Gevorgian, S.S.; Martinsson, T.; Linner, P.L.J.; Kollberg, E.L.
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     Volume 44, Issue 6, Date: Jun 1996, Pages: 896-904
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4.   **Lower order modes of YBCO/STO/YBCO circular disk resonators**
     Gevorgian, S.; Carlsson, E.; Linner, P.; Kollberg, E.; Vendik, O.; Wikborg, E.
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5.   **Open resonator technique for measuring multilayered dielectric plates**
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6.   **FVC2000: fingerprint verification competition**
     Maio, D.; Maltoni, D.; Cappelli, R.; Wayman, J.L.; Jain, A.K.
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8.   **WiMax and Wi-Fi: Separate and Unequal**
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9.   **What is Bluetooth?**
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10.   **A UWB CMOS transceiver**
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11.   **0.5-V analog circuit techniques and their application in OTA and filter design**
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13.   **All-digital PLL and transmitter for mobile phones**
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23.   **RFID: a technical overview and its application to the enterprise**
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33.   **A 3.1- to 8.2-GHz zero-IF receiver and direct frequency synthesizer in 0.18-/spl mu/m SiGe BiCMOS for mode-2 MB-OFDM UWB communication**
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36.   **An interference-robust receiver for ultra-wideband radio in SiGe BiCMOS technology**
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37.   **IEEE recommended practice for software requirements specifications**

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38.   **A road to future broadband wireless access: MIMO-OFDM-Based air interface**

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56.   **A 0.94-ps-RMS-jitter 0.016-mm/sup 2/ 2.5-GHz multiphase generator PLL with 360/spl deg/ digitally programmable phase shift for 10-Gb/s serial links**
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60.   **IEEE standard for information technology- telecommunications and information exchange between systems- local and metropolitan area networks- specific require
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67. **A 6.25-Gb/s binary transceiver in 0.13-/spl mu/m CMOS for serial data transmission across high loss legacy backplane channels**
Payne, R.; Landman, P.; Bhakta, B.; Ramaswamy, S.; Song Wu; Powers, J.D.; Erdogan, M.U.; Yee, A.-L.; Gu, R.; Lin Wu; Yiqun Xie; Parthasarathy, B.; Brouse, K.; Mohamme Heragu, K.; Gupta, V.; Dyson, L.; Wai Lee
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71. **The dual-tree complex wavelet transform**
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