



IEEE Solid-State Circuits Society

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SSCS Predoctoral Achievement Award

Predoctoral Achievement Award

For a small number of promising graduate students, IEEE-SSCS Predoctoral Achievement Awards provide a \$1000 honorarium and reimbursement for travel expenses (within certain limits) to ISSCC, the Society's flagship conference. Applicants must be members of IEEE and the [Solid State Circuits Society](#) and have completed at least one year of study in a Ph.D. program in the area of solid-state circuits. Awards are made on the basis of academic record and promise, quality of publications, and a graduate study program well matched to the charter of SSCS. Prior winners of the Predoctoral Achievement Award are not eligible. No more than two Awards will be granted in a given year to students of one Principal Advisor.

2015- 2016 Application Instructions

Application Timetable:

Begin Date: September 1, 2015

End Date: October 15, 2015

Winners announced by November 30, 2015.

All documents should be delivered electronically. There is no application form. Please supply the following:

- Biography (1 page) including IEEE member number and contact information.
- A copy of all relevant undergraduate and graduate transcripts
- Essay on graduate study plans (about 2 pages)
- Summarize what you have completed and what is planned.
- List authored and co-authored publications and include a copy of each.
- Describe work that must be done to complete your graduate program of study -- explain its importance and what is novel about its approach.
- Letters of Recommendation: At least two letters, one from the principal advisor, addressing academic record, accomplishments, and promise, and graduate research program.

It is best for students to collect all application documents into a single, complete package and submit it via email to sscspredocaward2015@ieee.org (<mailto:sscspredocaward2015@ieee.org>). Each file in the application may be attached individually or within a zip file collection. Please limit individual attached files to 10MB and total attachments to 25MB to avoid e-mail server size limits. You can spread attachments across multiple e-mails if necessary. Recommendations may be incorporated into this package or emailed directly by the author to sscspredocaward2015@ieee.org (<mailto:sscspredocaward2015@ieee.org>). In the latter case, each applicant should follow-up to make sure his or her references are submitted on time. Applications should be sent to the Award Committee at: sscspredocaward2015@ieee.org (<mailto:sscspredocaward2015@ieee.org>)

Predoctoral Achievement Award Winners for 2015-2016



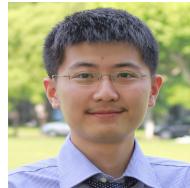
Zeshan Ahmad



Muhammad Awais Bin Altaf



Masoud Babaie

Ahmed ElkholySameed HameedHyek-Ki HongInjoon HongSong HuYouchang KimXiaosen LiuXilin LiuHans MeyvaertSaleh Heidary ShalmanyTeerachot SiriburanonAmritth SukumaranTong ZhangWouter VolkaertsJianxun ZhuJin Zhou

Zeshan Ahmad- Zeshan Ahmad (S'09): received the MSE degree in electrical engineering from the University of Michigan, Ann Arbor in 2010 and is currently working towards the Ph.D. degree at TxACE & Dept. of EE, Univ. of Texas at Dallas. His research interests include RFICs operating from mm-wave to far-infrared frequencies with applications in tera-bit links, early cancer detection, round the clock health monitoring, stand-off detection, automotive and industrial process control. During summers of 2013 and 2014, he was an intern with automotive-radar group of TI Dallas, TX, USA where he was involved in the design of power amplifiers capable of operating under extreme operating conditions, BIST methodologies, and device modeling. He is a member of the IEEE, SSCS, MTT-S, EDS Societies and a recipient of IEEE SSCS Predoctoral Achievement Award, the winner of 2009 TI design contest and finished 2nd place in the 2008 ADI analog-circuit design contest.

Muhammad Awais Bin Altaf- Muhammad Awais Bin Altaf (S'11) received the B.S. degree in electrical engineering from the University of Engineering and Technology (UET), Lahore, Pakistan, in 2008, and the M.Sc. degree in microsystems engineering, Masdar Institute of Science and Technology (MIST), Abu Dhabi, UAE, in 2012. Since 2013 he is pursuing the Ph.D. degree in interdisciplinary Engineering at MIST. From November 2012 to June 2013, he was a digital design engineer intern at Design Solutions, Global Foundries in Dresden, Germany where he was working on the implementation of digital test chips in support of 20 and 14nm technology. He was PhD-exchange student during fall 2015 in Massachusetts Institute of Technology (MIT), Cambridge, MA, USA. He developed an energy efficient machine-learning based feature extraction and classification processor for epileptic seizure detection. He is currently working on energy-efficient, patient-specific wearable seizure detection sensor with transcranial stimulation. His research interest involves low power bio-medical sensors and processors with special focus on mixed signal circuits.

Maousd Babaie- Masoud Babaie received his B.Sc. degree (with highest honors) in both Communication Systems and Electronics Engineering from Amirkabir University of Technology (Tehran Polytechnic), Tehran, Iran, in 2004, and the M.Sc. degree from Sharif University of Technology, Tehran, Iran, in 2006, in Microelectronics. Since November 2011, he has been pursuing his Ph.D. degree in Electrical Engineering at Delft University of Technology, Delft, The Netherlands. He joined Kavoshcom R&D Group, Tehran, Iran, in 2006, where he was involved in designing communication systems. He was appointed a CTO of the company between 2009 and 2011. He was consulting for RF group of TSMC, Hsinchu, Taiwan, during 2013–2015, designing 28-nm All-Digital PLL and Bluetooth Low Energy transceiver chips. During 2014–2015, Mr. Babaie was a visiting scholar researcher with Berkeley Wireless Research Center (BWRC), where he worked on CMOS switched-mode power amplifiers within the group of Prof. A. M. Niknejad. Since 2015, he has been an instructor of Linear Circuits and a lecturer of Digital-RF courses at TU Delft, The Netherlands. His research interest includes analog and RF/mm-wave integrated circuits and systems for wireless communications. This PhD research has so far resulted in over 10 IEEE journal and conference papers.

Ahmed Elkholly- Ahmed Elkholly (S'08) received his B.Sc. degree with honors and M.Sc. degree in electrical engineering from Ain Shams University, Cairo, Egypt, in 2008 and 2012, respectively. From 2008 to 2012, he has been an analog/mixed-signal design engineer at Si-Ware Systems, Cairo, Egypt, designing high performance clocking circuits and LC-based reference oscillators. Currently, he is a research assistant at University of Illinois, Urbana-Champaign, IL where he is pursuing the PhD degree. He was with Xilinx, San Jose, CA during summer of 2014, working on high-performance flexible clocking architectures. His research interests include frequency synthesizers, high speed serial links, and low power data converters. Mr. Elkholly has (co)authored over 20 publications, a book chapter, and one U.S. patent application. He received the IEEE Solid-State Circuits Society (SSCS) Student Travel Grant Award and Intel/IBM/Catalyst Foundation CICC Student Registration Award in 2015. He also received Edward N. Rickert Engineering Fellowship from Oregon State University (2012–2013), and best MSc degree award from Ain Shams University in 2012. He serves as a reviewer for the IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I, and IEEE International Symposium on Circuits and Systems (ISCAS).

Sameed Hameed- Sameed Hameed is a graduate student at the University of California, Los Angeles (UCLA), where he is currently working towards his Ph.D. degree in Electrical Engineering (EE). He received the Bachelor of Technology (B. Tech) degree from the Indian Institute of Technology Madras and the Master of Science (M.S.) degree from UCLA, both in EE, in 2011 and 2013, respectively. He held an internship position with Broadcom Corporation, Irvine, in 2013, where he was involved in the design of RF systems for WiFi transceivers. He was a recipient of a department fellowship from the EE department at UCLA during fall 2013 for coming first in the Ph.D. preliminary exam in the 'Circuits & Embedded Systems' area, a UCLA graduate division fellowship during the 2013–14 academic year, and the Broadcom UCLA fellowship during the 2015–16 academic year. His current research is in the area of RF transceiver design with an emphasis on signal processing techniques.

Hyeok-Ki Hong- Hyeok-Ki Hong received the B.S. double major in electrical engineering and IT business and M.S. degree from the Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea, in 2010 and 2012, respectively. He is a Ph.D. candidate since 2012 at KAIST. He is currently interested in mixed-signal IC design with emphasis on data converters, various types of A/D convertors for low power, high speed, digital calibrations techniques for ADC errors, digital logics for low power and high speed and embedded specific purpose circuit design. He had presented new architectures and designs of multi-bits per cycle SAR ADC with time interleaving techniques for low cost high speed ADC designs. He is currently working on a selective algorithm in massively time-interleaved ADC architectures to solve industry yield issues.

Injoon Hong- Injoon Hong (S'11) received the B.S. and M.S. degrees from the Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea in 2010 and 2012, respectively, where he is currently pursuing the Ph.D. degree in electrical engineering. During his doctoral program, from 2013 to present, he has been studied on energy-efficient hardware accelerator for Augmented Reality applications used in smart-glasses platform. In I-SSCC 2015, he presented novel CMOS image sensor which can estimate smart-glasses user's gaze point on the display for natural user interface. Since the sensor itself can accelerate vision algorithm on analog focal-plane domains, <10 mW power consumption can be achieved with real-time performances. In particular, he has successfully demonstrated chip's performance with actual smart-glasses system on live demonstration session in I-SSCC 2015, and becomes a leading recipient of the ISSCC 2015 Demonstration Session Certificate of Recognition. Injoon has published numerous papers in major SSCS conferences and journals for vision hardware research. As a leading author, he proposed 3D reconstruction vision accelerator, object recognition processor with low-power image sensor, and neural-network based cache controller in JSSC 2015, I-SSCC 2015, and Symposium on VLSI 2013, respectively. He has 19 co-authored international conference papers and 7 co-authored international journals.

Song Hu- Song Hu received the B.Eng. and M.Sc. degrees from Southeast University, China, and Fudan University, China, in 2009 and 2012, respectively. He is currently pursuing the Ph.D. degree in electrical engineering at Georgia Institute of Technology, USA. His master thesis was focused on the high-linearity RF front-end for cellular receivers. His current research interests include integrated biosensors, actuators, and digital-assisted reconfigurable RF integrated circuits and systems. Mr. Hu was the recipient of the Best Student Paper Award (1st Place) of the 2014 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium and the Best Student Paper Award of the 2011 IEEE Radio Frequency Integration Technology (RFIT) Symposium. He received the 2015–2016 IEEE Solid-State

Circuits Society Predoctoral Achievement Award, the 2015 IEEE Microwave Theory and Techniques Society Graduate Fellowship, and the 2014 Analog Devices Inc. Outstanding Student Designer Award. He was also the recipient of the Southeast University President Scholarship in 2006, the National Scholarship in 2007, the First Prize in the National Undergraduate Electronic Design Contest in 2008, the Microsoft Young Fellowship Award from Microsoft Research Asia in 2008, the Marvell Scholarship in 2011, the Shanghai Outstanding Graduate Student Award in 2012, and the Shanghai Excellent Master Thesis Award in 2013.

Youchang Kim- Youchang Kim (S'12) received the B.S. and M.S. degrees in electrical engineering from Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea, in 2012 and 2014, respectively. He is currently working toward the Ph.D. degree in electrical engineering from KAIST. His research interests include energy-efficient multi-core systems especially focused on low-power network-on-chip, near-threshold circuit, machine learning, and artificial intelligence. Mr. Kim received the Grand Prix from the Ministry of Knowledge Economy at Wearable Computer Contest in 2010, the IEEE International Symposium on Circuits and Systems (ISCAS) Student Travel Grant in 2013, and the IEEE Solid-State Circuits Society (SSCS) Predoctoral Achievement Award 2015-2016.

Xiaosen Liu- Xiaosen Liu is a PhD candidate at the Analog & Mixed Signal Center of the Department of Electrical and Computer Engineering, Texas A&M University, college station, Texas. He received the B.S. degree with honors from Southeast University, Nanjing, China, in 2008, and the M.Phil degree from Hong Kong University of Science & Technology in 2011. His research is focused on mixed signal CMOS ICs, power management ICs, and wide-bandgap power electronics. Working with Prof. Kevin J. Chen at HKUST, he designed and fabricated the world's first single-polarity power supply GaN OTA, which could operate up to 275°C. In 2012-2014, he was supervised by Prof. Edgar Sánchez-Sinencio and collaborated with Medtronic to develop the next generation piezoelectric surgical scalpels, and a hybrid signal sensing scheme for both RF and piezoelectric transducers. Currently, he is researching with Intel about energy harvesting for IoT, and the power management unit in ultra-low power (ULP) transceiver. He was the recipient of the IEEE SSCS 2015-2016 Predoctoral Achievement Award, and TAMU ECE Dept. 2015 second Best PhD Student Award. He has authored/coauthored over 20 academic papers including *IEEE JSSC, ISSCC, TIE, IEDM, EDL, TED*. He serves as a reviewer for *IEEE TIE, EDL, TCAS-II, APEC, ISCAS*.

Xinlin Liu- Xinlin Liu (S'13) received the B.S. degree in electrical engineering from the Harbin Institute of Technology, Harbin, China, in 2011, and M.S. degree in electrical engineering from the University of Pennsylvania, PA, USA, in 2013. He is currently working toward the Ph.D. degree at the University of Pennsylvania. His research interests include analog and mixed-signal integrated circuits design for low-power data converters, and CMOS sensors. In his doctoral research, he has been working on circuits and system design for medical application, with focus on bi-directional brain-computer interface (BCI) system-on-chip (SoC). He is the first author for the papers received the Best Paper Award (1st place) of the 2015 Biomedical Circuits and Systems Conference (BioCAS), and the Best Paper Award of the BioCAS Track of the 2014 IEEE International Symposium on Circuits and Systems (ISCAS). He is also the recipient of the Student-Research Preview Award (Honorable Mention) of the 2014 IEEE International Solid-State Circuits Conference (ISSCC).

Hans Meyvaert- Hans Meyvaert (S'09) was born in Sint-Truiden, Belgium, in 1985. He received the M.Sc. degree in Electrical Engineering from the University of Leuven (KU Leuven), Belgium, in 2009. Afterwards, he joined the ESAT-MICAS research laboratory of the same university, where he is currently working toward the Ph.D. degree in Electrical Engineering. The topic of his research is the pursuit and realization of highly miniaturized transformerless power converters to extract a low power (μ W and mW-level) directly from the mains voltage and convert this into a low voltage output, compatible with supply voltage specifications of modern integrated circuits. Such a power converter is targeted to be an auxiliary supply to efficiently power the standby functionality of mains connected devices, allowing the main power supply to be shut down in order to reduce standby power by more than an order of magnitude. His broader research interests are power management circuits in general, but with focus on monolithic integration. The underlying motivation is to improve energy efficiency in general. Mr. Meyvaert was awarded 3rd place in the 2014 Broadcom Foundation University Research Competition and received a Solid-State Circuits Society travel grant to attend the 2015 International Solid-State Circuits Conference.

Saleh Heidary Shalmany- Saleh Heidary Shalmany (S'12) received the B.Sc. degree (with excellence) in 2008 from Tehran University, Tehran, Iran, and M.Sc. degree (with distinction) in 2010 from Delft University of Technology (TUDelft), Delft, The Netherlands, both in electrical engineering. From 2010 to 2011, he was a researcher with TUDelft and NXP Semiconductors, working on the development of a micropower smart pH sensor intended for use in RFID tags. Since October 2011, he has been pursuing his Ph.D. at the Electronic Instrumentation Laboratory of TUDelft and in collaboration with Infineon Technologies. His thesis research focuses on integrated precision current sensing system for Coulomb counting applications. His research interests include precision analog and mixed-signal circuits, low power sensor interface, and data converters. From 2008 to 2010, he was the recipient of the Huygens scholarship provided by the Dutch Minister of Education, Culture, and Science. He also received the IEEE Solid-State Circuits Society Predoctoral Achievement Award 2015-2016.

Teerachot Siriburanon- Teerachot Siriburanon (S'10) received the B.E. degree in Telecommunications Engineering with honors from Sirindhorn International Institute of Technology (SIIT), Thammasat University, Thailand, and the M.E. degree in Physical Electronics from Tokyo Institute of Technology, Tokyo, Japan, in 2010 and 2012, respectively. He is finishing the Ph.D. degree in physical electronics at the Tokyo Institute of Technology. Currently, he is a research assistant at University College Dublin, Ireland. His research interests are clock/frequency generations for wireless and wireline communications, and RF transceiver systems. He has authored/coauthored over 20 peer-reviewed papers in international journals and conferences, including ISSCC, JSSC, ESSCIRC, and RFIC. He serves as a technical reviewer for the IEEE Transactions on Circuits and Systems I (TCAS-I), IEEE Transactions on Microwave Theory and Techniques (T-MTT), and IEEE International Symposium on Circuits and Systems (ISCAS). Mr. Siriburanon has been the recipient of the Japanese Government (MEXT) Scholarship from 2010-2015, the Young Researcher Best Presentation Award at Thailand-Japan Microwave in 2013, the ASP-DAC Best Design Award in 2014-2015, and the IEEE SSCS Student Travel Grant Award (STGA) in 2014. Finally, he received the IEEE SSCS Predoctoral Achievement Award in 2016.

Amrith Sukumaran- I obtained my Bachelor of Engineering in Electronics and Communications Engineering from Madras Institute of Technology, Chennai, India, in 2008. My bachelor's thesis was on the design of a 10 bit, 100 MSPS, pipelined analog to digital converter in 180nm CMOS. I obtained my masters (MS) degree from the Indian Institute of Technology-Madras, Chennai, India, in 2011. My master's thesis was on the design of a 1.2V, 285 micro Ampere, analog front end chip for a digital hearing aid in 130nm CMOS.

Since 2012, I am pursuing PhD at the Indian Institute of Technology- Madras. My research focuses on the design of Continuous Time Delta Sigma ADCs having excellent clock jitter immunity and loopfilter linearity. Towards achieving this goal, I designed a 280 micro Watt audio FIR-CTDSM with 103dB dynamic range and 102dB A-weighted SNR. It achieved a competitive FOM of 182.3 thereby proving the efficacy of the proposed techniques. Further, a CTDSM with 91dB dynamic range in a 2MHz signal bandwidth that used Dual Switched Capacitor Return to Zero DAC was designed that showed superior jitter immunity together with excellent loopfilter linearity. My current research interests are in designing high speed CTDSMs that have excellent immunity to both clock and signal dependent jitter.

Tong Zhang- Tong Zhang received degrees in electrical engineering from Southeast University, China (BS), and University of Washington, Seattle (MS) in 2011 and 2014, respectively. He is currently a Ph.D. candidate in Electrical Engineering Department at University of Washington. His research interests include developing transmitter self-interference mitigation (SIM) techniques for FDD/in-band full-duplex radios and providing high-speed point-to-point communication at millimeter wave (mmWave) frequencies using state-of-the-art technologies. A combination of SIM and mmWave techniques would enhance the spectrum efficiency of the existing crowded RF spectrum and eventually provide high-speed data service for next generation 5G standards. In the past, he has held internship positions at Qualcomm Inc., San Jose, CA (July 2013 - April 2014) and Google Inc., Mountain View, CA (April 2015 – July 2015), where he worked on various projects including mm-wave front-end circuits, and baseband analog circuits for different applications. He was a recipient of Analog Device Outstanding Student Designer Award (2014-2015) and SSCS Predoctoral Achievement Award (2015-1016). He has authored/co-authored more than 10 conference/journal paper and serves as a reviewer for IEEE Journal of Solid State Circuits (JSSC), IEEE Transactions on Microwave Theory and Technology (TMTT) and IEEE Transactions on Circuits and System 1: Regular Papers (TCAS-1).

Wouter Volckaerts- Wouter Volckaerts was born in Antwerp, Belgium, in 1986. In 2009 he received the degree of M.S. in Electrical Engineering from the KU Leuven, Belgium. The subject of his MS thesis was on the design of a PLL based sensor interface for ultra-low-power Wireless Sensor Networks. He joined the MICAS laboratories of the KU Leuven in 2009 as a research assistant. His research interests are millimeter wave CMOS circuits, and high speed wireless and wireline communication. He was an active member of the IEEE student branch Leuven and he was treasurer for two years. In 2011, he received the TSMC-Europpractice innovation award.

Jianxun Zhu- Jianxun Zhu received the B. Eng. in communications engineering from Beijing University of Posts and Telecommunications, Beijing, China, in 2010, and the M.S. in electrical engineering from Columbia University, New York, USA in 2012. He is currently pursuing his Ph.D. degree in electrical engineering at Columbia University advised by Dr. Peter R. Kinget. He has been an IEEE student member and Solid-State Circuits Society (SSCS) student member since 2012. His research interests include integrated circuits and communication systems for next-generation high-performance wireless communication systems. His research highlights are low power UWB radios and networks, reconfigurable radio architectures and building blocks, enhancement of RF and microwave circuits with analog and digital signal processing techniques, receiver linearity improvement with interferer mitigation, antenna interface and receiver impairment calibration, ultra-low-noise receivers and antenna interface for massive carrier aggregation in next-generation communication systems. He is a co-recipient of the “Best Student Demo Award” at the 2011 ACM Conference on Embedded Networked Sensor Systems (ACM SenSys). He received first prize at China National Undergraduate Electronic Design Contest (NUEDC) in 2009.

Jin Zhou- Jin Zhou received the B.S. degree in Electronics Science and Technology from Wuhan University, Wuhan, China, in 2008, the M.S. degree in Microelectronics from Fudan University, Shanghai, China, in 2011. He is currently working towards the Ph.D. degree at Columbia University, New York, NY. From 2008 to 2011, he was a graduate research assistant at Fudan University and worked on frequency synthesizers for multi-standard wireless transceivers. From 2011 to 2012, he was with MediaTek Singapore and worked on PLL/DLL for cellular and wireless applications. His research interests include the theory, design, and experimental varication of analog/RF/millimeter-wave integrated circuits and systems for emerging and high-impact applications. The focus of his doctoral research has been integrated radios for full-duplex wireless. Mr. Zhou is the recipient of the 2010 IEEE International Conference on Solid-State and Integrated Circuit Technology (ICSICT) Excellent Student Paper Award, the 2015-2016 Qualcomm Innovation Fellowship, and the 2015-2016 IEEE Solid-State Circuits Society (SSCS) Predoctoral Achievement Award.

Past Predoctoral Achievement Award Recipients

YEAR	RECIPIENT	AFFILIATION
2014-2015	Tejasvi Anand	University of Illinois-Urbana Champaign
	Chi-Hang Chan	University of Macau
	Run Chen	University of Southern California
	Lin Cheng	Hong Kong University of Science and Technology
	Kunal Datta	University of Southern California
	Brecht Francois	University of Leuven (KU Leuven)
	Sewook Hwang	Korea University
	Zhicheng Lin	University of macau
	Pramod Murali	University of California- Berkeley
	Natalie Reynders	University of Leuven (KU Leuven)

	Hao Wu Jiawei Xu	University of California- Los Angeles TU Delft
2013-2014	Benrooz Abiri	California Institute of Technology
	Jun-Chau Chien	National Taiwan University
	Ming-Shuan Chen	National Taiwan University
	Hyn-Sik Kim	KAIST
	Yan Lu	Hong Kong University of Science and Technology
	Yue Lu	UC Berkeley
	Zushu Yan	University of Macau
	Dixian Zhao	KU Leuven
2012-2013	Jiashu Chen	UC Berkeley
	Wei Deng	Tokyo Institute of Technology
	Ruonan Han	Cornell University
	Lingkai Kong	UC Berkeley
	Hanh-Phuc Le	UC Berkeley
	I-Ting Lee	National Taiwan University
	Ahmed Musa	Tokyo Institute of Technology
	Kamran Souri	Delft University School of Technology
2011-2012	Kris Myny	IMEC, Belgium
	Meisam Nazari	California Institute of Technology
	Kaushik Sengupta	California Institute of Technology
	Yahya Tousi	Cornell University

Predoctoral Fellowship Award Recipients

YEAR	RECIPIENT	AFFILIATION
2010-2011	Wooram Lee	Cornell University
2009-2010	Chengjie Zuo	University of Pennsylvania
2008-2009	Ankush Goel Shih-An Yu	USC Columbia University
2007-2008	Jintae Kim	UCLA
	Sudip Shekhar	University of Washington
2006-2007	Peter Popplewell Chinmaya Mishra	Carleton University Texas A&M University
2002-2006	none awarded	
2001-2002	Emad Hegazi John Wm. Rogers	UCLA Carleton University
2000-2001	Liang Dia Jafar Savoj	University of Minnesota UCLA
1999-2000	Mark Spaeth Shengwei Da	MIT UC-Davis
1998-1999	Jouko Vankka	Helsinki University of Technology
1997-1998	Farbod Behbahani	University of California, Los Angeles
1996-1997	Ozan E. Erdogan	University of California, Davis
1995-1996	Jan Crols	Katholieke Universiteit Leuven
1994-1995	Kenneth J. Schultz	University of Toronto
1993-1994	Andrew E. Stevens	Columbia University
1992-1993	Michael Flynn	Carnegie Mellon University
1991-1992	Fang Lu	University of California, Los Angeles
1990-1991	none awarded	
1989-1990	none awarded	
1988-1989	Terri Fiez	Oregon State University
1987-1988	Bernhard Boser	Stanford University

1986-1987 Lloyd Massengill
1985-1986 R. Wayne Johnson
1984-1985 Perry Robertson
1983-1984 Emil Girczyc

North Carolina State University
Auburn University
Clemson University
Carleton University