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Jikong BMS RS485 Modbus general protocol (V1.1)

Chengdu Jikong Technology Co., Ltd. 2024.02 Jikong BMS RS485 Modbus Universal Protocol V1.1

Version History			
Date version number		describe	author
2023.02	V1.0	Write communication protocol;	Zhang Peng
2024.01	V1.1	Fixed some errors and added some register support	Zhang Peng

Jikong BMS RS485 Modbus general protocol (V1.1)

The Jikong BMS RS485 Modbus general protocol uses a master-slave response method for data communication. The host can only initiate a request through a unique slave address, and the BMS (slave) responds according to the host request, that is, half-duplex communication. This protocol only allows the host to initiate a request and the slave to respond passively, so the slave will not actively occupy the communication line to cause data conflicts.

1. Physical interface

communication The electrical characteristics of the physical

interface are as	UART
follows:	RS485
Communication	115200bps
interface level	
standard Baud	8 1
rate Data bit Stop bit Parity bit	none

2. Protocol Format

Information transmission is asynchronous, using hexadecimal for communication, information frame format:

	Address code function	code data area	CRC check 1
Ī	byte 1 byte 1 byte 2	2 bytes	

1) Address

code The address code is the first byte of each communication information frame and supports 1 to 247. The address of each slave on the bus must be unique. Only the slave that matches the address code sent by the host can respond and return data.

2) Function

Code The function code is the second byte of each communication information frame. The host sends and informs the slave device through the function code.

What operation is to be performed. The function code is

defined as	follows: Function	
Read	Definition	Operation reads data from one or more registers
10H Reg	ister 03H Write Register D	ata written to one or more registers

3) Data area

The data area varies with the function code and data direction. These data can be different combinations of "register first address + read register number", "register address + operation data", "register first address + operation register number + data length + data", etc. The data area of different function codes is explained in detail in "Function Code Analysis".

3) CRC check

CRC check is used to ensure the correctness and integrity of data transmission.

3. Error feedback

Address and CRC check errors will not receive data feedback from the slave, and other errors will return error codes to the host. The second bit of the data frame plus 0x80 indicates that an error has occurred in the request (illegal function code, illegal data value, etc.). The error data frame is as follows:

address code function code		Error code area CRC check
The 1-byte	1 byte	1 byte 2 bytes

error code is defined as follows:

value	Function	illustrate		
01H	code with illegal name	This function code does not support register operation		
02H	Register address error	A register that is prohibited from being accessed by the slave is accessed		
03H	Illegal data	The data logic is illegal or exceeds the limit		
04H	CRC check error	CRC check error		

4. Information Transmission Process

When the communication command is sent from the host to the slave, the slave that matches the address code sent by the host receives the communication command.

If the CRC check is correct, the corresponding operation is executed, and then the execution result (data) is returned to the host. Return information

The address code, function code, executed data and CRC check code are included.

If an error occurs, no information will be returned.

5. Function code analysis

1) Function code 03H: Read register

For example: the host wants to read the data of two holding registers whose slave address is 01H and the starting register address is 05H.

According to the host, it sends:

Host sends		Data (HEX)
address code		01H
functio	n code	03H
Ctouting register address	High Byte	00H
Starting register address	Low Byte	05H
Number of registers	High Byte	00H
Number of registers	Low Byte	02H
CRC Check	Low Byte	D4H
ONG Offection	High Byte	ОАН

If the slave holds the data of registers 05H and 06H as 1122H and 3344H, the slave returns:

Slave r	eturn data (HEX)	
addres	ss code	01H
function	n code	03H
Number	of bytes	04H
Dogistor OF data	High byte	11am
Register 05 data	low byte	22H
Pogistor 06 data	high byte	33H
Register 06 data	low byte	44H
CRC Check	low byte	4BH
	high byte	C6H

2) Function code 10H: write register

For example: the host wants to save the data 0005H, 2233H to the slave address 01H, the starting register address is In the 2 registers of 0020H, the host sends:

The host s	sends	Data (HEX)	
the address		01H	
code fu	nction code	10H	
	High byte	00Н	
Starting register address	low byte	20H	
	high byte	00H	
Number of registers	low byte	02H	
number o	f bytes	04H	
0000H	written high byte	00H	
register to be written	Low byte	05H	
0001H	High byte	22H	
register to be written	Low Byte	33H	
000 1 1	Low Byte	В9Н	
CRC check high byt	e function code	03H	

10H operation, slave returns:

Slave returns		Data (HEX)	
address code		01H	
functio	n code	10H	
	High Byte	00H	
Starting register address	Low Byte	20H	
	High Byte	00H	
Number of registers	Low Byte	02H	
0.000	Low Byte	40H	
CRC Check	High Byte	02H	

						Register Map			
Starting address Address Field	code offset In	dex	type of data	length Length	R/W	Content	Unit	Note	
		0 UII	NT32		Enter	leep voltage VolSmartSleep 0x0000	m∨		
	0x0004	4 UII	NT32			dervoltage protection VolCellUV	mV		
	0x0008	8 UI	NT32			dervoltage protection recovery VolCellUVPR	mV		
	0x000C 12 U	NT32				ercharge protection VolCellOV	mV		
	0x0010	16 U	NT32	4 RW	Cell ov	ercharge protection recovery voltage VolCellOVPR	mV		
	0x0014	20 UI	NT32			palanced voltage difference VolBalanTrig	mV		
	0	24 UI	NT32			00% voltage VolSOC100% 0x0018	mV		
	28 UINT32			4 RW	SOC-0	% voltage VolSOC0% 0x001C	mV		
	0x0020	32 UI	NT32	4 RW	Recom	mended charging voltage VolCellRCV	mV		
	0x0024	36 UI	NT32	4 RW	Float	harge voltage VolCellRFV	mV		
	0x0028	40 UI	NT32	4 RW	Autom	atic shutdown voltage VolSysPwrOff	mV		
	0x002C 44 U	NT32				uous charging current CurBatCOC	mA		
	0x0030	48 UI	NT32	4 RW	Charge	overcurrent protection delay TIMBatCOCPDly	S		
	0x0034	52 UI	NT32			overcurrent protection release TIMBatCOCPRDIV	s		
	0x0038	56 UI	NT32			uous discharge current CurBatDcOC	mA		
	0x003C 60 U	NT32		4 RW	Discha	rge overcurrent protection delay TIMBatDcOCPDly	s		
	0x0040	64 UI	NT32			rge overcurrent protection release TIMBatDcOCPRDly	s		
	0x0044	68 UI	NT32			ircuit protection release TIMBatSCPRDly	s		
	0x0048	72 UI	NT32			um balancing current CurBalanMax	mA		
	TMPBatCOT	0x004C	76 INT32	4 RW	Chargi	ng over-temperature protection	0.1ÿ		
	0x0050	80 IN	T32	4 RW	Charge	over temperature recovery TMPBatCOTPR	0.1ÿ		
	TMPBatDcOT (0x080\$4N	T32	4 RW	Dischard	e over temperature protection	0.1ÿ		
	0x0058	88 IN	T32	4 RW	Dischar	ge over temperature recovery TMPBatDcOTPR	0.1ÿ		
	TMPBatCUT	0x005C	92 INT32	4 RW	Chargi	ng low temperature protection	0.1ÿ		
	0x0060	96 IN	T32	4 RW	Charge	low temperature recovery TMPBatCUTPR	0.1ÿ		
	TMPMosOT)x0064	100 INT32	4 RW	MOS d	ver temperature protection	0.1ÿ		
	0x0068	104 IN	VT32	4 RW	MOS ov	er temperature protection recovery TMPMosOTPR	0.1ÿ		
				4 RW	CellCo	unt 0x006C 108 UINT32	string		
	0x0070 112 l	JINT32		4 RW	Chargi	ng switch BatChargeEN		1: On; 0: Off	
	0x0074 116 l					rge switch BatDisChargeFN		1: On: 0: Off	
			INT32			N 0x0078		1: On: 0: Off	

0x1000

0x007C 124 UINT32	4 RW Battery design capacity (CapBatCell	
0x0080 128 UINT32	4 RW Short dircuit protection delay	•	
0x0084 132 UINT32	4 RW Balanced start voltage	, , ,	
0x0088 136 JINT32	4 RW Connection line internal resistance	ce 0CellConWireRes0 uÿ	
0x008C 140 UINT32	4 RW Connect on line internal resistance	ce 1CellConWireRes1 uÿ	
0x0090 144 UINT32	4 RW Connect on line internal resistance	ce 2CellConWireRes2 uÿ	
0x0094 148 UINT32	4 RW Connect on line internal resistance	ce 3CellConWireRes3 uÿ	
0x0098 152 JINT32	4 RW Connect on line internal resistance	ce 4CellConWireRes4 Uÿ	
0x009C 1\$6 UINT32	4 RW Connect on line internal resistance	ce 5CellConWireRes5 uÿ	
0x00A0 160 UINT32	4 RW Connect on line internal resistance	ce 6CellConWireRes6 uÿ	
0x00A4 164 UINT32	4 RW Connect on line internal resistance	ce 7CellConWireRes7 uÿ	
0x00A8 168 UINT32	4 RW Connect on line internal resistance	ce 8CellConWireRes8 uÿ	
0x00AC 172 UINT32	4 RW Connect on line internal resistance	ce 9CellConWireRes9 uÿ	
0x00B0 176 UINT32	4 RW Connection line internal resistan	ce 10CellConWireRes10 uÿ	
0x00B4 180 UINT32	4 RW Connection line internal resistan	ce 11CellConWireRes11 uÿ	
0x00B8 184 UINT32	4 RW Connection line internal resistan	ce 12CellConWireRes12 uÿ	
0x00BC 188 UINT32	4 RW Connection line internal resistand	ce 13CellConWireRes13 uÿ	
0x00C0 192 UINT32	4 RW Connection line internal resistan	ce 14CellConWireRes14 uÿ	
0x00C4 196 UINT32	4 RW Connection line internal resistand	ce 15CellConWireRes15 uÿ	
0x00C8 200 UINT32	4 RW Connection line internal resistan	ce 16CellConWireRes16 uÿ	
0x00CC 204 UINT32	4 RW Connection line internal resistant	ce 17CellConWireRes17 uÿ	
0x00D0 2\(\psi\)8 UINT32	4 RW Connection line internal resistand	ce 18CellConWireRes18 uÿ	
0x00D4 212 UINT32	4 RW Connection line internal resistan	ce 19CellConWireRes19 uÿ	
0x00D8 216 UINT32	4 RW Connection line internal resistan	ce 20CellConWireRes20 uÿ	
0x00DC 220 UINT32	4 RW Connection line internal resistan	ce 21CellConWireRes21 uÿ	
0x00E0 224 UINT32	4 RW Connection line internal resistan	ce 22CellConWireRes22 uÿ	
0x00E4 228 UINT32	4 RW Connection line internal resistance	ce 23CellConWireRes23 uÿ	
0x00E8 232 UINT32	4 RW Connection line internal resistant	ce 24CellConWireRes24 Uÿ	
0x00EC 236 UINT32	4 RW Connection line internal resistant	ce 25CellConWireRes25 uÿ	
0x00F0 240 UINT32	4 RW Connection line internal resistan	ce 26CellConWireRes26 uÿ	
0x00F4 244 UINT32	4 RW Connection line internal resistant		
0x00F8 248 UINT32	4 RW Connection line internal resistant	ce 28CellConWireRes28 uÿ	
0x00FC 252 UINT32	4 RW Connection line internal resistant	ce 29CellConWireRes29 uÿ	
0x0100 256 UINT32	4 RW Connection line internal resistant	ce 30CellConWireRes30 uÿ	
0x0104 200 UINT32	4 RW Connection line internal resistan	ce 31CellConWireRes31 uÿ	

0x0108 264 UINT32	4 R\	V Device address DevAddr	Н		
0x010C 268 UINT32	4 RV	Discharge precharge time TIMProdischarge	S		
		RW Heating switch HeatEN		1: On: 0: Off1: On:	BIT0
		RW Temperature sensor shield Disable temp-sensor		0: Off1: On: 0: Off	BIT1
		RW GPS Heartbeat		·	BIT2
		RW Multiplexing port function Port Switch		1: RS485: 0: CAN 1:	BIT3
0x0114 276 UINT16	2	RW LCD Always On		open: 0: close 1:	BIT4
0.0114 2/10 01141110	2	RW Special Charger		open: 0: close 1:	BIT5
		RW SmartSleep		open: 0: close 1:	BIT6
		RW Disable parallel current limiting DisablePCLModule		open: 0: close 1:	BIT7
		RW Data timing storage TimedStoredData		open: 0: close 1:	BIT8
		RW Charging Float Mode ChargingFloatMode		open: 0: close 1: open: 0: close	BIT9
0x0118 280 UINT8	2	RW Intelligent sleep time TIMSmartSleep	Н	, ,	
UINT8	2	R Data field enable control 0			
0x0000 0 UNT16	2 R	CellVol0	mV		
0x0002 2 UNT16	2 R	CellVol1	mV		
0x0004 4 UNT16	2 R	CellVol2	mV		
0x0006 6 UNT16	2 R	CellVol3	mV		
0x0008 8 UINT16	2 R	CellVol4	mV		
0x000A 10 UINT 16	2 R	CellVol5	mV		
0x000C 12 UINT 16	2 R	CellVol6	mV		
0x000E 14 ΨINT16	2 R	CellVol7	mV		
0x0010 16 ΨINT16	2 R	CellVol8	mV		
0x0012 18 ΨINT16	2 R	CellVol9	mV		
0x0014 20 U INT16	2 R	CellVol10	mV		
0x0016 22 UINT16	2 R	Cell voltage 11CellVol11	mV		
0x0018 24 UINT16		CellVol12	mV		
0x001A 26 UINT16	2 R	Cell voltage 13CellVol13	mV		
0x001C 28 UINT16		Cell voltage 14CellVol14	mV		1.
0x001E 30 UINT16		CellVdi15	mV		
0x0020 32 UINT16	7	Cell voltage 16CellVol16	mV		
0x0022 34 UINT16		Cell voltage 17CellVol17	mV		
0x0024 36 UINT16		Cell voltage 18CellVol18	mV		
0x0026 38 UINT16		Cell voltage 19CellVol19	mV		
0x0028 40 UINT16		CellVdi20	mV		1

0x002A 42 JINT16 0x002C	2 R Cell voltage 21CellVol21	mV
44 UINT16 0x002E 46	2 R Cell voltage 22CellVol22	mV
UINT16 0x0030	2 R Cell voltage 23CellVol23	mV
48 UNT16	2 R Cell voltage 24CellVol24	mV
0x0032 50 UNT16	2 R Cell voltage 25CellVol25	mV
0x0034 52 UNT16	2 R CellVol26	mV
0x0036 54 UNT16	2 R ¢ellVol27	mV
0x0038 56 UNT16	2 R Cell voltage 28CellVol28	mV
0x003A 58 JINT16 0x003C	2 R ¢ellVol29	mV
60 UINT16 0x003E 62	2 R CellVol30	mV
UINT16 0x0040	2 R Cell voltage 31CellVol31	mV
64 UNT32	4 R Battery status CellSta	BIT[n] is 1, indicating that the battery is present.
0x0044 68 UNT16	2 R QellVolAve average voltage of a single cell	mV
0x0046 70 UNT16	2 R Maximum pressure difference CellVdifMax	mV
0x0048 72 UINT8	R Maximum voltage cell number MaxVolCellNbr	
UINT8	R Minimum voltage cell number MinVolCellNbr	
0x004A 74 UINT16 0x004C	2 R Balance line resistance 0CellWireRes0	mÿ
76 UINT16 0x004E 78	2 R Balance line resistance 1CellWireRes1	mÿ
UINT16 0x0050	2 R Balance line resistance 2CellWireRes2	mÿ
80 UINT16	2 R Balance line resistance 3CellWireRes3	mÿ
0x0052 82 UNT16	2 R Balance line resistance 4CellWireRes4	mÿ
0x0054 84 UNT16	2 R Balance line resistance 5CellWireRes5	mÿ
0x0056 86 UNT16	2 R Balance line resistance 6CellWireRes6	mÿ
0x0058 88 UNT16	2 R Balance line resistance 7CellWireRes7	mÿ
0x005A 90 UINT16 0x005C	2 R Balance line resistance 8CellWireRes8	mÿ
92 UINT16 0x005E 94	2 R Balance line resistance 9CellWireRes9	mÿ
UINT16 0x0060	2 R Balance line resistance 10CellWireRes10	mÿ
96 UNT16	2 R Balance line resistance 11CellWireRes11	mÿ
0x0062 98 UNT16	2 R Balance line resistance 12CellWireRes12	mÿ
0x0064 100 UINT16 0x0066	2 R Balance line resistance 13CellWireRes13	mÿ
102 ΨINT16	2 R Balance line resistance 14CellWireRes14	mÿ
0x0068 104 ψINT16	2 R Balance line resistance 15CellWireRes15	mÿ
0x006A 10d UINT16 0x006C	2 R Balance line resistance 16CellWireRes16	mÿ
108 UINT16 0x006E 110	2 R Balance line resistance 17CellWireRes17	mÿ
UINT16	2 R Balance line resistance 18CellWireRes18	mÿ

x0070 112 L			2 R I	alance	line resistance 19CellWireRes19	mÿ		
	114 U	INT16	2 R E	alance	line resistance 20CellWireRes20	mÿ		
)x0074 116 L	INT16	0x0076	2 R E	alance	line resistance 21CellWireRes21	mÿ		
	118 U	INT16	2 R E	alance	line resistance 22CellWireRes22	mÿ		
0x0078	120 U	INT16	2 R E	alance	line resistance 23CellWireRes23	mÿ		
0x007A 122 L	JINT16	0x007C	2 R E	alance	line resistance 24CellWireRes24	mÿ		
124 UINT16	x007目	126	2 R E	alance	line resistance 25CellWireRes25	mÿ		
UINT16 0x00	30 128	UINT16	2 R E	alance	line resistance 26CellWireRes26	mÿ		
0x0082			2 R E	alance	line resistance 27CellWireRes27	mÿ		
	130 U	INT16	2 R E	alance	line resistance 28CellWireRes28	mÿ		
0x0084 132	INT16	0x0086	2 R E	alance	line resistance 29CellWireRes29	mÿ		
	134 U	INT16	2 R E	alance	line resistance 30CellWireRes30	mÿ		
0x0088	136 U	INT16	2 R E	alance	line resistance 31CellWireRes31	mÿ		
0x008A 138	VT16 (x008C	2 R F	ower b	pard temperature TempMos	0.1ÿ		
140 UINT32 0	x0090	144			line resistance status CellWireResSta		BIT[n] is 1, indicating that the balance line alarm	
UINT32 0x00	94 148	UINT32	4 R 7	Total bat	tery voltage BatVol	mV	2[] to 1, marcaling that the balance into diam.	
0x0098					ower BatWatt	mW		
	152 II	NT32			urrent BatCurrent	mA		51 57 61 63
0x009C 156	NT16 (x009E		+ <u> </u>	emperature TempBat 1	0.1ÿ		
158 INT16					perature TempBat 2 Balancing	0.1ÿ		
					line resistance is too large AlarmWireRes		1: Fault; 0: Normal1:	BIT0
				8	MOS overtemperature protectionAlarmMosOTP		Fault; 0: Normal1: Fault;	BIT1
					The number of cells does not match the set valueAlarmCellQuantity		0: Normal1: Fault; 0:	BIT2
				2	Current sensor abnormalityAlarmCurSensorErr Cell		Normal1: Fault; 0:	BIT3
				9	overvoltage protectionAlarmCellOVP Battery		Normal1: Fault; 0:	BIT4
					overvoltage protectionAlarmBatOVP Charging		Normal1: Fault; 0:	BIT5
				1	overcurrent protectionAlarmChOCP Charging		Normal1: Fault; 0:	BIT6
					short circuit protectionAlarmChSCP	3 10	Normal1: Fault: 0:	BIT7
				1	Charging overtemperature		Normal1: Fault; 0:	BIT8
					protectionAlarmChOTP Charging low		Normal1: Fault; 0:	BIT9
					temperature protectionAlarmChUTP Internal communication		Normal1: Fault; 0:	BIT10
		LUNITOO			abnormalityAlarmCPUAuxCommuErr Cell		Normal1: Fault; 0:	BIT11
0x00A0 160		UINT32 4		R	undervoltage protectionAlarmCellUVP Battery		Normal1: Fault; 0:	BIT12
					undervoltage protectionAlarmBatUVP	3 (S)	Normal1: Fault: 0:	BIT13
					Discharge overcurrent protectionAlarmDchOCP Discharge short circuit prote	- A A	,	

0x1200

					Discharge over temperature protection		1: Fault; 0: Normal1:	BIT15
					AlarmDchOTP Charging tube abnormality		Fault; 0: Normal1: Fault;	BIT16
					AlarmChargeMOS Discharge tube abnormality		0: Normal1: Fault; 0:	BIT17
					AlarmDischargeMOS GPS disconnected		Normal1: Fault; 0:	BIT18
					GPSDisconnecte d Please modify the authorization		Normal1: Fault; 0:	BIT19
					password in time Modify PWD. in time		Normal1: Fault; 0:	BIT20
					Discharge on Failed Discharge on Failed Battery over		Normal1: Fault; 0: Normal	BIT21
					temperature alarm Battery Over Temp Alar m Temperature			
					sensor anomaly Parallel module failure PLCModule anomaly			
0x00A4 164	NT16		2 R E	alanCu	rent	mA		
0x00A6	166	UINT8	2	R Bala	nced state BalanSta	% 2:	discharge; 1: charge; 0: off	
UXUUAO	100	UINT8	2	R Rem	aining power SOCStateOfchar ge			500
0x00A8 168	NT32 0	x00AC	4 R F		ng capacity SOCCa pRemai n	mAH		
172 UINT32	0x00B0	176			ctual capacity SOCFullChargeCap	mAH		
UINT32 0x00	B4 180	UINT32		vcle Co	, ,	ÿ		
			7	ľ	le capacity SOCC vcleCap	mAH		
0x00B8	184	UINT8	2		Valuation SOCSOH	%		
UXUUDO	104	UINT8	2	R Prec	harge state Precharge		1: On; 0: Off	
0x00BA 186	UINT16	0x00BC	2 R L		r alarm UserAlarm			
188 UINT32			4 R R	unTime		s		
0x00C0 192		UINT8	2	R Cha	ge status		1: On; 0: Off1: On; 0: Off	
000000 192		UINT8	2	R Disc	harge state Dischar ge			
0x00C2 194	UINT16	0x00C4	2 R L	ser laye	r alarm 2UserAlarm2			
196 UINT16	0x00C6	198	2 R D	ischarg	overcurrent protection release time TimeDcOCP R	S		
UINT16 0x00	C8 200	UINT16	2 R D	ischarg	short circuit protection release time TimeDcSCP R	S		
0x00CA 202	UINT16	0x00CC	2 R C	harge o	vercurrent protection release time TimeCOCP R	S		
204 UINT16	0x00CE	206	2 R C	harging	short circuit protection release time TimeCSCP R	S		
UINT16			2 R S	ngle cel	undervoltage protection release time TimeUVP R	S		
			2 R S	ngle ce	l overvoltage protection release time TimeOVP R	S		
					MOS Temperature SensorMOS TempSensorAbsent			BIT0
					Battery Temperature Sensor 1 BATTem pSensor1Absent		1: normal; 0: missing1:	BIT1
		UINT8	2 R		Battery Temperature Sensor 2 BATTem pSensor2Absent		normal; 0: missing1:	BIT2
0x00D0 208		UINTO	Z K		Battery Temperature Sensor 3 BATTem pSensor3Absent		normal; 0: missing1:	BIT3
					Battery Temperature Sensor 4 BATTem pSensor4Absent		normal; 0: missing1:	BIT4
					Battery Temperature Sensor 5 BATTem pSensor5Absent		normal; 0: missing1: normal; 0: missing	BIT5

			UINT8		R Heat	ing status		1: On; 0: Off
	0x00D2 210	UINT16	0x00D4	2 R F	eserve	d		
	212 UINT16	0x00D6	214	2 R E	meraer	cy switch time TimeEmer genc y	S	
	UINT16 0x00	D8 216	UINT16			e current correction factor BatDisCurCorrect		
	0x00DA 218	UINT16	0x00DC	2 R C	harging	current sensor voltage VolChar gCur	mV	
	220 FLOAT	0x00E4	228			e current sensor voltage VolDischar gCur	mV	
	UINT16 0x00	E6 230	INT16			oltage correction factor BatVolCorrect		
				9		oltage BatVol	0.01Vm	A
				2 R F	eating	current HeatCurrent		
	00055.000		UINT8			in RVD		
	0x00EE 238		UINT8	2	R Cha	ger statusChargerPlugged		1: inserted; 0: not inserted
	0x00F0 240	UINT32	0x00F8	4 R S		Beat SvsRunTicks	0.1S	
	248 INT16 0	k00FA 2	50	- 8	r -	emperature TempBat 3	0.1 ÿ	
	INT16 0x00F	C 252 I	NT16			emperature TempBat 4	0.1 ÿ	
	0x0100 256	JINT32	0x0108	11		emperature TempBat 5	0.1 ÿ	
	264 UINT32					nter RTCTicks		Starting from 2020-1-1
				1		ep time TimeEnterSlee p Parallel	s	
	0.0400.000		UINT8	2 R	I KOT OIL	current limiting module status PCLModuleSta		1: On: 0: Off
	0x010C 268		UINT8	7 ^{2 K}		Reserve RVD		
	0x0000	0 AS	CII	16 R	Manufa	turer Model ManufacturerDeviceID		
	0x0010	16 A	SCII	8 R F	lardwar	e version number HardwareVersion		
	0x0018	24 AS	SCII	8 R S	oftware	Version		
	0x0020	32 U	NT32	4 R A	ccumul	ated running time ODDRunTime	S	
	0x0024	36 U	NT32	4 R F	ower-o	n times PWROnTimes	times	
	0x00B2 178		UINT8	2	RW Se	rial port 1 protocol UART1MPRTOLNbr		
	UXUUB2 176		UINT8] 2	RW CA	N protocol CANMPRTOLNbr		
	0x00B4 180	UINT8 ()x00C4	16 R	Serial p	prt 1 protocol control UART1MPRTOLEnable		
	196 UINT8					otocol control UARTMPRTOLEnable[0-15]		
	0x00D4	212	UINT8			rial port 2 protocol UART2MPRTOLNbr		
	UXUUD4	212	UINT8	2		l port 2 protocol control UART2MPRTOLEnable[0]		
	0,000 4,000		UINT8	2 RW		LCD buzzer trigger source LCDBuzzerTrigger		
0v1400	0x00E4 228		UINT8] _		Drv node 1 triager source DRY1Triager		
0x1400	0x00E6 230		UINT8		RW Dr	v node 2 trigger source DRY2Trigger		
	UXUUE6 230		UINT8	2		T protocol library version UARTMPTLVer		
	0x00E8 232	INT32		4 R\\		uzzer trigger value LCDBuzzerTriggerVal		

Jikong BMS RS485 Modbus Universal Protocol V1.1

	0x00EC 236	INT32	0x00F0	4 RW	LCD b	uzzer recovery value LCDBuzzerReleaseVal		
	240 INT32 0	x00F4 2	244	4 RW	Drv no	de 1 trigger value DRY1TriggerVal	63 56	
	INT32 0x00F	8 248 I	NT32			de 1 trigger value DRY1ReleaseVal		
	0x00FC 252	INT32	0x0100	4 RW	Drv no	de 2 trigger value DRY2TriggerVal		
	256 INT32					de 2 recovery value DRY2ReleaseVal		
				4 RW	Data s	torage period DataStoredPeriod		
	0x0104	260 U	INITO	2	RW	Charging time RCVTime	0.1H	0
	000104	200 0	IINTO	2	IXVV	Floating charge time RFVTime	0.1H	
	0x0106	262 U	INITQ	2	R	CAN protocol library version CANMPTLVer		
	000100		_	2	IX.	Preserve RVD		
	0x0000		NT16	4 W \	/oltage	Calibration	mV	
	0x0004		NT16			on board shutdown	(d)	
	0x0006	6 UI	NT16	4 W (Current	Calibration	mA	
	0x000A 10 L	JINT16	0x000C	2 W (ne-but	on ternary LI-ION		
0x1600	12 UINT16			2 W (One-clic	k Lithium Iron LIFEPO4		
	0x000E	14 U	INT16	2 W (ne-clicl	Lithium Titanate LTO		
	0x0010		INT16	2 W I	merge	ncy start		
	0x0012	18 U	INT32			bration		

				Sample	Data		
Regis Base addres	ter data	lenath		Register Definition	set up	Sending instructions	Receiving Response
		.,	aeVolSm	artSleep 0x1000 0x0004 UINT32 4	3.54 01 1	0 10 00 00 02 04 00 00 0D D4 3A A0 2.83 01 10 10 04	01 10 10 00 00 02 45 08
			J	0x0008 UINT32 4 Cell	00 02 04	00 00 0B 0E B9 68 2.86 01 10 10 08 00 02 04 00 00	01 10 10 04 00 02 04 C9
dervoltage	protection i	ecoveryVolC	ellUVPR	0x1000 0x000C UINT32 4 Cell overcharge	0B 2C 39	24 4.3	01 10 10 08 00 02 C4 CA
otectionVol	CellOV 0x1	000 0x0010	JINT32 4	Cell overcharge protection	-	01 10 10 0C 00 02 04 00 00 10 CC 33 OFF	01 10 10 0C 00 02 85 0B
covery volta	ageVolCell(VPR 4.16 0	10 10 1	0 00 02 04 00 00 10 40 33 53 0x1000 0x0014 UINT32 4 T	riggering	palanced voltage differenceVolBalanTrig 0.003 01	01 10 10 10 00 02 44 CD
01 10 10 1	IC 00 02 04	00 00 0B 22	B8 1F 0:	 x1000 0x0020 UINT32 4 Recommended charging voltage	VolCelIR	V 4.2 01 10 10 20 00 02 04 00 00 10 4A B2 F2	01 10 10 14 00 02 05 0C
				SOC0% 2.85 01 10 10 1C 00 02 04 00 00 0B 22 B8 1F 0	2		01 10 10 18 00 02 C5 0F
ICellRCV 4	1.2 01 10 10	20 00 02 04	00 0x10	00 0x0024 UINT32 4 Float charge voltage VolCellRFV 4.1	6 01 10 1	0 24 00 02 04 00 00 10 40 31 B4 0x1000 0x0028	01 10 10 1C 00 02 84 CE
NT32 4 Aut	tomatic shu	down voltag	e VolSys	PwrOff 2.7 0x1000 0x002C UINT32 4 Continuous charge	current Cu	rBatCOC 30 0x1000 0x0030 UINT32 4 Charge	01 10 10 20 00 02 44 C2
ercurrent p	rotection de	lay TIMBatC	OCPDIy	0x1000 0x0034 UINT32 4 Charge overcurrent protection r	elease TII	BatCOCPRDly 40 0x1000 0x0038 UINT32 4	01 10 10 24 00 02 05 03
ntinuous d	ischarge cu	rrent CurBat	DcOC 0x	1000 0x003C UINT32 4 Discharge overcurrent protection		01 10 10 28 00 02 04 00 00 0A 8C 3A D4	01 10 10 28 00 02 C5 00
	,			NT32 4 Discharge overcurrent protection release		01 10 10 2C 00 02 04 00 00 75 30 1A A6	01 10 10 2C 00 02 84 C1
				2 4 Short circuit protection release	10	01 10 10 30 00 02 04 00 00 00 0A BD 7C	01 10 10 30 00 02 45 07
	-			ximum balancing current CurBalanMax 4 Charge		01 10 10 34 00 02 04 00 00 00 28 3C 96	01 10 10 34 00 02 04 C6
	-			0 0x004C INT32 4 Charge	149 01 1	10 38 00 02 04 00 02 46 08 AE BB	01 10 10 38 00 02 C4 C5
ertemperat	ure recover	v TMPBatCC	TPR 0x1	000 0x0050 INT32 4 Discharge overtemperature protection	n	01 10 10 3C 00 02 04 00 00 00 1E BD 26	01 10 10 3C 00 02 85 04
				ge overtemperature recovery TMPBatDcOTPR 0x1000		01 10 10 40 00 02 04 00 00 00 28 3A 41 6	01 10 10 40 00 02 44 DC
0058 INT32	2 4 Charge	low tempera	ure prote	ction TMPBatCUT 0x1000 0x005C INT32		01 10 10 44 00 02 04 00 00 00 06 BB AE 1	01 10 10 44 00 02 05 1D
	_	-		TPR 0x1000 0x0060 INT32 4 MOS		01 10 10 48 00 02 04 00 00 03 E8 3B 47 75	01 10 10 48 00 02 C5 1E
1000 0x0 0 6	64 INT32 4	MOS	overt	emperature protection TMPMosOT	-	01 10 10 4C 00 02 04 00 00 02 EE BB 26 65	01 10 10 4C 00 02 84 DF
1000 0x0 0 6	68 INT32 0x	1000 overter	nperature	protection recovery TMPMosOTPR		01 10 10 50 00 02 04 00 00 02 8A BB 94 75	01 10 10 50 00 02 45 19
		0x006	C UINT32	4 Cell number CellCount		01 10 10 54 00 02 04 00 00 02 EE BB 8C 65	01 10 10 54 00 02 04 D8
						01 10 10 58 00 02 04 00 00 02 8A BA 32 -25	01 10 10 58 00 02 C4 DB
						01 10 10 5C 00 02 04 FF FF FF 06 FA D0 -15	01 10 10 5C 00 02 85 1A
						01 10 10 60 00 02 04 FF FF FF 6A F9 BC	01 10 10 60 00 02 45 16
					105 01 1	10 64 00 02 04 00 00 04 1A BA BF 90 01 10 10 68	01 10 10 64 00 02 04 D7
					00 02 04	00 00 03 84 39 72 15	01 10 10 68 00 02 C4 D4
						01 10 10 6C 00 02 04 00 00 00 0F 78 16 On: 01 10	01 10 10 6C 00 02 85 15
1000					10 70 00	02 04 00 00 00 01 F8 8B Off: 01 10 10 70 00 02 04	01 10 10 70 00 02 44 D3
1000	0x0070 UIN	Γ32 4 Chargi	ng switch	BatChargeEN	(3	00 39 4B	01 10 10 70 00 02 44 D3

				00 00 01 00 00 00 FD 50 04 40 40 70 00 04	01 10 10 74 00 02 05 12
0x1000	0x0074 UI	NT32 4 Discha	rge switch BatDisChargeEN	00 02 04 00 00 00 38 ED 50 01 10 10 7C 00 02 04	
	oxeer i oi	1102 1 2100114	go (mich BaiBiochargo En	00 00 01 C3 50 69 D2 140 01 10 10 80 00 02 04 00	01 10 10 74 00 02 05 12
0x1000	0.0070 111	NITOO 4 Dallara	no duitab Dalan EN	00 00 38 B8 On: 01 10 10 78 00 02 04 00 00 00 01	01 10 16 20 00 01 04 4B
0.000	UXUU78 UI	NT 32 4 Ballanc	ng switch BalanEN	F9 2D Off: 01 10 10 78 00 02 04 00 00 00 38 ED 50	01 10 16 20 00 01 04 4B
0x1000 0x	007C UINT	32 4 Designed	battery capacity CapBatCell	01 10 10 7C 00 02 04 00 00 C3 50 69 D2 140 01 10	01 10 10 7C 00 02 84 D0
	1	~	it protection delay SCPDelay	10 80 00 02 04 01 10 10 80 00 02 44 E0	
			start voltage VolStartBalan 0x1000	3.1 01 1 00101644 00842005 0 21 04 00 00 0C 1C 33 35 01	
			ernal resistance 0 CellConWireRes0	0.1 01 1 0100168880002025420 0 00 00 64 36 42	
			n line internal resistance 1	0.1 01 1 010100000000000000000	
			NT32 4 Connection line internal	0.1 01 1 0010190100902045 02504 00 00 00 64 36 E8	
resistance	2 CellConV	VireRes2 0x10	00 0x0094 UINT32 4 Connection line	0.1 01 1 01019400942040£4 4 00 00 00 64 37 1B	
internal res	istance 3 (ellConWireRe	s3 0x1000 0x0098 UINT32 4 Wire	0.1 01 1 00101981009820042E0 4 00 00 00 64 37 4E	
resistance	4CellConW	/ireRes4 0x100	0 0x009C UINT32 4 Wire resistance	0.1 01 1 010190 00902085026 04 00 00 00 64 36 BD	
5CellConV	/ireRes5 0x	1000 0x00A0 I	JINT32 4 Wire resistance	0.1 01 1 00101A0100A0204502A 04 00 00 00 64 35 FC	
6CellConV	/ireRes6 0x	1000 0x00A4 I	JINT32 4 Wire resistance	0.1 01 1 01010400A920040£B 4 00 00 00 64 34 0F	
7CellConV	/ireRes7 0x	1000 0x00A8 I	JINT32 4 Wire resistance	0.1 01 1 0101A80A820C4E8 4 00 00 00 64 34 5A	
8CellConV	/ireRes8 0x	1000 0x00AC	UIN 32 4 Wire resistance	0.1 01 1 00 1 01 6 N N D A O 02 8 2 2 2 00 00 00 64 35 A 9	
9CellConV	/ireRes9 0x	1000 0x00B0 I	JINT32 4 Wire resistance	0.1 01 1 00101630 00802044 02F04 00 00 00 64 34 F0	
10CellCon	WireRes10	0x1000 0x00B	4 UINT32 4 Connection line internal	0.1 01 1 01016400842005 02 £ 04 00 00 00 64 35 03	
resistance	11CellCon	WireRes11 0x1	000 0x00B8 UINT32 4 Connection line	0.1 01 1 00101688008820C522D 4 00 00 00 64 35 56	
internal res	istance 12	CellConWireRe	s12 0x1000 0x00BC UINT32 4 Connection	0.1 01 1 00101BC00BO20840EO 4 00 00 00 64 34 A5	
			reRes13 0x1000 0x00C0 UINT32 4	0.1 01 1 00101 00 00 00 02045) 24 04 00 00 00 64 33 D4	
Connection	n line intern	al resistance 1	4CellConWireRes14 0x1000 0x00C4 UINT32 4 Conne	chon hind much recrete recent of the recrete 23	