

MSX BIOS

**The Complete
MSX BASIC
I/O Listing**



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Pages 1-280, see part2 for the rest.

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MSX[®] BIOS

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```
1 ( MSX ROM BASIC BIOS ) Macro-80      3 .44    01-Jan-85      PAGE   1
2 -BIOS header- BIOS calls (Basic Interpreter, Slot I/O)
3
4 ;                               ; (C) Copyright by ASCII Corp., 1983
5 ; Proprietary information. All rights reserved.
6 ;
7 File:  BIOHDR.MAC
8 USE:   Restart calls and ROM entries table
9 Written by Jey Suzuki, Rick Yamashita
10 ASCII Corporation, Japan
11
12 Edit: January, 1985
13 Reason: Zilog Z80 Mnemonic version and cleanup
14 Edited by: Steven M. Ting
15 ;
16 ;
17 ; Labels referenced in this listing, are the absolute locations
18 ; within the MSX ROM. However, "ONLY" this BIOS entry point table,
19 ; and RAM variables are guaranteed to be permanent.
20 ;
21 ; All other locations in the ROM, will be changed without notice.
22 ;
23 ; SUBTLL -BIOS header- BIOS calls (Basic Interpreter, Slot I/O)
```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE   2
-BIOS header- BIOS calls (Basic Interpreter, Slot I/O)

24
25
26 ; The following RST's (RST 0 thru RST 5) are reserved for BASIC
27 ; interpreter, RST 6 for inter-slot calls, and RST 7 for
28 ; hardware interrupt
29
30     0000    F3          BEGIN:  DI      ;Fail safe
31     0001    C3 02D7    JP      ;Finds all connected RAM
32
33
34
35 ; ** Special information for the VDP. **
36 ; Any program that accesses the VDP hardware directly
37 ; should read the I/O port address found here, to be certain
38 ; the software is compatible with future versions of the VDP.
39
40     0004    1BBFF      DW      CGTABL ;Address of character generator table,
41
42
43     0006    98          DB      98H
44     0007    98          DB      98H ;to allow use of other character ROM.
45
46     0008    C3 2683    ;Current port address for VDP Data read
47
48     000B    00          JP      SYNCNR ;Check byte following the RST 8, see
49     000C    C3 01B6    DB      0       ;if equal to the byte pointed by HL
50     000F    00          JP      RDslt ;Read a byte from another slot
51     0010    C3 2686    DB      0       ;Fetch next char from BASIC text
52     0013    00          JP      CHRgtr ;Write a byte to another slot
53     0014    C3 01D1    DB      0
54     0017    00          JP      WRSLT

```

```

3 PAGE 2-1

( MSX ROM BASIC BIOS ) Macro-80 3.44 01-Jan-85
-BIOS header- BIOS calls (Basic Interpreter, Slot I/O)

55 0018 C3 1B45 JP OUTDO ;Output a char to the Console or printer
56 001B 00 DB 0
57 001C C3 0217 JP CALSLT ;Perform Inter-slot call
58 001F 00 DB 0
59 0020 C3 146A JP DCOMPRT ;Compares [HL] to [DE]
60 0023 00 DB 0
61 0024 C3 025E JP ENASLT ;Permanently enables a slot
62 0027 00 DB 0
63 0028 C3 2689 JP GETYPR ;Returns the [FAC] type
64 002B 00 DB 0 ;ID Byte (1)
65 ;Format: ;B7 B6 B5 B4 B3 B2 B1 B0
66 ;+ + + + + + + + ;Type of character
67 ;+ + + + + + + + generator.
68 ;+ + + + + + + + 0:Japanese
69 ;+ + + + + + + + 1:International
70 ;+ + + + + + + + 2:Korea
71 ;+ + + + + + + + ;Date format
72 ;+ + + + + + + + 0: Y-M-D 1: M-D-Y
73 ;+ + + + + + + + 2: D-M-Y
74 ;+ + + + + + + + ;Interrupt frequency
75 ;+ + + + + + + + 0: 60 Hz 1: 50 Hz
76 ;+ + + + + + + + ;ID Byte (2)
77 ;Format: ;B7 B6 B5 B4 B3 B2 B1 B0
78 002C 00 DB 0 ;+ + + + + + + + ;Type of Keyboard
79 ;+ + + + + + + + 0:Japanese
80 ;+ + + + + + + + 1:French
81 ;+ + + + + + + + 2:French
82 ;+ + + + + + + + 3:UK
83 ;+ + + + + + + + 4:DIN
84 ;+ + + + + + + +
85

```

(MSX ROM BASIC BIOS) Macro-80

-BIOS header- BIOS calls (Basic Interpreter, Slot I/O)

```

86      ; ----- Version of BASIC
87      ; 0: Japanese
88      ; 1: International
89      002D  00 00 00   DB  0,0,0
90      0030  C3 0205   JP  CALLF
91      0033  00 00 00   DB  0,0,0,0
92      0037  00          ;
93      ;
94      ; Following are used for I/O initialization
95      ; ; Handlers for hardware interrupt
96      ; ; Do device initialization
97      0038  C3 0C3C   JP  KEYINT
98      003B  C3 049D   JP  INITIO
99      003E  C3 139D   JP  INIFNK
100     ; ; Reset all function key's text
101     SUBTTL -BIOS header- BIOS calls (Video display processor)

```

```
( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE    3
-BIOS header- BIOS calls (Video display processor)
```

```

102
103
104 ; The following entry points provides control of the
105 ; VDP's registers, screen mode settings, and memory block
106 ; move between DRAM and VRAM.
107 ;
108 0041 C3 0577          JP  DISSCR           ;Disables screen display
109 0044 C3 0570          JP  ENASCR           ;Enables screen display
110 0047 C3 057F          JP  WRTVDP           ;Write a byte to any VDP register
111 004A C3 07D7          JP  RDVRM            ;Read VRAM addressed using [HL]
112 004D C3 07CD          JP  WRTVRM           ;Write VRAM addressed using [HL]
113 0050 C3 07EC          JP  SETRD             ;Sets up VDP for read
114 0053 C3 07DF          JP  SETWRT            ;Sets up VDP for write
115 0056 C3 0815          JP  FILVRM           ;Fills VRAM with specified data
116 0059 C3 070F          JP  LDIRMV            ;Moves block of data from VRAM to memory
117 005C C3 0744          JP  LDIRVM            ;"      "      "      "      memory to VRAM
118 005F C3 084F          JP  CHGMOD            ;Change screen mode of VDP to [SCRMOD]
119 0062 C3 07F7          JP  CHGCLR            ;change foreground, background,
120 ;border, color
121 0065 00               DB  0
122 ; Handler for non-maskable interrupt
123
124 0066 C3 1398          JP  NMII
125 ; Init sprite data
126 0069 C3 06A8          JP  CLRSPR
127 006C C3 050E          JP  INITXT           ;Init VDP for 40 X 24 text mode (SCREEN 0)
128 006F C3 0538          JP  INIT32           ;"      "      "      32 X 24 text mode (SCREEN 1)
129 0072 C3 05D2          JP  INIGRP            ;High resolution mode (SCREEN 2)
130 0075 C3 061F          JP  INIMLT            ;Multi color mode (SCREEN 3)
131 0078 C3 0594          JP  SETTXT           ;Sets VDP to display 40 X 24 text mode
132 007B C3 05B4          JP  SETT32           ;"      "      "      32 X 24 text mode
```

				PAGE	3-1
(MSX ROM BASIC BIOS) Macro-80					
-BIOS header- BIOS calls (Video display processor)					
133	007E	C3 0602	JP	SETGRP	; " " " "
134	0081	C3 0659	JP	SETMLT	; " " " "
135	0084	C3 06E4	JP	CALPAT	;Get address of sprite pattern table
136	0087	C3 06F9	JP	CALATR	; " " " " attribute table
137	008A	C3 0704	JP	GSPSIZ	;Returns current sprite size
138	008D	C3 1510	JP	GRPPRT	;Print a character on the graphic screen
139					
140					
SUBTTL -BIOS header- BIOS calls (Programmable Sound Generator control)					

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE     4
-BIOS header- BIOS calls (Programmable Sound Generator cont

141
142 ; Following entry points are used for PSG initialization,
143 ; read and write PSG registers, and PLAY statement execution.
144 ;
145
146 0090 C3 04BD      JP      GICINI      ;Init PSG, and static data for PLAY
147 0093 C3 1102      JP      WRTPSG      ;Write data to PSG
148 0096 C3 110E      JP      RDPSG       ;Read data from PSG
149 0099 C3 11C4      JP      STRTMS      ;Checks and start background task for PLAY
150
151 ; SUBTTL -BIOS header- BIOS calls (Keyboard, CRT, and Printer)

```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85
 -BIOS header- BIOS calls (Keyboard, CRT, and Printer)

```

152          ;-----;
153          ;-----;
154          ;-----;
155          ;-----;
156          009C    C3 0D6A      JP    CHSNS      ;Checks status of keyboard status
157          009F    C3 10CB      JP    CHGET      ;Return char typed, with wait
158          00A2    C3 08BC      JP    CHPUT      ;Output character to console
159          00A5    C3 085D      JP    LPTOUT     ;"           " to printer, if possible
160          00A8    C3 0884      JP    LPTSTT     ;Checks status of line printer
161          00AB    C3 089D      JP    CNVCHR     ;Checks for graphic header byte
162          ;and convert code
163          00AE    C3 23BF      JP    PINLIN     ;Read line from keyboard to buffer
164          00B1    C3 23D5      JP    INLIN      ;Same as above, except in case of
165          ;AUTFLG is set
166          00B4    C3 23CC      JP    QINLIN     ;Print a "?", then jump to INLIN
167          00B7    C3 046F      JP    BREAKX     ;[Control-STOP] pressed??
168          00BA    C3 03FB      JP    ISCNTC     ;[Shift-STOP] pressed??
169          00BD    C3 10F9      JP    CKCNTC     ;Same as ISCNTC, but used by BASIC
170          00C0    C3 1113      JP    BEEP       ;Buzz
171          00C3    C3 0848      JP    CLS        ;Clear screen
172          00C6    C3 088E      JP    POSIT      ;Place cursor at Column [H], Row [L]
173          00C9    C3 0B26      JP    FNKSB      ;Display Function key, if necessary
174          00CC    C3 0B15      JP    ERAFNK     ;Stop displaying the Function keys
175          00CF    C3 0B2B      JP    DSPFNK     ;Enable Function key display
176          00D2    C3 083B      JP    TOTEXT     ;Force screen to text mode
177          ;-----;
178          ;-----;
SUBTTL -BIOS header- BIOS calls (Game and Cassette I/O, Queue handler)

```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85
 -BIOS header- BIOS calls (Game and Cassette I/O, Queue hand

PAGE 6

```

179      ; Following are used to read the value from Joysticks,
180      ; Graphic pad (tablet), and Paddles.
181      ;
182      ;
183      ;
184      00D5    C3 11EE    JP    GTSTCK      ;Return status of joystick
185      00D8    C3 1253    JP    GTTRIG       ;Read joystick trigger button
186      00DB    C3 12AC    JP    GTPAD        ;Returns status of graphic pad
187      00DE    C3 1273    JP    GTPDL        ;Read paddle
188      ;

189      ;
190      ; Following are used to access the cassette tape,
191      ; data read/write, and motor on/off
192      ;
193      00E1    C3 1A63    JP    TAPION      ;Turn on motor and read tape header
194      00E4    C3 1ABC     JP    TAPIN       ;Read tape data
195      00E7    C3 19E9     JP    TAPIOF      ;Stops reading from tape
196      00EA    C3 19F1     JP    TAPOON      ;Turn on motor and write tape header
197      00ED    C3 1A19     JP    TAPOUT      ;Write data to tape
198      00F0    C3 19DD     JP    TAPOFF      ;Stops writing to tape
199      00F3    C3 1384     JP    STMOTR      ;Start, stop cassette motor, or
200      ;flip motor (on to off, off to on)
201      ;
202      ;
203      ; BASIC queues
204      00F6    C3 14EB    JP    LFTQ        ;Bytes left in queue
205      00F9    C3 1492    JP    PUTQ        ;Send a byte to queue
206      ;
207      ;
208      ;SUBRTL -BIOS header- BIOS calls (Generalized graphics )
```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85

-BIOS header- BIOS calls (Generalized graphics)

```

209      ; For BASIC interpreter's GENGRP and ADVGRP modules use
210      C3 16C5  JP  RIGHTC ;Moves one pixel right
211      C3 16EE  JP  LEFTC ; "    "   " left
212      00FC  C3 175D  JP  UPC  ; "    "   " up
213      00FF  C3 173C  JP  TUPC ; "    "   " "
214      0102  C3 172A  JP  DOWNC ; "    "   " down
215      0105  C3 170A  JP  TDOWNC ; "    "   " "
216      0108  C3 1599  JP  SCALXY ;Scales X Y coordinates
217      010B  C3 15DF  JP  MAPXYC ;Maps coordinates to physical address
218      010E  C3 1639  JP  FETCHC ;Get current physical address and
219      0111  C3 1640  JP  STOREC ;mask pattern
220      0114  C3 1640  JP  STOREC ;Put current physical address and
221      ;mask pattern
222      0117  C3 1640  JP  SETATR ;Sets the color attribute byte
223      ;mask pattern
224      011A  C3 1676  JP  READC ;Reads attribute of current pixel
225      011D  C3 1647  JP  SETC  ;Sets current pixel to specified attribute
226      0120  C3 167E  JP  NSETCX ;Sets pixel horizontally
227      0123  C3 1809  JP  GTASPC ;Returns aspect ratio
228      0126  C3 18C7  JP  PNTINI ;Do paint initialization
229      0129  C3 18CF  JP  SCANR ;Scan pixels to the right
230      012C  C3 18E4  JP  SCANL ; "    "   " left
231      012F  C3 197A  JP
232      ;
233      ;SUBTTL -BIOS header- BIOS calls (Misc. Entries)

```

Page	Line Number	Call Address	Call Name	Call Description	Call Address	Call Name	Call Description
8	234						
	235						
	236						
	237	0132	C3 0F3D	CHGCAP	0135	C3 0F7A	CHGND
	238	0135	C3 144C	RSLREG	0138	C3 144F	WSLREG
	239	0138	C3 1449	RDVDP	013B	C3 1452	SNSMAT
	240	013B	C3 1449	PHYDIO	013E	C3 1452	FORMAT
	241	013E	C3 1452	FORMAT	0141	C3 148A	ISFLIO
	242	0141	C3 148A	OUTDLP	0144	C3 148E	OUTDLP
	243	0144	C3 148A	GETYCP	0147	C3 145F	GETYCP
	244	0144	C3 148A	GETY2	014A	C3 1B63	KILBUF
	245	0145	C3 1470	CALBAS	014D	C3 1474	CALBAS
	246	0147	C3 1470	DS	0153	C3 0468	005AH
	247	014A	C3 1474		0156	C3 01FF	
	248	014D	C3 0468		0159	C3 01FF	
	249	0150	C3 01FF		015C	C3 01FF	
	250	0153	C3 01FF		015C	C3 01FF	
	251	0156	C3 01FF		015C	C3 01FF	
	252	0159	C3 01FF		015C	C3 01FF	
	253	015C	C3 01FF		015C	C3 01FF	
	254						
	255						

(MSX ROM BASIC BIOS) Macro-80 Macro-80 -BIOS header- BIOS calls (Misc. Entries) -SUBTTL - SLOT - Slot handler stuff ;

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   9
- SLOT - Slot handler stuff

256
257      00A8          PPI.AR    EQU      0A8h    ;A8H      read from PPI Port A
258      00A8          PPI.AW    EQU      0A8h    ;A8H      Write to PPI Port A
259
; Every cartridge located at 0000-3FFFFH must contain codes in
; this module which are entered via following addresses.
260
261
262
263      0000H RDSLT
264      0014H WRSLT
265      001CH CALSLT
266      0024H ENASLT
267
268
269
----- RDSLT -----
270
271 ; Selects the appropriate slot according to the value given
272 ; through registers, and read the content of memory from the
273 ; slot.
274
275 ; Input parameters:
276 ; A - FxxxxSSPP
277 ; | | | | +--- primary slot # (0-3)
278 ; | | | +--- secondary slot # (0-3)
279 ; +----- 1 if secondary slot # specified
280
281
282 ; HL - address of target memory
283 ; Returned value
284 ; A - content of memory
285
286 ; Note: Interrupts are disabled automatically but never enabled

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    9-1
- SLOT - Slot handler stuff               3.44    01-Jan-85

287      ;                                PAGE    9-1
288      ;                                PAGE    9-1
289      01B6    CD 027E    RDSLT:        CALL    SELPRM   ;Calculate bit pattern and mask code
290      01B6    FA 01C6    JP      M,RDESLT ;Expanded slot specified
291      01B9    DB A8     IN      A,(PPI.AR)
292      01BC    57       LD      D,A      ;Save current setting
293      01BE    A1       AND    C         ;Cancel current setting for target address
294      01BF    B0       OR     B         ;Add new setting
295      01C0    CD F380    CALL    RAMLOW  ;Call read primitive routine (in system area)
296      01C1    7B       LD      A,E      ;Return value via [Acc]
297      01C4    C9       RET
298      01C5
299      01C6    E5       RDESLT:    PUSH   HL       ;Save target address
300      01C6    CD 02A3    CALL   SELEXP  ;Select secondary slot
301      01C7    E3       EX     (SP),HL ;Restore target address and save [HL]
302      01CA    C5       PUSH   BC
303      01CB    C5       CALL   RDSSLT
304      01CC    CD 01B6    JR     WRESED  ;Restore old slot select register
305      01CF    18 1B     SUBTL -SLOT-  SLT     Slot handler (Write slot)
306

```

```
( MSX ROM BASIC BICS ) Macro-80
-SLOT- Slot handler (Write slot)
```

```

307
308
309
310
311 ; WRSLT
312 ; Selects the appropriate slot according to the value given
313 ; through registers, and write to the memory in the specified
314 ; slot.
315 ; Input parameters:
316 ; A - FxxxSSPP
317 ; | | |
318 ; | +--- primary slot # (0-3)
319 ; | +--- secondary slot # (0-3)
320 ; +--- 1 if secondary slot # specified
321
322 HL - address of target memory
323
324 E - value to be written
325
326
327 ; Note: Interrupts are disabled automatically but never enabled
328 ; by this routine.
329
330 01D1 D5
331 01D1 CD 027E
332 01D2 FA 01E1
333 01D5 01D8 D1
334 01D9 DB A8
335 01DB 57
336 01DC A1
337 01DD B0

WRSLT:
PUSH DE ;Save data to be written
CALL SELPRM ;Calculate bit pattern and mask code
JP M,WRESLT ;Expanded slot specified
POP DE ;Restore data to be written
IN A,(PPI.AR)
LD D,A ;Save current setting
AND C ;Cancel current setting for target address
OR B ;Add new setting

```

					PAGE	10-1
(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85			
--SLOT-- Slot handler (Write slot)						
338 01DE C3 F385	WRESLT:	JP	WPRIM	;	Call write primitive routine (in system area)	
339 01E1 E3		EX (SP),HL	;	Save target address, get data to be written		
340 01E1 E5		PUSH HL	;	Save data to be written		
341 01E2 E5		CALL SELEXP	;	Select secondary slot		
342 01E3 CD 02A3		POP DE	;	Restore data to be written		
343 01E6 D1		EX (SP),HL	;	Restore target address and save [HL]		
344 01E7 E3		PUSH BC				
345 01E8 C5		CALL WRSLT				
346 01E9 CD 01D1	WRESED:	POP BC	;	Save target address and get old [HL]		
347 01EC C1		EX (SP),HL	;	Save value returned by RDSTLT		
348 01ED E3		PUSH AF	;			
349 01EE F5		LD A,B	;	Get current setting		
350 01EF 78		AND 0011111B	;	Cancel current setting for 0C000H..0FFFFH		
351 01F0 E6 3F		OR C				
352 01F2 B1		OUT (PPI.AW),A				
353 01F3 D3 A8		LD A,L		Enable 0C000H..0FFFFH of target bank		
354 01F5 7D		LD (OFFFFH),A		;Restore old setting of slot register		
355 01F6 32 FFFF		LD A,B				
356 01F9 78		OUT (PPI.AW),A				
357 01FA D3 A8		POP AF		Finally restore old primary slot register		
358 01FC F1		POP HL				
359 01FD E1		POP C9				
360 01FE		RET				

(MSX ROM BASIC BIOS) Macro-80
--SLOT- Slot handler (Write slot) 3.44 01-Jan-85

362	01FF	FD 2A FCC0	CALBAS:	LD	IY,(EXPTBL-1)
363	01FF	0203 18 12		JR	CALSLT
364	0203	E3	CALLF:	EX	(SP),HL
365	0205	F5		PUSH	AF
366	0205	D5		PUSH	DE
367	0208	7E		LD	A,(HL)
368	0206	F5		PUSH	AF
369	0207	23		POP	IY
370	0208	5E		INC	HL
371	0209	23		LD	E,(HL)
372	020A	FD E1		INC	HL
373	020C	23		LD	D,(HL)
374	020D	5E		INC	HL
375	020E	23		LD	D,(HL)
376	020F	56		INC	HL
377	0210	23		PUSH	DE
378	0211	D5		POP	IX
379	0212	DD E1		POP	DE
380	0214	D1		POP	AF
381	0215	F1		EX	(SP),HL
382	0216	E3	SUBTTL	-SLOT-	
383					

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44    01-Jan-85      PAGE   12
-SLOT-
17

; ----- CALSLT -----
;
; Performs inter-slot call to specified address.
;
; Input parameters:
; IY - FxxxSSPP
;           | | |
;           |+--- primary slot # (0-3)
;           +---- secondary slot # (0-3)
;           +----- 1 if secondary slot # specified
;
; IX - address to call
;
; Note: Interrupts are disabled automatically but never enabled
; by this routine.
; You can never pass arguments via alternate registers
; of Z80.
;
; CALSLT:
;----- CALSLT: -----
0217    D9      EXX     ; Save environments
0218    08      EX     AF,AF' ; Get target slot information
0219    FD      PUSH    IY
021B    E5      POP     AF
021C    DD      PUSH    IX
021E    E1      POP     HL
021F    CD      CALL    SELPRM
0222    027E   JP     M,CALESL ; Call expanded slot
022E   IN     A,(PPI.AR) ; Save current value of primary slot register
0225    DB      PUSH    AF
0227    A8      IN     AF
0227    F5      PUSH    AF

```

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( MSX ROM BASIC BIOS ) Macro-80          3 .44    01-Jan-85    PAGE   12-1

-SLOT-
        415    0228    A1          AND      C
        416    0229    B0          OR       B
        417    022A    D9          EXX
        418    022B    C3 F38C    JP       CLPRIM
        419    022E    CD 02A3    CALESL: CALL    SELEXP
        420    022E    CD 02A3    CALL    SELEXP
        421    0231    F5          PUSH   AF
        422    0232    FD E1      POP    IY
        423    0234    E5          PUSH   HL
        424    0235    C5          PUSH   BC
        425    0236    4F          LD     C,A
        426    0237    06 00      LD     B,0
        427    0239    7D          LD     A,L
        428    023A    A4          AND   H
        429    023B    B2          OR    D
        430    023C    21 FCC5    LD     HL,SLTTBL
        431    023F    09          ADD   HL,BC
        432    0240    77          LD     (HL),A
        433    0241    E5          PUSH   HL
        434    0242    08          EX    AF,AF'
        435    0242    08          EXX
        436    0243    D9          CALL   CALSLT
        437    0244    CD 0217    EXX
        438    0247    D9          EX    AF,AF'
        439    0248    08          POP   HL
        440    0249    E1          POP   BC
        441    024A    C1          POP   DE
        442    024B    D1          LD    A,B
        443    024C    78          AND   0011111B
        444    024D    E6 3F      OR    C
        445    024F    B1          OR

;Cancel current setting for target address
;Add new setting
;Restore environments except PSW
;Jump to primitive routine (in system area)
;Select secondary slot register
;Move primary slot # in [IYH]
;Save [B,C,L] which contain information
;for restoring slot environments
;Move primary slot # to [BC]
;Re-calculate what is currently output
;to expansion slot register
;Calculate address into SLTTBL
;Set current value output to expansion
;slot register
;Remember this address
;Restore possible arguments passed
;via registers
;Call by primary slot #
;Save possible values returned via
;registers
;Restore address into SLTTBL
;Restore information about old slots
;Get current setting
;Cancel current setting for 0C000H..0FFFFH

```

(MSX ROM BASIC BIOS) Macro-80 PAGE 12-2
 -SLOT-

446	0250	F3	DI	(PPI.AW),A	;Enable 0C000H..0FFFFH of target bank
447	0251	D3 A8	OUT	A,E	;Restore old setting of slot register
448	0253	7B	LD	(0FFFFH),A	
449	0254	32 FFFF	LD	A,B	;Finally restore old primary slot register
450	0257	78	LD	(PPI.AW),A	
451	0258	D3 A8	OUT	(HL),E	;And change SLTtbl also
452	025A	73	LD	AF,AF'	;Restore possible returned values
453	025B	08	EX		
454	025C	D9	EXX		
455	025D	C9	RET		

(MSX ROM BASIC BIOS) Macro-80
-SLOT-

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20

```
456 ; ----- ENASLT -----
457 ;
458 ; Selects the appropriate slot according to the value given
459 ; through registers, and permanently enables the slot.
460 ;
461 ;
462 ;
463 ; Input parameters:
464 ;
465 ; A - FxxxxSSPP
466 ; | | | | |
467 ; | | +--- primary slot # (0-3)
468 ; | +--- secondary slot # (0-3)
469 ; +----- 1 if secondary slot # specified
470 ;
471 ; HL - address of target memory
472 ;
473 ; Note: Interrupts are disabled automatically but never enabled
474 ; by this routine.
475 ;
476 025E ENASLT: CALL SELPRM ; Calculate bit pattern and mask code
477 025E CD 027E JP M,ENESLT ; Expanded slot specified
478 0261 FA 026B IN A,(PPI.AR)
479 0264 DB A8 AND C ; Cancel current setting for target address
480 0266 AL OR B ; Add new setting
481 0267 B0 OUT (PPI.AW),A
482 0268 D3 A8 RET
483 026A C9
484 026B E5 ENESLT: PUSH HL ; Save target address
485 026B CD 02A3 CALL SELEXP ; Select secondary slot
486 026C
```

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	13-1
-SLOT-					
487	026F	4F		LD C,A	
488	0270	06 00		LD B,0	
489	0272	7D		LD A,L	
490	0273	A4		AND H	
491	0274	B2		OR D	
492	0275	21	FCC5	LD HL,SLTTBL	
493	0278	09		ADD HL,BC	
494	0279	77		LD (HL),A	
495				;Set current value output to expansion	
496	027A	E1		POP HL	
497	027B	79		LD A,C	
498	027C	18	E0	JR ENASLT	

;Move primary slot # to [BC]
 ;Re-calculate what is currently output
 ;to expansion slot register
 ;Calculate address into SLTTBL
 ;Set current value output to expansion
 ;slot register
 ;Restore target address
 ;Restore primary slot # to [Acc]
 ;Enable by primary slot register

```

499      027E    F3          DI          AF          ;Save slot address
500      027E    F5          PUSH        AF          ;Extract upper 2 bits
501      027F    F5          LD          A,H
502      027F    7C          RLCA
503      0280    07          RLCA
504      0281    07          AND         00000011B
505      0282    07          LD          E,A
506      0283    E6 03      LD          A,0C0H
507      0285    5F          SLPRM1:   ;Format mask pat. correspond to address
508      0286    3E C0      LD          00000000
509      0288    07          RLCA
510      0288    07          RLCA
511      0289    07          DEC         E
512      028A    1D          P,SLPRM1; Save mask pattern
513      028B    F2 0288    JP          E,A
514      028E    5F          LD          ;0000-3FFF
515      ;           ;00001100
516      ;           ;4000-7FFF
517      ;           ;8000-BFFF
518      ;           ;C000-FFFF
519      028F    2F          CPL        C,A
520      0290    4F          LD          ;Save mask pattern
521      ;           ;11111100
522      ;           ;11110011
523      ;           ;11001111
524      ;           ;00111111
525      0291    F1          POP        AF          ;Restore slot address
526      0292    F5          PUSH        AF          ;Extract primary slot #
527      0293    E6 03      AND         00000011B
528      0295    3C          INC         A
529      0296    47          LD          B,A

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85      PAGE    14-1
-SLOT-
      530    0297    3E AB           LD      A,10101011B      ;Convert slot # to proper bit pattern
      531    0299    C6 55           SLP RM2: ADD    A,01010101B
      532    0299    10 FC           DJNZ   D,SLPRM2
      533    029B    57           LD      D,A
      534    029D
      535
      536
      537
      538
      539    029E    A3           AND    E
      540    029F    47           LD     B,A
      541    02A0    F1           POP    AF
      542    02A1    A7           AND    A
      543    C2A2    C9           RET
      544    02A3    F5           SELEXP: PUSH   AF
      545    02A3    F5           PUSH   AF
      546    02A4    7A           LD     A,D
      547    02A5    E6 C0           AND   11000000B
      548    02A7    4F           LD     C,A
      549    02A8    F1           POP    AF
      550    02A9    F5           PUSH   AF
      551    02AA    57           LD     D,A
      552    02AB    DB A8           IN    A,(PPI.AR)
      553    02AD    47           LD     B,A
      554    02AE    E6 3F           AND   0011111B
      555    02B0    B1           OR    C
      556    02B1    D3 A8           OUT   (PPI.AW),A
      557    02B3    7A           LD     A,D
      558    02B4    0F           RRCA
      559    02B5    0F           RRCA
      560    02B6    E6 03           AND   00000011B      ;Extract secondary slot #

```

;Save bit pattern for primary slot #
;slot #0
;slot #1
;slot #2
;slot #3

;Extract significant bits
;Set it to [B]
;Expanded slot specified?
;Set sign flag if so

;Save target slot
;Get bit pattern for primary slot
;Extract slot # for 0C000H..0FFFFH
;Save it
;Restore target slot
;Save target slot
;iLoad [D] with specified slot address
;Save current setting
;Cancel current setting for 0C000H..0FFFFH
;Enable 0C000H..0FFFFH or target bank
;iLoad slot information

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   14-2
-SLOT-
561  02B8  57      LD      D,A
562  02B9  3E AB   LD      A,10101011B ;Convert secondary slot # to proper
563  02BB  C6 55   SLEXPL: ADD    A,01010101B ;bit pattern
564  02BB  15      DEC    D
565  02BD  F2 02BB JP     P,SLEXPL
566  02BE
567
568
569
570  02C1  A3      AND    E
571  02C2  57      LD      D,A
572  02C3  7B      LD      A,E
573  02C4  2F      CPL
574  02C5  67      LD      H,A
575  02C6  3A FFFF  LD      A,(0FFFFH) ;Save this
576  02C9  2F      CPL
577  02CA  6F      LD      L,A
578  02CB  A4      AND    H
579  02CC  B2      OR     D
580  02CD  32 FFFF  LD      (0FFFFH),A ;Set secondary slot register
581  02D0  78      LD      A,B
582  02D1  D3 A8   OUT   (PPI.AW),A ;Restore original primary port
583  02D3  F1      POP    AF
584  02D4  E6 03   AND    00000011B ;Restore target slot
585  02D6  C9      RET
586

SUBTTL - MSXIO - I/O Module

```

```

587
588
589 ; VDP address definition
590 ; Port definition
591 ;
592 ;
593 ;
594 ;
595 ; VDP address definition
596 VDP.DRW EQU 10011000B ;98H Read/write data VDP
597 VDP.CW EQU 10011001B ;99H write command to VDP
598 VDP.SR EQU 10011001B ;99H read status from VDP
599 ;
600 V.COLR EQU 7 ; In text mode, foreground and background color
601 ; Otherwise background color
602 ;
603 ; PSG address definition
604 ; PSG.LW EQU 10100000B ;A0H latch address for PSG
605 PSG.DW EQU 10100001B ;A1H write data to PSG
606 PSG.DR EQU 10100010B ;A2H read data from PSG
607 ;
608 ; PSG.PA EQU 14 ;Port A of PSG
609 PSG.PB EQU 15 ;Port B of PSG
610 ;
611 ;
612 ;
613 ; PPI address definition
614 PPI.AR EQU 10101000B ;A8H read from PPI Port A
615 PPI.BR EQU 10101001B ;A9H read from PPI Port B
616 PPI.CR EQU 10101010B ;AAH read from PPI Port C
617 PPI.AW EQU 10101000B ;A8H Write to PPI Port A

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   15-1
- MSXIO - I/O Module

618    00AA          PPI.CW    EQU     10101010B ;AAH   write to PPI Port C
619    00AB          PPI.CM    EQU     10101011B ;ABH   write to PPI command register
620
621
622
623    0091          LPT.DW    EQU     10010001B ;Data port
624    0090          LPT.SB    EQU     10010000B ;Strobe output
625    0090          LPT.ST    EQU     10010000B ;Printer status
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648

; Printer port definition

; Text mode (40*24)
; TXTNAM,TXTCGP
; SCREEN 0

; Text mode (graphics 1)
; T32NAM,T32COL,T32CGP,T32ATR,T32PAT
; SCREEN 1

; Hires mode
; GRPNAM,GRPCOL,GRPCGP,GRPATR,GRPPAT
; SCREEN 2

; Multi-color mode
; MLTNAM,MLTCGP,MLTATR,MLTPAT
; SCREEN 3

; Screen size
; LINLEN,CRTCNT,LINL32,LINL40
; External constants
;
```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE   15-2
- MSXIO - I/O Module

649          ; CGTABL      Character generator table
650          ;
651          ;
652          ;
653          ; External variables
654          ;
655          ;
656          ;
657          ;
658          ;
659          ;
660          ;
661          ;
662          ;
663          ;
664          ;
665          ;
666          ;
667          ;
668          ;
669          ;
670          ;
671          ;
672          ;
673          ;
674          ;
675          ;
676          ;
677          ;
678          ;
679          ;

; FORCLR      Foreground color
; BAKCLR      Background color
; BDRCLR     Border color for PAINT
; SCRMOD     Current screen mode
;             ; 0 - 40*24 text
;             ; 1 - 32*24 text
;             ; 2 - hiresolution graphics
;             ; 3 - Multicolor graphics

; OLDSCR      Base of current name table
; NAMBAS      Base of current cgen table
; CGPBAS     Base of current sprite pattern table
; PATBAS      Base of current sprite attribute table
; ATRBAS      Jiffy count
; JIFFY       Click switch
; CLIKSW      Click flag to suppress multiple key clicks
; CLIKFL      VDP register #0 save area
; RGOSAV     VDP register #1 save area
; RG1SAV     STATFL
;             ; VDP status register
;             ; PATWRK
;             ; Work area for pattern converter

; External routines
;             ; GETQ
;             ; PUTQ
;             ; INITQ
;             ; SUBTTL - MSXIO - Find available RAM

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Find available RAM

```

 3.44      01-Jan-85      PAGE     16

680      02D7      CHKRAM:
681      02D7      ; -----
682      ; ----- CHKRAM -----
683      ; -----
684      ; Look into every slot from 0FFFFH to C000H, and set system work
685      ; area. Note that we cannot use RAM as work area nor perform
686      ; subroutine calls 'cause we do not yet know where the available
687      ; RAM exits. Everything has to be done inside ROM and CPU's
688      ; register until the RAM is found.
689      ; -----
690      ; -----
691      02D7      3E 82      LD      A,82H      ;Port A - output (mode 0)
692      02D9      D3 AB      OUT    (PPI.CM),A   ;Port B - input (mode 0)
693      02DB      AF          XOR    A           ;Port C - output (mode 0)
694      02DC      D3 A8      OUT    (PPI.AW),A   ;Select slot 0 for all addresses
695      02DE      3E 50      LD      A,'P'       ;Disable all cassette related outputs
696      02E0      D3 AA      OUT    (PPI.CW),A   ;Motor off
697      ; -----
698      ; Start searching
699      ; -----
700      ; Register usage:
701      ; B - non 0 if we're now checking secondary slot
702      ; SPH - slot # of the biggest RAM block
703      ; SPL - secondary slot # of the biggest RAM block (if any)
704      ; DE - lowest address of the biggest RAM block ever found
705      ; C - 'slot-expanded' flag
706      ; -----
707      ; 0000xxxx
708      ; | | | |
709      ; | | | +--- slot #3 expanded
710      ; | | | -+--- slot #2 expanded

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Find available RAM

PAGE 16-1

29

```
    711          ; ;+--- slot #1 expanded
    712          ; +--- slot #0 expanded
    713          ; ;+--- slot #1 expanded
    714          02E2 11 FFFF LD DE,0FFFFH ;Initialize lowest address ever found
    715          02E5 AF XOR A ;Start from slot #0
    716          02E6 4F LD C,A ;Clear bit pattern
    717          02E7 CKRM05:    OUTT (PPI.AW),A ;Select the slot
    718          02E7 D3 A8 SLA C ;Shift bit pattern
    719          02E9 CB 21 LD B,0 ;Assume this slot is not expanded
    720          02EB 06 00 LD HL,0FFFFH ;Read from possible expansion slot register
    721          02ED 21 FFFF LD (HL),0FO0H ;Write a binary 11110000
    722          02F0 36 F0 LD A,(HL) ;Read back as 00001111?
    723          02F2 7E LD 0FH ;No, this is not an expanded slot
    724          02F3 D6 0F SUB NZ,CKRM15 ;Write 00000000
    725          02F5 20 0B JR (HL),A ;Read back as 11111111?
    726          02F7 77 LD A,(HL) ;No, not expanded slot
    727          02F8 7E INC A ;We're checking expanded slot
    728          02F9 3C INC B ;Say this slot is expanded
    729          02FA 20 06 JR NZ,CKRM15
    730          02FC 04 INC SET 0,C
    731          02FD CB C1 CKRM10:    INC A
    732          02FF 7E INC A
    733          ; ;Start from expansion slot #0
    734          ; ;Start from expansion slot #0
    735          ; ;Start from expansion slot #0
    736          02FF 32 FFFF LD (0FFFFH),A ;Select the expanded slot
    737          0302 CKRM15:    LD HL,0BF00H ;Start checking from 0BF00H to 8000H
    738          0302 21 BF00 CKRM20:    LD A,(HL)
    739          0305 7E CPL
    740          0305 2F
    741          0306
```

```

(
  MSX ROM BASIC BIOS ) Macro-80      PAGE    16-2
- MSXIO - Find available RAM          3.44    01-Jan-85

    742    0307    77      LD   (HL),A
    743    0308    BE      CP   (HL)
    744    0309    2F      CPL
    745    030A    77      LD   (HL),A
    746    030B    20 07   JR   NZ,CKRM25 ; RAM not equipped in this page
    747    030D    2C      INC
    748    030E    20 F5   JR   NZ,CKRM20 ; Make sure it's not a coincidence
    749    0310    25      DEC
    750    0311    FA      JP   M,CKRM20 ; Check more
    751    0314    CKRM25: ; Check next page

    752    0314    2E 00   LD   L,0
    753    0316    24      INC H
    754    0317    7D      LD   A,L ; Below the one ever found
    755    0318    93      SUB E
    756    0319    7C      LD   A,H
    757    031A    9A      SBC A,D
    758    031B    30 0A   JR   NC,CKRM30 ; No
    759    031D    EB      EX   DE,HL ; Register this address as the lowest
    760    031E    3A FFFF  LD   A,(0FFFFH) ; Set possible secondary slot #
    761    0321    2F      CPL
    762    0322    6F      LD   L,A ; Set primary slot #
    763    0323    DB A8   IN   A,(PPI.AR) ; Are we checking secondary slot
    764    0325    67      LD   H,A ; No
    765    0326    F9      LD   SP,HL ; Register these slot #'s
    766    0327    CKRM30: ; Prepare to select next secondary slot
    767    0327    78      LD   A,B
    768    0328    A7      AND A
    769    0329    28 0A   JR   Z,CKRM35
    770    032B    3A FFFF  LD   A,(0FFFFH)
    771    032E    2F      CPL
    772    032F    C6 10   ADD A,10H

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   16-3
- MSXIO - Find available RAM

    773  0331  FF 40          CP     0100000B
    774  0333  38 CA          JR     C,CKRM10 ;Continue if more secondary slots remain
    775  0335          CKRM35:
    776  0335  DB A8          IN     A,(PPI.AR)
    777  0337  C6 50          ADD    A,0101000B ;Prepare to select next slot
    778  0339  30 AC          JR     NC,CKRM05 ;Continue if more primary slots remain

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Find available RAM

```

3.44      01-Jan-85      PAGE    17

779          ; Check is done, select the biggest one
780
781          ; 
782          ; 
783      033B    21 0000      LD      HL,'0
784      033E    39          ADD    HL,SP
785      033F    7C          LD      A,H
786      0340    D3 A8      OUT   (PPI.AW),A      ;Set primary slot register
787      0342    7D          LD      A,L
788      0343    32 FFFF      LD      (0FFFFH),A      ;Set possible secondary slot register
789          ; 
790          ; Next, check 0C000H..0FFFFH
791          ; 
792      0346    79          LD      A,C
793      0347    07          RLCA
794      0348    07          RLCA
795      0349    07          RLCA
796      034A    07          RLCA
797      034B    4F          LD      C,A
798      034C    11 FFFF      LD      DE,0FFFFH      ;Initialize lowest address ever found
799      034F    DB A8      IN    A,(PPI.AR)
800      0351    E6 3F      AND   0011111B      ;Start from slot #0
801      0353    CKRM50:    CKRM50:
802      0353    D3 A8      OUT   (PPI.AW),A      ;Select the slot
803      0355    06 00      LD      B,0      ;Assume this slot is not expanded
804      0357    CB 01      RLC
805      0359    30 0A      JR    NC,CKRM60      ;Shift bit pattern
806      035B    04          INC   B      ;This slot is not expanded
807      035C    3A FFFF      LD      A,(0FFFFH)      ;We're checking expanded slot
808      035F    2F          CPL
809      0360    E6 3F      AND   0011111B

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Find available RAM

```

 3 . 4 . 4      01-Jan-85      PAGE      17-1

810    0362      CKRM55:      LD      (0FFFFFH),A      ;Select the expanded slot
811    0362      32 FFFF
812    0365      CKRM60:      LD      HL,0FFE0H      ;Start checking from 0FE00H to 0C000H
813    0365      21 FE00
814    0368      CKRM65:      LD      A,(HL)
815    0368      7E
816    0369      2F      CPL
817    036A      77      LD      (HL),A
818    036B      BE      CP      (HL)
819    036C      2F      CPL
820    036D      77      LD      (HL),A      ;RAM not equipped in this page
821    036E      20 09      JR      NZ,CKRM70      ;Make sure it's not a coincidence
822    0370      2C      INC     L
823    0371      20 F5      JR      NZ,CKRM65      ;Check more
824    0373      25      DEC     H
825    0374      7C      LD      A,H
826    0375      FE C0      CP      0C0H      NC,CKRM65      ;Check next page
827    0377      30 EF      JR
828    0379      CKRM70:      LD      L,0
829    0379      2E 00      LD      INC     H
830    037B      24      LD      A,L
831    037C      7D      SUB     E
832    037D      93      LD      A,H
833    037E      7C      SBC     A,D
834    037F      9A      JR      NC,CKRM75      ;NO
835    0380      30 0A      EX      DE,HL      ;Register this address as the lowest
836    0382      EB      LD      A,(0FFFFFH)      ;Set possible secondary slot #
837    0383      3A FFFF      CPL
838    0386      2F
839    0387      6F      LD      L,A      ;Set primary slot #
840    0388      DB A8      IN      A,(PPI.AR)

```

PAGE 17-2

```
( MSX ROM BASIC BIOS ) Macro-80
- MSXIO - Find available RAM
```

```
3 . 4 4      01-Jan-85      PAGE 17-2

841    038A    67      LD      H,A
842    038B    F9      LD      SP,HL ;Register these slot #'s
843    038C    78      CKRM75: LD      A,B
844    038C    78      AND     A
845    038D    A7      AND     A
846    038E    28      JR      Z,CKRM80 ;Are we checking secondary slot
847    0390    3A      LD      A,(0FFFFH) ;No
848    0393    2F      CPL
849    0394    C6      ADD     A,0100000B ;Prepare to select next secondary slot
850    0396    40      ADD     NC,CKRM55 ;Continue if more secondary slots remain
851    0398    30      CA      CKRM80: IN      A,(PPI.AR)
852    0398    DB      A8      ADD     A,0100000B ;Prepare to select next slot
853    039A    C6      ADD     NC,CKRM50 ;Continue if more primary slots remain
854    039C    30      B5      JR      NC,CKRM50
855          SUBTLL - MSXIO - Slot attribute setup
```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE     18
- MSXIO - Slot attribute setup

856
857
858
859      ; ; Check is done, select the biggest one
860      039E  21 0000      LD      HL,0
861      03A1  39          ADD    HL,SP
862      03A2  7C          LD      A,H
863      03A3  D3 A8      OUT   (PPI.AW),A ;Set primary slot register
864      03A5  7D          LD      A,L
865      03A6  32 FFFF      LD      (0FFFFFH),A ;Set possible secondary slot register
866      03A9  79          LD      A,C
867
868      ; ; Clear work area with zero
869
870      03AA  01 0C49      LD      BC,0C49H ;length of work area
871      03AD  11 F381      LD      DE,RAMLOW+1
872      03B0  21 F380      LD      HL,RAMLOW ;beginning of work
873      03B3  36 00          LD      (HL),0 ;init first byte
874      03B5  ED B0          LDIR   ;transfer it to rest of area
875
876      ; ; set EXPTBL
877
878      03B7  4F          LD      C,A
879      03B8  06 04          LD      B,4
880      03BA  21 FCC4      LD      HL,EXPTBL+3
881      03BD
882      CB 19          SSIITLP: RR      C
883      03BF  9F          SBC    A,A
884      03C0  E6 80          AND    80H
885      03C2  77          LD      (HL),A
886      03C3  2B          DEC    HL

```

; Set carry if LSB is set
; [Acc]=255 if expanded, 0 if not expanded
; Affects only MSB
; Set table for each slot

```

( MSX ROM BASIC BIOS ) Macro-80
- MSXIO - Slot attribute setup          PAGE    18-1

887   03C4    10 F7      ;           3.44    01-Jan-85
888
889
890
891   03C6    DB A8      ;           IN      A,(PPI.AR)
892   03C8    4F          LD      C,A
893   03C9    AF          XOR    A
894   03CA    D3 A8      OUT    (PPI.AW),A
895   03CC    3A FFFF      LD      A,(0FFFFFH)
896   03CF    2F          CPL    L,A
897   03D0    6F          LD      A,01000000B
898   03D1    3E 40      OUT    (PPI.AW),A
899   03D3    D3 A8      LD      A,(0FFFFFH)
900   03D5    3A FFFF      CPL    H,A
901   03D8    2F          LD      A,80H
902   03D9    67          OUT    (PPI.AW),A
903   03DA    3E 80      LD      A,(0FFFFFH)
904   03DC    D3 A8      CPL    E,A
905   03DE    3A FFFF      LD      A,0C0H
906   03E1    2F          OUT    (PPI.AW),A
907   03E2    5F          LD      A,(0FFFFFH)
908   03E3    3E C0      OUT    DE,HL
909   03E5    D3 A8      LD      (SLTTBL),HL
910   03E7    3A FFFF      EX    (SLTTBL+2),HL
911   03EA    2F          LD      DE,HL
912   03EB    57          OUT    (SLTTBL),HL
913   03EC    79          LD      DE,HL
914   03ED    D3 A8      OUT    (SLTTBL),HL
915   03EF    22 FCC5      LD      DE,HL
916   03F2    EB          EX    (SLTTBL+2),HL
917   03F3    22 FCC7      LD      DE,HL
; Set SLTTBL
; Remember primary slot register's content
; Read from slot #0
; Read from slot #1
; Read from slot #2
; Read from slot #3
; Restore primary slot register
; Set SLTTBL
;
```

{ MSX ROM BASIC BIOS) Macro-80
-- MSXIO - Slot attribute setup

3.44 01-Jan-85 PAGE 18-2

918 03F6 ED 56 IM 1 ;IM 1
919 03F8 C3 2680 JP INIT

920 SUBTTL - MSXIO - Control-[C] processing

MSX ROM BASIC BIOS Macro-80 processing
MSXIO - Control-[C] 3.44 01-Jan-85

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   1.9-1
- MSXIO - Control-[C] processing           3.44  01-Jan-85

952    0421   F5          PUSH    AF
953    0422   CD 0A27    CALL    CKERC0
954    0425   F1          POP     AF
955    0426   C1          POP     BC
956    0427   D1          POP     DE
957    0428   E1          POP     HL
958    0429   FE 03      CP      3
959    042B   C0          RET    NZ
960    042C   EXCABO:    ;Abort?
961    042C   E5          PUSH    HL
962    042D   CD 0468    CALL    KILBUF
963    0430   CD 0454    CALL    CKSTTP
964    0433   30 0A      JR      NC,EXABOL
965    0435   21 FC6A    LD      HL,REQSTP
966    0438   F3          DI      REQTRP
967    0439   CD 0EFL    CALL    EI
968    043C   FB          POP    HL
969    043D   EI          RET
970    043E   C9          EXABOL:
971    043F   ;                         ;Save text pointer
972    ;                         ;Cancel any input
973    043F   CD 083B    CALL    TCTEXT
974    0442   3A FCC1    LD      A,(EXPTBL)
975    0445   26 40      LD      H,01000000B
976    0447   CD 025E    CALL    ENASLT
977    044A   E1          POP    HL
978    044B   AF          XOR    A
979    044C   ED 7B F6B1  LD      SP,(SAVSTK)
980    0450   C5          PUSH   BC
981    0451   C3 63E6    JP      STOP
982

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    19-2
- MSXIO - Control-[C] processing           3.44    01-Jan-85

983   0454                                CKSTTP:
984   985      ; Check for STOP trap
986   987      ;
987   988      0454  3A FC6A      LD      A, (REQSTP)    ;IS STOP trap ON
988   989      0457  0F          RRCA
989   990      0458  D0          RET     NC
990   991      0459  2A FC6B      LD      HL, (REQSTP+1) ;No, accept this break
991   992      045C  7C          LD      A,H
992   993      045D  B5          OR     L
993   994      045E  C8          RET     Z
994   995      045F  2A F41C      LD      HL, (CURLIN) ;No, accept this break
995   996      0462  23          INC    HL
996   997      0463  7C          LD      A,H
997   998      0464  B5          OR     L
998   999      0465  C8          RET     Z
999   1000     0466  37          SCF
1000  1001     0467  C9          RET
1001  1002     0468                                KILBUF:
1002  1003      ; Set flag to indicate STOP trap active
1003  1004     0468  2A F3F8      LD      HL, (PUTPNT) ;Empties ring buffer
1004  1005     046B  22 F3FA      LD      (GETPNT), HL
1005  1006     046E  C9          RET
1006

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Control-[C] processing

```

1007          046F          BREAKX:
1008          046F          ; Check if stop key pressed. If pressed, return with carry set.
1009          046F          ;
1010          046F          ; IN A,(PPI.CR)
1011          046F          ; AND 0F0H      ;Leave others unaffected
1012          0471          F6 07          OR 7           ;Select 6th row
1013          0471          E6 F0          OUT (PPI.CW),A
1014          0473          F6 07          IN A,(PPI.BR)
1015          0475          D3 AA          AND 10H        ;STOP key is assigned to bit 4
1016          0477          DB A9          RET NZ         ;0 when pressed
1017          0479          E6 10          IN A,(PPI.CR)
1018          047B          C0             DEC A
1019          047C          DB AA          OUT (PPI.CW),A
1020          047E          3D             IN A,(PPI.BR)
1021          047F          D3 AA          AND 2
1022          0481          DB A9          RET NZ
1023          0483          E6 02          PUSH HL       ;Cancel any input
1024          0485          C0             LD HL,(PUTPNT)
1025          0486          E5             LD HL,(GETPNT),HL
1026          0487          2A F3F8        POP HL
1027          048A          22 F3FA        LD A,(OLDKEY+7)
1028          048D          E1             LD 0EFH      ;STOP pressed, mark as pressed to prevent
1029          048E          3A FBEL        AND (OLDKEY+7),A
1030          0491          E6 EF          LD 0EFH      ; to be doubly recognized
1031          0493          32 FBEL        LD A,0DH
1032          0496          3E 0D          LD (REPCNT),A
1033          0498          32 F3F7        SCF
1034          049B          37             RET
1035          049C          C9             SUBRNL - MSXIO - PSG Initialization
1036

```

(* MSX ROM BASIC BIOS) Macro-80
- MSXIO - PSG Initialization

```

1037      049D          INITIO:
1038      049D          ; Initialize I/O
1039
1040      049D          ; Initialize I/O
1041      049D          ; Initialize I/O
1042      049F          3E 07    LD A,7
1043      049F          1E 80    LD E,80H
1044      04A1          CD 1102  CALL WRTPSG      ; Set Port A to input mode
1045      04A4          3E 0F    LD A,0FH
1046      04A6          1E CF    LD E,0CPH      ; Port B to output mode
1047      04A8          CD 1102  CALL WRTPSG      ; Dummy write cycle to wake up the PSG
1048      04AB          3E 0B    LD A,0BH
1049      04AD          5F      LD E,A
1050      04AE          CD 1102  CALL WRTPSG      ; envelope register
1051      04B1          CD 110C  CALL INGI        ; Any value is OK!
1052      04B4          E6 40   AND 01000000B
1053      04B6          32 FCAD  LD (KANAMD),A
1054      04B9          3E FF   LD A,0FFH
1055      04BB          D3 90   OUT (LPT.SB),A
1056      04BD          04BD          GICINI:
1057
1058
1059
1060
1061
1062      04BD          E5      PUSH HL
1063      04BE          D5      PUSH DE
1064
1065      04BF          C5      PUSH BC
1066      04C0          F5      PUSH AF
1067

```

; Initialize GI sound chip, queues, and static data.
; Entry - Interrupts must be disabled
; Exit - All registers preserved.
; save caller's registers

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    21-1
- MSXIO - PSG Initialization

1068          ; First, clear all static data
1069          ;
1070 04C1 21 FB3F ; First, clear all static data
1071 04C4 06 71   LD HL,MUSICF
1072 04C6 AF     LD B,71H      ;=VCBC + VCBSTIZ + MUSCIF
1073 04C7       XOR A
1074 04C7 77     LD (HL),A
1075 04C8 23     INC HL
1076 04C9 10 FC  DJNZ MUSCLL
1077          ;
1078          ; Then clear music dynamic queue
1079          ;
1080 04CB 11 F975 ; Address of music queue
1081 04CE 06 7F   LD DE,VOICAQ
1082 04D0 21 0080 LD B,7FH      ; Mask pattern, 7F = Music queue len - 1
1083 04D3       LD HL,80H      ; Queue length
1084 04D3 E5     PUSH HL      ; Save length of queue
1085 04D4 D5     PUSH DE      ; Save address of queue
1086 04D5 C5     PUSH BC      ; Save mask pattern
1087 04D6 F5     PUSH AF      ; Save queue ID
1088 04D7 CD 14DA CALL INITQ  ; Initialize a queue by [Acc],[B],[DE]
1089 04DA F1     POP AF      ; write to regs 8,9,10
1090 04DB C6 08  ADD A,8
1091 04DD 1E 00  LD E,0
1092 04DF CD 1102 CALL WRTPSG
1093 04E2 D6 08  SUB 8       ; 0 out amplitude (turn voice off)
1094 04E4 F5     PUSH AF      ; Restore [Acc]
1095 04E5 2E 0F  LD L,0FH      ; Save queue ID
1096 04E7 CD 1477 CALL GETVCL
1097 04EA EB     EX DE,HL
1098 04EB 21 0508 LD HL,MUSITB ; [HL] points to default value table

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - PSG Initialization

PAGE 21-2

44

```
1099    04EE    01 0006    LD      BC,6      ;EMSITB - MUSITB
1100    04F1    ED B0      LDIR    LDIR      ;default variables for this voice
1101    04F3    F1      POP     AF      ;Restore queue ID
1102    04F4    C1      POP     BC      ;Restore queue mask
1103    04F5    E1      POP     HL      ;Restore queue address
1104    04F6    D1      POP     DE      ;Restore queue length
1105    04F7    19      ADD     HL,DE    ;Update queue address
1106    04F8    EB      EX     DE,HL    ;Next channel
1107    04F9    3C      INC     A      ;A
1108    04FA    FE 03      CP     3      ;Loop till done all three voices
1109    04FC    38 D5      JR     C,GICINL ;Write to reg 7 mixer control
1110    04FE    3E 07      LD     A,7      ;input port A, output port B
1111    0500    1E B8      LD     E,0B8H   ;disable noise, enable all 3 tones
1112    0502    CD 1102    CALL    WRTPSG   ;Restore environments
1113    0505    C3 08DA    JP     POPALL
1114    0508    MUSITB:  ; table of default values for music variables
1115
1116
1117    ;      ; default octave
1118    0508    04      DB     04H    ;default note length
1119    0509    04      DB     04H    ;default tempo
1120    050A    78      DB     78H    ;default volume
1121    050B    88      DB     88H    ;default envelope period
1122    050C    FF      DB     0FFH   ;end of music table
1123    050D    00      DB     00H    ;for VDP
1124    050E    ;      ;SUBTL - MSXIO - Utility routines for VDP
1125
```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   22    45
- MSXIO - Utility routines for VDP

1126
1127      050E          INITXT:
1128          ; Initialize VDP for text mode (40 by 24)
1129          ; Initialize VDP for text mode (40 by 24)
1130
1131      050E          CD 0577          CALL   DISSCR
1132      0511          AF              XOR    A
1133      0512          32 FCAF          LD     (SCRMOD),A
1134      0515          32 FCB0          LD     (OLDSSCR),A
1135      0518          3A F3AE          LD     A,(LINL40)
1136      051B          32 F3B0          LD     (LINLEN),A
1137      051E          2A F3B3          LD     HL,(TXTNAM)
1138      0521          22 F922          LD     (NAMBAS),HL
1139      0524          2A F3B7          LD     HL,(TXTCGP)
1140      0527          22 F924          LD     (CGPBAS),HL
1141      052A          CD 07F7          CALL   CHGCLR
1142      052D          CD 077E          CALL   CLRTXT
1143      0530          CD 071E          CALL   INIPAT
1144      0533          CD 0594          CALL   SETTXT
1145      0536          18 38          JR    ENASCR
1146      0538          INIT32:
1147          ; Initialize VDP for text mode (graphics 1)
1148          ; Initialize VDP for text mode (graphics 1)
1149
1150      0538          CD 0577          CALL   DISSCR
1151      053B          3E 01          LD     A,1
1152      053D          32 FCAF          LD     (SCRMOD),A
1153      0540          32 FCB0          LD     (OLDSSCR),A
1154      0543          3A F3AF          LD     A,(LINE32)
1155      0546          32 F3B0          LD     (LINLEN),A
1156      0549          2A F3BD          LD     HL,(T32NAM)

```

```

054C 22 F922 LD (NAMBAS),HL
054F 2A F3C1 LD HL,(T32CGP)
0552 22 F924 LD (CGPBAS),HL
0555 2A F3C5 LD HL,(T32PAT)
0558 22 F926 LD (PATBAS),HL
055B 2A F3C3 LD HL,(T32ATR)
055E 22 F928 LD (ATRBAS),HL
1157 0561 CD 07F7 CALL CHGCLR
1158 0564 CD 077E CALL CLRXTT
1159 0567 CD 071E CALL INIPAT
1160 056A CD 06BB CALL ERASPR
1161 056D CD 05B4 CALL SETT32
1162 0570 ENASCR: ;Set border foreground background color
1163 0571 ; Initialize character pattern
1164 0572 ;Clear sprites
1165 0573 ;Actually set VDP registers
1166 0574 ;Enable screen display
1167 0575 ;Disable screen display
1168 0576 ;Enable screen display
1169 0577 ;Disable screen display
1170 0578 ;Set border foreground background color
1171 0579 ;Initialize character pattern
1172 0580 ;Clear sprites
1173 0581 ;Actually set VDP registers
1174 0582 ;Enable screen display
1175 0583 ;Disable screen display
1176 0584 ;Enable screen display
1177 0585 ;Disable screen display
1178 0586 ;Set border foreground background color
1179 0587 ;Initialize character pattern
1180 0588 ;Clear sprites
1181 0589 ;Actually set VDP registers
1182 0590 ;Enable screen display
1183 0591 ;Disable screen display
1184 0592 ;Enable screen display

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE    23
- MSXIO - Utility routines for VDP

1185      057F          WRTVDP:
1186      ;           ; Write data to VDP
1187      ;           ; C = register #
1188      ;           ; B = value to be set
1189      ;           ; Register save area for the register is properly set
1190      ;           ; LD A,B
1191      ;           ; DI
1192      ;           ; OUT (VDP.CW),A
1193      ;           ; LD A,C
1194      ;           ; OR 80H
1195      057F          78          LD A,B
1196      0580          F3          DI
1197      0581          D3 99     OUT (VDP.CW),A
1198      0583          79          LD A,C
1199      0584          F6 80     OR 80H
1200      0586          D3 99     OUT (VDP.CW),A
1201      0588          FB          EI
1202      0589          E5          PUSH HL
1203      058A          78          LD A,B
1204      058B          06 00     LD B,0
1205      058D          21 F3DF   LD HL, RGOSAV
1206      0590          09          ADD HL, BC
1207      0591          77          LD (HL), A
1208      0592          E1          POP HL
1209      0593          C9          RET
1210      0594          SETTXT:
1211      ;           ; Set VDP for text mode (40 by 32)
1212      ;           ;
1213      0594          3A F3DF   LD A,(RGOSAV)
1214      0597          E6 01     AND 1
1215      ;           ; Set register #0

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    23-1
- MSX IO - Utility routines for VDP      3.44    01-Jan-85

1216  0599   47           LD      B,A
1217  059A   0E 00        LD      C,0
1218  059C   CD 057F      CALL   WRTVDP
1219  059F   3A F3E0      LD      A,(RG1SAV) ; Set register #1
1220  05A2   E6 E7        AND    0E7H
1221  05A4   F6 10        OR     10H
1222  05A6   47           LD      B,A
1223  05A7   0C           INC    C
1224  05A8   CD 057F      CALL   WRTVDP
1225  05AB   21 F3B3      LD      HL,TXTNAM
1226  05AE   11 0000      LD      DE,0      ; Set mask pattern
1227  05B1   C3 0677      JP      SETSCM   ; Set screen mode
1228  05B4   SETT32:      ; Set VDP for text mode (graphics 1)
1229
1230
1231
1232  05B4   3A F3DF      LD      A,(RG0SAV) ; Set register #0
1233  05B7   E6 01        AND    1
1234  05B9   47           LD      B,A
1235  05BA   0E 00        LD      C,0
1236  05BC   CD 057F      CALL   WRTVDP
1237  05BF   3A F3E0      LD      A,(RG1SAV) ; Set register #1
1238  05C2   E6 E7        AND    0E7H
1239  05C4   47           LD      B,A
1240  05C5   0C           INC    C
1241  05C6   CD 057F      CALL   WRTVDP
1242  05C9   21 F3BD      LD      HL,T32NAM
1243  05CC   11 0000      LD      DE,0      ; Set mask pattern
1244  05CF   C3 0677      JP      SETSCM   ; Set screen mode
1245  05D2   INIGRP:      ; Set VDP for text mode (graphics 1)
1246

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85    PAGE   23-2
- MSXIO - Utility routines for VDP

1247                                ; Initialize VDP for graphics mode
1248
1249      05D2    CD 0577    ; Initialize VDP for graphics mode
1250      05D5    3E 02    CALL    DISSCR
1251      05D7    32 FCAF    LD      A,2
1252      05DA    2A F3CF    LD      (SCRMOD),A
1253      05DD    22 F926    LD      HL,(GRPPAT)
1254      05E0    2A F3CD    LD      (PATBAS),HL
1255      05E3    22 F928    LD      HL,(GRPATR)
1256      05E6    2A F3C7    LD      (ATRBAS),HL
1257      05E9    CD 07DF    LD      HL,(GRPNAM) ;Initialize name table
1258      05EC    AF        CALL    SETWRT
1259      05ED    06 03    XOR    A
1260      05EF    06 03    LD      B,3
1261      05EF    D3 98    INIGRL:
1262      05F1    3C        OUT    (VDP.DRW),A
1263      05F2    20 FB    INC    A
1264      05F4    10 F9    JR     NZ,INIGRL
1265      05F6    CD 07A1    DJNZ  INIGRL
1266      05F9    CD 06BB    CALL  CLSHRS
1267      05FC    CD 0602    CALL  ERASPR
1268      05FF    C3 0570    CALL  SETGRP
1269      0602    SETGRP:  JP     ENASCR
1270                                ; Set VDP for graphics mode (graphics 2)
1271
1272
1273      0602    3A F3DF    LD      A,(RG0SAV) ;Set register #0
1274      0605    F6 02    OR     2
1275      0607    47        LD      B,A
1276      0608    0E 00    LD      C,0
1277      060A    CD 057F    CALL  WRITVDP

```

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( MSX ROM BASIC BIOS ) Macro-80          3 .44    01-Jan-85      PAGE   23-3
- MSXIO - Utility routines for VDP

1278    060D    3A F3E0    LD      A, (RG1SAV) ;Set register #1
1279    0610    E6 E7    AND    0E7H
1280    0612    47    LD      B,A
1281    0613    0C    INC    C
1282    0614    CD 057F    CALL   WRTVDP
1283    0617    21 F3C7    LD      HL, GRPNAM
1284    061A    11 7F03    LD      DE, 7F03H
1285    061D    18 58    JR      SETSCM
1286    061F    INIMLT:  ; Initialize VDP for multi-color mode
1287
1288
1289
1290    061F    CD 0577    CALL   DISSCR
1291    0622    3E 03    LD      A, 3
1292    0624    32 FCAF    LD      (SCRMOD), A
1293    0627    2A F3D9    LD      HL, (MLTPAT)
1294    062A    22 F926    LD      (PATBAS), HL
1295    062D    2A F3D7    LD      HL, (MLTATR)
1296    0630    22 F928    LD      (ATRBAS), HL
1297    0633    2A F3D1    LD      HL, (MLTNAM) ;Initialize name table
1298    0636    CD 07DF    CALL   SETWRT
1299    0639    11 0006    LD      DE, 6
1300    063C    INIML1:  LD
1301    063C    0E 04    LD      C, 4
1302    063E    INIML2:  LD      A,D
1303    063E    7A    LD      B, '
1304    063F    06 20    INIML3: OUT   (VDP.DRW), A
1305    0641    D3 98    INC    A
1306    0641    INIML3: DJNZ
1307    0643    3C
1308    0644    10 FB

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Utility routines for VDP

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PAGE

```

1309 0646 0D          DEC      C
1310 0647 20 F5       JR      NZ,INIML2
1311 0649 57          LD      D,A
1312 064A 1D          DEC      E
1313 064B 20 EF       JR      NZ,INIML1
1314 064D CD 07B9     CALL    CLSMLT      ;Clear pattern table
1315 0650 CD 06BB     CALL    ERASPR
1316 0653 CD 0659     CALL    SETMLT      ;Actually set VDP mode
1317 0656 C3 0570     JP      ENASCR

1318 0659             SETMLT:
1319                   ; Set VDP for multicolor mode
1320
1321
1322 0659 3A F3DF     ; Set register #0
1323 065C E6 01       AND     1
1324 065E 47          LD      B,A
1325 065F 0E 00       LD      C,0
1326 0661 CD 057F     CALL    WRITVDP
1327 0664 3A F3E0     LD      A,(RG0SAV) ; Set register #1
1328 0667 E6 E7       AND     0E7H
1329 0669 F6 08       OR      8
1330 066B 47          LD      B,A
1331 066C 0E 01       LD      C,1
1332 066E CD 057F     CALL    WRITVDP
1333 0671 21 F3D1     LD      HL,MLTNAM
1334 0674 11 0000     LD      DE,0      ; Set mask pattern
1335 0677             SETSCM:
1336 0677 01 0602     LD      BC,SETGRP
1337 067A CD 0690     CALL    SETREG      ; Set name table
1338 067D 06 0A       LD      B,0AH
1339 067F 7A          LD      A,D

```

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( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   23-5
- MSXIO - Utility routines for VDP

1340    0680    CD 0691          CALL    SETRG1      ; Set color table
1341    0683    06 05          LD      B,5
1342    0685    7B             LD      A,E
1343    0686    CD 0691          CALL    SETRG1      ; Set pattern table
1344    0689    06 09          LD      B,9
1345    068B    CD 0690          CALL    SETREG      ; Set sprite attribute table
1346    068E    06 05          LD      B,5
1347    0690    AF             XOR    A
1348    0690    AF             SETREG: XOR    A
1349    0691    E5             PUSH   HL
1350    0691    F5             PUSH   AF
1351    0692    F5             PUSH   A,(HL)
1352    0693    7E             LD      HL
1353    0694    23             INC    HL
1354    0695    66             LD      H,(HL)
1355    0696    6F             LD      L,A
1356    0697    AF             XOR    A
1357    0698    29             ADD    HL,HL
1358    0698    29             ADC    A,A
1359    0699    8F             DJNZ  SETRG2
1360    069A    10  FC          LD      L,A
1361    069C    6F             POP    AF
1362    069D    F1             OR     L
1363    069E    B5             LD      B,A
1364    069F    47             CALL   WRIVDP
1365    06A0    CD 057F          POP    HL
1366    06A3    E1             INC    HL
1367    06A4    23             INC    HL
1368    06A5    23             INC    C
1369    06A6    0C             INC    RET
1370    06A7    C9

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Utility routines for VDP

PAGE 24

```

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1371 1372 06A8 CLRSPR:
1373 1374 ; Clear all sprites
1375 ; Set register #1
1376 06A8 3A F3E0 LD A,(RG1SAV) ;Set register #1
1377 06AB 47 LD B,A
1378 06AC 0E 01 LD C,1
1379 06AE CD 057F CALL WRTVDP
1380 06B1 2A F926 LD HL,(PATBAS) ;Clear sprite pattern table
1381 06B4 01 0800 LD BC,0800H ;Length of sprite pattern table
1382 06B7 AF XOR A
1383 06B8 CD 0815 CALL FILVRM
1384 06BB ERASPR: LD A,(FORCLR) ;Load foreground color (default) to [E]
1385 06BB 3A F3E9 LD E,A
1386 06BE 5F LD HL,(ATRBAS)
1387 06BF 2A F928 LD BC,2000H ;Set number of sprite plane to [B]
1388 06C2 01 2000 CLSPR2: ; default sprite name to [C]
1389 06C5 3E D1 LD A,0D1H ;Erase code (i.e. vertical position)
1390 06C7 CD 07CD CALL WRTRVM ;Set vertical position
1391 06CA 23 INC HL
1392 06CB 23 INC HL
1393 06CC 79 LD A,C ;Load default sprite name
1394 06CD 07CD CALL WRTRVM
1395 06D0 23 INC HL
1396 06D1 0C INC C ;Prepare for next
1397 06D2 3A F3E0 LD A,(RG1SAV)
1398 06D5 0F RRCA

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   24-1
- MSXIO - Utility routines for VDP

1402  06D6  0F          RRCA           ;16*16?
1403  06D7  30 03       JR   NC,CLSPR3  ;No
1404  06D9  0C          INC  C          ;Yes, C=C+4
1405  06DA  0C          INC  C
1406  06DB  0C          INC  C
1407  06DC  CLSPR3:     INC  C          ;Load default color
1408  06DC  7B          LD   A,E        WRTVRM
1409  06DD  CD 07CD     CALL HL
1410  06E0  23          INC HL
1411  06E1  10  E2      DJNZ CLSPR2
1412  06E3  C9          RET
1413  06E4  CALPAT:     ;
1414  ;                         LD   L,A
1415  06E4  6F          LD   H,0        ;Assume 8 byte long
1416  06E5  26 00       ADD  HL,HL
1417  06E7  29          ADD  HL,HL
1418  06E8  29          ADD  HL,HL
1419  06E9  29          ADD  HL,HL
1420  06EA  CD 0704     GSPSIZ CALL 8          ;Check size of sprite
1421  06ED  FE 08       CP   Z,GSPADL
1422  06EF  28  02      ADD  HL,HL
1423  06F1  29          ADD  HL,HL
1424  06F2  29          ADD  HL,HL
1425  06F3  GSPAD1:     EX   DE,HL
1426  06F3  EB          LD   HL,(PATBAS) ;Get base address of sprite pattern table
1427  06F4  2A F926     ADD  HL,DE
1428  06F7  19          ADD  HL,DE
1429  06F8  C9          RET
1430  06F9  CALATR:     ;
1431  ;                         LD   L,A        ;Get plane number to [L]
1432  06F9  6F          LD   L,A

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    24-2
- MSXIO - Utility routines for VDP          3.44    01-Jan-85

1433  06FA  26 00      LD      H,0           ;Sprite attribute consists of 4 bytes
1434  06FC  29      ADD    HL,HL
1435  06FD  29      ADD    HL,HL
1436  06FE  EB      EX     DE,HL
1437  06FF  2A F928   LD     HL,(ATRBAS)  ;Load base address
1438  0702  19      ADD    HL,DE           ;Calculate target address
1439  0703  C9      RET
1440  0704  GSSIZ:    ;Get sprite size
1441
1442
1443
1444  0704  3A F3E0   LD      A,(RG1SAV)
1445  0707  0F      RRCA
1446  0708  0F      RRCA
1447  0709  3E 08   LD      A,8           ;Assume 8 byte long
1448  070B  D0      RET    NC             ;Good assumption
1449  070C  3E 20   LD      A,32          ;32 byte long sprite
1450  070E  C9      RET

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Utility routines for VDP

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44    01-Jan-85      PAGE   25-1
- MSXIO - Utility routines for VDP

1482  0735  CD 01B6          CALL    RDSL T ;Read from specified slot
1483  0738  FB              EI
1484  0739  C1              POP    BC ;Restore counter
1485  073A  D3 98          OUT    (VDP.DRW),A
1486  073C  23              INC    HL ;Bump character source pointer
1487  073D  0B              DEC    BC
1488  073E  79              LD     A,C
1489  073F  B0              OR     B
1490  0740  20  EF          JR     NZ,INIPTL ;Discard stack
1491  0742  F1              POP    AF
1492  0743  C9              RET
1493  0744  ;DIRVM:
1494  ;DIRVM:                EX     DE,HL
1495  0744  EB              CALL    SETWRT
1496  0745  CD 07DF          LDIVML: CALL
1497  0748  ;DIRVM:          LD     A,(DE)
1498  0748  1A              OUT    (VDP.DRW),A
1499  0749  D3 98          INC    DE
1500  074B  13              DEC    BC
1501  074C  0B              LD     A,C
1502  074D  79              OR     B
1503  074E  B0              JR     NZ,LDIVML
1504  074F  20  F7          RET
1505  0751  C9              ;GETPAT:
1506  0752  ;GETPAT:          ; Get pattern corresponding to ASCII code in [A]
1507  ;GETPAT:          ; Pattern is returned to 8 byte work area (PATWKRK). Entered
1508  ;GETPAT:          ; by GRPPRT (print a character to graphic screen) subroutine.
1509
1510
1511
1512

```

```

1513
1514          ; All registers are completely destroyed
1515          0752    26 00          LD      H,0
1516          0754    6F             LD      L,A
1517          0755    29             ADD    HL,HL
1518          0756    29             ADD    HL,HL
1519          0757    29             ADD    HL,HL
1520          0758    EB             EX      DE,HL
1521          0759    2A F920        LD      HL,(CGPNT+1)
1522          075C    19             ADD    HL,DE
1523          075D    11 FC40        LD      DE,PATWRK
1524          0760    06 08          LD      B,8
1525          0762    3A F91F        LD      A,(CGPNT)
1526          0765          GTPAT1:   PUSH   AF
1527          0765    F5             PUSH   HL
1528          0766    E5             PUSH   DE
1529          0767    D5             PUSH   BC
1530          0768    C5             PUSH   RDSLRT
1531          0769    CD 01B6        CALL   EI
1532          076C    FB             POP    BC
1533          076D    C1             POP    DE
1534          076E    D1             POP    HL
1535          076F    E1             LD    (DE),A
1536          0770    12             INC    DE
1537          0771    13             INC    HL
1538          0772    23             INC    INC
1539          0773    F1             POP    AF
1540          0774    10 EF          DJNZ  GTPAT1
1541          0776    C9             RET
1542          0777          CLSSUB: ;
1543

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Utility routines for VDP

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```

1544 0777 CD 0B9F           CALL  CHKSCR      ;Check current screen mode
1545 077A 28 25           JR   Z,CLSHRS    ;Hires
1546 077C 30 3B           JR   NC,CLSMLT    ;Multi-color
1547 077E CLRXTXT:        ;
1548 ; Clear screen (text mode)
1549 ;                               ;
1550 ;                               ;
1551 077E 3A FCAF           LD   A,(SCRMOD)
1552 0781 A7               AND  A
1553 0782 2A F922           LD   HL,(NAMBAS)  ;Set address for write
1554 0785 01 03C0           LD   BC,03C0H   ;40 * 24
1555 0788 28 03           JR   Z,CLRTX1   ;32 * 24
1556 078A 01 0300           LD   BC,0300H
1557 078D CLRXTX1:         LD   A,' '          ;Fill space character code
1558 078D 3E 20           LD   A,' '
1559 078F CD 0815           CALL FILVRM
1560 0792 CD 0A7F           CALL CSHOME
1561 0795 21 FBB2           LD   HL,LINTTB  ;Set cursor at home position
1562 0798 06 18           LD   B,18H       ;Say all lines are terminated
1563 079A CLRXTX2:         LD   (HL),B
1564 079A 70               LD   INC HL          ;Load non 0 value
1565 079B 23               INC  HL
1566 079C 10 FC             DJNZ CLRTX2
1567 079E C3 0B26           JP   FNKSB
1568 07A1 CLSHRS:          ;
1569 ;                               ;
1570 07A1 CD 0832           CALL CHGBDR     ;Set border color
1571 07A4 01 1800           LD   BC,1800H   ;Initialize color
1572 07A7 C5               PUSH BC          ;Save this for future use
1573 07A8 2A F3C9           LD   HL,(GRPCOL)
1574 07AB 3A F3EA           LD   A,(BAKCLR)  ;Load background color

```

PAGE 25-4

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Utility routines for VDP

```

1575 07AE CD 0815 CALL FILVRM
1576 07B1 2A F3CB LD HL,(GRPCGP)
1577 07B4 C1 POP BC
1578 07B5 AF XOR A ;Load 6144
1579 07B6 JFLVRM: A
1580 07B6 C3 0815 JP FILVRM
1581 07B9 CLSMLT: ; ; Set border color
1582 ; ; Set all pixels to background color
1583 07B9 CD 0832 CALL CHGBDR
1584 07BC 21 F3EA LD HL,BAKCLR
1585 07BF 7E LD A,(HL)
1586 07C0 87 ADD A,A
1587 07C1 87 ADD A,A
1588 07C2 87 ADD A,A
1589 07C3 87 ADD A,A
1590 07C4 B6 OR (HL)
1591 07C5 2A F3D5 LD HL,(MLTCGP) ;Set up address for write
1592 07C8 01 0600 LD BC,0600H
1593 07CB 18 E9 JR JFLVRM ;Clear sprites (except sprite pattern)

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   26
- MSXIO - Utility routines for VDP

1594      07CD          WRTVRM:
1595      07CE          ; Write a byte to VRAM
1596      07CD          ; Save data to be written
1597      F5            ; Write a byte to VRAM
1598      CD 07DF        ; Save data to be written
1599      07CE          ; Write a byte to VRAM
1600      07D1          ; Save data to be written
1601      E3            ; Write a byte to VRAM
1602      07D2          ; Save data to be written
1603      E3            ; Write a byte to VRAM
1604      07D3          ; Save data to be written
1605      F1            ; Write a byte to VRAM
1606      07D4          ; Save data to be written
1607      D3 98          ; Write a byte to VRAM
1608      C9            ; Save data to be written
1609      07D7          ; Read a byte from VRAM
1610      CD 07EC        ; Read a byte from VRAM
1611      07DA          ; Set address for write to VDP
1612      E3            ; Address is passed to HL
1613      07DB          ; Set address for write to VDP
1614      E3            ; Address is passed to HL
1615      07DC          ; Set address for write to VDP
1616      DB 98          ; Address is passed to HL
1617      C9            ; Set address for write to VDP
1618      07DE          ; Address is passed to HL
1619      07DF          ; Set address for write to VDP
1620      7D            ; Address is passed to HL
1621      07E0          ; Set address for write to VDP
1622      F3            ; Address is passed to HL
1623      07E1          ; Set address for write to VDP
1624      D3 99          ; Address is passed to HL
1625      07E3          ; Set address for write to VDP

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE    26-1
- MSXIO - Utility routines for VDP

1625  07E4    E6 3F          AND    00111111B
1626  07E6    F6 40          OR     0100000B
1627  07E8    D3 99          OUT    (VDP.CW),A
1628  07EA    FB             EI
1629  07EB    C9             RET
1630  07EC    SETRD:
1631          ; Set address for read from VDP
1632          ; Address is passed to HL
1633          ; Address is passed to HL
1634          ;
1635  07EC    7D             LD     A,L
1636  07ED    F3             DI
1637  07EE    D3 99          OUT   (VDP.CW),A
1638  07F0    7C             LD     A,H
1639  07F1    E6 3F          AND   0011111B
1640  07F3    D3 99          OUT   (VDP.CW),A
1641  07F5    FB             EI
1642  07F6    C9             RET
1643  07F7    CHGCLR:
1644          ; CHGCLR - changes foreground, background, and border color
1645          ;
1646          ; CHGCLR - changes foreground, background, and border color
1647  07F7    3A FCAF          LD     A, (SCRMOD)    ;Are we in text mode
1648  07FA    3D             DEC   A
1649  07FB    FA 0824          JP    M,CHCL.TX    ;Yes , change color in 40*24 text mode
1650  07FE    F5             PUSH  AF
1651  07FF    CD 0832          CALL  CHGBDR    ;Change border color for all
1652  0802    F1             POP   AF
1653  0803    C0             RET   NZ    ;No
1654  0804    3A F3E9          LD     A, (FORCLR)  ;We're in 32*24 text mode
1655

```

(MSX ROM BASIC BIOS) Macro-80		Macro-80 Utility routines for VDP		PAGE	26-2
				3.44	01-Jan-85
1656	0807	87		ADD	A,A
1657	0808	87		ADD	A,A
1658	0809	87		ADD	A,A
1659	080A	87		ADD	A,A
1660	080B	21 F3EA		LD	HL,BAKCLR (HL)
1661	080E	B6		OR	
1662	080F	2A F3BF		LD	HL,(T32COL)
1663	0812	01 0020		LD	BC,20H
1664	0815		FILVRM:		
1665	0815	F5		PUSH	AF
1666	0816	CD 07DF		CALL	SETWRT
1667	0819		FLVRML:	POP	AF
1668	0819	F1		OUT	(VDP.DRW),A
1669	081A	D3 98		PUSH	AF
1670	081C	F5		DEC	BC
1671	081D	0B		LD	A,C
1672	081E	79		OR	B
1673	081F	B0		JR	NZ,FLVRML
1674	0820	20 F7		POP	AF
1675	0822	F1		RET	
1676	0823	C9			
1677	0824		CHCLTX:	;	
1678				LD	A,(FORCLR)
1679	0824	3A F3E9		ADD	A,A
1680	0827	87		ADD	A,A
1681	0828	87		ADD	A,A
1682	0829	87		ADD	A,A
1683	082A	87		ADD	A,A
1684	082B	21 F3EA		LD	HL,BAKCLR (HL)
1685	082E	B6		OR	
1686	082F	47		LD	B,A

```
( MSX ROM BASIC BIOS ) Macro-80          PAGE    26-3
- MSXIO - Utility routines for VDP

1687  0830   18 03      CHGDDR:    JR      CHGDD1
1688  0832           ;                                ;Get border color
1689
1690  0832   3A F3EB   CHGDD1:    LD      A,(BDRCLR)
1691  0835           ;                                ;Get border color
1692  0835   47        LD      B,A
1693  0836   0E 07        LD      C,7
1694  0838   C3 057F   JP      WRTVDP
```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Utility routines for VDP

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Some entry points

3.44 01-Jan-85

```

1725      085D          LPTOUT:
1726      1728          ; Output a character to printer
1727      1729          ;
1728      1730          ; Output a character to printer
1729      1731          ;
1730      085D          CD FFB6          CALL H.LPTO      ; Save character to output
1731      0860          F5              PUSH AF
1732      0861          046F          CHPLP1:    CALL BREAKX   ; Check if aborted
1733      0861          CD 046F        PUSH AF
1734      0864          38 12          JR C,LPTABO
1735      0866          CD 0884        CALL LPTSTT   ; No
1736      0869          28 F6          JR Z,CHPLP1   ; Restore character
1737      086B          F1              POP AF
1738      086C          F5              CHPLP2:    PUSH AF      ; Save it again
1739      086C          F5              PUSH AF      ; Send to output port
1740      086D          D3  91          OUT (LPT.DW),A ; Generate strobe
1741      086F          AF              XOR A
1742      0870          D3  90          OUT (LPT.SB),A
1743      0872          3D              DEC A
1744      0873          D3  90          OUT (LPT.SB),A
1745      0875          F1              POP AF      ; Restore data output
1746      0876          A7              AND A
1747      0877          C9              RET
1748      0878          LPTABO:    ;
1749      0878          AF              XOR A      ; Reset carriage position
1750      0879          32 F415        LD (LPTPOS),A
1751      0879          3E 0D          LD A,0DH
1752      087C          CD 086C        CALL CHPLP2
1753      087E          F1              POP AF
1754      0881          F1              SCF
1755      0882          37

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44   01-Jan-85    PAGE   28-1
- MSXIO - Some entry points

1756  0883  C9           RET
1757  0884  ;LPTSTT:
1758          ;CALL H.LPTS
1759  0884  CD FFBB      IN A,(90H) ;LSB is 0 if ready
1760  0887  DB 90
1761  0889  0F
1762  088A  0F
1763  088B  3F
1764  088C  9F
1765  088D  C9
1766  088E  POSIT:
1767          ; Position cursor to specified position
1768          ;
1769          ;
1770  088E  3E 1B
1771  0890  DF
1772  0891  3E 59
1773  0893  DF
1774  0894  7D
1775  0895  C6 1F
1776  0897  DF
1777  0898  7C
1778  0899  C6 1F
1779  089B  DF
1780  089C  C9
1781  089D  CNVCHR:
1782          ;
1783          ;
1784          ;
1785  089D  E5
1786  089E  F5

```

; Convert character code

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Some entry points

	PAGE	28-2
3.44	01-Jan-85	
1787 089F 21 FCA6	LD HL,GRPHED	;Preceeded by a header byte
1788 08A2 AF	XOR A	
1789 08A3 BE	CP (HL)	
1790 08A4 77	LD (HL),A	;Clear this since seen
1791 08A5 28 0D	JR Z,CNVCH3	;No
1792 08A7 F1	POP AF	
1793 08A8 D6 40	SUB 01000000B	;Get rid of offset
1794 08AA FE 20	CP '	;Valid range
1795 08AC 38 04	JR C,CNVCH2	;Yes
1796 08AE C6 40	ADD A,01000000B	;Compensate value
1797 08B0	CNVCH1:	
1798 08B0 BF	CP A	;Set Z flag
1799 08B1 37	SCF	;Make sure carry is cleared
1800 08B2	CNVCH2:	
1801 08B2 E1	POP HL	
1802 08B3 C9	RET	
1803 08B4	CNVCH3:	
1804	;	
1805 08B4 F1	POP AF	
1806 08B5 FE 01	CP 1	;Graphic header
1807 08B7 20 F7	JR NZ,CNVCH1	;No, do not modify
1808 08B9 77	LD (HL),A	;Set GRPHD flag
1809 08BA E1	POP HL	;Carry is clear indicating one more byte is required
1810 08BB C9	RET	
1811	SUBTTL - MSXIO - Output a character to CRT	

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Output a character to CRT

		3 . 44	01-Jan-85	PAGE	29
1812	08BC	CHPUT:			
1813	;				
1814	08BC	E5	PUSH	HL	
1815	08BD	D5	PUSH	DE	
1816	08BE	C5	PUSH	BC	
1817	08BF	F5	PUSH	AF	
1818	08C0	CD FDA4	CALL	H.CHPU	
1819	08C3	CD 0B9F	CALL	CHKSCR	
1820	08C6	30 12	JR	NC , POPALL	
1821	08C8	CD 0A2E	CALL	CKERCS	
1822	08CB	F1	POP	AF	
1823	08CC	F5	PUSH	AF	
1824	08CD	CD 08DF	CALL	CHPUT1	
1825	08D0	CD 09E1	CALL	CKDPCS	
1826	08D3	3A F3DD	LD	A , (CSRX)	
1827	08D6	3D	DEC	A	
1828	08D7	32 F661	LD	(TTYPOS) .A	
1829	08DA	POPALL:			
1830	08DA	F1	POP	AF	
1831	08DB	PBDHRT:			
1832	08DB	C1	POP	BC	
1833	08DB	D1	POP	DE	
1834	08DC	E1	POP	HL	
1835	08DD	C9	RET		
1836	08DE				
1837	08DF				
1838		;			
1839	08DF	CD 089D	CALL	CNVCHR	
1840	08E2	D0	RET	NC	
1841	08E3	4F	LD	C , A	
1842	08E4	20 0D	JR	NZ , CHPUT3	

;Convert character code
 ;Was a graphic header, wait for next
 ;Save character code in [C]
 ;Converted code, send as is

PAGE 29-1

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Output a character to CRT

```

1843 08E6 21 FCA7 LD HL,ESCCNT
1844 08E9 7E LD A,(HL)
1845 08EA A7 AND A
1846 08EB C2 098F JP NZ,INESC
1847 08EE 79 LD A,C
1848 08EF FE 20 CP -
1849 08F1 38 21 JR C,CNTPUT
1850 08F3 CHPUT3: LD HL,(CSRY)
1851 08F3 2A F3DC LD 7FH
1852 08F6 FE 7F CP ;Rbout
1853 08F8 CA 0AE3 JP Z,RBOUT
1854 08FB CD 0BE6 CALL PUTVRM
1855 08FE CD 0A44 CALL RIGHT
1856 0901 C0 RET NZ
1857 0902 AF XOR A
1858 0903 CD 0C2B CALL SETTRM
1859 0906 26 01 LD H,1 ;Unterminate this line
1860 0908 LF: LD ;Go to start of the next line
1861 1862 ;Line feed
1863 1864 CD 0A61 CALL DOWN
1865 090B C0 RET NZ
1866 090C CD 0A69 CALL STOCSR
1867 090F 2E 01 LD L,1 ;L:=window top line
1868 0911 C3 0A88 JP DELLN0 ;Scroll up by deleting the first line
1869 0914 CNTPUT: ;Following control codes are supported
1870 1871 ; ;7 Bell
1872 1873

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE      29-2
- MSXIO - Output a character to CRT          3.44      01-Jan-85

1874          ; 8 Back space
1875          ; 9 Tab
1876          ; 10 Line feed
1877          ; 11 Cursor home
1878          ; 12 Clear screen
1879          ; 13 Carriage return
1880          ;
1881          ; 27 Enter escape sequence
1882          ; 28 Cursor right
1883          ; 29 Cursor left
1884          ; 30 Cursor up
1885          ; 31 Cursor down
1886          ;
1887 0914    21 092D          LD       HL,JMPBC
1888 0917    0E 0C          LD       C,0CH
1889 0919    23          INDJMP: INC     HL
1890 0919    23          INDJMP: INC     HL
1891 091A    23          INDJMP: INC     HL
1892 091B    A7          AND     A
1893 091C    0D          DEC     C
1894 091D    F8          RET     M
1895 091E    BE          CP      (HL)
1896 091F    23          INC     HL
1897 0920    20  F7          JR      NZ,INDJMP
1898 0922    4E          LD      C,(HL)
1899 0923    23          INC     HL
1900 0924    46          LD      B,(HL)
1901 0925    2A F3DC          LD      HL,(CSRY)
1902 0928    CD 092D          CALL   JMPBC
1903 092B    AF          XOR    A
1904 092C    C9          RET

```

; Make sure carry is cleared
 ; Undefined function
 ; Found?
 ; No
 ; Get routine address in BC
 ;
 ; Jump to each routine with cursor pos
 ; Tell screen editor not to echo this character

(MSX ROM BASIC BIOS)	Macro-80	PAGE	29-3
- MSXIO - Output a character to CRT			
1905 092D	JMPBC:	3.44	01-Jan-85
1906 092F	;		
1907 092D	PUSH BC		
1908 092E	RET		
1909 ;	;		
1910 ;	;		
1911 ;	;		
1912 092F	CNTTBL:	DB	7
1913 092F	;	DW	BEEP
1914 0930	1113	DB	8
1915 0932	08	DW	BS
1916 0933	0A4C	DB	9
1917 0935	09	DW	TAB
1918 0936	0A71	DB	10
1919 0938	0A	DW	LF
1920 0939	0908	DB	11
1921 093B	0B	DW	CHOME
1922 093C	0A7F	DB	12
1923 093E	0C	DW	CLRTXT
1924 093F	077E	DB	13
1925 0941	0D	DW	CR
1926 0942	0A81	DB	27
1927 0944	1B	DW	ENTESC
1928 0945	0989	DB	28
1929 0947	1C	DW	ADVCUR
1930 0948	0A5B	DB	29
1931 094A	1D	DW	BS
1932 094B	0A4C	DB	30
1933 094D	1E	DW	UP
1934 094E	0A57	DB	31
1935 0950	1F	DW	;
			Function dispatch table

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Output a character to CRT

1936 0951 0A61 SUBTTL - DW DOWN
1937

Macro-80
01-Jan-85
PAGE 29-4

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Escape sequence handler

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74

```
1938
1939      0953      ESCTBL:    DB      "j"      ;Clear screen
1940      0953      DW      CLRXTXT
1941      0954      DB      "E"      ;Clear screen
1942      0956      DW      CLRXTXT
1943      0957      DB      "K"      ; To maintain compatibility with VT52
1944      0959      DB      "J"      ;Erase to end-of-line
1945      095A      DW      EOL
1946      095C      DB      "J"      ;Erase to end-of-page
1947      095D      DW      EOP
1948      095F      DB      "1"      ;Erase entire line
1949      0960      DW      ELN
1950      0962      DB      "L"      ;Insert a line
1951      0963      DW      LN
1952      0965      DB      "M"      ;Delete a line
1953      0966      DW      DLN
1954      0968      DB      "Y"      ;Locate cursor
1955      0969      DW      LOC
1956      096B      DB      "A"      ;Cursor up
1957      096C      DW      UP
1958      096E      DB      "B"      ;Cursor down
1959      096F      DW      DOWN
1960      0971      DB      "C"      ;Cursor right
1961      0972      DW      RIGHT
1962      0974      DB      "D"      ;Cursor left
1963      0975      DW      LEFT
1964      0977      DB      "H"      ;Cursor home
1965      0978      DW      CSHOME
1966      097A      DB      "X"      ;Set modes
1967      097B      DW      SETMOD
1968      0980      DB      "Y"      ;Reset modes
```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   30-1
- MSXIO - Escape sequence handler          3.44    01-Jan-85

1969  097E  0983      SETMOD:           DW     RSTMOD
1970  0980
1971
1972
1973
1974  0980  3E 01      ; Function dispatch table
1975  0982  01         LD     A,1
                         DB     1
1976  0983      RSTMOD:           LD     A,2
1977  0983  3E 02      DB     1
1978  0985  01
1979  0986
1980  0986  3E 04      LOC:             LD     A,4
1981  0988  01         DB     1
1982  0989      ENTESC:          LD     A,0FFH
1983  0989  3E FF      ;Say row is expected next
1984  098B  32 FCA7    ;'LXI B' instruction
1985  098E  C9         LD     (ESCCNT),A
                         LD     RET

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44   01-Jan-85   PAGE   31
- MSXIO - Escape sequence handler

1986
1987    098F           INESC:          ;
1988
1989    098F    F2 099D      JP P,INESC1      ;Arguments expected
1990    0992    36 00      LD (HL),0      ;Exit from escape sequence
1991    0994    79         LD A,C        ;Restore character
1992    0995    21 0951      LD HL,ESCTBL-2
1993    0998    0E 0F      LD C,0FH       ;Number of ESC handler entries
1994    099A    C3 0919      JP INDJMP

1995    099D           INESC1:         ;
1996
1997    099D    3D         DEC A          ;Set modes?
1998    099E    28 1E      JR Z,GOSET     ;Yes
1999    09A0    3D         DEC A          ;Reset modes?
2000    09A1    28 25      DEC A          Z,GORSET
2001    09A3    3D         DEC A          ;
2002    09A4    77         LD (HL),A      ;Update ESCCNT
2003    09A5    3A F3B0      LD A,(LINLEN)  ;Assume column expected
2004    09A8    11 F3DD      LD DE,CSRX    ;
2005    09AB    28 06      JR Z,INESC2   ;Column expected
2006    09AD    36 03      LD (HL),3      ;
2007    09AF    CD 0C32      CALL GETLEN   ;Row expected
2008    09B2    1B         DEC DE        ;Point CSRY
2009    09B3           INESC2:         ;
2010    09B3    47         LD B,A        ;Get max limit in B
2011    09B4    79         LD A,C        ;Restore character
2012    09B5    D6 20      SUB '          ;0-xx
2013    09B7    B8         CP B          ;
2014    09B8    3C         INC A          ;
2015    09B9    12         LD (DE),A    ;Legal value
2016    09BA    D8         RET C          ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85      PAGE    31-1
- MSXIO - Escape sequence handler

2017  09BB   78      LD      A,B      ;Substitute by possible largest value
2018  09BC   12      LD      (DE),A
2019  09BD   C9      RET
2020  09BE   GOSET:
2021
2022
2023
;
; Set various modes
;
2024  09BE   77      LD      (HL),A      ;Exit from escape sequence
2025  09BF   79      LD      A,C      ;Restore character
2026  09C0   D6 34    SUB   '4'      ;Block cursor?
2027  09C2   28 0B    JR      Z,STSTYL
2028  09C4   3D      DEC   A        ;Yes
2029  09C5   28 0F    JR      Z,STCSSW
2030  09C7   C9      RET
2031  09C8   GORSET:
2032
2033
2034
;
; Reset various modes
;
2035  09C8   77      LD      (HL),A      ;Exit from escape sequence
2036  09C9   79      LD      A,C      ;Restore character
2037  09CA   D6 34    SUB   '4'      ;Underscore cursor?
2038  09CC   20 05    JR      NZ,RSET10
2039  09CE   3C      INC   A        ;No, try next
2040  09CF   32 FCAA  STSTYL: LD      (CSTYLE),A
2041  09D2   C9      RET
2042  09D3   RSET10:
2043
2044
;
2045  09D3   3D      DEC   A        ;Cursor on?
2046  09D4   C0      RET   NZ      ;No, unimplemented feature
2047  09D5   3C      INC   A

```

```

PAGE 31-2

MSX ROM BASIC BIOS ) Macro-80
MSXIO - Escape sequence handler

2048 09D6 STCSSW: LD (CSRSW),A
2049 09D6 32 FCA9
2050 09D9 C9 CKDPC0: RET
2051 09DA ; Display cursor if disabled
2052
2053
2054 ; Display cursor if disabled
2055 09DA 3A FCA9 LD A, (CSRSW)
2056 09DD A7 AND A
2057 09DE C0 RET NZ
2058 09DF 18 05 JR DSPCSR
2059 09E1 CKDPCS: ; Display cursor if enabled
2060
2061 ; Display cursor if enabled
2062 ; Display cursor if enabled
2063 09E1 3A FCA9 LD A, (CSRSW)
2064 09E4 A7 AND A
2065 09E5 C8 RET Z
2066 09E6 DSPCSR: ; Display a cursor
2067
2068
2069 ; Display a cursor
2070 09E6 CD FD99 CALL H,DSPC
2071 09E9 CD 0B9F CALL CHKSCR
2072 09EC D0 RET NC ;Get current
2073 09ED 2A F3DC LD HL,(CSRY) ;Save it for
2074 09F0 E5 PUSH HL ;Get VRM
2075 09F1 CD 0BD8 CALL (CODSAV),A ;Remember th
2076 09F4 32 FBCC LD L,A ;Then read pa
2077 09F7 6F LD LD,0 ;Display cursor
2078 09F8 26 00 LD LD,0

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44    01-Jan-85      PAGE   31-3
- MSXIO - Escape sequence handler

2079  09FA    29          ADD     HL,HL      ; [A] * 8
2080  09FB    29          ADD     HL,HL
2081  09FC    29          ADD     HL,HL
2082  09FD    EB          EX      DE,HL
2083  09FE    2A F924    LD      HL,(CGPBAS)
2084  0A01    E5          PUSH   HL
2085  0A02    19          ADD     HL,DE
2086  0A03    CD 0BA5    CALL   GET8B
2087  0A06    21 FC1F    LD      HL,BUFEND+7 ; Make a complement of this pattern
2088  0A09    06 08      LD      B,8       ; Assume full reverse cursor
2089  0A0B    3A FCAA    LD      A,(CSTYLE)
2090  0A0E    A7          AND    A
2091  0A0F    28 02      JR      Z,DSPCSI ; Good assumption
2092  0A11    06 03      LD      B,3       ; No, reverse bottom 3 lines only
2093  0A13    7E          DSPCSI: LD     A,(HL)
2094  0A13    7E          LD      A,(HL)
2095  0A14    2F          CPL   (HL),A
2096  0A15    77          LD      HL
2097  0A16    2B          DEC   HL
2098  0A17    10 FA      DJNZ  DSPCSI
2099  0A19    E1          POP   HL      ; Assign this pattern to 255
2100  0A1A    01 07F8    LD      BC,07F8H
2101  0A1D    09          ADD   HL,BC
2102  0A1E    CD 0BBE    CALL  PUT8B
2103  0A21    E1          POP   HL      ; Restore cursor position
2104  0A22    0E FF      LD      C,0FFH
2105  0A24    C3 0BE6    JP      PUTVRM ; Get code for cursor
2106  0A27    ; Set it at cursor position
2107  ; Erase cursor if disabled
2108  ;
2109  ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   31-4
- MSXIO - Escape sequence handler

2110  0A27  3A FCA9                      LD      A, (CSR$W)
2111  0A2A  A7                           AND    A
2112  0A2B  C0                           RET    NZ
2113  0A2C  18  05                      JR     ERACSR
2114  0A2E
2115          CKERCS:                   ; Erase cursor if enabled
2116          ; 
2117          ; 
2118  0A2E  3A FCA9                      LD      A, (CSR$W)
2119  0A31  A7                           AND    A
2120  0A32  C8                           RET    Z
2121  0A33
2122          ERACSR:                   ; Erase cursor
2123          ; 
2124          ; 
2125  0A33  CD FDAE                      CALL   H.ERAC
2126  0A36  CD 0B9F                      CALL   CHK$CR
2127  0A39  D0                           RET    NC
2128  0A3A  2A F3DC                      LD     HL, (CSR$Y)
2129  0A3D  3A FBCC                      LD     A, (CODSAV)    ;Get old code
2130  0A40  4F                           LD     C,A
2131  0A41  C3  0BE6                      JP     PUTVRM
2132          ; Restore old code
2133          ; 

```

; SUBTTL - MSXIO - Cursor movements

(MSX ROM BASIC BIOS) Macro-80
- MSX10 - Cursor movements

```

2134      0A44      RIGHT:
2135
2136
2137
2138      0A44      ; Cursor right
2139      0A44      ; LD      A, (LINLEN)
2140      0A47      BC      CP      H
2141      0A48      C8      RET     Z
2142      0A49      24      INC     H
2143      0A4A      18      1D      JR      STOCSSR
2144      0A4C      BS:
2145
2146
2147      ; Back space
2148      0A4C      CD      0A55      LEFT:
2149      0A4F      C0      CALL    NZ
2150      0A50      3A      F3B0      ;Not at left-end
2151      0A53      67      LD      A, (LINLEN)
2152      0A54      11      LD      H,A
2153      0A55      LEFT:   DB      11H      ; 'LXI D,' instruction
2154
2155      ; Cursor left
2156
2157      0A55      25      DEC     H
2158      0A56      3E      DB      3EH      ;Are we at the left-end of line?
2159      0A57      UP:    -
2160
2161
2162      0A57      2D      DEC     L
2163      0A58      C8      RET     Z
2164

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   32-1
- MSXIO - Cursor movements

2165  0A59    18 0E          JR      STOCSR
2166  0A5B          ADVCUR:
2167          ; Advance cursor
2168          ; cursor down
2169          ; cursor down
2170  0A5B    CD 0A44          CALL    RIGHT
2171  0A5E    C0          RET     NZ
2172  0A5F    26 01          LD      H,1
2173  0A61          DOWN:
2174          ; cursor down
2175          ; cursor down
2176          ; cursor down
2177  0A61    CD 0C32          CALL    GETLEN
2178  0A64    BD          CP      L
2179  0A65    C8          RET     Z
2180  0A66    38 05          JR      C,DOWN1
2181  0A68    2C          INC
2182  0A69          STOCSR:
2183  0A69    22 F3DC          LD      (CSRY),HL
2184  0A6C    C9          RET
2185  0A6D          DOWN1:
2186          ; cursor down
2187  0A6D    2D          DEC    L
2188  0A6E    AF          XOR    A
2189  0A6F    18 F8          JR      STOCSR
2190  0A71          TAB:
2191          ; Tabulation
2192          ; Tabulation
2193          ; Tabulation
2194  0A71    3E 20          LD      A,' '
2195  0A73    CD 08DF          CALL    CHPUTL

```

				PAGE
(MSX ROM BASIC BIOS)	Macro-80	3 . 44	01-Jan-85	32-2
- MSXIO -	CURSOR movements			
2196 0A76	3A F3DD	LD	A, (CSRX)	
2197 0A79	3D	DEC	A	
2198 0A7A	E6 07	AND	7	
2199 0A7C	20 F3	JR	NZ,TAB	
2200 0A7E	C9	RET		
2201 0A7F		CSHOME:		
2202		i	Cursor home	
2203		;		
2204		;		
2205 0A7F	2E 01	LD	L,1	
2206 0A81		CR:		
2207		;		
2208		;	Carriage return	
2209		;		
2210 0A81	26 01	LD	H,1	
2211 0A83	18 E4	JR	STOCSR	
2212		;		
2213		SUBTTL	- MSXIO - Line insert and delete of CRT	

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Line insert and delete of CRT

```

2214      0A85          DLN:
2215      0A85          ; Delete a line specified by [L]
2216      0A85          ; Cursor should be set at the top of line
2217      0A85          ; cursor
2218      0A85          ; cursor
2219      0A85          ; cursor
2220      0A85          ; cursor
2221      0A85          CD 0A81          CALL    CR
2222      0A88          CD 0C32          CALL    GETLEN
2223      0A88          CD 0C32          CALL    L
2224      0A8B          95              SUB    C
2225      0A8C          D8              RET    Z,ELN
2226      0A8D          CA 0AEC          JP    HL
2227      0A90          E5              PUSH   AF
2228      0A91          F5              PUSH   AF
2229      0A92          4F              LD    C,A
2230      0A93          06 00          LD    B,0
2231      0A95          CD 0C1D          CALL    GETTRM
2232      0A98          6B              LD    L,E
2233      0A99          62              LD    H,D
2234      0A9A          23              INC   HL
2235      0A9B          ED B0          LDIR
2236      0A9D          21  FBCA          LD    HL,FSTPOS
2237      0AA0          35              DEC   (HL)
2238      0AA1          F1              POP   AF
2239      0AA2          E1              POP   HL
2240      0AA3          F5              DELLN1:
2241      0AA3          F5              PUSH   AF
2242      0AA4          2C              INC   L
2243      0AA5          CD 0BAA          CALL   GETLN
2244      0AA8          2D              DEC   L

```

; Save counter
; Save counter
; Get 1 line specified by L

```

( MSX ROM BASIC BIOS ) Macro-80          3.44
- MSXIO - Line insert and delete of CRT      PAGE    33-1

2245    0AA9    CD 0BC3           CALL    PUTLN      ;Put 1 line specified by L
2246    0AAC    2C               INC     L
2247    0AAD    F1               POP    AF          ;Restore counter
2248    0AAE    3D               DEC     A
2249    0AAF    20 F2           DEC     NZ,DELLNL   ;Blank bottom line
2250    0AB1    C3 0AEC          JP      ELN
2251    0AB4    ILN:             ; Insert a line
2252    ; Insert a line
2253    ; Cursor should be set at the top of line
2254    ; Cursor should be set at the top of line
2255    ; Cursor should be set at the top of line
2256    ; Cursor should be set at the top of line
2257    0AB4    CD 0A81           CALL    CR
2258    0AB7    CD 0C32           CALL    GETLEN     ;Get an actual height of sc
2259    0AB7    CD 0C32           CALL    LD     H,A
2260    0ABA    67               LD     H,A
2261    0ABB    95               SUB    L
2262    0ABC    D8               RET    C          ;Something is wrong!!
2263    0ABD    CA 0AEC          JP      Z,ELN
2264    0AC0    6C               LD     L,H
2265    0AC1    E5               PUSH   HL          ;Save row to be inserted
2266    0AC2    F5               PUSH   AF          ;Save # of lines to be move
2267    0AC3    4F               LD     C,A
2268    0AC4    06 00           LD     B,0
2269    0AC6    CD 0C1D          CALL   GETTRM    ;Form source address
2270    0AC9    6B               LD     L,E
2271    0ACA    62               LD     H,D
2272    0ACB    E5               PUSH   HL          ;Save pointer to [LINTTB] f
2273    0ACC    2B               DEC    HL
2274    0ACD    ED B8           POP    HL
2275    0ACF    E1

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   33-2
- MSXIO - Line insert and delete of CRT

2276 0ADD 74           LD      (HL),H
2277 0AD1 F1           POP    AF
2278 0AD2 E1           POP    HL
2279 0AD3             INSLNL: ;Make sure the bottom line is terminated
2280 0AD3 F5           PUSH   AF
2281 0AD4 2D           DEC    L
2282 0AD5 CD 0BAA       CALL   GETLN
2283 0AD8 2C           INC    L
2284 0AD9 CD 0BC3       CALL   PUTLN
2285 0ADC 2D           DEC    L
2286 0ADD F1           POP    AF
2287 0ADE 3D           DEC    A
2288 0ADF 20 F2         JR    NZ,INSLNL
2289 0AE1 18 09         JR    ELN
2290                   ;
2291                   ; SUBTTL - MSXIO - Character(s) erase

```

```

( MSX ROM BASIC BIOS ) Macro-80      PAGE    34
- MSXIO - Character(s) erase

2292          0AE3          RUBOUT:
2293          0AE3          ; Erase previous character
2294          0AE3          ; Erase previous character
2295          CD 0A4C          ; Back space
2296          CD 0A4C          ; We're at the top of screen
2297          0AE3          C8          ; Overstrike with a space
2298          0AE6          C8          ; Back space
2299          0AE7          0E 20        ; Cursor should remain unchanged
2300          0AE9          C3 0BE6        ; Cursor should remain unchanged
2301          0AEC          ELN:         ; Cursor should remain unchanged
2302          ; Erase entire line
2303          ; Cursor should remain unchanged
2304          ; Cursor should remain unchanged
2305          ; Cursor should remain unchanged
2306          0AEC          26 01          ; Cursor should remain unchanged
2307          0AEC          0AEE          ; Cursor should remain unchanged
2308          0AEE          EOL:          ; Erase to end-of-line
2309          ; Cursor should remain unchanged
2310          ; Cursor should remain unchanged
2311          ; Cursor should remain unchanged
2312          ; Cursor should remain unchanged
2313          ; Cursor should remain unchanged
2314          0AEE          CD 0C29          TERMIN
2315          0AF1          E5          PUSH HL
2316          0AF2          CD 0BF2          CALL VADDR
2317          0AF5          CD 07DF          CALL SETWRT
2318          0AF8          E1          POP HL
2319          0AF9          3E 20          ; Restore current position
2320          0AF9          3E 20          LD A, ' '
2321          0AFB          D3 98          OUT (VDP.DRW),A
2322          0AFD          24          INC H

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Character(s) erase

```

2323 0A9E 3A F3B0      LD    A,(LINLEN)
2324 0B01 BC          CP    H
2325 0B02 30 F5      JR    NC,ERFOLL
2326 0B04 C9          RET
2327 0B05 EOP:
2328 ;           ; Erase to end-of-page
2329 ;           ; Cursor should remain unchanged
2330 ;
2331 ;
2332 ;
2333 0B05 E5          PUSH   HL
2334 0B06 CD 0AEE      CALL   EOL
2335 0B09 E1          POP    HL
2336 0B0A CD 0C32      CALL   GETLEN
2337 0B0D BD          CP    L
2338 0B0E D8          RET    C
2339 0B0F C8          RET    Z
2340 0B10 26 01          LD    H,1
2341 0B12 2C          INC    L
2342 0B13 18 F0          JR    EOP
2343 ;           ; SUBTTL - MSXIO - Function keys display/erase.
2344

```

```

LD    A,(LINLEN)
CP    H
JR    NC,ERFOLL
RET
;
```

```

; Save current position
; Erase to end-of-line
; Restore current position
; Get an actual height of CRT
;
```

```

CP    L
RET   C
RET   Z
LD    H,1
INC    L
JR    EOP
;
```

SUBTTL - MSXIO - Function keys display/erase.

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Function keys display/erase.

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```

2345 0B15 ERAFNK:          ; Erase function key
2346 0B15 ; Erase function key
2347 ; Erase function key
2348 ; Erase function key
2349 ; Erase function key
2350 0B15 CD FDB8          ; Say no function key is displayed
2351 0B18 AF               ; H.ERAF
2352 0B19 CD 0B9C          ; XOR A
2353 0B1C D0               ; CALL SETCHK
2354 0B1D E5               ; RET NC
2355 0B1E 2A F3B1          ; PUSH HL
2356 0B21 CD 0AEC          ; Save possible text pointer
2357 0B24 E1               ; LD HL,(CRTCNT)
2358 0B25 C9               ; ERASE last line
2359 0B26 FNKSB:          ; CALL ELN
2360 ; Restore possible text pointer
2361 ; Display function key if enabled
2362 ; Display function key if enabled
2363 0B26 3A F3DE          ; Now being displayed?
2364 0B29 A7               ; A, (CNSDFG)
2365 0B2A C8               ; AND A
2366 0B2B DSPFNK:          ; RET Z
2367 ; No
2368 ; Display function key
2369 ; Display function key
2370 0B2B CD FDB3          ; Call H.DSPF
2371 0B2E 3E FF             ; LD A,0FFH
2372 0B30 CD 0B9C          ; CALL SETCHK
2373 0B33 D0               ; RET NC
2374 0B34 E5               ; PUSH HL
2375 0B35 3A F3DC          ; A, (CSRY)

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   35-1
- MSXIO - Function keys display/erase.      90

2376    0B38    21 F3B1    LD     HL,CRTCNT
2377    0B3B    BE        CP     (HL)
2378    0B3C    3E 0A    LD     A,0AH
2379    0B3E    20 01    LD     NZ,NTBOTM
2380    0B40    DF        JR     18H
2381    0B41    NTBOTM:  RST
2382    0B41    3A FBEB    LD     A,(SFTKEY) ;Get current shift status
2383    0B44    0F        RRCA
2384    0B45    21 F87F    LD     HL,FNKSTR ;Assume shift not pressed
2385    0B48    3E 01    LD     A,1
2386    0B4A    38 04    LD     C,DSPFK1 ;Good assumption
2387    0B4C    21 F8CF    JR     HL,FNKSTR+80
2388    0B4F    AF        LD     A
2389    0B50    DSPEK1:  XOR
2390    0B50    32 FBCD    LD     (FNKSWI),A ;Mark which part of function key is displayed
2391    0B53    11 FC18    LD     DE,BUFFEND ;Set temporary destination
2392    0B56    D5        PUSH
2393    0B57    06 28    DE     DE
2394    0B59    3E 20    PUSH
2395    0B5B    DSFKCL:  DE     B,'(' ;=40
2396    0B5B    12        LD     A
2397    0B5C    13        LD     (DE),A
2398    0B5D    10 FC    INC
2399    0B5F    D1        DJNZ
2400    0B60    0E 05    DSFKCL
2401    0B62    3A F3B0    POP
2402    0B65    D6 04    DE
2403    0B67    38 2B    LD     C,5 ;Restore temporary destination in [DE]
2404    0B69    06 FF    LD     A,(LINLEN) ;Total number of keys
2405    0B6B    04        LD     SUB 4 ;Calculate (LINLEN-4) / 5
2406    0B6B    INC      JR     C,DSPFKE ;Not enough room for function keys
                                         LD     B,0FFH
                                         INC      B

```

PAGE 35-2	01-Jan-85	3.44	Macro-80	MSX ROM BASIC BIOS	MSXIO - Function keys display/erase.
				{	-
				0B6C D6 05	0B6E 30 FB
				SUB 5	SUB 5
				JR NC,DSPFK4	JR NC,DSPFK4
				LD A,B	LD A,B
				AND A	AND A
				JR Z,DSPFKE	JR Z,DSPFKE
				DB 3EH	DB 3EH
				DSPFK2:	DSPFK2:
				INC DE	INC DE
				PUSH BC	PUSH BC
				LD C,0	LD C,0
				DSPFK5:	DSPFK5:
				LD A,(HL)	LD A,(HL)
				INC HL	INC HL
				INC C	INC C
				CALL CNVCHR	CALL CNVCHR
				JR NC,DSPFK5	JR NC,DSPFK5
				JR NZ,DSPFK8	JR NZ,DSPFK8
				CP '	CP '
				JR C,DSPFK6	JR C,DSPFK6
				DSPFK8:	DSPFK8:
				LD (DE),A	LD (DE),A
				DSPFK6:	DSPFK6:
				INC DE	INC DE
				DJNZ DSPFK5	DJNZ DSPFK5
				LD A,10H	LD A,10H
				SUB C	SUB C
				LD C,A	LD C,A
				ADD HL,BC	ADD HL,BC
				POP BC	POP BC
				DEC C	DEC C
				JR NZ,DSPFK2	JR NZ,DSPFK2
				2427 0B87 12	2427 0B87 12
				2428 0B88 13	2428 0B88 13
				2429 0B88 10 EE	2429 0B88 10 EE
				2430 0B89 3E 10	2430 0B89 3E 10
				2431 0B8B 91	2431 0B8B 91
				2432 0B8D 4F	2432 0B8D 4F
				2433 0B8E 09	2433 0B8E 09
				2434 0B8F C1	2434 0B8F C1
				2435 0B90 0D	2435 0B90 0D
				2436 0B91 20 E1	2436 0B91 20 E1
				2437	

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Function keys display/erase.

```

2438 0B94      DSPFKE: LD    HL,(CRTCNT) ;Display at the lowest line
2439 0B94      2A F3B1
2440 0B97      CD 0BC3   CALL PUTLN
2441 0B9A      E1          POP  HL      ;Restore possible text pointer
2442 0B9B      C9          RET
2443
2444 ; SUBTTL - MSXIO - Low level routines

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    36
- MSXIO - Low level routines

2445
2446 0B9C      SETCHK:           ; Set CNSDFG and check current screen mode
2447
2448
2449 0B9C      32 F3DE      ; Set CNSDFG and check current screen mode
2450 0B9F      LD (CNSDFG),A
2451 0B9F      CHKSCR:           ; Check current screen mode
2452
2453
2454 0B9F      3A FCAF      ; Check current screen mode
2455 0BA2      FE 02      LD A,(SCRMOD)
2456 0BA4      C9          CP 2
2457 0BA4      RET         ; Return with the status
2458 0BA5      GET8B:           ; Get 8 bytes from HL
2459
2460
2461
2462 0BA5      E5          PUSH HL
2463 0BA6      0E 08      LD C,8
2464 0BA8      18 0A      JR GET1LL
2465 0BAA      GET1LN:           ; Get character and attribute of position specified by HL
2466
2467
2468
2469
2470
2471 0BAA      E5          PUSH HL
2472 0BAB      26 01      LD H,L
2473 0BAD      CD 0BF2      CALL VADDR
2474 0BB0      3A F3B0      LD A,(LINLEN)
2475 0BB3      4F          LD C,A

```

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	36-1
- MSXIO - Low level routines					
2476 0BB4	06 00	GETILL:	LD B,0		
2477 0BB4	11 FC18		LD DE,BUFEND		
2478 0BB6	CD 070F		CALL LDIRMV		
2479 0BB9	E1		POP HL		
2480 0BBC	C9		RET		
2481 0BBD		PUT8B:			
2482 0BBE		;	PUSH HL		
2483 0BBE	E5		LD C,8		
2484 0BBE	0F 08		PUTILL1		
2485 0BBF	0C1 18		JR		
2486 0BC1	0A	PUTILN:			
2487 0BC3		;	PUSH HL		
2488 0BC3	E5		LD H,1		
2489 0BC4	26 01		CALL VADDR		
2490 0BC4	CD 0BF2		LD A,(LINLEN)		
2491 0BC6	3A F3B		LD C,A		
2492 0BC9	4F		PUTILL1:		
2493 0BCC	0BCD	06 00	LD B,0		
2494 0BCD	EB		EX DE,HL		
2495 0BCD	00		LD HL,BUFEND		
2496 0BCF			CALL LDIRMV		
2497 0BD0	21 FC18		POP HL		
2498 0BD3	CD 0744		RET		
2499 0BD6	E1				
2500 0BD7	C9	GETVRM:			
2501 0BD8		;	PUSH HL		
2502 0BD8	E5		CALL VADDR		
2503 0BD8	0BF2		CALL SETRD		
2504 0BD9	CD 07EC		EX (SP),HL		
2505 0BDC					
2506 0BDF	E3				

;Save coordinate
;Calculate VRAM address
;Set up VDP for read

```

( MSX ROM BASIC BIOS ) Macro-80      3 .44    01-Jan-85      PAGE   36-2
- MSXIO - Low level routines

2507  0BE0    E3          EX      (SP),HL
2508  0BE1    DB 98       IN      A,(VDP.DRW) ;Get character code in C
2509  0BE3    4F          LD      C,A
2510  0BE4    E1          POP    HL
2511  0BE5    C9          RET
2512  0BE6    PUTVRM:    ;Restore coordinate
2513
2514  0BE6    E5          PUSH   HL
2515  0BE7    CD 0BF2     CALL   VADDR
2516  0BEA    CD 07DF     CALL   SETWRIT
2517  0BED    79          LD      A,C
2518  0BEE    D3 98       OUT   (VDP.DRW),A
2519  0BF0    E1          POP    HL
2520  0BF1    C9          RET
2521  0BF2    VADDR:    ; calculate buffer address out of H,L (column, row)
2522
2523
2524
2525
2526
2527  0BF2    C5          PUSH   BC
2528  0BF3    5C          LD      E,H
2529  0BF4    26 00       LD      H,0
2530  0BF6    54          LD      D,H
2531  0BF7    2D          DEC   L
2532  0BF8    29          ADD   HL,HL
2533  0BF9    29          ADD   HL,HL
2534  0BFA    29          ADD   HL,HL
2535  0BFB    4D          LD      C,L
2536  0BFC    44          LD      B,H
2537  0BFD    29          ADD   HL,HL

```

(MSX ROM BASIC BIOS)	Macro-80	PAGE	36-3
- MSXIO - Low level routines			
2538 0BFF	29	ADD	HL, HL
2539 0BFF	19	ADD	HL, DE
2540 0C00	3A FCAF	LD	A, (SCRMOD)
2541 0C03	A7	AND	A
2542 0C04	3A F3B0	LD	A, (LINLEN)
2543 0C07	28 04	JR	Z, VADDR1
2544 0C09	D6 22	SUB	" "
2545 0C0B	18 03	JR	VADDR2
2546 0C0D		VADDR1:	
2547	;		
2548 0C0D	09	ADD	HL, BC
2549 0C0E	D6 2A	SUB	41+1
2550 0C10		VADDR2:	
2551 0C10	2F	CPL	
2552 0C11	A7	AND	A
2553 0C12	1F	RR A	
2554 0C13	5F	LD	E, A
2555 0C14	19	ADD	HL, DE
2556 0C15	EB	EX	DE, HL
2557 0C16	2A F922	LD	HL, (NAMBAS)
2558 0C19	19	ADD	HL, DE
2559 0C1A	2B	DEC	HL
2560 0C1B	C1	POP	BC
2561 0C1C	C9	RET	
2562 0C1D		GETTRM:	
2563		;	Get value of line-terminator-table and affect flags
2564		;	
2565		;	Entry: L has the line #
2566		;	Exit: DE has the address of corresponding terminator byte.
2567		;	z flag is affected.
2568			

```

( MSX ROM BASIC BIOS ) Macro-80      PAGE   36-4
- MSXIO - Low level routines          3.44    01-Jan-85

2569          ;                                ; Save HL
2570  0C1D  E5          PUSH    HL
2571  0C1E  11 FBB1     LD      DE,BASROM
2572  0C21  26 00      LD      H,0           ;Get address of table
2573  0C23  19          ADD     HL,DE
2574  0C24  7E          LD      A,(HL)
2575  0C25  EB          EX      DE,HL
2576  0C26  E1          POP    HL
2577  0C27  A7          AND    A
2578  0C28  C9          RET
2579  0C29          TERMIN:             ;Affect flags
2580          ;                                ;Load non 0 value in Acc
2581  0C29  3E          DB      3EH
2582  0C2A          UNTERM:            ;Load non 0 value in Acc
2583  0C2A  AF          XOR    A
2584  0C2B          SETTRM:            ;Get address of terminator byte in DE
2585  0C2B  F5          PUSH    AF
2586  0C2C  CD 0C1D    CALL    GETTRM
2587  0C2F  F1          POP    AF
2588  0C30  12          LD      (DE),A
2589  0C31  C9          RET
2590  0C32          GETLEN:            ;Change table
2591          ;                                ;Get an actual height of screen
2592          ;                                ;Get an actual height of screen
2593          ;                                ;0 or -1
2594  0C32  3A F3DE    LD      A,(CNSDFG)
2595  0C35  E5          PUSH    HL
2596  0C36  21 F3B1    LD      HL,CRTCNT
2597  0C39  86          ADD    A,(HL)
2598  0C3A  E1          POP    HL
2599  0C3B  C9          RET

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Low level routines

2600 ;
2601 SUBTTL - MSXIO - Keyboard encoding routines

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```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85      PAGE    37      99
- MSXIO - Keyboard encoding routines

2602          0C3C          KEYINT:
2603          ; KEYINT:
2604          ;   i
2605          ;     ; Encode keyboard
2606          ;   i
2607          ;     ; Timer interrupt routine
2608          ;   i
2609          0C3C          E5          PUSH    HL
2610          0C3D          D5          PUSH    DE
2611          0C3E          C5          PUSH    BC
2612          0C3F          F5          PUSH    AF
2613          0C40          D9          EXX
2614          0C41          08          EX      AF,AF'
2615          0C42          E5          PUSH    HL
2616          0C43          D5          PUSH    DE
2617          0C44          C5          PUSH    BC
2618          0C45          F5          PUSH    AF
2619          0C46          FD          E5          PUSH    IY
2620          0C48          DD          E5          PUSH    IX
2621          0C4A          CD          FD9A        H.KEYI
2622          0C4D          DB          99          CALL    A,(VDP.SR)
2623          0C4F          A7          AND     A
2624          0C50          F2          0D02        IN      A, (VDP.SR)
2625          0C53          CD          FD9F        AND     A
2626          0C56          FB          EI          JP      P, INTRET
2627          0C56          FB          EI          CALL    H.TIMI
2628          ; Now that it became obvious that VDP
2629          ; generated the interrupt, we re-enable
2630          ; interrupt here to allow RS232C's
2631          0C57          32          F3E7        LD      (STATFL),A
2632          0C5A          E6          20          AND     ,

```

;Store this new status
;Collision detected?

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Keyboard encoding routines

```

2633 0C5C   21 FC6D      LD    HL,TRPTBL+33  PAGE   3.44
2634 0C5F   C4 0EFL      CALL  NZ,REQTRP
2635 ; Assume so
2636 ; Request trap if so
2637 ; Check interval trap
2638 0C62   2A FCA2      LD    HL,(INTCNT) ;Count down interval count
2639 0C65   2B           DEC   HL
2640 0C66   7C           LD    A,H
2641 0C67   B5           OR    L
2642 0C68   20 09        JR    NZ,NTINTT ;Not yet reached 0
2643 0C6A   21 FC7F      LD    HL,TRPTBL+3*17 ;Request trap
2644 0C6D   CD 0EFL      CALL  REQTRP
2645 0C70   2A FCA0      LD    HL,(INTVAL) ;Load initial value
2646 0C73   22 FCA2      NTINTT: LD    (INTCNT),HL ;Update interval count
2647 0C73   22 FCA2      ; Increment jiffy count
2648 ; Increment jiffy count
2649 ; Increment jiffy count
2650 0C76   2A FC9E      LD    HL,(JIFFY)
2651 0C76   23           INC   HL
2652 0C79   22 FC9E      LD    (JIFFY),HL
2653 0C7A   22           ; Check music queue
2654 ; Check music queue
2655 ; Check music queue
2656 0C7D   3A FB3F      LD    A,(MUSICF) ;Check music flag
2657 0C80   4F           LD    C,A
2658 0C81   AF           XOR   A
2659 ; Start with queue 0
2660 0C82   MUSINT: RR    C
2661 0C82   CB 19        PUSH  AF
2662 0C84   F5           PUSH  BC
2663 0C85   C5           PUSH  BC

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Keyboard encoding routines

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PAGE

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```

2664    0C86   DC 113B          CALL   C,ACTION
2665    0C89   C1              POP    BC
2666    0C8A   F1              POP    AF
2667    0C8B   3C              INC    A
                                ;Next queue
                                ;All done?
                                ;Not yet
2668    0C8C   FE 03          CP    3
                                ;MUSINT
2669    0C8E   38 F2          JR    C,SCNCNT
2670    0C90   21 F3F6          LD    HL,SCNCNT
2671    0C93   35              DEC   (HL)
                                ;Need to scan?
2672    0C94   20 6C          JR    NZ,INTRET
2673    0C96   36 03          LD    (HL),3
                                ;No, return soon
                                ;Time delay of first repeat
2674
2675
2676
2677    0C98   AF              XOR   A
2678    0C99   CD 120C          CALL   SLSTCK
                                ;Read joystick A
2679    0C9C   E6 30          AND   00110000B
2680    0C9E   F5              PUSH  AF
2681    0C9F   3E 01          LD    A,1
                                CALL   SLSTCK
2682    0CA1   CD 120C          AND   '0'
2683    0CA4   E6 30          RLCA
                                RLCA
2684    0CA6   07              POP   BC
2685    0CA7   07              OR    B
2686    0CA8   C1              PUSH  AF
                                CALL   GTROW8
2687    0CA9   B0              AND   1
2688    0CAA   F5              POP   BC
2689    0CAB   CD 1226          OR    B
                                C,A
2690    0CAE   E6 01          LD    HL,TRGFLG
2691    0CBO   C1              OR    B
2692    0CBL   B0              LD    C,A
                                ;Save this
2693    0CB2   4F              LD    HL,TRGFLG
2694    0CB3   21 F3E8          LD    HL,TRGFLG

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

```

2695  OCB6    AE      XOR    (HL)      ;Any transition?
2696  OCB7    A6      AND   (HL)      ;Is this transition negative
2697  OCB8    71      LD    (HL),C    ;Update trigger status
2698  OCB9    4F      LD    C,A      ;Check space key trigger
2699  OCBA    0F      RRCA
2700  OCBB    21      FC70
2701  OCBE    0EF1   CALL  C,REQTRP
2702  OCC1    CB11   RL   C
2703  OCC3    21      FC7C
2704  OCC6    0EF1   CALL  C,REQTRP
2705  OCC9    CB11   RL   C
2706  OCCB    21      FC76
2707  OCCE    0EF1   CALL  C,REQTRP
2708  OCD1    CB11   RL   C
2709  OCD3    21      FC79
2710  OCD6    0EF1   CALL  C,REQTRP
2711  OCD9    CB11   RL   C
2712  OCDB    21      FC73
2713  OCDE    0EF1   CALL  C,REQTRP
2714
2715
2716
2717  OCE1    AF      XOR    A
2718  OCE2    32      FBD9
2719  OCE5    CD0D12  LD    (CLIKFL),A
2720  OCE8    2018   CALL  KEYCHK
2721  OCEA    21      F3F7  JR   NZ,INTRET
2722  OCED    35      LD    HL,REPCNT
2723  OCEE    2012   DEC   (HL)
2724  OCFO    3601   JR   NZ,INTRET
2725  OCF2    21      FBDA  LD   (HL),1
                                LD   HL,OLDKEY
                                LD   HL,TRPTBL+3*13
                                CALL C,REQTRP
                                ;Scan keyboard
                                ;
                                ;Scan keyboard
                                ;
                                ;Enable first key click
                                ;Detect valid key transition and check buffer
                                ;Some characters still remain, don't repeat
                                ;Need to enter repeat mode
                                ;No
                                ;Set short time repeat
                                ;Clear OLDKEY status

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

		PAGE	37-4
	Macro-80	01-Jan-85	
2726	OCF5	11 FBDB	LD DE, OLDKEY+1
2727	OCF8	01 000A	LD BC, 0AH
2728	OCFB	36 FF	LD (HL), OFFH
2729	OCFD	ED B0	LDIR
2730	OCFF	CD 0D4E	CALL KEYCK4
2731	0D02	INTRET:	;Check if currently pressed key is valid
2732	0D02	DD E1	POP IX
2733	0D04	FD E1	POP IY
2734	0D06	F1	POP AF
2735	0D07	C1	POP BC
2736	0D08	D1	POP DE
2737	0D09	E1	POP HL
2738	0D0A	08	EX AF, AF'
2739	0D0B	D9	EXX
2740	0D0C	F1	POP AF
2741	0D0D	C1	POP BC
2742	0D0E	D1	POP DE
2743	0D0F	E1	POP HL
2744	0D10	FB	EI
2745	0D11	C9	RET
2746	0D12	KEYCHK:	
2747		;	
2748	0D12	DB AA	IN A,(PPI CR)
2749	0D14	E6 F0	AND 0F0H
2750	0D16	4F	LD C,A
2751	0D17	06 0B	LD B,0BH
2752	0D19	21 FBE5	LD HL, NEWKEY
2753	0D1C	79	KEYCK1:
2754	0D1C		LD A,C
2755	0D1D	D3 AA	OUT (PPI.CW),A
2756	0D1F	DB A9	IN A,(PPI.BR)
			;Select row
			;Get column information of selected row

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Keyboard encoding routines

```

 3 . 4 4      01-Jan-85      PAGE 37-5

2757  0D21    77      LD   (HL),A      ;Move it
2758  0D22    0C      INC  C          ;Select next row
2759  0D23    23      INC  HL
2760  0D24    10 F6    DJNZ KEYCK1      ;Loop until all rows are sensed
2761  0D26    3A FBB0   LD   A,(ENSTOP) ;Warm start enabled?
2762  0D29    A7      AND  A
2763  0D2A    28 0E    JR   Z,NOSTOP    ;No
2764  0D2C    3A FBEB   LD   A,(SFTKEY) ;Get current status of the 6th row
2765  0D2F    FE E8    CP   0E8H        ;Check if KANA, GRAPH, CTRL and SHIFT
2766  0D31    20 07    JR   NZ,NOSTOP    ;are pressed simultaneously
2767  0D33    DD 21 409B   LD   IX,READYR
2768  0D37    C3 01FF    JP   CALBAS
2769  0D3A    NOSTOP:  ;                         ;Set number of rows
2770
2771  0D3A    11 FBE5   LD   DE,NEWKEY  ;[OLDKEY] + 11
2772  0D3D    06 0B      LD   B,0BH
2773  0D3F    KEYCK2:   DEC  DE
2774  0D3F    1B      DEC  HL
2775  0D40    2B      DEC  A,(DE)
2776  0D41    1A      LD   (HL)
2777  0D42    BE      CP   NZ,KEYCK3
2778  0D43    20 04    JR   KEYCK2
2779  0D45    10 F8      DJNZ KEYCK4
2780  0D47    18 05    JR   KEYCK4
2781  0D49    KEYCK3:  ;                         ;Set number of rows
2782
2783  0D49    3E 0D      LD   A,0DH
2784  0D4B    32 F3F7    LD   (REPCNT),A
2785  0D4E    KEYCK4:   LD   B,0BH
2786  0D4E    06 0B      LD   HL,OLDKEY
2787  0D50    21 FBDA    ;                         ;Set number of rows

```

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( MSX ROM BASIC BIOS ) Macro-80          3.44   01-Jan-85      PAGE    37-6
- MSXIO - Keyboard encoding routines

2788  0D53   11  FB E5                 LD      DE, NEWKEY
2789  0D56   1A                   KEYCK5: LD      A,(DE)           ;Get current key status
2790  0D56   4F                   LD      C,A
2791  0D57   AE                   XOR    (HL)
2792  0D58   A6                   AND    (HL)
2793  0D59   71                   LD      (HL),C           ;See if any bit changed
2794  0D5A   C4 0D89             CALL   NZ,KEYANY        ;See if this change is negative transition
2795  0D5B   13                   INC    DE
2796  0D5E   23                   INC    HL
2797  0D5F   10  F4               DJNZ  KEYCK5         ;Update old status
2798  0D60   0D62             CHKBUF:          ;Active transition, go find it
2799   0D62   ; Check if buffer is empty or not
2800   ; Load GETPNT
2801   ; Load lower 8 bit of PUTPNT
2802   ; Check if same
2803  0D62   2A  F3 FA             LD      HL, (GETPNT)
2804  0D65   3A  F3 F8             LD      A,(PUTPNT)
2805  0D68   95                   SUB    L
2806  0D69   C9                   RET
2807  0D6A   ; CHNSN:
2808   ; EI
2809  0D6A   FB                   PUSH   HL
2810  0D6B   E5                   PUSH   DE
2811  0D6C   D5                   PUSH   BC
2812  0D6D   C5                   PUSH   CHKSCR
2813  0D6E   CD 0B9F             CALL   NC,CHNSN1        ;Are we in text mode?
2814  0D71   30  0F               JR    A,(FNKSWI)       ;No, do not flip function keys
2815  0D73   3A  FB CD             LD    HL,SFTKEY        ;Get current shift status
2816  0D76   21  FB EB             LD    (HL)            ;Get current function key display
2817  0D79   AE                   XOR    HL,CNSDFG       ;Are they different
2818   0D7A   21  F3 DE             LD    HL,CNSDFG       ;Function key displayed at all?

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   37-7
- MSXIO - Keyboard encoding routines

2819  0D7D  A6          AND      (HL)
2820  0D7E  0F          RRCA
2821  0D7F  DC 0B2B     CALL    C,DSPFNK
2822  0D82  CD 0D62     CHNSN1: CALL    CHKBUF
2823  0D82  C1          POP     BC
2824  0D85  D1          POP     DE
2825  0D86  E1          POP     HL
2826  0D87  C9          RET
2827  0D88  KEYANY:    ; Save environments
2828  0D89  ;           ; Set up counter for 8 bit
2829  ;           ; Set up counter for 8 bit
2830  ;           ; Restore pressed bit
2831  ;           ; Restore base code
2832  0D89  E5          PUSH   HL
2833  0D8A  D5          PUSH   DE
2834  0D8B  C5          PUSH   BC
2835  0D8C  F5          PUSH   AF
2836  0D8D  3E 0B        LD     A,0BH
2837  0D8F  90          SUB   B
2838  0D90  87          ADD   A,A
2839  0D91  87          ADD   A,A
2840  0D92  87          ADD   A,A
2841  0D93  4F          LD     C,A
2842  0D94  06 08        LD     B,8
2843  0D96  F1          POP   AF
2844  0D97  1F          RRA
2845  0D97  C5          PUSH   BC
2846  0D98  F5          PUSH   AF
2847  0D99  DC 0E3B     CALL   C,KEYCOD
2848  0D9A  F1          POP   AF
2849  0D9D  ;           ; If pressed bit, call key coder.

```

```

( MSX ROM BASIC BIOS ) Macro-80      PAGE    37-8
- MSXIO - Keyboard encoding routines          3.44    01-Jan-85

2850    0D9E    C1          POP     BC
2851    0D9F    0C          INC     C
2852    0DA0    10 F5          INC     KYANYL
2853    0DA2    C3 08DB          DZNZ    PBDHRT
2854    ;          ;          JP      ;Try next code
2855    ;          ;          ;      ;Loop until all bits are checked
2856    ;          ;          ;      ;Restore environments
2857    ;          ;          ;      ;[[[ SUBROUTINE 'KEYCOD' ]]]
2858    ;          ;          ;      ;Return key-code in buffer if valid
2859    0DA5    0A          KYJTAB: DB     10
2860    0DA5    0A          DW     KYNUM   ;0..9
2861    0DA6    0E67          DW     22
2862    0DA8    16          DB     KYCOD1
2863    0DA9    0EA1          DW     48
2864    0DAB    30          DB     KYALP   ;A..Z
2865    0DAC    0E7E          DW     51
2866    0DAE    33          DB     KYEASY
2867    0DAF    0F10          DW     52
2868    0DB1    34          DB     KYLOCK ;Capital lock
2869    0DB2    0F36          DW     53
2870    0DB4    35          DB     KYKLOK ;Kana lock
2871    0DB5    0F1F          DW     KYFUNC ;Function key
2872    0DB7    3A          DW     60
2873    0DB8    0EBB          DW     KYEASY
2874    0DBA    3C          DW     61
2875    0DBB    0F10          DW     KYSTOP ;Stop key
2876    0DBD    3D          DW     65
2877    0DBE    0F46          DW     KYEASY
2878    0DC0    41          DB     66
2879    0DC1    0F10          DW     66
2880    0DC3    42          DB     66

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85      PAGE    37-9
- MSXIO - Keyboard encoding routines

2881 0DC4 0F06          DW      KYCLS
2882 0DC6 FF             DB      255
2883 0DC7 0F10           DW      KYEASY
2884 ;NMSFTB:             ;CL/S/HOME key
2885 0DC9 FF             DB      255
2886 0DC9 21             DB      " "
2887 0DCA 22             DB      34
2888 0DCB 23             DB      "#$%& ' ()"
2889 0DCC 24             DB      ;Double quote
2890 0DD0 25             DB      " "
2891 0DD1 26             DB      ;
2892 0DD3 0F55           DW      PUTCHR
2893 0DD3 0F55           DW      ;CTRL+shift
2894 0DD5 0F55           DW      ;CTRL
2895 0DD7 0E93           DW      KEYSET
2896 0DD9 0E95           DW      KEYNOM
2897 ;ALPJMP:              ;CTRL+shift
2898 0DDB 0DFD           DW      KY1 SFC-10
2899 0DDB 0DF1           DW      KY1 CNT-10
2900 0DDD 0DEF            DW      KY1 SFT-10
2901 0DDF 0DE5            DW      KY1 NOM-10
2902 0DEL 0DD9           DW      ;
2903 0DE3 2D 5E 5C 40     KY1 NOM:
2904 0DE3 5B 3B 3A 5D     DB      "_^[@[ ; : ], ./"
2905 0DE7 2C 2E 2F
2906 0DEB 2C 2E 2F
2907 0DDE FF             DB      255
2908 0DEF KY1SFT:         DB      "=~|`{+*})"
2909 0DEF 3D 7E 7C 60     DB      00111100B
2910 0DF3 7B 2B 2A 7D     DB      ;Less than sign
2911 0DF7 3C

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Keyboard encoding routines

		3 . 4 . 4	01-Jan-85	PAGE	37-10
2912	0DF8	3E		DB	00111110B
2913	0DF9	3F 5F		DB	"?" "__"
2914	0DFB	2D		DB	"-" "^" "<" ">"
2915	0DFB	1E		DB	">" "\" "@" "@"
2916	0DFC	1C		DB	"[" ";" "]" "/"
2917	0DFD	00		DB	"[" ";" "]" "/"
2918	0FFE	0FF		DB	"[" ";" "]" "/"
2919	0FFF	1B		DB	"[" ";" "]" "/"
2920	0E00	3B 3A		DB	"[" ";" "]" "/"
2921	0E02	1D		DB	"[" ";" "]" "/"
2922	0E03	2C 2E 2F		DB	"[" ";" "]" "/"
2923	0E06	FF		DB	255
2924	0E07	3D		DB	"=" "^" "<" ">"
2925	0E07	1E		DB	"\" "\" "@" "@"
2926	0E08	09		DB	"[" ";" "]" "/"
2927	0E09	1C		DB	"[" ";" "]" "/"
2928	0E0A	00		DB	"[" ";" "]" "/"
2929	0E0B	1B		DB	"[" ";" "]" "/"
2930	0E0C	2B 2A		DB	"+" "]" "/"
2931	0E0E	1D		DB	00111100B
2932	0E0F	3C		DB	00111110B
2933	0E10	3E		DB	"?" "?" "_"
2934	0E11	3F		DB	"?" "_"
2935	0E12	1F		DB	"?" "_"
2936				;	EASYTB:
2937	0E13	00		DB	0
2938	0E13	00		DB	0
2939	0E14	00		DB	0
2940	0E15	00		DB	0
2941	0E16	00		DB	0
2942	0E17	00		DB	0

;Shift (48)
 ;Control (49)
 ;Graph (50)
 ;Cap lock (51)
 ;Kana lock (52)

(MSX ROM BASIC BIOS) Macro-80
— MSXIO - Keyboard encoding routines

```

(53) 00 DB 0 ;F1
      01 DB 0 ;F2
      02 DB 0 ;F3
      03 DB 0 ;F4
      04 DB 0 ;F5
      05 DB 27 ;Es cape
      06 DB 9 ;Tab
      07 DB 0 ;Stop
      08 DB 8 ;Back space
      09 DB 0 ;Select
      0A DB 13 ;Enter
      0B DB 32 ;Space
      0C DB 12 ;Clear
      0D DB 127 ;Insert
      0E DB 127 ;Rubout
      0F DB 29 ;Left
      10 DB 30 ;Up
      11 DB 31 ;Down
      12 DB 28 ;Right
      13 DB 0 ;;
      14 DB 0 ;;
      15 DB 0 ;;
      16 DB 0 ;;
      17 DB 0 ;;
      18 DB 0 ;;
      19 DB 0 ;;
      1A DB 0 ;;
      1B DB 0 ;;
      1C DB 0 ;;
      1D DB 0 ;;
      1E DB 0 ;;
      1F DB 0 ;;
      20 DB 0 ;;
      21 DB 0 ;;
      22 DB 0 ;;
      23 DB 0 ;;
      24 DB 0 ;;
      25 DB 0 ;;
      26 DB 0 ;;
      27 DB 0 ;;
      28 DB 0 ;;
      29 DB 0 ;;
      2A DB 0 ;;
      2B DB 0 ;;
      2C DB 0 ;;
      2D DB 0 ;;
      2E DB 0 ;;
      2F DB 0 ;;
      30 DB 0 ;;
      31 DB 0 ;;
      32 DB 0 ;;
      33 DB 0 ;;
      34 DB 0 ;;
      35 DB 0 ;;
      36 DB 0 ;;
      37 DB 0 ;;
      38 DB 0 ;;
      39 DB 0 ;;
      3A DB 0 ;;
      3B DB 0 ;;
      3C DB 0 ;;
      3D DB 0 ;;
      3E DB 0 ;;
      3F DB 0 ;;
      40 DB 0 ;;
      41 DB 0 ;;
      42 DB 0 ;;
      43 DB 0 ;;
      44 DB 0 ;;
      45 DB 0 ;;
      46 DB 0 ;;
      47 DB 0 ;;
      48 DB 0 ;;
      49 DB 0 ;;
      4A DB 0 ;;
      4B DB 0 ;;
      4C DB 0 ;;
      4D DB 0 ;;
      4E DB 0 ;;
      4F DB 0 ;;
      50 DB 0 ;;
      51 DB 0 ;;
      52 DB 0 ;;
      53 DB 0 ;;
      54 DB 0 ;;
      55 DB 0 ;;
      56 DB 0 ;;
      57 DB 0 ;;
      58 DB 0 ;;
      59 DB 0 ;;
      5A DB 0 ;;
      5B DB 0 ;;
      5C DB 0 ;;
      5D DB 0 ;;
      5E DB 0 ;;
      5F DB 0 ;;
      60 DB 0 ;;
      61 DB 0 ;;
      62 DB 0 ;;
      63 DB 0 ;;
      64 DB 0 ;;
      65 DB 0 ;;
      66 DB 0 ;;
      67 DB 0 ;;
      68 DB 0 ;;
      69 DB 0 ;;
      6A DB 0 ;;
      6B DB 0 ;;
      6C DB 0 ;;
      6D DB 0 ;;
      6E DB 0 ;;
      6F DB 0 ;;
      70 DB 0 ;;
      71 DB 0 ;;
      72 DB 0 ;;
      73 DB 0 ;;
      74 DB 0 ;;
      75 DB 0 ;;
      76 DB 0 ;;
      77 DB 0 ;;
      78 DB 0 ;;
      79 DB 0 ;;
      80 DB 0 ;;

For additional key matrix

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

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111

	3 . 4 4	0 1 - J a n - 8 5	PAGE	3 7 - 1 2
2974	0E34	00	DB	C
2975	0E35	00	DB	0
2976	0E36	00	DB	0
2977	0E37	00	DB	0
2978	0E38	00	DB	0
2979	0E39	00	DB	0
2980	0E3A	00	DB	0

			i	(81)
			i	(82)
			i	(83)
			i	(84)
			i	(85)
			i	(86)
			i	(87)

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE    38      112
- MSXIO - Keyboard encoding routines

2981
2982
2983 0E3B
2984
2985
2986
2987
2988
2989 0E3B    79      LD      A,C      ;Get raw code
2990 0E3C    FE FF    CP      0FFH     ;Just for fail safe
2991 0E3E    C8      RET     Z
2992 0E3F    21 0DA5  LD      HL,KYJTAB
2993 0E42    CD FDCC  CALL    H,KEYC
2994 0E45    FE 30    CP      48      ;Possibly a KANA or graphic character
2995 0E47    30 13    JR      NC,KYCLAS
2996 0E49    3A FBEB  LD      A,(SFTKEY)   ;No
2997 0E4C    0F      RRCA    RRCA    ;Get shift key status
2998 0E4D    0F      RRCA    RRCA    ;Control pressed?
2999 0E4E    30 0B    JR      NC,KYCLA0
3000 0E50    0F      RRCA    RRCA    ;Yes, this supersedes everything
3001 0E51    D2 107D  JP      NC,KYGRAP
3002 0E54    3A FCAC  LD      A,(KANAST)
3003 0E57    A7      AND    A       ;KANA lock active
3004 0E58    C2 0F83  JP      NZ,KYKANA
3005 0E5B    KYCLA0: LD      A,C
3006 0E5B    79      KYCLAS: CP      (HL)
3007 0E5C    BE      INC    HL      ;Compare range
3008 0E5C    23      INC    HL
3009 0E5D    23      LD      E,(HL)
3010 0E5E    5E      INC    HL      ;Get jump address in [DE]
3011 0E5F    23

```

```

-- MSXIO - Keyboard encoding routines

3012 0E60 56 LD D, (HL)
3013 0E61 23 INC HL
3014 0E62 D5 PUSH DE
3015 0E63 D8 RET C
3016 0E64 D1 POP DE
3017 0E65 18 F5 JR KYCLAS
3018 0E67 KNUM: ; Assume no shift
3019 ; Assume matched
3020 0E67 C6 30 ADD A, '0'
3021 0E69 47 LD B,A
3022 0E6A 3A FBEB LD A,(SFTKEY)
3023 0E6D 0F RRCA ;Check shift status
3024 0E6E 78 LD A,B
3025 0E6F 38 0A C,JPUTCH
3026 0E71 06 00 LD B,0
3027 0E73 21 0DC9 LD HL,NMSFTB
3028 0E76 09 ADD HL,BC
3029 0E77 7E LD A,(HL)
3030 0E78 FE FF CP OFFH
3031 0E7A C8 RET Z
3032 0E7B C3 0F55 JPUTCH: ;This must not be 'DADF'
3033 0E7B 0F55 JP PUTCHR ;Get code for shift-numb
3034 0E7E KYALP: ;Shift '0'?
3035 ;Yes, ignore this
3036 0E7E 3A FBEB ;Put this in buffer
3037 0E81 E6 03 LD A,(SFTKEY)
3038 0E83 87 AND 3
3039 0E84 5F ADD A,A
3040 0E85 16 00 LD E,A
3041 0E87 21 0DD3 LD D,0
3042 0E8A ADD HL,ALPUMP

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   38-2
- MSXIO - Keyboard encoding routines

3043  0E8B    7E      LD     A,(HL)      ;Get jump address
3044  0E8C    23      INC    HL
3045  0E8D    66      LD     H,(HL)
3046  0E8E    6F      LD     L,A
3047  0E8F    79      LD     A,C
3048  0E90    15      SUB   15H
3049  0E92    E9      JP    (HL)
3050  0E93    KEYSFT: ;Get jump address
3051
3052  0E93    C6 20   ADD   A,' '
3053  0E95    KEYNOM: LD     B,A      ;Save code
3054  0E95    47      LD     A,(CAPST)
3055  0E96    3A FCAB CPL
3056  0E99    2F      AND   0010000B ;Bit 5 is on if CAP lock not active
3057  0E9A    E6 20   XOR   B
3058  0E9C    A8      ADD   A,0100000B
3059  0E9D    C6 40   JR    JPUTCH
3060  0E9F    18 DA   KYCDL: ;Extract shift and control status
3061  0EA1    KEYCDL: ;Extract shift and control status
3062
3063  0EAI    21 0DDB LD     HL,KYCLTB
3064  0EA4    3A FBEB LD     A,(SFTKEY)
3065  0EA7    E6 03   AND   3
3066  0EA9    87      ADD   A,A
3067  0EAA    5F      LD     E,A
3068  0EAB    16 00   LD     D,0
3069  0EAD    19      ADD   HL,DE
3070  0EAE    7E      LD     A,(HL)
3071  0EAF    23      INC   HL
3072  0EB0    66      LD     H,(HL)
3073  0EB1    6F      LD     L,A

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Keyboard encoding routines

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```

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3074 0EB2 59 LD E,C
3075 0EB3 19 ADD HL,DE
3076 0EB4 7E LD A,(HL)
3077 0EB5 FE FF CP OFFH
3078 0EB7 C2 0F55 JP NZ,PUTCHR
3079 0EBA C9 RET ;Should generate some code?
3080 0EBB KYFUNC: ;Yes
3081 ; ;Function keys
3082 ; ;Function keys
3083 ; ;Function keys
3084 0EBB 3A FBEB LD A,(SFTKEY)
3085 0EBE 0F RRCA ;Is shift pressed?
3086 0EBF 38 04 JR C,KYFNCL ;No
3087 0EC1 79 LD A,C
3088 0EC2 C6 05 ADD A,5
3089 0EC4 4F LD C,A
3090 0EC5 59 LD E,C ;[DE] is (56..65)
3091 0EC6 16 00 LD D,0
3092 0EC8 21 FB99 LD HL,FNKFGL-53 ;Check if this function key is an event device
3093 0ECB 19 ADD HL,DE
3094 0ECC 7E LD A,(HL)
3095 0ECD A7 AND A
3096 0ECE 20 13 JR NZ,FNKINT ;Request trap if not in direct mode
3097 0ED0 EX DE,HL
3098 0ED0 EB ADD HL,HL
3099 0ED0 29 ADD HL,HL
3100 0ED1 29 ADD HL,HL
3101 0ED2 29 ADD HL,HL
3102 0ED3 29 ADD HL,HL
3103 0ED4 29 ADD HL,HL
3104 0ED5 11 F52F LD DE,FNKSTR-53*16

```

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

			3.44	01-Jan-85	PAGE
3105	0ED8	19	ADD	HL, DE	38-4
3106	0ED9	EB	EX	DE, HL	
3107	0EDA	1A	KYFNC3:	A, (DE)	;Get function key string address
3108	0EDA	A7	LD	A	;Move address to DE
3109	0EDB	C8	AND	A	
3110	0EDC	CD	RET	Z	;Get from function key string
3111	0EDD	0F55	CALL	PUTCHR	;End of string
3112	0EE0	13	INC	DE	;Yes
3113	0EE1	F7	INC	DE	;Put this character in buffer
3114	0EE3		JR	KYFNC3	;Check next character
3115			FNKINT:		
3116	0EE3	2A F41C	;		
3117	0EE6	23	LD	HL, (CURLIN)	
3118	0EE7	7C	INC	HL	
3119	0EE8	B5	LD	A, H	
3120	0EE9	28 E5	OR	L	
3121	0EEB	21 FBAD	JR	Z, KYFNC2	;Yes, treat as normal function key
3122	0EEE	19	LD	HL, TRPTBL-53*3	
3123	0EEF	19	ADD	HL, DE	
3124	0EOF0	19	ADD	HL, DE	
			ADD	HL, DE	

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

```

3125      ;REQTRP:
3126      0EFF1
3127      ;REQTRP:
3128      ; Request trap (called to request trap for event devices)
3129      ;
3130      ;
3131      ;
3132      ; Since REQTRP is mostly called from within an interrupt routine,
3133      ; don't touch the interrupt mask through DI or EI.
3134      ;
3135      0EFF1    7E    LD   A,(HL)    ;Trap on?
3136      0EFF2    E6    01    AND  1       ;TRAP NOT ON
3137      0EFF4    C8    RET   Z       ;
3138      0EFF5    7E    LD   A,(HL)    ;Trap request
3139      0EFF6    F6    04    OR   4       ;No change
3140      0EFF8    BE    CP   (HL)    ;
3141      0EFF9    C8    RET   Z       ;
3142      0EFA     77    LD   (HL),A  ;Trap on + Trap request
3143      0EFB     EE    05    XOR   5       ;
3144      0EFD     C0    RET   NZ      ;
3145      0EFFE    3A    FBD8   LD   A,(ONGSBF)
3146      0F01     3C    FBD8   INC  A       ;ONGSBF
3147      0F02     32    FBD8   LD   (ONGSBF),A
3148      0F05     C9    RET   ;
3149      ;KYCLS:
3150      0F06     3A    FBEB   LD   A,(SFTKEY)  ;Set carry if shift not pressed
3151      0F06     3A    FBEB   RRCA
3152      0F09     0F    RET   ;
3153      0FOA     3E    0C    LD   A,0CH    ;Load code for CLS
3154      0F0C     DE    00    SBC  A,0      ;Change to HOME if shift not pressed
3155      0FOE     18    45    JR   PUTCHR

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44      01-Jan-85      PAGE    39-1
- MSXIO - Keyboard encoding routines

3156  0F10          KYEASY:          ; Eas i ly converted keys
3157
3158
3159
3160  0F10          CD FDD1          CALL    H.KYEAS
3161  0F13          5F               LD      E,A
3162  0F14          16 00           LD      D,0
3163  0F16          21 0DE3          LD      HL,EASYTB-48
3164  0F19          19               ADD    HL,DE
3165  0F1A          7E               ADD    A,(HL)
3166  0F1B          A7               AND    A
3167  0F1C          C8               RET    Z
3168  0F1D          18 36           JR     PUTCHR
3169  0F1F          ;                 ; Yes
3170
3171
3172
3173  0F1F          21 FCAC          ;                 ; Kana lock key
3174  0F22          7E               LD      HL,KANAST
3175  0F23          2F               LD      A,(HL)
3176  0F24          77               CPL
3177  0F25          3E 0F           LD      (HL),A
3178  0F27          D3 A0           LD      A,0FH
3179  0F29          DB A2           OUT   (PSG.LW),A
3180  0F2B          E6 7F           IN    A,(PSG.DR)
3181  0F2D          47               AND   7FH
3182  0F2E          7E               LD      B,A
3183  0F2F          2F               LD      A,(HL)
3184  0F30          E6 80           CPL
3185  0F32          B0               AND   80H
3186  0F33          D3 A1           OR    B
                                OUT   (PSG.DW),A

```

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Keyboard encoding routines

			3.44	01-Jan-85	PAGE	39-2
3187	0F35	NOKEY:				
3188	0F35	C9	RET			
3189	0F36	KYLOCK:				
3190			;			
3191			Capital lock key			
3192			;			
3193	0F36	21 FCAB		LD HL,CAPST		
3194	0F39	7E		LD A,(HL)		Toggle capital status
3195	0F3A	2F		CPL		
3196	0F3B	77		LD (HL),A		Update capital status
3197	0F3C	2F		CPL		
3198	0F3D	A7		CHGCGAP:	AND A	
3199	0F3D				LD A,0CH	Assume 'turn off'
3200	0F3E	3E 0C			JR Z,CGCAP1	Good assumption
3201	0F40	28 01			INC A	Change to 'turn on'
3202	0F42	3C		CGCAP1:		
3203	0F43				OUT (PPI.CM),A	
3204	0F43	D3 AB			RET	
3205	0F45	C9				
3206	0F46			KYSTOP:		
3207					;	
3208					STOP key	
3209					;	
3210	0F46	3A FBEB		LD A,(SFTKEY)		
3211	0F49	0F		RRCA		Mov e CTRL status to carry
3212	0F4A	0F		RRCA		
3213	0F4B	3E 03		LD A,3		
3214	0F4D	30 01		JR NC,KYSTP1		Assume CTRL pressed also
3215	0F4F	3C		INC A		Good assumption
3216	0F50			KYSTP1:		CTRL not pressed, just treat as pause
3217	0F50	32 FC9B			LD (INTFLG),A	

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Keyboard encoding routines

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```

3218 0F53 38 0F          JR   C,GENCLK
3219 0F55          PUTCHR:
3220
3221          ; Put one character in key buffer.
3222          ;
3223 0F55 2A F3F8          LD   HL,(PUTPNT) ;Load PUTPNT in [HL]
3224 0F58 77          LD   (HL),A  ;Save the character to buffer
3225 0F59 CD 10C2          CALL UPDATE ;Increment PUTPNT
3226 0F5C 3A F3FA          LD   A,(GETPNT) ;Load lower 8bit of GETPNT
3227 0F5F BD          CP   L    ;Compare it with new PUTPNT
3228 0F60 C8          RET  Z   ;If same skip next step
3229 0F61 22 F3F8          LD   (PUTPNT),HL ;Save HL in PUTPNT
3230 0F64          GENCLK:
3231 0F64 3A F3DB          LD   A,(CLIKSW) ;Key click enabled?
3232 0F67 A7          AND  A   ;No
3233 0F68 C8          RET  Z   ;Yes, don't click any more
3234 0F69 3A FB09          LD   A,(CLIKFL) ;Already generated?
3235 0F6C A7          AND  A   ;Set flag to disable more clicks
3236 0F6D C0          RET  NZ
3237 0F6E 3E 0F          LD   A,0FH
3238 0F70 32 FB09          LD   (CLIKFL),A
3239 0F73 D3 AB          OUT (PPI.CM),A
3240 0F75 3E 0A          LD   A,0AH
3241 0F77          CLICKW:
3242 0F77 3D          DEC  A   ;Assume 'turn off'
3243 0F78 20 FD          JR   NZ,CLICKW
3244 0F7A          CHGSND:
3245 0F7A A7          AND  A   ;Good assumption
3246 0F7B 3E 0E          LD   A,0EH
3247 0F7D 28 01          JR   Z,CGSND1
3248 0F7F 3C          INC  A   ;Change to 'turn on'

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Keyboard encoding routines

			3 . 4 4	01-Jan-85	PAGE	3 9 - 4
3249	0F80	D3 AB	CGSND1:	OUT	(PPI.CM),A	
3250	0F80	C9		RET		
3251	0F82		KYKANA:			
3252	0F83					
3253						
3254						
3255						
3256	0F83	3A FCAD		LD	A, (KANAMD)	
3257	0F86	A7		AND	A	
3258	0F87	3A FBEB		LD	A, (SFTKEY)	
3259	0F8A	0F		RRCA		
3260	0F8B	28 0A		JR	Z, KAIUEO	
3261	0F8D	21 101D		LD	HL, KANJNO	
3262	0F90	38 0D		JR	C, KYKANI	
3263	0F92	21 104D		LD	HL, KANJSF	
3264	0F95	18 08		JR	KYKANI	
3265	0F97		KAIUEO:			
3266						
3267	0F97	21 0FB0		LD	HL, KANANO	
3268	0F9A	38 03		JR	C, KYKANI	
3269	0F9C	21 0FED		LD	HL, KANASF	
3270	0F9F	06 00	KYKANL:			
3271	0F9F	09		LD	B, 0	
3272	0FA1	09		ADD	HL, BC	
3273	0FA2	01 0F55		LD	BC, PUTCHR	
3274	0FA5	C5		PUSH	BC	
3275	0FA6	3A FCAB		LD	A, (CAPST)	
3276	0FA9	A7		AND	A	
3277	0FAA	7E		LD	A, (HL)	
3278	0FAB	C0		RET	NZ	
3279	0FAC	FE A6		CP	165+1	;Special characters?

(MSX ROM BASIC BIOS) Macro-80

- MSX IO - Keyboard encoding routines

3280	0FAE	D8	RET	C		
3281	0FAF	FE B0	CP	0B0H		
3282	0FB1	C8	RET	Z		
3283	0FB2	FE DE	CP	0DEH		
3284	0FB4	D0	RET	NC		
3285	0FB5	D6 20	SUB	'		
3286	0FB7	FE A0	CP	191-32+1		
3287	0FE9	D8	RET	C		
3288	0FBA	C6 40	ADD	A,32+32		
3289	0FBC	C9	RET			
3290	0FBD				KANANO:	
3291					;	Kana table (AIEUO order, un-shifted
3292					;	
3293	0FBD	C9 B1 B2 B3	DB	0C9H,0B1H,0B2H,0B3H,0B4H,0B5H,0C5H		
3294	0FC1	B4 B5 C5	DB	0C6H,0C7H,0C8H,0D7H,0D8H,0D9H,0DAH		
3295	0FC4	C6 C7 C8 D7	DB	0DBH,0D3H,0DEH,0DFH,0D6H,0DCCH,0A6H		
3296	0FC8	D8 D9 DA	DB	0DDH,0BBH,0C4H,0C2H,0BDH,0B8H,0BEH		
3297	0FCB	DB D3 DE DF	DB	0BFH,0CFH,0CCH,0D0H,0D1H,0D2H,0D5H		
3298	0FCF	D6 DC A6	DB	0D4H,0CDH,0CEH,0B6H,0B9H,0BCH,0BAH		
3299	0FD2	DD BB C4 C2	DB	0CBH,0C3H,0B7H,0C1H,0CAH,0C0H		
3300	0FD6	BD B8 BE	DB		KANASF:	
3301	0FD9	BF CF CC D0	DB		;	Shifted
3302	0FDD	D1 D2 D5	DB			
3303	0FE0	D4 CD CE B6	DB			
3304	0FE4	B9 BC BA	DB			
3305	0FE7	CB C3 B7 C1	DB			
3306	0FEB	CA C0	DB			
3307	0FED					
3308						
3309						
3310	0FED	C9 A7 A8 A9	DB	0C9H,0A7H,0A8H,0A9H,0AAH,0ABH,0C5H		

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Keyboard encoding routines

		3.44	01-Jan-85	PAGE	39-6
3311	OFF1	AA AB C5		DB	0C6H,0C7H,0C8H,0D7H,0D8H,0D9H,0DAH
3312	OFF4	C6 C7 C8 D7		DB	0C6H,0C7H,0C8H,0D7H,0D8H,0D9H,0DAH
3313	OFF8	D8 D9 DA		DB	0A2H,0D3H,0B0H,0A3H,0AEH,0A4H,0A1H
3314	OFFB	A2 D3 B0 A3		DB	0A5H,0BBH,0C4H,0AFH,0BDH,0B8H,0BEH
3315	OFFF	AE A4 A1		DB	0BFH,0CFH,0CCH,0D0H,0D1H,0D2H,0ADH
3316	1002	A5 BB C4 AF		DB	0AACH,0CDH,0CEH,0B6H,0B9H,0BCH,0BAH
3317	1006	BD B8 BE		DB	0CBH,0C3H,0B7H,0C1H,0CAH,0C0H
3318	1009	BF CF CC D0		DB	0DCH,0C7H,0CCH,0B1H,0B3H,0B4H,0B5H
3319	100D	D1 D2 AD		DB	0D4H,0D5H,0D6H,0CEH,0CDH,0B0H,0DEH
3320	1010	AC CD CE B6		DB	0DFH,0DAH,0B9H,0D1H,0C8H,0D9H,0D2H
3321	1014	B9 BC BA		DB	0DBH,0C1H,0BAH,0BFH,0BCH,0B2H,0CAH
3322	1017	CB C3 B7 C1		DB	0B7H,0B8H,0C6H,0CFH,0C9H,0D8H,0D3H
3323	101B	CA C0		DB	0D0H,0D7H,0BEH,0C0H,0BDH,0C4H,0B6H
3324	101D		KANJNO:		KANJSF:
3325		;	;		
3326		DC C7 CC B1		DB	
3327	101D	B3 B4 B5		DB	
3328	1021	D4 D5 D6 CE		DB	
3329	1024	CD B0 DE		DB	
3330	1028	DF DA B9 D1		DB	
3331	102B	C8 D9 D2		DB	
3332	102F	DB C1 BA BF		DB	
3333	1032	BC B2 CA		DB	
3334	1036	B7 B8 C6 CF		DB	
3335	1039	C9 D8 D3		DB	
3336	103D	D0 D7 BE C0		DB	
3337	1040	BD C4 B6		DB	
3338	1044	C5 CB C3 BB		DB	
3339	1047	DD C2		DB	
3340	104B				
3341	104D				

(MSX ROM BASIC BIOS) Macro-80

- MSXIO - Keyboard encoding routines

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3342	;	Shifted		
3343	;			
3344	104D	A6 C7 CC A7	DB	0A6H,0C7H,0CCCH,0A7H,0A9H,0AAH,0ABH
3345	1051	A9 AA AB	DB	0A9H,0ADH,0AEH,0CEH,0CDH,0B0H,0DEH
3346	1054	AC AD AE CE	DB	0A2H,0DAH,0B9H,0A3H,0A4H,0A1H,0A5H
3347	1058	CD B0 DE	DB	0DBH,0C1H,0BAH,0BFH,0BCH,0A8H,0CAH
3348	105B	A2 DA B9 A3	DB	0B7H,0B8H,0C6H,0CFH,0C9H,0D8H,0D3H
3349	105F	A4 A1 A5	DB	0D0H,0D7H,0BEH,0C0H,0BDH,0C4H,0B6H
3350	1062	DB C1 BA BF	DB	0C5H,0CBH,0C3H,0BBH,0DDH,0AFH
3351	1066	BC A8 CA		
3352	1069	B7 B8 C6 CF	DB	
3353	106D	C9 D8 D3	DB	
3354	1070	D0 D7 BE C0	DB	
3355	1074	BD C4 B6		
3356	1077	C5 CB C3 BB		
3357	107B	DD AF		

```

( MSX ROM BASIC BIOS ) Macro-80      3 . 44    01-Jan-85      PAGE     40
- MSXIO - Keyboard encoding routines

3358          ; KYGRAP:
3359          107D          ; Graphic characters
3360          107D          LD      B,0
3361          107F          LD      HL,GRPTAB
3362          1082          ADD   HL,BC
3363          1083          LD      A,(HL)      ;Get from graphic key table
3364          1084          AND   A           ;Should generate some code
3365          1085          C8
3366          1086          FE    80           ;No
3367          1088          F5
3368          1089          3E    01           ;1 byte code?
3369          108A          DC    0F55         ;Assume not
3370          108B          F1
3371          108C          C3    0F55         ;Was 2 byte code, put header byte
3372          108D          CALL  C,PUTCHR
3373          108E          POP   AF
3374          108F          JP    PUTCHR
3375          1090          ; GRPTAB:
3376          1091          ; GRPTAB:
3377          1092          4F    47 41 42      DB      4FH,47H,41H,42H,43H,44H,45H
3378          1093          43    44 45      DB      46H,4DH,4EH,57H,00H,49H,00H
3379          1094          46    4D 4E 57      DB      84H,82H,81H,85H,5FH,5DH,80H
3380          1095          00    49 00      DB
3381          1096          84    82 81 85      DB
3382          1097          5F    5D 80      DB
3383          1098          83    00 5B 5A      DB      83H,00H,5BH,5AH,54H,58H,55H
3384          1099          5A
3385          10A0          54    58 55      DB      53H,4AH,56H,00H,00H,5EH,4BH
3386          10A1          53    4A 56 00      DB
3387          10A2          00    5E 4B      DB
3388          10A3          00    00 50 00      DB      00H,00H,50H,00H,52H,4CH,59H

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( MSX ROM BASIC BIOS ) Macro-80          PAGE    40-1
- MSXIO - Keyboard encoding routines      3.44    01-Jan-85

 3389  10B9   52 4C 59          DB      00H,51H,00H,5CH,48H,00H
 3390  10BC   00 51 00 5C
 3391  10C0   48 00
 3392
 3393  10C2   ; UPDATE:
 3394
 3395   ; Update pointer
 3396
 3397  10C2   23   INC    HL
 3398  10C3   7D   LD     A,L
 3399  10C4   FE   18   CP     18H
 3400  10C6   C0
 3401  10C7   21   FBF0  RET    NZ
 3402  10CA   C9
 3403  10CB   CHGET:  LD     HL,KEYBUF
 3404   ; Get one character from keyboard
 3405
 3406
 3407  10CB   E5   PUSH   HL
 3408  10CC   D5   PUSH   DE
 3409  10CD   C5   PUSH   BC
 3410  10CE   CD   FDC2  CALL   H,CHGE
 3411  10D1   CD   0D6A  CALL   CHSNS
 3412  10D4   20 0B   JR     NZ,CHGET2
 3413  10D6   CD   09DA  CALL   CKDPC0
 3414  10D9   CD   0D6A  CHGET1: CALL   CHSNS
 3415  10DC   28 FB   JR     Z,CHGET1
 3416  10DE   CD   0A27  CALL   CKERC0
 3417  10E1   21 FC9B  CHGET2: LD     HL,INTELG
 3418
 3419

```

{ MSX ROM BASIC BIOS } Macro-80
- MSXIO - Keyboard encoding routines

```

3420 10E4    7E      LD     A, (HL)   ;Code for pause?
3421 10E5    FE 04   CP     4        ;No
3422 10E7    20 02   JR     NZ, CHGET3
3423 10E9    36 00   LD     (HL), 0  ;Clear this
3424 10EB    CHGET3: LD
3425 10EB    2A F3FA LD     HL, (GETPNT)
3426 10EE    4E      LD     C, (HL)   ;Save pressed key
3427 10EF    CD 10C2   CALL   UPDATE  ;Update [GETPNT]
3428 10F2    22 F3FA   LD     (GETPNT), HL
3429 10F5    79      LD     A, C    ;Set new [GETPNT]
3430 10F6    C3 08DB   JP     PBDHRT
3431 10F9    CKCNTC: LD
3432 ;       ; Check ctrl-C
3433 ;       ;
3434 ;       ;
3435 10F9    E5      PUSH   HL
3436 10FA    21 0000   LD     HL, 0  ;To disable continuing
3437 10FD    CD 03FB   CALL   ISCNTRC
3438 1100    E1      POP    HL
3439 1101    C9      RET
3440
3441 ;       ;

```

; SUBTTL - MSXIO - Music routines

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Music routines

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```
3442          1102          F3          01-Jan-85          PAGE    41
3443          1102          D3          A0          OUT    (PSG.LW),A      ;LATCH ADDRESS
3444          1102          F3          A0          OUT    AF
3445          1103          D3          A0          PUSH   AF
3446          1105          F5          A0          LD     A,E
3447          1106          7B          A0          OUT    (PSG.DW),A      ;OUTPUT DATA
3448          1107          D3          A1          OUT    AF
3449          1102          F3          A0          OUT    AF
3450          1102          D3          A0          OUT    AF
3451          1102          F3          A0          OUT    AF
3452          1102          D3          A0          OUT    AF
3453          1102          F3          A0          OUT    AF
3454          1102          D3          A0          OUT    AF
3455          1102          F3          A0          OUT    AF
3456          1102          D3          A0          OUT    AF
3457          1102          F3          A0          OUT    AF
3458          1102          D3          A0          OUT    AF
3459          1102          F3          A0          OUT    AF
3460          1102          D3          A0          OUT    AF
3461          1102          F3          A0          OUT    AF
3462          1102          D3          A0          OUT    AF
3463          1102          F3          A0          OUT    AF
3464          1102          D3          A0          OUT    AF
3465          1102          F3          A0          OUT    AF
3466          1102          D3          A0          OUT    AF
3467          1102          F3          A0          OUT    AF
3468          1102          F3          A0          OUT    AF
3469          1103          D3          A0          OUT    (PSG.LW),A      ;LATCH ADDRESS
3470          1105          F5          A0          PUSH   AF
3471          1106          7B          A0          LD     A,E
3472          1107          D3          A1          OUT    (PSG.DW),A      ;OUTPUT DATA
```

WRTPSG:

```
; Write data to specified register of GI sound chip
; Entry - (E)=data,(A)=register number
; Exit - All regs preserved
;
; GI Reg# - usage
;
; 0 voice A fine tune
; 1 voice A coarse tune
; 2 voice B fine tune
; 3 voice B coarse tune
; 4 voice C fine tune
; 5 voice C coarse tune
; 7 B7,B6 = Reg 14,15 Input Output flags
; B5,B4,B3 = voice C,B,A noise enable (0=enabled)
; B2,B1,B0 = voice C,B,A tone enable (0=enabled)
; 8 voice A volume (0..15 = volume, 16=use envelope)
; 9 voice B volume (0..15 = volume, 16=use envelope)
; 10 voice C volume (0..15 = volume, 16=use envelope)
; 11-12 envelope period
; 13 envelope shape (0..15)
; 14 joystick 1 port
; 15 joystick 2 port
;
```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Music routines

			3 . 44	01-Jan-85	PAGE	41-1
3473	1109	FB	EI			
3474	110A	F1	POP	AF		
3475	110B	C9	RET			
3476	110C		INGI:			
3477			;	Input data from PAD		
3478			;	;		
3479	110C	3E 0E	LD	A,PSG.PA		
3480	110E	D3 A0	RDPMSG:	OUT (PSG.LW),A		
3482	110E	DB A2	IN	A, (PSG.DR)		
3483	1110	C9	RET			
3484	1112		BEEP:			
3485	1113		;	;		
3486			;	BEEP causes a 'bell' sound		
3487			;	;		
3488			;	;		
3489			;	Exit - all registers are destroyed		
3490	1113	AF	XOR	A	:[A]=fine tune register for voice A	
3491	1114	1E 55	LD	E,01010101B	;data to be written on R0	
3492	1116	CD 1102	CALL	WRTPSG		
3493	1119	5F	LD	E,A	;0 to coarse tune register	
3494	111A	3C	INC	A		
3495	111B	CD 1102	CALL	WRTPSG	;RL coarse	
3496	111E	1E BE	LD	E,10111110B	;enable voice [A] tone	
3497	1120	3E 07	LD	A,7	;[A]-voice enable register	
3498	1122	CD 1102	CALL	WRTPSG	;R7	
3499	1125	5F	LD	E,A	;set volume to 7	
3500	1126	3C	INC	A	;[A]=voice A volume register	
3501	1127	CD 1102	CALL	WRTPSG		
3502	112A	01 07D0	LD	BC,07D0H		
3503						

```

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- MSXIO - Music routines                  3.44    01-Jan-85

3504    112D    CD 1133    CALL    CSDLY1      130
3505    1130    C3 04BD    JP      GICINI      ;reset GI sound chip
3506    1133    CSDLY1:   ; Delay by [ BC ]
3507
3508
3509    1133    0B        DEC     BC
3510    1134    E3        EX      (SP),HL
3511    1135    E3        EX      (SP),HL
3512    1136    78        LD      A,B
3513    1137    B1        OR      C
3514    1138    20 F9    JR      NZ,CSDLY1
3515    113A    C9        RET
3516
3517    113B    ACTION:   ; Get action information from specified music queue. Perform
3518
3519    113B    ; action with synchronization. Called by interrupt routine
3520
3521
3522
3523
3524
3525
3526
3527
3528
3529
3530
3531
3532
3533
3534
;
```

; - Action information -

; ITEM 1 - 2 BYTES

; + Number of bytes that follow this item

; NNNNTTTTTTTTTTTT

; +Period of time

; ITEM 2, 3, 4 - FROM 1 TO 5 BYTES

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Music routines

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```

3535      ; IF HO 2 BITS = 0 then this is the HO byte of the tone period.
3536      ; IF HO 2 BITS = 2 then this is just a volume control byte.
3537      ; IF BIT 4 IS ON, envelope control is in effect, and bits
3538      ; 0-3 give shape number of envelope.
3539      ; IF BIT 4 IS OFF, BITS 0-3 give amplitude number.
3540      ; IF HO 2 BITS = 3 THEN this byte will be followed by a 2 byte
3541      ; envelope period, HO first.
3542      ;
3543      ; ENTRY - (A)=Channel count number (0...2)
3544      ;
3545      LD B,A          ; Save channel number
3546      CALL GETVCP     ; Get pointer into vcb of channel
3547      CD 1470        ; Save channel number
3548      LD HL           ; Get pointer into vcb of channel
3549      LD D,(HL)       ; Set pointer into vcb of channel
3550      LD HL           ; [DE]=countdown timer for voice
3551      LD E,(HL)       ; [DE]=countdown timer for voice
3552      LD DE           ; Decrement timer
3553      LD (HL),E       ; Put it back to first
3554      LD HL           ; Set queue ID for further 'CALL XGETQ'
3555      LD (HL),D       ; Set queue ID for further 'CALL XGETQ'
3556      LD A,D           ; No action if not zero
3557      OR E            ; Voice 0 uses queue 0
3558      RET NZ          ; Set queue ID for further 'CALL XGETQ'
3559      LD A,B           ; Voice 0 uses queue 0
3560      LD (QUEUEN),A     ; Set queue ID for further 'CALL XGETQ'
3561      CALL XGETQ       ; Set queue ID for further 'CALL XGETQ'
3562      CP 0FFH          ; Branch if EOF marker
3563      JR Z,VOLCOF     ; SAVE IN [D]
3564      LD D,A           ; Get number of following items
3565      AND 0EOH          ;

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Music routines

	3.44	01-Jan-85	PAGE	41-4
3566	1158	07	RLCA	
3567	1159	07	RLCA	
3568	115A	07	RLCA	
3569	115B	4F	LD C,A	;Save in [C]
3570	115C	7A	LD A,D	
3571	115D	E6 1F	AND 1FH	;GET LO 5 BITS OF [D]
3572	115F	77	LD (HL),A	;Set MSB of new countdown
3573	1160	CD 11E2	XGETQ	;Get LSB of new countdown
3574	1163	2B	DEC HL	
3575	1164	77	LD (HL),A	;Set it
3576	1165	0C	INC C	
3577	1166		MORACT:	
3578	1166	0D	DEC C	;Done all items?
3579	1167	C8	RET Z	;Yes
3580	1168	CD 11E2	CALL XGETQ	;Get next item from queue
3581	116B	57	LD D,A	;Save this to [D]
3582	116C	E6 C0	AND 0C0H	;Get H0 2 bits
3583	116E	20 11	JR NZ,XVOL	;Execute volume action
3584			;	
3585			;	; Set tone
3586			;	
3587	1170	CD 11E2	CALL XGETQ	;Get low byte for tone
3588	1173	5F	LD E,A	
3589	1174	78	LD A,B	;Get back voice number
3590	1175	07	RLCA	;X 2
3591	1176	CD 1102	CALL WRTPSG	;Output fine tune register
3592	1179	3C	INC A	;Point to coarse tune register
3593	117A	5A	LD E,D	;Restore saved value
3594	117B	CD 1102	CALL WRTPSG	;Output coarse tune reg
3595	117E	0D	DEC C	;Decrement since we took 2 bytes from queue
3596	117F	18 E5	JR MORACT	

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   01-Jan-85      41-5
- MSXIO - Music routines

3597    1181           XVOL:          ;[A] has junk in h which shouldn't matter
3598    ;               ;Get back voice number
3599    1181           67             ;Regs 8,9,10
3600    1182           E6  80         ;Output amplitude reg
3601    1184           28  0F         ;Check envelope generate bit
3602    ;               ;Set envelope shape if enabled
3603    ;               ;Set volume
3604    ;               ;[A] has junk in h which shouldn't matter
3605    1186           5A             ;Get back voice number
3606    1187           78             ;Regs 8,9,10
3607    1188           C6  08         ;Output amplitude reg
3608    118A           CD 1102       ;Check envelope generate bit
3609    118D           7B             ;Reg 13 for shape
3610    118E           E6  10         ;Set envelope shape if enabled
3611    1190           3E  0D         ;Set envelope shape if enabled
3612    1192           C4  1102       ;Set envelope period
3613    1195           XEPER:        ;Set envelope period
3614    ;               ;Set envelope period
3615    ;               ;Set envelope period
3616    ;               ;Set envelope period
3617    1195           7C             ;See if set envelope period
3618    1196           E6  40         ;No
3619    1198           28  CC         ;Get byte of envelope period
3620    119A           CD 11E2       ;Get low byte of envelope period
3621    119D           57             ;Register 11 for fine tune
3622    119E           CD 11E2       ;Point to coarse tune
3623    11A1           5F             ;Point to coarse tune
3624    11A2           3E  0B         ;Register 11 for fine tune
3625    11A4           CD 1102       ;Point to coarse tune
3626    11A7           3C             ;Register 11 for fine tune
3627    11A8           5A             ;Point to coarse tune

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   41-6
- MSXIO - Music routines                  3.44    01-Jan-85

3628  11A9  CD 1102          CALL    WRTPSG
3629  11AC  0D              DEC     C
3630  11AD  0D              DEC     C
3631  11AE  18 B6          JR     MORACT
3632  11B0                VOICOF:
3633          ; Comes here when an EOF mark has been found for a specified
3634          ; channel
3635          ; channel
3636          ; channel
3637  11B0  78              LD     A,B
3638  11B1  C6  08          ADD    A,8
3639  11B3  1E  00          LD     E,0
3640  11B5  CD 1102          CALL   WRTPSG
3641  11B8  04              INC    B
3642  11B9  21 FB3F          LD     HL,MUSICF
3643  11BC  AF              XOR    A
3644  11BD  37              SCF
3645  11BE                RSTFL1:
3646  11BE  17              RL A
3647  11BF  10 FD          DJNZ  RSTFL1
3648  11C1  A6              AND   (HL)
3649  11C2  AE              XOR   (HL)
3650  11C3  77              LD    (HL),A
3651  11C4                STRTMS:
3652          ; STRTMS starts the background music task if:
3653          ; 1) - it is currently idle (MUSICF=0) and
3654          ; 2) - there is work queued for it (PLYCNT .GTR. 0)
3655          ;
3656          ; LD   A,(MUSICF)
3657  11C4  3A FB3F          OR    A
3658  11C7  B7              OR    A

```

```

( MSX ROM BASIC BIOS ) Macro-80      PAGE    41-7
- MSXIO - Music routines               01-Jan-85

3659  11C8  C0                      RET     NZ
3660  11C9  21 FB40                  LD      HL, PLYCNT
3661  11CC  7E                      LD      A, (HL)
3662  11CD  B7                      OR     A
3663  11CE  C8                      RET     Z
3664  11CF  35                      DEC    (HL)
3665  11D0  21 0001                 LD      HL, 1
3666  11D3  22 FB41                  LD      (VCBA), HL
3667  11D6  22 FB66                  LD      (VCBB), HL
3668  11D9  22 FB8B                  LD      (VCBC), HL
3669  11DC  3E 07                  LD      A, 0111B
3670  11DE  32 FB3F                  LD      (MUSICF), A
3671  11E1  C9                      RET
3672  XGETQ:                         ;Get queue ID
3673
3674  11E2  3A FB3E                  LD      A, (QUEUEN)
3675  11E5  E5                      PUSH   HL
3676  11E6  D5                      PUSH   DE
3677  11E7  C5                      PUSH   BC
3678  11E8  CD 14AD                  CALL   GETQ
3679  11EB  C3  08DB                  JP     PBDHRT
3680
3681 ;SUBTTL - MSXIO - Joystick and Paddle interface

```

(MSX ROM BASIC BIOS) Macro-80 3 .44 01-Jan-85 PAGE 42
- MSXIO - Joystick and Paddle interface

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```
3682      11EE          GTSTCK:           ;  
3683      11EE          ;  
3684      3D             DEC               A  
3685      FA 1200        JP    M,KYSTCK   ;STICK(0) - read cursor keys  
3686      CD 120C        CALL  SLSTCK    ;Read joystick  
3687      21 1233        LD    HL ,STKTBL  
3688      11F8          STICK1:          AND   0FH  
3689      E6 0F           LD    E,A  
3690      11F8          5F             LD    D,0  
3691      11FA          16 00          LD    HL,DE  
3692      11FB          19             ADD  A,(HL)  
3693      11FD          19             ADD  HL,DE  
3694      11FE          7E             LD    A,(HL)  
3695      11FF          C9             RET  
3696      1200          RYSTCK:         ;  
3697      CD 1226        ;  
3698      1200          CALL  GTRW8     ;Read keyboard  
3699      1203          0F             RRCA  ;Move cursor status to lower four bits  
3700      1204          0F             RRCA  
3701      1205          0F             RRCA  
3702      1206          0F             RRCA  
3703      1207          21 1243        LD    HL,KSTKTB  
3704      120A          18 EC           JR    STICK1  
3705      120C          SLSTCK:         ;  
3706      47             ; Select proper joystick and read from it  
3707      ;  
3708      3E 0F           LD    B,A  
3709      F3             LD    A,PSG.PB  
3710      CD 110E        DI    CALL  RDPSG    ;Read what is currently output to port B  
3711      120F          CD 110E        CALL  RDPSG
```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   42-1
- MSXIO - Joystick and Paddle interface

3713    1213    10 06          SJNZ    SLSTC1           ;STICK(1)
3714    1215    E6 DF          AND     0DFH            ;Make sure P8 is low state
3715    1217    F6 4C          OR      4CH             ;Select joystick 2, enable P6,P7
3716    1219    18 04          SLSTC2
3717    121B
3718
3719    121B    E6 AF          AND     0AFH           ;Select joystick 1, make sure P8 is low state
3720    121D    F6 03          OR      3               ;Enable P6,P7
3721    121F
3722    121F    D3 A1          OUT    (PSG.DW),A
3723    1221    CD 110C         CALL   INGI           ;Read status of joystick port
3724    1224    FB
3725    1225    C9
3726    1226    RET
3727
3728
3729
3730
3731    1226    F3
3732
3733
3734
3735
3736
3737
3738
3739
3740
3741
3742
3743
;
```

; Get keyboard's 8th row, bit assignments are as follows.

; RDULxxxxS

; | | | | +----- space
; | | | +----- left
; | | +----- up
; | +----- down
; +----- right

; DI
; IN A,(PPI.CR)
; AND 0F0H
; ADD A,8
; OUT (PPI.CW),A
; IN A,(PPI.BR)

(MSX ROM BASIC BIOS) Macro-80
 - MSXIO - Joystick and Paddle interface

			Macro-80	3.44	01-Jan-85	PAGE
3744	1231	FB	EI			
3745	1232	C9	RET			
3746			;			
3747	1233		STRKTB:			
3748	1233	00		DB	0	
3749	1234	05		DB	5	
3750	1235	01		DB	1	
3751	1236	00		DB	0	
3752	1237	03		DB	3	
3753	1238	04		DB	4	
3754	1239	02		DB	2	
3755	123A	03		DB	3	
3756	123B	07		DB	7	
3757	123C	06		DB	6	
3758	123D	08		DB	8	
3759	123E	07		DB	7	
3760	123F	00		DB	0	
3761	1240	05		DB	5	
3762	1241	01		DB	1	
3763	1242	00		DB	0	
3764			;	KSTKTB:		
3765	1243			DB	0	
3766	1243	00		DB	3	
3767	1244	03		DB	5	
3768	1245	05		DB	4	
3769	1246	04		DB	1	
3770	1247	01		DB	2	
3771	1248	02		DB	0	
3772	1249	00		DB	3	
3773	124A	03		DB	7	
3774	124B	07				

```

00          DB      0      ; BF
124C       DB      6      ; B L
124D       DB      5      ; B
124E       DB      8      ; FL
124F       DB      1      ; F
06          DB      7      ; L
05          DB      0      ; ;
08          DB      0      ; ;
01          DB      1      ; ;
07          DB      7      ; ;
00          DB      0      ; ;

; GTTRIG:
1253       3D      A      M,KEYTRG
3784       1253   DEC     JP      AF
3785       1254   DEC     PUSH    AF
3786       1254   FA     126C
3787       1257   F5
3788       1258   E6     01
3789       125A   CD     120C
3790       125D   C1
3791       125E   05
3792       125F   05
3793       1260   06     10
3794       1262   FA     1267
3795       1265   06     20
3796       1267   A0
3797       1267   AND    B
3798       1268   TRIG2:
3799       1268   D6     01
3800       126A   9F
3801       126B   C9
3802       126C
3803       126C   CD     1226
3804       126F   E6     01

;STRIG(0), use keyboard
;Read joystick
;Read space status
;Read keyboard
;Extract trigger status
;Return 255 if [Acc]=0, 0 if non-0
;Read space status
;Read keyboard
;Extract trigger status
;Read space status

```

```

( MSX ROM BASIC BIOS ) Macro-80          3 .44      01-Jan-85      PAGE    42-4
- MSXIO - Joystick and Paddle interface

3806    1271    18 F5          GTPDL:        JR      TRIG2
3807    1273
3808
3809
3810
3811
3812
3813
3814
3815
3816
3817
3818
3819
3820
3821
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3831
3832
3833
3834
3835
3836

; Get value of paddle
; Input parameters (passed via [Acc])
;
; 1 - Paddle A connected to joystick port 1
; 2 - Paddle A connected to joystick port 2
; 3 - Paddle B connected to joystick port 1
; 4 - Paddle B connected to joystick port 2
; 5 - Paddle C connected to joystick port 1
; 6 - Paddle C connected to joystick port 2
; 7 - Paddle D connected to joystick port 1
; 8 - Paddle D connected to joystick port 2
; 9 - Paddle E connected to joystick port 1
; 10 - Paddle E connected to joystick port 2
; 11 - Paddle F connected to joystick port 1
; 12 - Paddle F connected to joystick port 2
;

;Force parameter 2 based
INC A
AND A
RRA
PUSH AF
LD B,A
XOR A
SCF

PDL1:
RLA
DJNZ PDL1
LD B,A
;Form mask pattern
;Save port # (carry reset if port 1)
;
```

	(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	42-5
- MSXIO -	Joystick and Paddle interface					
3837	127E	F1		POP	A,F	
3838	127F	0E 10		LD	C,10H	; Assume port 1
3839	1281	11 03AF		LD	DE,03AFH	
3840	1284	30 05		JR	NC,PDL1	
3841	1286	0E 20		LD	C,' '	;Good assumption
3842	1288	11 4C9F		LD	DE,4C9FH	
3843	128B		PDL1:			
3844	128B	3E 0F		LD	A,PSG.PB	
3845	128D	F3		DI		
3846	128E	CD 110E		CALL	RDPSC	;Get current port B content
3847	1291	A3		AND	E	
3848	1292	B2		OR	D	
3849	1293	Bl		OR	C	
3850	1294	D3 A1		OUT	(PSG.DW),A	;Set trigger high
3851	1296	A9		XOR	C	
3852	1297	D3 A1		OUT	(PSG.DW),A	;Set trigger low again
3853	1299	3E 0E		LD	A,0EH	
3854	129B	D3 A0		OUT	(PSG.LW),A	
3855	129D	0E 00		LD	C,0	;Initialize counter
3856	129F		PDL2:			
3857	129F	DB A2		IN	A,(PSG.DR)	
3858	12A1	A0		AND	B	;End of pulse?
3859	12A2	28 05		JR	Z,PDL3	;Yes
3860	12A4	0C		INC	C	;Bump counter
3861	12A5	C2 129F		JP	NZ,PDL2	;No overflow yet
3862	12A8	0D		DEC	C	;Make it 255
3863	12A9		PDL3:			
3864	12A9	FB		EI		
3865	12AA	79		LD	A,C	
3866	12AB	C9		RET		
3867	12AC		GTPAD:			

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE    42-6
- MSXIO - Joystick and Paddle interface

; Read touch pad (NEC PC-6051 compatible)
; Input parameter (passed via [Acc])
;
; 0 - sense touch pad status --- |for touch pad connected
; 1 - return X coordinate |to joystick port 1
; 2 - return Y coordinate |to joystick port 1
; 3 - return switch status ----
;
; 4 - sense touch pad status --- |for touch pad connected
; 5 - return X coordinate |to joystick port 2
; 6 - return Y coordinate |
; 7 - return switch status ----
;
; Result is returned via [Acc]. As for status, 255 is returned
; if true, 0 if false.
;
; Read pad connected to port 1
; Assume so
; Good assumption
; Connected to port 2
;
GTPDP1:          DEC     A           ;Argument=0?
                                JP     M,GTPAD0   ;If so, read pad and return status
                                DEC     A
                                LD     A,(PADX)  ;Assume PAD(1) - X coordinate
                                RET    M           ;Good assumption
                                LD     A,(PADY)  ;Return Y coordinate
                                RET    Z
;
3885          12AC          FE 04
3886          12AE          11 0CEC
3887          12B1          38 05
3888          12B3          11 03D3
3889          12B6          D6 04
3890          12B8          3D
3891          12B8          3D
3892          12B9          FA 12C5
3893          12BC          3D
3894          12BD          3A FC9D
3895          12C0          F8
3896          12C1          3A FC9C
3897          12C4          C8

```

(MSX ROM BASIC BIOS) Macro-80 3 . 44 01-Jan-85 PAGE 42-7

- MSXIO - Joystick and Paddle interface

```

3899 12C5      GTPAD0:    PUSH AF      ;Save status (minus if PAD(0) specified)
3900 12C5      EX   DE,HL   ;[L]=bits that are not to be modified
3901 12C6      LD   (RUNFLG),HL ;[H]=bits that are to be added
3902 12C7      22 F866
3903 12CA      9F
3904 12CB      2F
3905 12CC      E6 40
3906 12CE      4F
3907 12CF      3E 0F
3908 12D1      F3
3909 12D2      CD 110E
3910 12D5      E6 BF
3911 12D7      B1
3912 12D8      D3 A1
3913 12DA      F1
3914 12DB      FA 12E8
3915 12DE      CD 110C
3916 12E1      FB
3917 12E2      E6 08
3918 12E4      D6 01
3919 12E6      9F
3920 12E7      C9
3921 12E8      TRYAGN:
3922 ;          ;
3923 12E8      0E 00
3924 12EA      CD 1332
3925 12ED      CD 1332
3926 12F0      38 28
3927 12F2      CD 1320
3928 12F5      38 23
3929 12F7      D5

;[L]=bits that are not to be modified
;[H]=bits that are to be added
;0 if port 1 specified, 100 octal if port 2
;disable interrupt till done
;Select proper port
;PAD(0) specified
;in z
;sense Panel input and select x
;branch if no input
;read first coordinate
;branch if input released
;save for comparison

GTPAD0:    PUSH AF      ;Save status (minus if PAD(0) specified)
            EX   DE,HL   ;[L]=bits that are not to be modified
            LD   (RUNFLG),HL ;[H]=bits that are to be added
            SBC  A,A
            CPL
            AND 0100000B
            LD   C,A      ;0 if port 1 specified, 100 octal if port 2
            LD   A,PSG.PB
            DI   ;disable interrupt till done
            CALL RDPSG
            AND 0BFH
            OR   C
            OUT (PSG.DW),A
            POP AF
            JP   M,TRYAGN
            INGI
            CALL EI
            AND 8
            SUB 1
            SBC A,A
            RET

TRYAGN:   ;          ;
            LD   C,0      ;in z
            CALL RDPPAD
            CALL REDPAD
            JR   C,PADX1
            CALL REDCOD
            JR   C,PADX1
            PUSH DE

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    42-8
- MSXIO - Joystick and Paddle interface   3.44    01-Jan-85

3930  12F8  CD 1320          CALL    REDCOD
3931  12FB  C1              POP    BC
3932  12FC  38 1C          JR     C, PADX1
3933  12FE  78              LD     A,B
3934  12FF  92              SUB    D
3935  1300  30 02          JR     NC,NONEGL1
3936  1302  2F              CPL    INC A
3937  1303  3C              INC
3938  1304          NONEGL1: CP     5
3939  1304  FE 05          ;less than 5?
3940  1306  30 E0          ;no, try again
3941  1308  79              LD     A,C
3942  1309  93              SUB    E
3943  130A  30 02          JR     NC,NONEGL2
3944  130C  2F              CPL    INC A
3945  130D  3C              INC
3946  130E          NONEGL2: CP     5
3947  130E  FE 05          ;less than 5?
3948  1310  30 D6          ;no, try again
3949  1312  7A              LD     A,D
3950  1313  32 FC9D          LD     (PADX),A
3951  1316  7B              LD     A,E
3952  1317  32 FC9C          LD     (PADY),A
3953  131A          PADX1: EI
3954  131A  FB              LD     A,H
3955  131B  7C              SUB    1
3956  131C  D6 01          SBC    A,A
3957  131E  9F              RET
3958  131F  C9              REDCOD:
3959  1320
3960

```

(MSX ROM BASIC BIOS) Macro-80 3 .44 01-Jan-85
 - MSXIO - Joystick and Paddle inter face

```

3961          ; Read X,Y coordinate into [ D,E ]
3962          ;
3963      1320  0E 0A          ; change to channel to [ Y ] when done
3964      1322  CD 1332          ; read [ X ]
3965      1325  D8          ; return if input released
3966      1326  55
3967      1327  D5          ; change to [ X ] after read
3968      1328  0E 00          ; read [ Y ]
3969      132A  CD 1332          ; store Y read out
3970      132D  D1          ; clear carry
3971      132E  5D          ; force input is OK
3972      132F  AF          ; store X read out
3973      1330  67
3974      1331  C9          ; set if input released
3975      1332  RET          ; make sure AD completed
3976          ; input 8 bits
3977          ; input channel# after done
3978          ; serial clock (SCK)=1
3979          ; read touch panel input into [ L ]
3980      1332  CD 135B          ; read PAD
3981      1335  06 08          ; save SENSE status
3982      1337  51          ; read RRA
3983      1338  CB 82          ; read RRA
3984      1338  CB 92          ; read RRA
3985      133A  CD 136D          ; read RRA
3986      133C  CD 110C          ; read RRA
3987      133F  CD 110C          ; read RRA
3988      1342  67
3989      1343  1F
3990      1344  1F
3991      1345  1F

```

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```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   42-10
- MSXIO - Joystick and Paddle interface

3992  1346   CB 15          RL      L      ;bit 2 to LSB of [L]
3993  1348   CB C2          SET    0,D
3994  134A   CB D2          SET    2,D
3995  134C   CD 136D        CALL   OUTGI
3996  134F   10 E7          DJNZ  REDLOP
3997  1351   CB E2          SET    4,D
3998  1353   CB EA          SET    5,D
3999  1355   CD 136D        CALL   OUTGI
4000  1358   7C             LD     A,H
4001  1359   1F             RRA
4002  135A   C9             RET
4003  135B   C9             CHKEOC:
4004
4005           ; Check and wait for EOC
4006
4007  135B   3E 35          LD     A,001110101B
4008  135D   B1             OR     C
4009  135E   57             LD     D,A
4010  135F   CD 136D        CALL   OUTGI
4011  1362   EOCCCHK:       ;reset CS
4012  1362   CD 110C        CALL   INGI
4013  1365   E6 02          AND   2
4014  1367   28 F9          JR     Z,EOCCHK
4015  1369   CB A2          RES   4,D
4016  136B   CB AA          RES   5,D
4017  136D   C9             OUTGI:
4018
4019           ; Output [D] to PAD
4020
4021  136D   E5             PUSH  HL
4022  136E   D5             PUSH  DE

```

(MSX ROM BASIC BIOS)	Macro-80	3 . 44	01-Jan-85	PAGE	42-11
- MSXIO - Joystick and Paddle interface					

```

4023 136F 2A F866 LD HL, (RUNFLG) ;Also known as [ PADWRK ]
4024 1372 7D LD A,L
4025 1373 2F CPL
4026 1374 A2 AND D
4027 1375 57 LD D,A
4028 1376 3E OF LD A,PSG.PB
4029 1378 D3 A0 OUT (PSG.LW),A
4030 137A DB A2 IN A,(PSG.DR)
4031 137C A5 AND L
4032 137D B2 OR D
4033 137E B4 OR H
4034 137F D3 A1 OUT (PSG.DW),A
4035 1381 D1 POP DE
4036 1382 E1 POP HL
4037 1383 C9 RET
4038
4039
;
```

SUBTTL - MSXIO - Misc. routines for MSXIO

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   43
- MSXIO - Misc. routines for MSXIO          3.44    01-Jan-85

4040
4041  1384      STMOTR:    AND     A
4042  1384      A7          JP      M,FLPMOT ;Flip motor switch
4043  1385      FA 1392    STMOT1:   JR     NZ,MOTRON
4044  1388      20 03     LD     A,00001001B ;Stop motor
4045  1388      3E 09     DB     0C2H    ;Skip next 2 bytes ('JNZ' instruction)
4046  138A      C2          MOTRON:
4047  138C      138D      LD     A,8      ;NMI handler
4048  138D      3E 08     OUT    (PPI.CM),A
4049  138F      D3 AB     RET
4050  138F      1391      C9          FLPMOT:
4051  1391      1392      ;           IN     A,(PPI.CR)
4052  1392      DB AA     AND    10H
4053  1392      E6 10     JR     STMOT1
4054  1394      1396      NMI:
4055  1394      18 F0     ;           IN     H.NMI
4056  1396      1398      ;           CALL   RETN
4057  1398      139B      ;           ;RETN
4058
4059
4060
4061  1398      CD FDDE   CALL   H.NMI
4062  139B      ED 45     ;           ;RETN

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44      01-Jan-85      PAGE   44
- MSXIO - Misc. routines for MSXIO

4063
4064      ;           ; Initialize function key strings
4065      139D      ; INFNK:
4066      ;           ; INFNK:
4067      ;           ; Initialize function key strings
4068      ;           ; FNKDEF:
4069      139D      01 00A0      LD      BC,0A0H
4070      13A0      11 F87F      LD      DE,FNKSTR
4071      13A3      21 13A9      LD      HL,FKTABL
4072      13A6      ED B0      LDIR
4073      13A8      C9      RET
4074      ;           ; FNKDEF:
4075      13A9      63 6F 6C 6F      DB      "color "
4076      13A9      72 20      DS      10
4077      13AD      61 75 74 6F      DB      "auto "
4078      13AF      20      DS      11
4079      13B9      67 6F 74 6F      DB      "goto "
4080      13BD      20      DS      "list "
4081      13BE      6C 69 73 74      DS      11
4082      13C9      20      DS      "color 15,4,7"
4083      13CD      0D      DB      "run"
4084      13CE      0D      DB      13
4085      13D9      20      DS      12
4086      13DD      0D      DB      13
4087      13DE      0D      DS      11
4088      13E9      72 75 6E      DB      "run"
4089      13EC      0D      DB      13
4090      13ED      63 6F 6C 6F      DS      12
4091      13F9      72 20 31 35      DB      "color 15,4,7"
4092      13FD      2C 34 2C 37      DS      13
4093

```

	(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	44-1
- MSXIO -	Misc. routines for MSXIO					
4094	1405	0D	DB	13		
4095	1406		DS	3		
4096	1409	63 6C 6F 61	DB	"cload"		
4097	140D	64				
4098	140E	22	DB	34		
4099	140F		DS	10		
4100	1419	63 6F 6E 74	DB	"cont"		
4101	141D	0D	DB	13		
4102	141E		DS	11		
4103	1429	6C 69 73 74	DB	"list."		
4104	142D	2E				
4105	142E	0D 1E 1E	DB	13,30,30		
4106	1431		DS	8		
4107	1439	0C	DB	12		
4108	143A	72 75 6E	DB	"run"		
4109	143D	0D	DB	13		
4110	143E		DS	11		
4111		;				
4112	1449		RVDP:			
4113		;				
4114	1449	DB 99	IN	A, (VDP.SR)		
4115	144B	C9	RET			
4116	144C		RSLREG:			
4117		;				
4118	144C	DB A8	IN	A, (PPI.AR)		
4119	144E	C9	RET			
4120	144F		WSLREG:			
4121		;				
4122	144F	D3 A8	OUT	(PPI.AW),A		
4123	1451	C9	RET			
4124	1452		SNSMAT:			

(MSX ROM BASIC BIOS) Macro-80
- MSXIO - Misc. routines for MSXIO

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15.1

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PAGE

44-2

```

4125      ;                               PAGE    44-2
4126      1452    4F      ;                               15.1
4127      1453    F3      LD      C,A
4128      1454    DB AA   DI      A,(PPI.CR)
4129      1456    E6 F0   IN      0F0H   ;Get what is currently output to Port C
4130      1458    81      AND    A,C   ;Leave higher 4 bits unaffected
4131      1459    D3 AA   ADD    (PPI.CW),A
4132      145B    DB A9   OUT    A,(PPI.BR) ;Select row
4133      145D    FB      IN     EI   ;Get column information of selected row
4134      145E    C9      RET
4135      145F    ;ISFILEO:
4136      ;       ; Check if we're doing device I/O
4137      ;       ; Save [H,L]
4138      ;       ; Get file pointer
4139      145F    CD FEDF  CALL   H.ISFL
4140      1462    E5      PUSH   HL
4141      1463    2A F864  LD     HL,(PTRFILE)
4142      1466    7D      LD     A,L
4143      1467    B4      LD     A,L
4144      1468    E1      OR     H
4145      1469    C9      POP    HL   ;No zero?
4146      146A    ;DCOMPR:
4147      ;       ; COMPARES [H,L] with [D,E] unsigned
4148      ;       ; [H,L] less than [D,E] set carry
4149      ;       ; [H,L] = [D,E] set zero
4150      ;       ; [A] is the only register used
4151      ;       ; [A] is the only register used
4152      ;       ; [A] is the only register used
4153      ;       ; [A] is the only register used
4154      146A    7C      LD     A,H
4155      ;       ; [A] is the only register used

```

{ MSX ROM BASIC BIOS } Macro-80
- MSXIO - Misc. routines for MSXIO

		PAGE	44-3
4156	146B	92	
4157	146C	C0	
4158	146D	7D	
4159	146E	93	
4160	146F	C9	
4161	1470		
4162			GETVCP:
4163			;
4164			;
4165			;
4166			;
4167	1470	2E 02	
4168	1472	18 03	
4169	1474		GETVC2:
4170			;
4171			;
4172			;
4173			;
4174			;
4175	1474	3A FB38	
4176	1477		GETVC1:
4177			;
4178			;
4179			;
4180			;
4181			;
4182			;
4183	1477	D5	
4184	1478	11 FB41	
4185	147B	26 00	
4186	147D	19	

; Entry - [A] = voice id (0..2)
 ; Exit - [HL] = pointer to QLENGX for voice (within static var buf)
 ; [A] = 0 . All other registers preserved.
 ;
 LD L,2
 JR GETVC1
 ;
 ; Entry - [L] = desired displacement into voice buffer
 ; Exit - [HL] = pointer to desired variable for voice VOICEN
 ; [A] = 0 . All other registers preserved.
 ;
 LD A,(VOICEN)
 ;
 ; Entry - [A] = voice id (0..2)
 ; [L] = desired displacement into voice buffer
 ; Exit - [HL] = pointer to desired variable for voice VOICEN
 ; [A] = 0 . All other registers preserved.
 ;
 PUSH DE
 LD DE,VCBA
 LD H,0
 ADD HL,DE

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   44-4
- MSXIO - Misc. routines for MSXIO

 4187    147E    B7          OR      A
 4188    147F    28 07      JR      Z,GETVCX
 4189    1481    11 0025      LD      DE,25H      ; VCB size
 4190    1484    19          GETVCL:
 4191    1484    19          ADD     HL,DE
 4192    1485    3D          DEC     A
 4193    1486    20 FC      JR      NZ,GETVCL
 4194    1488    D1          GETVCX:
 4195    1488    C9          POP    DE
 4196    1489    C9          RET
 4197    148A    PHYDIO:
 4198    ;           ;           CALL   H.PHYD
 4199    148A    CD FFA7      ;           ;           CALL   H.PHYD
 4200    148D    C9          RET
 4201    148E    FORMAT:
 4202    ;           ;           CALL   H.FORM
 4203    148E    CD FFAC      ;           ;           CALL   H.FORM
 4204    1491    C9          RET
 4205    SUBTTL - QUEUTL - Queue utility routines

```

(MSX ROM BASIC BIOS) Macro-80
- QUEUTL - Queue utility routines

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```
4206 ; Copyright (C) 1980 by Microsoft Corporation
4207 ; Written by Marc Wilson
4208 ;
4209 ;
4210 ;
4211 ;
4212 ;
4213 ;
4214 ;
4215 ;
4216 ;
4217 ;
4218 ;
4219 ;
4220 ;
4221 ;
4222 ;
4223 ;
4224 ;
4225 ;
4226 ;
4227 ;
4228 ;
4229 ;
4230 ;
4231 ;
4232 ;
4233 ;
4234 ;
4235 ;
4236 ;
```

; This utility provides for multiple queues with the following capabilities:

; Queues of varying length - 1,3,7,15,31,63,127,255

; Each queue can be any of the possible lengths

; The queues can be initialized at any time and be located anywhere a single pointer (QUEUES) provides the address of the queue table.

; The queue table has all information for each queue, 6 bytes per queue. A single non-zero character can be pushed back on top of the queue table.

; The entry for each queue is as follows:

; +0	PUT OFFSET
; +1	GET OFFSET
; +2	BACK CHARACTER
; +3	QUEUE LENGTH
; +4,+5	QUEUE ADDRESS

; The utility assumes that the queue table is valid for all queue numbers passed to the routines

;ROUTINES:

; All routines assume that [A] equals the queue number,

; [QUEUES] contains the address of the queue table.

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{ MSX ROM BASIC BIOS } Macro-80
- QUEUTL - Queue utility routines

```

4237 ; Other requirements follow.
4238 ;   GETQ  - Returns current top of queue in [A],
4239 ;   zero flag set if queue empty
4240 ;   PUTQ  - Puts byte in [E] reg on end of queue,
4241 ;   zero set if queue is full
4242 ;
4243 ;NOTE:
4244 ; The routines are designed to be reentrant, however
4245 ; there are some restrictions for cases involving a
4246 ; single queue (in any case operating on different
4247 ; queues is alright). The first restriction is that
4248 ; the same routine cannot be reentered. The second
4249 ; is that INITQ and POPQ do not allow PUTQ,
4250 ; GETQ or BCKQ to be entered.
4251 ; LFTQ  - Returns unused number of bytes in queue in [A] reg
4252 ; INITQ - Initialize queue to empty state,
4253 ;          B reg=1,length, (DE)=ADDR
4254 ;          *** All routines destroy the registers ***
4255
4256 ;SUBTTL - QUEUTL - Queue routines
4257

```

(MSX ROM BASIC BIOS) Macro-80
 - QUEUTL - Queue routines

```

4258      1492          PUTQ:
4259      1492          ; Put data on queue
4260      1492          ; Put data on queue
4261      1492          ; Put data on queue
4262      1492          ; Put data on queue
4263      1492          CD 14FA
4264      1495          78   CALL GETPTR
4265      1496          3C   LD A,B
4266      1497          23   INC A
4267      1498          A6   INC HL
4268      1499          B9   AND (HL)
4269      149A          C8   CP C
4270      149B          E5   RET z
4271      149C          2B   PUSH HL
4272      149D          2B   DEC HL
4273      149E          2B   DEC HL
4274      149F          E3   EX (SP),HL
4275      14A0          23   INC HL
4276      14A1          4F   LD C,A
4277      14A2          7E   LD A,(HL)
4278      14A3          23   INC HL
4279      14A4          66   LD H,(HL)
4280      14A5          6F   LD L,A
4281      14A6          06   LD B,0
4282      14A8          09   ADD HL,BC
4283      14A9          73   LD (HL),E
4284      14AA          E1   POP HL
4285      14AB          71   LD (HL),C
4286      14AC          C9   RET
4287      14AD          ; Set new pointer
4288          ; GetQ:

```

(MSX ROM BASIC BIOS) Macro-80
 - QUEUTL - Queue routines

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```

4289          ; Get data from QUEUE
4290          ; 
4291          CD 14FA          ; Get queue pointers
4292          14B0 36 00          ; (HL),0
4293          14B2 20 1D          ; NZ,GETBAK
4294          14B4 79             ; zero back character
4295          14B5 B8             ; A,C
4296          14B6 C8             ; A
4297          14B7 23             ; CP,B
4298          14B8 3C             ; RET,Z
4299          14B9 A6             ; INC,HL
4300          14BA 2B             ; INC,A
4301          14BB 2B             ; AND,(HL)
4302          14BC E5             ; DEC,HL
4303          14BD 23             ; PUSH,HL
4304          14BE 23             ; INC,HL
4305          14BF 23             ; INC,HL
4306          14C0 4F             ; INC,HL
4307          14C1 7E             ; LD,C,A
4308          14C2 23             ; LD,A,(HL)
4309          14C3 66             ; LD,(HL)
4310          14C4 6F             ; LD,L,A
4311          14C5 06 00          ; LD,B,0
4312          14C7 09             ; ADD,HL,BC
4313          14C8 7E             ; LD,A,(HL)
4314          14C9 E1             ; POP,HL
4315          14CA 71             ; LD,(HL),C
4316          14CB B7             ; OR,A
4317          14CC C0             ; RET,NZ
4318          14CD 3C             ; INC,A
4319          14CE 3E 00          ; LD,A,0

```

; Get data from QUEUE
 ; Get queue pointers
 ; zero back character
 ; QUEUE empty!
 ; Bump GET offset
 ; wrap around
 ; Save place to store pointer
 ; offset in C
 ; [HL] = QUEUE address
 ; get char from QUEUE

MSX ROM BASIC BIOS) Macro-80 3

QUESTION = One or more routes

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(MSX ROM BASIC BIOS) Macro-80
- QUEUTL - Queue routines

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```
4351 14EB CD 14FA          CALL    CETPTR      ;Get QUEUE ptrs
4352 14EE 78              LD      A,B
4353 14EF 3C              INC    A
4354 14F0 23              INC    HL
4355 14F1 A6              AND    (HL)
4356 14F2 47              LD      B,A
4357 14F3 79              LD      A,C
4358 14F4 90              SUB    B
4359 14F5 A6              AND    (HL)
4360 14F6 6F              LD      L,A
4361 14F7 26 00           LD      H,0
4362 14F9 C9              RET
4363 14FA               GETPTR:
4364 14FA               ; GETPTR:
4365 ;                 ; QUEUE general routines
4366 ;                 ; QUEUE general routines
4367 14FA CD 1504          CALL    QSTART     ;Get start of QUEUE TABLE entry
4368 14FD 46              LD      B,(HL)   ;B = PUT OFFSET
4369 14FE 23              INC    HL
4370 14FF 4E              LD      C,(HL)   ;C = GET OFFSET
4371 1500 23              INC    HL
4372 1501 7E              LD      A,(HL)   ;A = BACK CHARACTER
4373 1502 B7              OR     A
4374 1503 C9              RET
4375 ;                 ; QSTART:
4376 1504               QSTART: RLC A
4377 1504 07              LD      B,A      ;*2
4378 1505 47              RLC A
4379 1506 07              ADD   A,B      ;*4
4380 1507 80              ADD   A,B      ;*6
4381
```

(MSX ROM BASIC BIOS) Macro-80			PAGE 46-4
- QUEUTTL - Queue routines			
4382	1508	4F	LD C,A
4383	1509	06 00	LD B,0
4384	150B	2A F3F3	LD HL,(QUEUES)
4385	150E	09	ADD HL,BC
4386	150F	C9	RET
4387			SUBTTL - MSXGRP - Graphic driver (Print a character on GRP screen)

(MSX ROM BASIC BIOS) Macro-80
 - MSXGRP - Graphic driver (Print a character on GRP screen

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```

4388      1510          GRPRT:
4389      1510          ; Print a character on the graphic screen
4390      1510          ;
4391      1510          ; Print a character on the graphic screen
4392      1510          ;
4393      1510          E5          PUSH    HL
4394      1511          D5          PUSH    DE
4395      1512          C5          PUSH    BC
4396      1513          F5          PUSH    AF
4397      1514          CD 089D    CALL   CNVCHR
4398      1517          30 62     JR      NC,JPPPAL
4399      1519          20 08     JR      NZ,GPRT05
4400      151B          FE 0D     CP      0DH
4401      151D          28 5F     JR      Z,GRPCR
4402      151F          FE 20     CP      ,
4403      1521          38 58     JR      C,JPPPAL
4404      1523          GPRT05:
4405      1523          CD 0752    CALL   GETPAT
4406      1526          3A F3E9    LD      A,(FORCLR)
4407      1529          32 F3F2    LD      (ATRBYT),A
4408      152C          2A FCB9    LD      HL,(GRPACY)
4409      152F          EB          EX      DE,HL
4410      1530          ED 4B FCB7  LD      BC,(GRPACX)
4411      1534          CD 1599    CALL   SCALXY
4412      1537          30 42     JR      NC,JPPPAL
4413      1539          CD 15DF    CALL   MAPXYC
4414      153C          11 FC40    LD      DE,PATWRK
4415      153F          0E 08     LD      C,8
4416      1541          GPRT10:
4417      1541          06 08     LD      B,8
4418      1543          CD 1639    CALL   FETCHC

```

; Convert code
; Graphic header byte, return soon
; Converted graphic code
; CR?
; Do not ignore CR even on graphic screen
; Control character?
; Yes, ignore this
; Get character pattern in PATWRK
; Set color of character
; Current Y coordinate in [DE]
; Current X coordinate in [BC]
; Do the scaling
; Do not print if already out of screen
; Map to CLOC and CMASK
; Row counter
; Column counter
; Get current CLOC and CMASK

```

( MSX ROM BASIC BIOS ) Macro driver (Print a character on GRP screen
- MSXGRP - Graphic driver (Print a character on GRP screen
          3 .44   01-Jan-85
          PAGE   47-1

4419    1546    E5      PUSH   HL
4420    1547    F5      PUSH   AF
4421    1548    1A      LD     A,(DE)      ;Save these
4422    1549    87      GPRT20: ADD    A,A
4423    154A    F5      PUSH   AF
4424    154B    DC 167E  CALL   C,SETC      ;Get pattern for a row
4425    154C    CD 16AC  CALL   TRIGHT     ;Check each bit
4426    1551    E1      POP    HL
4427    1552    38 04   JR     C,GPRT30      ;Set it if 1
4428    1554    E5      PUSH   HL
4429    1555    F1      POP    AF
4430    1556    10 F1   DJNZ   GPRT20      ;Move 1 pixel right
4431    1558    F1      POP    AF
4432    1558    F1      POP    AF
4433    1558    F1      POP    HL      ;Loop till done all columns
4434    1559    E1      POP    HL
4435    155A    CD 1640  STOREC
4436    155D    CD 170A  CALL   TDOWNC      ;Restore CLOC and CMASK
4437    1560    38 04   JR     C,GPRT40      ;Set these
4438    1562    13      INC    DE
4439    1563    0D      DEC    C
4440    1564    20 DB   JR     NZ,GPRT10      ;Move 1 pixel down
4441    1566    CD 15D9  GPRT40: CALL   CHKMOD      ;Out of screen, skip rest and return
4442    1566    CD 15D9  LD     A,(GRPACX)
4443    1569    3A FCB7  JR     Z,GPRT50      ;Point to next row
4444    156C    28 06   ADD    A,
4445    156E    C6 20   JR     C,GRPCR      ;We're in high-resolution mode
4446    1570    38 0C   JR     GPRT60      ;We're going out of screen
4447    1572    18 04   GPRT50: ;
4448    1574
4449

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE   47-2
- MSXGRP - Graphic driver (Print a character on GRP screen

4450    1574    C6 08      ADD      A,8
4451    1576    38 06      JR      C,GRPCR
4452    1578      GPRT60: ;Update cursor position
4453    1578    32 FCB7    LD      (GRPACX),A
4454    157B      JPPPAL: ;Update cursor position
4455    157B    C3 08DA    JP      POPALL
4456    157E      GPRCR: ;
4457      ;             XOR      A
4458    157E    AF      LD      (GRPACX),A
4459    157F    32 FCB7    XOR      A
4460    1582    CD 15D9    LD      CHKMOD
4461    1585    3A FCB9    CALL    A,(GRPACY)
4462    1588    28 03      LD      Z,GPRT70
4463    158A    C6 20      ADD    A,4*8
4464    158C    01      DB      1
4465    158D      GPRT70: ;
4466    158D    C6 08      ADD      A,8
4467    158F    FE C0      CP      0C0H
4468    1591    38 01      JR      C,GPRT80
4469    1593    AF      XOR      A
4470    1594      GPRT80: ;Reset Y position also
4471    1594    32 FCB9    LD      (GRPACY),A
4472    1597    18 E2      JR      JPPPAL
4473      ;             SUBTTL - MSXGRP - (Routines for general graphics)

```

```

4474      1599          SCALXY:
4475          ; SCALXY - Clips X,Y to max values in physical size and flags out
4476          ; of range values.
4477          ;
4478          ;
4479          ; ENTRY [BC] = X (0 ... max X) , [DE] = Y (0 ... max Y)
4480          ; EXIT [BC] = X clipped,
4481          ; [DE] = Y clipped
4482          ; CARRY is reset if one of the value was out of bound
4483          ;
4484          PUSH   HL           ;save [HL]
4485          PUSH   BC           ;save [BC] - X coordinate
4486          LD     B,1           ;no-error flag
4487          EX     DE,HL         ;Y coordinate to [HL]
4488          LD     A,H           ;Is Y coordinate negative?
4489          ADD    A,A           ;No, positive
4490          JR    NC,YPOSTV      ;Substitute by 0 is negative
4491          LD     HL,0           ;And set out of bound flag
4492          JR    YNEGTV         ;YNEGTV
4493          YPOSTV:
4494          ;
4495          LD     DE,0C0H         ;Maximum Y+1
4496          RST   20H           ;Test [HL] with [DE]
4497          JR    C,SCLYOK        ;if carry, not out of bound
4498          EX     DE,HL         ;[HL] = 192
4499          DEC   HL           ;Y = 191 ,maximum Y coordinate
4500          YNEGTV:
4501          LD     B,0           ;set out of bound flag
4502          SCLYOK:          (SP),HL
4503          EX     LD     A,H           ;save Y and get X to [HL]
4504          LD     7C           ;Is X coordinate negative?

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXGRP - (Routines for general graphics)

```

 3.44          01-Jan-85      PAGE   48-1
ADD    A,A
JR    NC,XPOSTV      ;No, positive
LD    HL,0           ;Substitute by 0 if negative
JR    XNEGTW         ;And set out of bound flag

;max X +]
;Test [HL] with [DE]
C,SCLXOK
EX    DE,HL
DEC   HL
;[HL] = 256
;[HL] = 255 - max X coordinate

;error flag
LD    B,0

;restore [DE] = Y
CHKMOD
CALL Z,HRSSCL
;We're in high-resolution mode
;Divide both X and Y by 4 because we're
;in multi-color mode

HRSSCL:
POP  DE
CALL CHKMOD
JR    Z,HRSSCL
SRL  L
SRL  L
SRL  E
SRL  E

;set carry if no error
;[BC] = X
;restore [HL]

CHKMOD:
RET

; Check current screen mode

```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85

- MSXGRP - (Routines for general graphics)

```

4536      ; Macro-80
4537      15D9  3A FCAF    LD     A, (SCRMOD)
4538      15DC  D6 02    SUB    2      ; In what mode are we now?
4539      15DE  C9      RET
4540      15DF
MAPXYC:
4541      ; MAPXYC - Maps X,Y coordinates to "C" (address, mask)
4542      ; Entry: [BC] = X,   [DE] = Y
4543      ; Exit:  CLOC = [HL] -- Video Ram address
4544      ; CMASK = [A] -- Bit Mask
4545      ; [ High-resolution mode ]
4546      ; X coord - XXXXXXXX ( 8 bits, max=255 )
4547      ; Y coord - YYYYYYYY ( 8 bits, max=191 )
4548      ; CLOC = YYYYYYYYYY
4549      ; CMASK = 10000000 000
4550      ;               01000000 001
4551      ;               00100000 010
4552      ;               00010000 011
4553      ;               00001000 100
4554
4555
4556
4557
4558
4559
4560
4561
4562
4563
4564
4565
4566

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44      01-Jan-85      PAGE     48-3
- MSXGRP - (Routines for general graphics)                                167

4567          ; 000000100 101
4568          ; 000000010 110
4569          ; 000000001 111
4570          ; [ Multi-color mode ]
4571          ; X coord - XXXXXX ( 6 bits, max=63 )
4572          ; Y coord - YYYYYY ( 6 bits, max=47 )
4573          ; CLOC = YYYYXXXXXX
4574          ; 543210
4575          ; CMASK = 11110000 if X0=0 (even)
4576          ; CMASK = 00001111 if X0=1 (odd)
4577          ;
4578          ;
4579          ;
4580          ;
4581          ;
4582          ;
4583          ;
4584          ;
4585          ; Note: The boundary check has already been done by a call
4586          ; to SCALXY, so no range checking is needed.
4587          ;
4588          C5          ;Save X
4589          CD 15D9    ;Check current screen mode
4590          20 2E      ;Multi-color mode
4591          51          ;Save X to D also
4592          15E6      79          LD   A,C
4593          15E7      E6 07      AND  7
4594          15E9      4F          LD   C,A
4595          15EA      21 160B    LD   HL,TWOPWR
4596          15ED      09          ADD  HL,BC
4597          15EE      7E          LD   A,(HL)    ;read bit mask CMASK

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85    PAGE   48-4
- MSXGRP - (Routines for general graphics)          

4598  15EF    32 F92C      LD  (CMASK),A
4599  15F2    7B          LD  A,E      ;Get Y coordinate
4600  15F3    0F          RRCA
4601  15F4    0F          RRCA
4602  15F5    0F          RRCA
4603  15F6    E6 1F      AND  00011111B
4604  15F8    47          LD  B,A      ;Get X coordinate
4605  15F9    7A          LD  A,D      ;Get Y coordinate
4606  15FA    E6 F8      AND  11111000B
4607  15FC    4F          LD  C,A      ;Get X coordinate
4608  15FD    7B          LD  A,E      ;Get Y coordinate
4609  15FE    E6 07      AND  00000111B
4610  1600    B1          OR   C
4611  1601    4F          LD  C,A      ;Table of power of two
4612  1602    2A F3CB      LD  HL,(GRPCGP)
4613  1605    09          ADD  HL,BC
4614  1606    22 F92A      LD  (CLOC),HL
4615  1609    C1          POP BC
4616  160A    C9          RET
4617  160B    ;TWOPWR:
4618  ;        ;Table of power of two
4619  ;        ;Table of power of two
4620  ;        ;Table of power of two
4621  160B    80 40 20 10    DB  80H,40H,20H,10H
4622  160F    08 04 02 01    DB  08H,04H,02H,01H
4623  ;        ;MMPYC:
4624  1613    ;        ;Map XY for multi-color mode
4625  ;        ;Map XY for multi-color mode
4626  ;        ;Get X position
4627  ;        ;Get X position
4628  1613    79          LD  A,C

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE    48-5
- MSXGRP - (Routines for general graphics)           ;Even or odd?
                                                ;Assume even
                                                ;Good assumption
                                                ;Odd

4629   1614   0F          RRCA
4630   1615   3E F0      LD     A,11110000B
4631   1617   30 02      JR     NC,MMPXYL
4632   1619   3E 0F      LD     A,00001111B
4633   161B   MMPXYL:   LD     (CMASK),A      ;Set up mask pattern
4634   161B   32 F92C
4635   161E   79          LD     A,C
4636   161F   87          ADD   A,A
4637   1620   87          ADD   A,A
4638   1621   E6 F8      AND   11111000B
4639   1623   4F          LD     C,A      ;Get lower byte
4640   1624   7B          LD     A,E
4641   1625   E6 07      AND   01111B
4642   1627   B1          OR    C
4643   1628   4F          LD     C,A
4644   1629   7B          LD     A,E
4645   162A   0F          RRCA
4646   162B   0F          RRCA
4647   162C   0F          RRCA
4648   162D   E6 07      AND   0111B
4649   162F   47          LD     B,A      ;Get higher byte
4650   1630   2A F3D5      LD     HL,(MLTCGP)
4651   1633   09          ADD   HL,BC
4652   1634   22 F92A      LD     (CLOC),HL
4653   1637   C1          POP   BC
4654   1638   C9          RET

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE   49
- MSXGRP - (Routines for general graphics)                               

4655          1639          FETCHC:
4656          1639          ; FETCHC - Reads the value of the graphics accumulator
4657          1639          ; Exit: [HL] = CLOC, [A] = CMASK
4658          1639          ;
4659          163C          ; FetchC - Reads the value of the graphics accumulator
4660          163F          ; Exit: [HL] = CLOC, [A] = CMASK
4661          1640          ;
4662          1639          3A F92C          LD A,(CMASK)
4663          163C          2A F92A          LD HL,(CLOC)
4664          163F          C9              RET
4665          1640          STOREC:
4666          1640          ; STOREC - Sets the graphics accumulator
4667          1640          ; Entry: [HL] = CLOC, [A] = CMASK
4668          1640          ; Exit: [HL] = CLOC, [A] = CMASK
4669          1640          32 F92C          LD (CMASK),A
4670          1640          22 F92A          LD (CLOC),HL
4671          1643          C9              RET
4672          1643          1646          READC:
4673          1646          C9              READC:
4674          1647          1647          ; READC - Get the attribute of the current graphics accumulator
4675          1647          ; position
4676          1647          C5              PUSH BC
4677          1647          E5              PUSH HL
4678          1647          ; Get CLOC and CMASK
4679          1647          C5              CALL FETCHC
4680          1648          E5              CALL B,A
4681          1649          CD 1639          ; Save CMASK
4682          164C          47              LD B,A
4683          164D          CD 15D9          ; Check current screen mode
4684          1650          20 1A           CALL CKMOD
4685          1652          CD 07D7          JR NZ,MREADC
4685          1652          RDVRM           CALL RDVRM
4685          1652          ; Read VDP's VRAM (pattern)

```

```

(
  MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE   49-1
- MSXGRP - (Routines for general graphics)                                171

4686  1655  A0          AND      B
4687  1656  F5          PUSH     AF
4688  1657  01 2000    LD       BC,GRPDIF
4689  165A  09          ADD      HL,BC
4690  165B  CD 07D7    CALL    RDVRM
4691  165E  47          LD       B,A
4692  165F  F1          POP     AF
4693  1660  78          LD       A,B
4694  1661  28 04      JR       Z,READC1
4695                               ;Specified dot is off, return
                               ;background color

4696  1663  READC0:    RRCA
4697  1663  0F          RRCA
4698  1664  0F          RRCA
4699  1665  0F          RRCA
4700  1666  0F          RRCA
4701  1667  READC1:    AND      0FH
4702  1667  E6 0F      POP     HL
4703  1669  E1          POP     BC
4704  166A  C1          RET
4705  166B  C9          MREADC:
4706  166C
4707                               ;
4708  166C  CD 07D7    CALL    RDVRM
4709  166F  04          INC      B
4710  1670  05          DEC      B
4711  1671  F2 1667    JP      P,READC1
4712  1674  18 ED      JR      READC0

```

;Extract specified pixel
;Save whether the pixel is on or off
;Read VDP's VRAM (color)
;Save this to B
;Restore condition
;Restore color
;Specified dot is off, return
;background color
;Specified dot is on, return foreground color
;Make it a legal value
;Read VRAM
;Check if specified pixel is even or odd
;Odd, return lower nibble
;Even, return upper nibble

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE   50      172
- MSXGRP - (Routines for general graphics)

4713          1676          SETATTR:
4714          1676          ; SETATTR - Sets the attribute (color, reverse, etc...) to be
4715          1676          ; used in future actions.
4716          1676          ; Entry - [A] = Attribute
4717          1676          ; Exit - carry set if illegal value
4718          1676          ;
4719          1676          ;
4720          1676          ;
4721          1676          ; Must be less than 16
4722          1676          CP     16
4723          1678          CCF
4724          1679          RET    C
4725          167A          LD    (ATTRBYT),A
4726          167D          RET
4727          167E          SETC:
4728          167E          ;
4729          167F          ; SETC - Sets the point indicated by the graphics accumulator
4730          167F          ; to ATTRBYT
4731          1680          ;
4732          1680          ; All registers except AF must be preserved.
4733          1680          ;
4734          167E          E5
4735          167F          C5
4736          1680          CD 15D9
4737          1683          CD 1639
4738          1686          20 08
4739          1688          D5
4740          1689          CD 186C
4741          168C          D1
4742          168D          C1
4743          168E          E1
4744          168F          HL
4745          168F          BC
4746          168F          CALL  CHKMOD
4747          168F          CALL  FETCHC
4748          168F          JR    NZ,MSETC
4749          168F          PUSH  DE
4750          168F          CALL  PATWRT
4751          168F          POP   DE
4752          168F          POP   BC
4753          168F          POP   HL

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE   50-1
- MSXGRP - (Routines for general graphics)                                173

4744 168F C9
4745 1690          MSETC:          ; Set a pixel in multi-color mode
4746
4747
4748
4749 1690 47          ; Set a pixel in multi-color mode
4750 1691 CD 07D7          LD     B,A      ; Save CMASK in [B]
4751 1694 4F          CALL   RDVRM   ; Read VRAM
4752 1695 78          LD     C,A      ; Read VRAM
4753 1696 2F          LD     A,B      ; Leave another unaffected
4754 1697 A1          CPL    CPL     ; Leave another unaffected
4755 1698 4F          AND    C       ; Get specified color
4756 1699 3A F3F2          LD     C,A      ; Check if even or odd
4757 169C 04          LD     A,(ATRBYT)
4758 169D 05          INC    B       ; Get specified color
4759 169E F2 16A5          LD     A,(ATRBYT)
4760 16A1 87          DEC    B       ; Check if even or odd
4761 16A2 87          JP    P,MSETC1
4762 16A3 87          ADD    A,A      ; Odd
4763 16A4 87          ADD    A,A      ; Even
4764 16A5 B1          ADD    A,A      ; Form new color
4765 16A5 B1          OR     C       ; Form new color
4766 16A6 CD 07CD          CALL   WRTRVM
4767 16A9 C1          POP    BC     ; Write new pattern
4768 16AA E1          POP    HL
4769 16AB C9          RET
4770
SUBTTL - MSXGRP - (Graphic cursor movements)

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXGRP - (Graphic cursor movements)

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```

4771      ; UPC, DOWNC, RIGHTC, LEFTC
4772      ;
4773      ;
4774      ; These are the C relative movement routines. They
4775      ; adjust the current graphics accumulator in the indicated
4776      ; direction without checking boundary conditions.
4777      ;
4778      ;
4779      ;
4780      ; TRIGHT:
4781      16AC      ; TRIGHT - move 1 pixel right
4782      ; Return carry set if already on border
4783      ;
4784      ;
4785      ;
4786      16AC      E5      PUSH    HL
4787      16AD      CD 15D9  CALL    CHKMOD
4788      16B0      C2 1779  JP      NZ,MTRGT
4789      16B3      CD 1639  CALL    FETCHC
4790      16B6      0F      RRCA    ;Get CLOC,CMASK
4791      16B7      30 4B    JR      NC,HRZMV1   ;Move 1 pixel right
4792      16B9      7D      LD      A,L    ;Within byte, just change CMASK
4793      16BA      E6 F8    AND    0F8H   ;Get low byte of CLOC
4794      16BC      FE F8    CP     0F8H   ;On right edge?
4795      16BE      3E 80    LD     A,80H  ;Assume not
4796      16C0      20 10    JR     NZ,RGHTC1 ;Good assumption
4797      16C2      C3 175A  JP     ONBRDI ;On border, set carry and return
4798      16C5      RIGHTC: ;
4799      ; RIGHTC - move 1 pixel right
4800      ;
4801      ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE   51-1
- MSXGRP - (Graphic cursor movements)           ;Load offset to new position
                                                ;Change CLOC also

4802    16C5      E5      PUSH    HL
4803    16C6      CD 15D9    CALL   CHKMOD
4804    16C9      C2 178B    JP     NZ,MRGTC
4805    16CC      CD 1639    CALL   FETCHC
4806    16CF      OF      RRCA   JR     NC,HRZMV1
4807    16D0      30 32    RGHTCL:  JR     NC,HRZMV1
4808    16D2      D5      PUSH    DE
4809    16D2      D5      LD      DE,8
4810    16D3      11 0008    LD      HRZMOV
4811    16D6      18 27    JR
4812    16D8      TLEFT:   ;LEFT - move 1 pixel left
4813          ; Return carry set if already on border
4814          ;LEFT - move 1 pixel left
4815          ; Return carry set if already on border
4816          ;
4817    16D8      E5      PUSH    HL
4818    16D9      CD 15D9    CALL   CHKMOD
4819    16DC      C2 179C    JP     NZ,MTLFT
4820    16DF      CD 1639    CALL   FETCHC
4821    16E2      07      RLCA   JR     NC,HRZMV1
4822    16E3      30 1F    LD     A,L
4823    16E5      7D      AND    OF8H
4824    16E6      E6 F8    LD     A,1
4825    16E8      3E 01    JR     NZ,LEFTC1
4826    16EA      20 0F    ONBRDI
4827    16EC      18 6C    JR
4828    16EE      LEFTC:  ;LEFTC - move 1 pixel left
4829          ; We're on border, set carry and return
4830          ;
4831          ;
4832    16EE      E5      PUSH    HL

```

(MSX ROM BASIC BIOS) Macro-80

- MSXGRP - (Graphic cursor movements)

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```

4833    16EF    CD 15D9    CALL    CHKMOD
4834    16F2    C2 17AC    JP      NZ,MLFTC
4835    16F5    CD 1639    CALL    FETCHC
4836    16F8    07          ;move left 1 pixel
4837    16F9    30 09    RLCA   ;within byte boundary, just change CMASK
4838    16FB    LEFTC1:  JR     NC,HRZMV1
4839    16FB    D5          PUSH   DE
4840    16FC    11 FFFF8   LD     DE,0FFFF8H ;Load offset to new position
4841    16FF    HRZMOV:   ADD    HL,DE
4842    16FF    19          ;Add offset to new position
4843    1700    22 F92A   LD     (CLOC),HL ;Update pattern address
4844    1703    D1          POP    DE
4845    1704    HRZMV1:   POP    (CMASK),A ;Update CMASK
4846    1704    32 F92C   LD     A
4847    1707    A7          AND    A
4848    1708    E1          POP    HL ;Clear carry
4849    1709    C9          RET
4850    170A    TDOWNC:   ; TDOWNC - move 1 pixel down.
4851    E5          ; ; Return carry set if already on screen border.
4852    ; TDOWNC
4853    ; ; Return carry set if already on screen border.
4854    ; ; Return carry set if already on screen border.
4855    170A    E5          ; ; Return carry set if already on screen border.
4856    170B    D5          PUSH   HL
4857    170B    D5          PUSH   DE
4858    170C    2A F92A   LD     HL,(CLOC)
4859    170F    CD 15D9   CALL   CHKMOD
4860    1712    C2 17C6   JP     NZ,MTDNC
4861    1715    E5          PUSH   HL
4862    1716    2A F3CB   LD     HL,(GRPCGP)
4863    1719    11 1700   LD     DE,1700H

```

(MSX ROM BASIC BIOS) Macro-80
- MSXGRP - (Graphic cursor movements)

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```

4864 171C 19 ADD HL,DE
4865 171D EB EX DE,HL
4866 171E EL POP HL
4867 171F E7 RST 20H ;Test [HL] with [DE]
4868 1720 38 13 JR C,DWNC10 ;Looks like on border?
4869 1722 7D LD A,L ;Possibly on border
4870 1723 3C INC A
4871 1724 E6 07 AND 7 ;Really?
4872 1726 20 0D NZ,DWNC10 ;No
4873 1728 18 2F ONBRDR ;Yes, set carry and return
4874
4875
4876 172A DWNC: ;;
4877
4878 ; DWNC - move 1 pixel down
4879 ;;
4880 172A E5 PUSH HL
4881 172B D5 PUSH DE
4882 172C 2A F92A LD HL,(CLOC)
4883 172F CD 15D9 CALL CHKMOD
4884 1732 C2 17DC JP NZ,MDNC
4885 1735 DWNC10: ;move down 1 pixel
4886 1735 23 INC HL ;Prepare for boundary check
4887 1736 7D LD A,L ;Load possible offset to new location
4888 1737 11 00F8 LD DE,0F8H ;Check
4889 173A 18 31 JR VRTMOV
4890 173C TUPC: ;;
4891 ; TUPC - move 1 pixel up.
4892 ; Return carry set if already on screen border .
4893
4894
;
```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE      51-4
- MSXGRP - (Graphic cursor movements)          

4895    173C    E5          PUSH     HL
4896    173D    D5          PUSH     DE
4897    173E    2A F92A    LD       HL, (CLOC)
4898    1741    CD 15D9    CALL    CHKMOD
4899    1744    C2 17E3    JP       NZ, MTUPC
4900    1747    E5          PUSH     HL
4901    1748    2A F3CB    LD       HL, (GRPCGP)
4902    174B    11 0100    LD       DE, 0100H
4903    174E    19          ADD     HL, DE
4904    174F    EB          EX      DE, HL
4905    1750    E1          POP     HL
4906    1751    E7          RST     20H      ;Test [HL] with [DE]
4907    1752    30 14        JR      NC, UPC10
4908    1754    7D          LD       A,L
4909    1755    E6 07        AND    7          ;Possibly on border
4910    1757    20 0F        JR      NZ, UPC10
4911    1759    D1          ONBRDR: DE
4912    1759    D1          ONBRDL: POP
4913    175A    37          SCF     POP
4914    175A    37          SCF     HL
4915    175B    E1          POP
4916    175C    C9          RET
4917    175D    C9          UPC:
4918    175D    C9          UPC:
4919    4920    ;          ; UPC - move 1 pixel up
4921    175D    E5          ;          ; 
4922    175E    D5          PUSH     HL
4923    175F    2A F92A    PUSH     DE
4924    1762    CD 15D9    LD       HL, (CLOC)
4925                ;get current position
                           CALL    CHKMOD

```

; Set carry indicating we're on border

```

( MSX ROM BASIC BIOS ) Macro-80          3.44   01-Jan-85    PAGE   51-5
- MSXGRP - (Graphic cursor movements)           PAGE   51-5

4926 1765 C2 17F8          JP      NZ,MUPC
4927 1768    UPCL0:        LD     A,L
4928 1768 7D              DEC    HL
4929 1769 2B              LD     DE,OFF08H
4930 176A 11 FF08          VRIMOV: AND   7
4931 176D E6 07          JR     NZ,VRTMV1
4932 176D 20 01          ADD   HL,DE
4933 176F 20 01          JR     NZ,VRTMV1
4934 1771 19              VRTMV1: LD     (CLOC),HL
4935 1772 22 F92A          AND   A
4936 1772 22 F92A          POP   DE
4937 1775 A7              POP   HL
4938 1776 D1              RET
4939 1777 E1              RET
4940 1778 C9              RET
4941 1779 MTRGT:          ; Graphics cursor movement in multi-color mode
4942                                     ; [ Horizontal movements ]
4943                                     ; Graphics cursor movement in multi-color mode
4944                                     ; [ Horizontal movements ]
4945                                     ; Graphics cursor movement in multi-color mode
4946 1779 CD 1639          CALL   FETCHC
4947 177C A7              AND   A
4948 177D 3E 0F          LD     A,0FH
4949 177F FA 17C0          JP     M,MHZMV1
4950 1782 7D              LD     A,L
4951 1783 E6 F8          AND   0F8H
4952 1785 FE F8          CP     0F8H
4953 1787 20 0B          JR     NZ,MRGTC1
4954 1789 18 CF          JR     ONBRD1
4955 178B MRGTC:          ; On right edge?
4956                                     ; No, move to next pixel
                                     ; We're on right edge, set carry and return

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXGRP - (Graphic cursor movements)

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```

4957 178B CD 1639 CALL   FETCHC
4958 178E A7 AND    A
4959 178F 3E OF LD     A,0FH
4960 1791 FA 17C0 JP     M,MHZMV1
4961 1794 MRGTC1: ;Assume CMASK is even
4962 1794 D5 PUSH   DE
4963 1795 11 0008 LD     DE,8
4964 1798 3E F0 LD     A,0FOH
4965 179A 18 1F JR     MHCMOV
4966 179C MTLFT: ;Next pixel is 8 byte far
4967 ;from the current position
4968 179C CD 1639 CALL   FETCHC
4969 179F A7 AND    A
4970 17A0 3E F0 LD     A,0FOH
4971 17A2 F2 17C0 JP     P,MHZMV1
4972 17A5 7D A,L
4973 17A6 E6 F8 AND    0F8H
4974 17A8 20 0B JR     NZ,MLFTC1
4975 17AA 18 AE ONBRDI ;No
4976 17AC MLFTC: ;We're on left edge, set carry and return
4977 17AC ; ;On left edge?
4978 17AC CD 1639 CALL   FETCHC
4979 17AF A7 AND    A
4980 17B0 3E F0 LD     A,0FOH
4981 17B2 F2 17C0 JP     P,MHZMV1
4982 17B5 D5 PUSH   DE
4983 17B5 11 FFFF8 LD     DE,0FFFF8H
4984 17B6 3E OF LD     A,0FH
4985 17B9 17BB MHCMOV: ;We're on left edge, set carry and return
4986
4987

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   51-7
- MSXGRP - (Graphic cursor movements)          

4988  17BB    19          ADD     HL,DE
4989  17BC    22 F92A      LD      (CLOC),HL
4990  17BF    D1          POP    DE
4991  17C0    32 F92C      MHZMV1: LD      (CMASK),A
4992  17C0    32 F92C      AND    A
4993  17C3    A7          POP    HL
;Clear carry
4994  17C4    E1          RET
4995  17C5    C9          MTDNC:
4996  17C6    19          ; [ Vertical movements ]
4997
4998
4999
5000  17C6    E5          PUSH   HL
5001  17C7    2A F3D5      LD      HL,(MLTCGP)
5002  17CA    11 0500      LD      DE,0500H
5003  17CD    19          ADD    HL,DE
5004  17CE    E1          POP    HL
5005  17CF    E7          RST    20H
5006  17D0    38 0A          JR    C,MDNC
5007  17D2    7D          LD    A,L
5008  17D3    3C          INC    A
5009  17D4    E6 07          AND    7
5010  17D6    20 04          JR    NZ,MDNC
5011  17D8    37          SCF
;No
;We are at the bottom border,
;set carry and return
5012
5013  17D9    D1          POP    DE
5014  17DA    E1          POP    HL
5015  17DB    C9          RET
5016  17DC    19          MTDNC:
;INC
5017
5018  17DC    23          INC    HL
;Move down 1 byte

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXGRP - (Graphic cursor movements)

			PAGE	51-8
		3.44	01-Jan-85	
5019	17DD	7D	LD A,L	
5020	17DE	11 00F8	LD DE,0F8H	;Load possible offset to next block
5021	17E1	18 1A	JR MVTMOV	;Check
5022	17E3			
5023		;		
5024	17E3	E5	PUSH HL	
5025	17E4	2A F3D5	LD HL,(MLTCCGP)	
5026	17E7	11 0100	LD DE,0100H	
5027	17EA	19	ADD HL,DE	
5028	17EB	E1	POP HL	
5029	17EC	E7	RST 20H	
5030	17ED	30 09	JR NC,MUPC	
5031	17EF	7D	LD A,L	
5032	17F0	E6 07	AND 7	
5033	17F2	20 04	JR NZ,MUPC	
5034	17F4	37	SCF	
5035	17F5	D1	POP DE	
5036	17F6	E1	POP HL	
5037	17F7	C9	RET	
5038	17F8		MUPC:	
5039		;		
5040	17F8	7D	LD A,L	
5041	17F9	2B	DEC HL	
5042	17FA	11 FF08	LD DE,0FF08H	;Load possible offset to next block
5043	17FD		MVTMOV:	
5044	17FD	E6 07	AND 7	
5045	17FF	20 01	JR NZ,MVTMV1	
5046	1801	19	ADD HL,DE	
5047	1802		MVTMV1:	
5048	1802	22 F92A	LD (CLOC),HL	
5049	1805	A7	AND A	
				;Clear carry

(MSX ROM BASIC BIOS)		Macro-80	3 . 44	01-Jan-85	PAGE
-	MSXGRP -	(Graphic cursor movements)			51-9
5050	1806	D1	POP	DE	
5051	1807	E1	POP	HL	
5052	1808	C9	RET		
5053			SUBTTL	-MSXGRP-	(Box fill and Misc.)

```

5054      1809      NSETCX:
5055      180C      ; NSETCX - Per forms SETC, RIGHTC [HL] times
5056      180F      ; In fact, SETC and RIGHTC are never called to increase speed,
5057      1810      ; and for the reason described below.
5058      1813      ;
5059      1814      ; Since only 2 colors can be displayed in a byte, some special
5060      1815      ; handling is required when a full-byte is set when writing left
5061      1817      ; or right extras. In this case, we can completely ignore the
5062      1818      ; background color for that byte, allowing 2 colors displayed
5063      181B      ; in a byte.
5064      0F        ; All registers may be destroyed.
5065      01        ;
5066      F5        ;
5067      F0        ;
5068      CD 15D9  CALL CHKMOD
5069      C2 18BB  JP  NZ, MNSTCX   ; Multi-color mode
5070      180C      PUSH HL          ; Save count
5071      180F      CALL FETCHC  ; Get CLOC and CMASK
5072      E5        EX  (SP), HL    ; Reget count, save CLOC
5073      1810      ADD A,A       ; Beginig at leftmost position?
5074      1813      JR  C, NSTC20  ; Yes, no extra dots at the left
5075      1814      PUSH AF          ; Save mask pattern*2
5076      1815      LD  BC, 0FFFFFH
5077      1817      RRCA
5078      01  FFFF  NSTC10: ADD HL, BC      ; Decrement pixel count
5079      181B      JR  NC, NSTCSP  ; The whole dots are within a byte
5080      181C      RRCA
5081      181C      JR  NC, NSTC10
5082      30  45
5083      181F      0F
5084      1820      30  FA

```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP- (Box fill and Misc.)

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```

5085 1822 F1          POP AF
5086 1823 3D          DEC A
5087 1824 E3          EX (SP),HL
5088 1825 E5          PUSH HL
5089 1826 CD 186C     CALL PATWRT
5090 1829 E1          POP HL
5091 182A 11 0008     LD DE,8
5092 182D 19          ADD HL,DE
5093 182E E3          EX (SP),HL
5094 182F NSTC20:    ;Restore mask pattern*2
5095 182F 7D          LD A,L
5096 1830 E6 07        AND 7
5097 1832 4F          LD C,A
5098 1833 7C          LD A,H
5099 1834 0F          RRCA
5100 1835 7D          LD A,L
5101 1836 1F          RRA
5102 1837 0F          RRCA
5103 1838 0F          RRCA
5104 1839 E6 3F        AND 0011111B
5105 183B EL          POP HL
5106 183C 47          LD B,A
5107 183D 28 14        JR Z,NSTC40
5108 183F NSTC30:    ;[HL]=[HL]/8
5109 183F AF          XOR A
5110 1840 CD 07CD     CALL WRTRVM
5111 1843 11 2000     LD DE,GRPDIF
5112 1846 19          ADD HL,DE
5113 1847 3A F3F2     LD A,(ATRBYT)
5114 184A CD 07CD     CALL WRTRVM
5115 184D 11 2008     LD DE,GRPDIF+8
                                         ;Form left-extra pattern
                                         ;Reget CLOC, save count
                                         ;Save CLOC
                                         ;Write to VRAM (pattern and color)
                                         ;Restore CLOC
                                         ;Load an offset to next byte
                                         ;Update pattern address
                                         ;Reget count, save CLOC
                                         ;Get low byte of count
                                         ;[A]=count mod 8
                                         ;save count after byte boundary
                                         ;[B]=counter
                                         ;No dots in this part
                                         ;Make specified color a background color
                                         ;Write to VRAM (pattern)
                                         ;Calculate address of color table
                                         ;Get specified color
                                         ;Write to VRAM (color)
                                         ;Load an offset to next byte

```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP- (Box fill and Misc.)

```

5116 1850 19 ADD HL,DE ;Bump CLOC
5117 1851 10 EC DJNZ NSTC30 ;Loop until done
5118 1853 0D DEC C ;dot count in char boundary
5119 1853 F8 RET M ;No dots in right extra
5120 1854 E5 PUSH HL ;Save CLOC
5121 1855 21 185D LD HL,RGTEXT ;Load address for 'right-extra' pattern table
5122 1856 09 ADD HL,BC ;Get pattern
5123 1859 7E LD A,(HL)
5124 185A 18 0E JR NSTC50
5125 185B 185D RGTEXT: ;Get pattern
5126 ; ;Get pattern for the right (1111100)
5127 185D 80 C0 E0 F0 DB 80H,0C0H,0E0H,0F0H
5128 1861 F8 FC FE DB 0F8H,0FCFH,0FEH
5129 1864 NSTCSP: ;Get mask pattern for the right (1111100)
5130 1864 ; ;Get mask pattern for the right (1111100)
5131 1864 87 ADD A,A ;Get mask pattern for the right (1111100)
5132 1865 3D DEC A ;Get mask pattern for the right (1111100)
5133 1865 2F CPL ;Save it
5134 1866 47 LD B,A ;Get mask pattern for the left (00011111)
5135 1867 F1 POP AF ;Make a pattern to write (00011100)
5136 1868 3D DEC A ;Restore CLOC ex.
5137 1869 A0 AND B
5138 186A NSTC50: POP HL
5139 186B E1
5140 186B

```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP- (Box fill and Misc.)

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```
5141    5142    186C          PATWRT:  
5143          ; PATWRT - Write a pattern to high-resolution screen  
5144          ;  
5145          ; Entry: A - Pattern to be written  
5146          ; HL - Address of pattern table  
5147          ; ATRBYT - Color of this pattern  
5148          ;  
5149          ;  
5150    186C    47          LD      B,A          ;Save pattern to be added  
5151    186D    CD 07D7    CALL   RDVRM         ;Read VRAM (pattern)  
5152    1870    4F          LD      C,A          ;Save current pattern  
5153    1871    11 2000    LD      DE,GRPDIF  
5154    1874    19          ADD   HL,DE          ;Form address of color table  
5155    1875    CD 07D7    CALL   RDVRM         ;Read from VRAM (color)  
5156    1878    F5          PUSH  AF          ;Extract background color  
5157    1879    E6 0F      AND   0FH           ;Save background color  
5158    187B    5F          LD      E,A           ;Restore foreground and background color  
5159    187C    F1          POP   AF           ;Set foreground color in the upper 4 bit  
5160    187D    93          SUB   E             ;[B] has the specified pattern,  
5161    187E    57          LD      D,A           ;[C] has the current pattern,  
5162          ;[D] has the current foreground color  
5163          ;shifted left 4 times,  
5164          ;[E] has the current background color,  
5165          ;[HL] has the address of color table.  
5166          ;Get specified color  
5167          ;Same with current background?  
5168    187F    3A F3F2    LD      A,(ATRBYT)  
5169    1882    BB          CP      E             ;Yes  
5170    1883    28 19      JR      Z,SAMEBG  
5171    1885    87          ADD   A,A
```

```

( MSX ROM BASIC BIOS ) Macro-80
-MSXGRP- (Box fill and Misc. ) 3.44 01-Jan-85 PAGE 53-1 188

5172 1886 87 ADD A,A
5173 1887 87 ADD A,A
5174 1888 87 ADD A,A
5175 1889 BA CP D
      ;Same with current foreground?
5176 188A 28 16 JR Z,SAMEFG
      ;Yes
5177 188C F5 PUSH AF
      ;Save new foreground color
5178 188D 78 LD A,B
5179 188E B1 OR C
5180 188F FE FF CP 0FFF
      ;All pixels are going to be set?
5181 1891 28 17 JR Z,PATWRL
      ;Yes, Spock will use a new repair technique
      ;logically...
5182                                ;Save address of color table
5183 1893 E5 PUSH HL
5184 1894 D5 PUSH DE
5185 1895 CD 18A2 CALL SAMEFG
      ;Save current background color
      ;Write to VRAM (pattern)
5186 1898 D1 POP DE
5187 1899 E1 POP HL
5188 189A F1 POP AF
      ;Restore current background in [E]
      ;Restore color table address
5189                                ;Res tore new foreground color in upper
      ;4 bits of [Acc]
5190 189B B3 OR E
      ;Form new foreground and background color
5191 189C 18 1A JMPWRT
5192 189E SAMEBG: JR
      ;Write to color table

5193 ; LD A,B
5194 189E 78 CPL
5195 189F 2F AND C
5196 18A0 A1 DB 11H
      ;Skip next 2 bytes (LXI D)
5197 18A1 11 SAMEFG: LD A,B
5198 18A2 78 OR C
5199 18A2 78 LD A,B
5200 18A3 B1 OR C
5201 18A4 WPTTAB: LD DE, GRPDIF
5202 11 2000 LD

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE      53-2
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5203    18A7    19          ADD      HL,DE
5204    18A8    18  0E      JR      JMPWRT   ;Write to pattern table
5205    18AA          PATWRL: ;
5206          ;Discard new foreground color
5207    18AA    F1          POP     AF
5208    18AB    78          LD      A,B
5209    18AC    2F          CPL
5210    18AD    E5          PUSH    HL
5211    18AE    D5          PUSH    DE
5212    18AF    CD 18A4      WTPTAB
5213    18B2    D1          CALL    WTPTAB
5214    18B3    E1          POP     DE
5215    18B4    3A F3F2      HL
5216          A, (ATRBYT) ;Get new color (this will be the
5217    18B7    B2          POP     HL
5218    18B8          JMPWRT: ;background color)
5219    18B8    C3 07CD      OR      D
                                JP      WRITVRM ;Add current foreground color
                                JP      WRITVRM ;Write to VRAM (color)

```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP- (Box fill and Misc.)

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```

5220          18BB      MNSTCX:           PAGE 54
5221          18BB      ; NSETCX for multicolor screen
5222          18BB      ; NSETCX for multicolor screen
5223          18BB      ; NSETCX for multicolor screen
5224          18BB      E5                  ; Save counter
5225          18BC      CD 167E          ; Set pixel
5226          18BF      CD 16C5          ; Move to right
5227          18C2      E1                  ; Restore counter
5228          18C3      2D
5229          18C4      20 F5
5230          18C6      C9
5231          18C7      GTASPC:          ; GTASPC - load aspect ratio for CIRCLE
5232          18C7      2A F40B          ; LD   HL, (ASCPCT1)
5233          18CA      EB                  ; EX   DE, HL
5234          18CB      2A F40D          ; LD   HL, (ASCPCT2)
5235          18CE      C9                  ; RET
5236          18C7      2A F40B          ; LD   HL, (ASCPCT1)
5237          18CA      EB                  ; EX   DE, HL
5238          18CB      2A F40D          ; LD   HL, (ASCPCT2)
5239          18CE      C9                  ; RET
5240          SUBTTL -MSXGRP - (Routines for paint)
```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

```

      3 .44    01-Jan-85    PAGE    55

5241    5242    18CF          PNTINI:
5243    5244          ; PNTINI - Initialize border color
5245          ; ; Save specified color
5246    18CF    F5          ; In what mode are we now?
5247    18D0    CD 15D9      ; High-resolution mode
5248    18D3    28 06
5249    18D5    F1
5250    18D6    FE 10
5251    18D8    3F
5252    18D9    18 05
5253    18DB          PNTIERT:
5254          ; ; Discard specified color
5255    18DB    F1          ; Always ignore specified border
5256    18DC    3A F3F2      ; Always legal
5257    18DF    A7
5258    18E0          PNTIERT:
5259    18E0    32 FCB2      LD   (BRDATTR),A
5260    18E3    C9          RET
5261    18E4          SCANR:
5262          ; ; SCANR - scan pixels to right
5263          ; ; Maximum number of pixels to test is passed in [ DE ] .
5264          ;
5265          ;
5266    18E4    21 0000      LD   HL,0
5267    18E7    4D          LD   C,L
5268    18E8    CD 15D9      CALL CHKMOD
5269    18EB    20 64          JR   NZ,MSCANR
5270          ; ; Check current screen mode
5271          ; ; Multi-color mode
5272          ; ; Scan to right in high-resolution mode

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   55-1
-MSXGRP - (Routines for paint)

5272                                ; [B] set to 0 is need to suspend painting, 1 otherwise.
5273
5274                                ; Work1 = Temporary storage for 'suspend painting'
5275                                ; Work2 = Save area for pixel count to draw right
5276                                ; Work3 = Save area for 'pixel changed' flag
5277
5278    18ED    78
5279    18EE    32 F866
5280    18F1    AF
5281    18F2    32 F869
5282    18F5    3A FCB2
5283    18F8    47
5284    18F9    CD 1647
5285    18FC    B8
5286    18FD    20 0D
5287    18FF    1B
5288    1900    7A
5289    1901    B3
5290    1902    C8
5291    1903    CD 16AC
5292    1906    30 F1
5293    1908    11 0000
5294    190B    C9
5295    190C
5296
5297
5298                                ; A pixel with non-border attribute is found. Start painting
5299
5300    190C    CD 19AE
5301    190F    D5
5302    1910    CD 1639

```

; [B] set to 0 is need to suspend painting, 1 otherwise.
; Work1 = Temporary storage for 'suspend painting'
; Work2 = Save area for pixel count to draw right
; Work3 = Save area for 'pixel changed' flag
; LD A,B
; LD (RUNFLG),A ;Remember to suspend or not
; XOR A ;Clear 'pixel changed' flag
; LD (WORK3),A
; LD A,(BRDATR)
; LD B,A ;Set border color to [B] for comparison
; READC ;Read current color
; CP B ;Still on border?
; JR NZ,SCANR2 ;No, start painting
; DEC DE ;All pixels tested?
; LD A,D
; OR E
; RET Z ;Yes
; CALL TRIGHT ;Advance to right, and check if out of screen
; JR NC,SCANR1 ;Not yet out of screen, continue
; LD DE,0 ;All pixels has border attribute on
; RET ;this row, let BRDCNT be 0, and return
; SCANR2:
; ; A pixel with non-border attribute is found. Start painting
; CALL CHKCHG ;Check if pixel changed
; PUSH DE ;Save BRDCNT
; CALL FETCHC ;Get current CLOC, CMASK

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

	3.44	01-Jan-85	PAGE	55-2
5303	1913	22 F942	LD (CSAVEA),HL	
5304	1916	32 F944	LD (CSAVEM),A	
5305	1919	11 0000	LD DE,0	
5306	191C		SCANR3:	
5307	191C	13	INC DE	
5308	191D	CD 16AC	CALL TRIGHT	; Set first non-border pixel encountered
5309	1920	38 0B	JR C,SCANR4	
5310	1922	CD 1647	CALL READC	
5311	1925	B8	CP B	
5312	1926	28 05	JR Z,SCANR4	
5313	1928	CD 19AE	CALL CHKCHG	
5314	192B	19 EF	JR SCANR3	
5315	192D		;	
5316	192D	D5	PUSH DE	
5317	192E	CD 1639	CALL FETCHC	; Save PNTCNT
5318	1931	E5	PUSH HL	; Since NSETCX does not update 'C', these value
5319	1932	F5	PUSH AF	; must be saved
5320	1933	2A F942	LD HL,(CSAVEA)	
5321	1936	3A F944	LD A,(CSAVEM)	
5322	1939	CD 1640	CALL STOREC	
5323	193C	EB	EX DE,HL	; Set where to start painting
5324	193D	22 F867	LD (WORK2),HL	
5325	1940	3A F866	LD A,(WORK1)	
5326	1943	A7	AND A	
5327	1944	C4 1809	CALL NZ,NSETCX	
5328	1944	F1	POP AF	
5329	1947	E1	POP HL	
5330	1948	CD 1640	CALL STOREC	
5331	1949	E1	POP HL	
5332	194C	D1	POP DE	
5333	194D		POP DE	

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

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5334 194E C3 19A9 JP SCANL4

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(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

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```
5335      1951          MSCANR:  
5336      1951          ; Scan to right in multi-color mode  
5337      1951          ; Scan to right in multi-color mode  
5338      CD 19C7          ; Is it border color?  
5339      30 0D          ; No, start painting  
5340      1954          ; All pixels tested?  
5341      1954          CALL MTSBRD  
5342      1956          JR NC,MSCNR1  
5343      1957          DEC DE  
5344      1958          LD A,D  
5345      1959          OR E  
5346      195A          RET Z  
5347      195D          CALL TRIGHT  
5348      195F          JR NC,MSCANR  
5349      1962          LD DE,0  
5350      1963          RET  
5351      1963          MSCNRL:  
5352      CD 1639          ; Get CLOC,CMASK  
5353      1966          LD (CSAVEA),HL  
5354      1969          LD (CSAVEM),A  
5355      196C          LD HL,0  
5356      196F          MSCNRL2:  
5357      196F          INC HL  
5358      1970          CALL TRIGHT  
5359      1973          RET C  
5360      1974          CALL MTSBRD  
5361      1977          JR NC,MSCNR2  
5362      1979          RET
```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

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```
5363      197A          ; SCANL:
5364      197A          ; ; SCANL - Scan pixels to left
5365          ; ; SCANL - Scan pixels to left
5366          ; ; SCANL - Scan pixels to left
5367          ; ; SCANL - Scan pixels to left
5368 197A    21 0000          ; Initialize PNTCNT
5369 197D    4D          ; Initialize PNTDFL
5370 197E    CD 15D9          ; Check current screen mode
5371 1981    20 37          ; NZ, MSCANL
5372          ; ; Multi-color mode
5373          ; ; Scan to left in high-resolution mode
5374          ; ; XOR A
5375 1983    AF          ; Clear 'pixel changed' flag
5376 1984    32 F869          ; WORK3, A
5377 1987    3A FCB2          ; A, (BRDADR)
5378 198A    47          ; Set border color to [B] for comparison
5379 198B    CD 16D8          ; Advance to left, and check if out of screen
5380 198B    CD 16D8          ; On left edge
5381 198E    38 0F          ; Read color of target pixel
5382 1990    CD 1647          ; Reached border?
5383 1993    B8          ; Advance to left, and check if out of screen
5384 1994    28 06          ; On left edge
5385 1996    CD 19AE          ; Reached border?
5386 1999    23          ; Yes
5387 199A    18 EF          ; Check if pixel changed
5388 199C    ; ; Update PNTCNT
5389          ; ; SCANL2:
5390 199C    CD 16C5          ; Save PNTCNT
5391 199F    SCANL3:        ; Load suspended pixels which remain
5392 199F    E5          ; Push HL
5393 19A0    ED 5B F867          ; LD DE, (WORK2)
```

(MSX ROM BASIC BIOS) Macro-80
-MSXGRP - (Routines for paint)

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```

5394 19A4 19 ADD HL,DE ;to the right
5395 19A5 CD NSETCX ;Draw [HL] pixel from current 'C'
5396 19A8 E1 POP HL ;Restore PNTCNT

5397 19A9 SCANL4: LD A,(WORK3) ;Non 0 if pixels changed attribute
5398 19A9 3A F869 LD C,A
5399 19AC 4F
5400 19AD C9 RET

5401 19AE ;CHKCHG:
5402 ;      PUSH HL
5403 19AE E5 LD HL,ATRBYT ;Get specified paint attribute
5404 19AF 21 F3F2 CP (HL)
5405 19B2 BE POP HL ;Same?
5406 19B3 E1 RET Z ;Yes, no change of attribute
5407 19B4 C8 INC A ;Load non 0 to [Acc]
5408 19B5 3C LD (WORK3),A ;Remember this temporarily
5409 19B6 32
5410 19B9 C9 RET

5411 19BA MSCANL: ;MSCANL:
5412 ;      ; scan to left in multi-color mode
5413 ;      ; scan to left in multi-color mode
5414 ;      ; scan to left in multi-color mode
5415 19BA CD 16D8 CALL TLEFT ;Advance to left, and check if out of screen
5416 19BD D8 RET C ;going out of screen
5417 19BE CD 19C7 CALL MTSBRD ;Reached border color?
5418 19C1 DA 16C5 JP C,RIGHTC ;Yes, adjust CLOC, CMASK and return
5419 19C4 23 INC HL ;Increment PNTCNT
5420 19C5 18 F3 JR MSCANL ;Continue

5421 19C7 ;MTSBRD:
5422 ;      ; test border subroutine for multi-color mode
5423 ;
5424 ;

```

```

( MSX ROM BASIC BIOS ) Macro-80
-MSXGRP - (Routines for paint)          PAGE      57-2

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      5425    19C7    CD 1647    CALL     READC    ;Get the color of target pixel
      5426    19CA    47        LD       B,A      ;Load specified border color
      5427    19CB    3A FCB2   LD       A,(BRDATTR)
      5428    19CE    90        SUB     B         ;Reached border?
      5429    19CF    37        SCF      ;Assume so
      5430    19D0    C8        RET     Z         ;Yes , return with carry flag set
      5431    19D1    3A F3F2   LD       A,(ATRBYT)
      5432    19D4    B8        CP      B         ;Is current pixel same as ATRBYT?
      5433    19D5    C8        RET     Z         ;Yes , no changes made.
      5434    19D6    CD 167E   CALL     SETC     ;Return with carry reset
      5435    19D9    0E 01     LD       C,1      ;Set this pixel to ATRBYT
      5436    19DB    A7        AND     A         ;Set 'pixel-changed' flag
      5437    19DC    C9        RET     ;Tell caller that we plot a dot
      5438    19DC    C9        SUBTTL -CASET- Cassette drivers stuff
      5439

```

(MSX ROM BASIC BIOS) Macro-80
 -CASET- Cassette drivers stuff

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```

5440 ; Cassette read/write stuff
5441 ;
5442 ;
5443 ; Following driver assumes that T cycle is 279.365 nS
5444 ;
5445 ; Variables referenced
5446 ; PPI.CM      To write to cassette
5447 ; PSG.DR      To read from cassette
5448 ; BREAKX     Routine to check for [ STOP ] key pressed
5449 ;
5450 19DD :TAPOFF:
5451 ;
5452 19DD C5 PUSH BC
5453 19DE F5 PUSH AF
5454 19DF 01 LD BC,0
5455 19E2 0000 CTWOF1: DEC BC
5456 19E2 0B LD A,B
5457 19E3 78 OR C
5458 19E4 B1 JR NZ,CTWOF1
5459 19E5 20 FB POP AF
5460 19E7 F1 POP BC
5461 19E8 C1 POP BC
5462 19E9 F5 TAPIOF:
5463 19E9 LD AF
5464 19EA 3E 09 LD A,00001001B
5465 19EC D3 AB OUT (PPI.CM),A
5466 19EE F1 POP AF
5467 19EF FB EI
5468 19F0 C9 RET
5469 19F1 TAPOON:
5470 ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   58-1      200
-CASET- Cassette drivers stuff

5471                                     ; Write out header, if [A]=0 then write short header
5472                                     ; otherwise write long header ( 5sec )
5473                                     ;
5474     19F1    B7          OR      A
5475     19F2    F5          PUSH   AF
5476     19F3    3E 08      LD      A,8
5477     19F5    D3 AB      OUT    (PPI.CM),A
5478     19F7    21 0000    LD      HL,0
5479     19FA    MOTRWT:   DEC    HL
5480     19FA    2B          LD      A,H
5481     19FB    7C          OR      L
5482     19FC    B5          JR      NZ,MOTRWT
5483     19FD    20 FB      POP   AF
5484     19FF    F1          LD      A,(HEADER)
5485     1A00    3A F4 0A    JR      Z,SYNCWL
5486     1A03    28 02      ADD   A,A
5487     1A05    87          ADD   A,A
5488     1A06    87          SYNCWL: LD      B,A
5489     1A07    47          LD      C,0
5490     1A07    0E 00      DI      ;set up counter
5491     1A08    F3          ;Don't disturb during writing to cassette
5492     1A0A    0E 00      ;Write enough marks
5493     1A0B    CD 1A4D    SYNLPL: CALL   BIT1OT
5494     1A0B    CD 1A3F    CALL   RETRET
5495     1A0E    0B          DEC    BC
5496     1A11    0B          LD      A,B
5497     1A12    78          OR      C
5498     1A13    B1          JR      NZ,SYNLPL
5499     1A14    20 F5      JP      BREAKX
5500     1A16    C3 046F    TAPOUT:
5501     1A19

```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff 3.44 01-Jan-85

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```

5502      1A19          DATAW:          ; Output a byte
5503          ; Initialize counter
5504          ; Output space
5505          ; compensate loss time since last stop bit
5506          ; get time constants for space
5507          ; output start bit
5508          ; Initialize counter
5509          ; next bit to carry
5510          ; output mark if the bit is 1
5511          ; output space
5512          ; loop until 8 bits sent
5513          ; output stop bit
5514          ; check if break pressed and return
5515          ; output space
5516          ; output space
5517          ; output space
5518          ; output space
5519          ; output space
5520          ; output space
5521          ; output space

```

(MSX ROM BASIC BIOS) Macro-80

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```

5522      1A39          BIT0:
5523      1A39          ; Output a bit to cassette
5524          ; Absolute jumps are used to improve accuracy
5525          ;
5526          ;
5527          ;
5528          ;
5529      2A F406          LD      HL,(LOW)    ;Output 0 (space)
5530      CD 1A50          CALL   BITOUT     ;(17 T)
5531      1A3C          RETRET:    ;(18 T)
5532      1A3F          C9          RET       ;(11 T)
5533      1A40          BIT1:
5534          ;
5535      1A40          CD 1A4D          CALL   BIT1LOT   ;(18 T)
5536      1A43          E3          EX      (SP),HL    ;(20 T)
5537      1A44          E3          EX      (SP),HL    ;compensate overhead
5538      1A45          00          NOP      ;(Total 60 state)
5539      1A46          00          NOP      ;( 5 T)
5540      1A47          00          NOP      ;( 5 T)
5541      1A48          00          NOP      ;( 5 T)
5542      1A49          CD 1A4D          CALL   BIT1LOT   ;To compensate time
5543      1A4C          C9          RET       ;Don't change this (11 T)
5544      1A4D          BIT1LOT:   ;(18 T)
5545          ; output a single cycle
5546          ;
5547          ; Total number of states =16 x [L] + 16 x [H] + 71
5548          ; =4.47uS x [L] + 4.47uS x [H] + 19.8usec
5549          ;
5550          ;
5551      1A4D          2A F408          BITOUT:    ;(17 T)
5552      1A50          LD      HL,(HIGH)
```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

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```
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PUSH AF ; (12 T)  
;  
5553 1A50 F5 ;  
5554 1A51 2D ;  
5555 1A51 2D ;  
5556 1A51 C2 1A51 ;  
5557 1A52 C2 1A51 ;  
5558 1A55 3E 0B ;  
5559 1A57 D3 AB ;  
5560 1A59 25 ;  
5561 1A5A C2 1A59 ;  
5562 1A5D 3E 0A ;  
5563 1A5D 3E 0A ;  
5564 1A5F D3 AB ;  
5565 1A61 F1 ;  
5566 1A62 C9 ;  
5567 1A62 C9 ;  
5568 1A63 TAPION: ;  
5569 1A63 TAPION: ;  
5570 1A63 3E 08 ;  
5571 1A63 3E 08 ;  
5572 1A63 D3 AB ;  
5573 1A65 D3 AB ;  
5574 1A67 F3 ;  
5575 1A68 3E 0E ;  
5576 1A6A D3 A0 ;  
5577 1A6C SYN05: ;  
5578 1A6C 21 0457 ;  
5579 1A6C 21 0457 ;  
5580 1A6C 21 0457 ;  
5581 1A6C HL,0457H ;Initialize counter  
5582 1A6F SYN10: ;Number of pulse to detect header
```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

```

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5584      1A6F      51      LD      D,C      ;Remember last value
5585      1A70      CD 1B34      CALL    CNTFUL
5586      1A73      D8      RET     C      ;Count full cycle
5587      1A74      79      LD      A,C      ;Aborted
5588      1A75      FE DE      CP      ODEH      ;Get count
5589      1A77      30 F3      JR      NC,SYN05      ;ODE = Max count
5590      1A79      FE 05      CP      5      ;Too long, reset number of pulses
5591      1A7B      38 EF      JR      C,SYN05      ;5 = Min count
5592
5593      ; Now compare with last pulse width and approve this as a good pulse
5594      ; if this is similar to last one.
5595
5596      1A7D      92      SUB     D      ;current - last
5597      1A7E      30 02      JR      NC,SYN11      ;result was negative, negate it
5598      1A80      2F      CPL
5599      1A81      3C      INC     A
5600      1A82      SYN11:    INC
5601      1A82      FE 04      CP      4      ;within a wow allowance?
5602      1A84      30 E6      JR      NC,SYN05      ;no, reset number of pulse ever seen
5603      1A86      2B      DEC     HL
5604      1A87      7C      LD     A,H
5605      1A88      B5      OR     L
5606      1A89      20 E4      JR      NZ,SYN10      ;Loop till seen enough good pulses
5607
5608      1A8B      SYN20:    ;Next, calculate the mean width of pulse.
5609
5610
5611      1A8B      21 0000      ;Initialize sum
5612      1A8E      45      LD     HL,0      ;Initialize high byte of [BC] pair
5613      1A8F      55      LD     B,L      ;Loop 256 times
5614

```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

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```

5615 1A90          CD 1B34          SYN30 :      CALL    CNTFUL
5616 1A90          CD 1B34          SYN30 :      RET     C
5617 1A93          D8              CNTFUL
5618 1A94          09              ADD    HL,BC
5619 1A95          15              DEC    D
5620 1A96          C2 1A90          JP     NZ,SYN30
5621 1A99          01 06AE          LD     BC,06AEH
5622 1A9C          09              ADD    HL,BC

; Set various values for read routine. Those are,
5623
5624
5625 ; LOWLIM - lower limit of the width of start bit. [H]*1.5
5626 ; WINWID - width of window to count the transition.
5627
5628
5629 1A9D          7C              LD     A,H           ;[H] has mean pulse width
5630 1A9E          1F              RRA
5631 1A9F          E6 7F          AND    7FH
5632 1AA1          57              LD     D,A           ;[D]=[mean]/2
5633 1AA2          29              ADD    HL,HL
5634 1AA3          7C              LD     A,H           ;[A]=[mean]*2
5635 1AA4          92              SUB    D
5636 1AA5          57              LD     D,A           ;[A]=[mean]*1.5
5637 1AA6          D6 06          SUB    6
5638 1AA8          32 FCA4          LD     (LOWLIM),A
5639
5640
5641
5642
5643
5644
5645
;
```

; Set width of window 'WINWID'
; CNTFUL takes 40T for a loop, RDDBIT takes 60T for loop
; set WINWID as 3 times wider than single short pulse ([mean]/2)
; [WINWID]=[mean] x 1.5 x 40/60
; =[D] x 2/3

```

( MSX ROM BASIC BIOS ) Macro-80      PAGE   59-4
-CASSET- Cassette drivers stuff          3.44    01-Jan-85

5646  1AAB  7A           LD     A,D      ;get [ mean width ] xl.75
5647  1AAC  87           ADD   A,A      ;x2
5648  1AAD  06  00        LD     B,0      ;clear quotient
5649  1AAF  D6  03      SULOP: SUB   3       ;loop till get carry
5650  1AAF  D6  03      INC   B       ;[ A ]=[mean] xl.75x2/3
5651  1AB1  04           INC   NC,SULOP ;compensate overhead in RDBIT routine
5652  1AB2  30 FB         JR    A,B      ;[ A ]=A
5653  1AB4  78           LD     A,B      ;compensate overhead in RDBIT routine
5654  1AB5  D6  03      SUB   3       ;WINWID),A
5655  1AB7  32 FCA5      LD    (WINWID),A
5656  1ABA  B7           OR    A
5657  1ABB  C9           RET

```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

```

5658      1ABC
5659      TAPIN:          ; Read a byte from cassette
5660          i
5661          ; Read a byte from cassette
5662          i
5663      1ABC 3A FCA4    LD A,(LOWLIM)
5664 1ABF 57          LD D,A           ;[D] has lower limit for start bit
5665 1AC0 CD 046F    DATA0:          CALL BREAKX
5666 1AC0 D8          RET C            ;Aborted
5667 1AC3 DB A2    IN A,(PSG.DR)   ;Get cassette
5668 1AC4 07          RLC.A          ;High state?
5669 1AC6 30 F7    JR NC,DATAR   ;No
5670 1AC7
5671 1AC9
5672 1AC9 CD 046F    DATA0:          CALL BREAKX
5673 1ACC D8          RET C            ;Aborted
5674 1ACD DB A2    IN A,(PSG.DR)   ;Get cassette
5675 1ACF 07          RLC.A          ;falling edge?
5676 1AD0 38 F7    JR C,DATAR0   ;No
5677 1AD2 1E 00    LD E,0            ;Initialize edge mask
5678 1AD4 CD 1B1F    CALL CNTHLF   ;Get width in [C]
5679 1AD7
5680 1AD7 41          LD B,C            ;Save old width
5681 1AD8 CD 1B1F    CALL CNTHLF   ;Get new width in [C]
5682 1ADB D8          RET C            ;aborted
5683 1ADC 78          LD A,B            ;Add width of 2 pulses
5684 1ADD 81          ADD A,C          ;Pulse too long
5685 1ADE DA 1AD7    JP C,DATAR1   ;Longer than lower limit?
5686 1AE1 BA          CP D             ;No
5687 1AE2 38 F3          JR C,DATAR1   ;
5688

```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

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```
5689 ; Now, a valid start bit has been found.
5690 ; [E] = 0 if NORMAL polarity,
5691 ; =255 if REVERSE polarity.
5692 ;
5693 1AE4 2E 08 DATARL: LD L,8 ;Initialize counter
5694 1AE6 CD 1B03 CALL RDBIT
5695 1AE6 FE 04 CP 3+1 ;Legal transitions?
5696 1AE9 3F CCF
5697 1AEB D8 RET C ;Too many transitions
5698 1AEC FE 02 CP 2
5699 1AED 3F CCF ;Set carry if 2 or 3 transitions
5700 1AEF CB 1A RR D
5701 1AF0
5702 ;
5703 ; We've just assembled a bit. A check must be done to make sure
5704 ; that we're at the start of next bit field.
5705 ;
5706 1AF2 79 LD A,C ;ReGet number of transitions
5707 1AF3 0F RRCA
5708 1AF4 D4 1B23 CALL NC,CNTHL0 ;Wait for next transition if 0 or 2
5709 1AF7 CD 1BLF CALL CNTLHF
5710 1AFA 2D DEC L
5711 1AFB C2 1AE6 JP NZ,DATAFL ;Loop till done
5712 1AFE CD 046F CALL BREAKX ;return with carry set if breaked
5713 1B01 7A LD A,D
5714 1B02 C9 RET
5715 1B03 RDBIT:
5716 ;
5717 ; Count number of transitions within a period specified by 'WINWID'
5718 ;
5719 ; length of window = 17uSec x [WINWID] + 12.3 uSec
```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

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```
5720      ; [D],[H] and [L] are preserved.  
5721      ; [E] is updated to prepare for next edge  
5722      ;  
5723      ;  
5724      1B03  3A FCA5      LD A,(WINWID)    ;Get width of window  
5725      1B06  47          LD B,A  
5726      1B07  0E 00       LD C,0           ;Clear # of transitions seen  
5727      1B09  RDBITL:    IN A,(PSG.DR)    ;Get a bit  
5728      1B0B  DB A2       XOR E           ;Any changes?  
5729      1B0B  AB          JP P,NOTRAN   ;No  
5730      1B0C  F2 1B17     LD A,E           ;Transition seen  
5731      1B0F  7B          CPL E           ;Prepare for next transition  
5732      1B10  2F          LD E,A           ;Increment # of transitions  
5733      1B11  5F          INC C           ;  
5734      1B12  0C          DJNZ RDIBTL   ;Get transition count  
5735      1B13  10 F4       LD A,C           ;  
5736      1B15  79          RET             ;  
5737      1B16  C9          NOTRAN:  
5738      1B17  ;  
5739      ;  
5740      1B17  00          NOP             ;Compensate time  
5741      1B18  00          NOP             ;  
5742      1B19  00          NOP             ;  
5743      1B1A  00          NOP             ;  
5744      1B1B  10 EC       ;  
5745      ;  
5746      1B1D  79          LD A,C           ;Get transition count  
5747      1B1E  C9          RET
```

(MSX ROM BASIC BIOS) Macro-80
-CASET- Cassette drivers stuff

```

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5748      1B1F          CNTHLF:
5749      1B1F          ; Count half cycle
5750      1B1F          ; 1T =279.4nS
5751      1B22          ; period=[C] x 11.18 + 35.48uS
5752      1B23          ;
5753      1B23          ;
5754      1B1F          CD 046F          CALL  BREAK?        (87 T)
5755      1B22          D8              RET   C             ;Yes, aborted
5756      1B23          0E  00          CNTHL0:       ;Initialize counter ( 8 T)
5757      1B23          0E  00          LD   C,0           ;# of state for this loop
5758      1B23          0E  00          CNTHL1:       ;40T=11.18usec ( 5 T)
5759      1B25          0C              INC   C             ;Pulse too long
5760      1B25          0C              CNTHL1:       ;Read cassette
5761      1B26          28  0A          IN    A,(PSG.DR)  ;Desired transition?
5762      1B26          DB  A2          XOR  E             ;No
5763      1B28          DB  A2          JP   CNTHL1    ;Complement edge mask
5764      1B2A          AB              XOR  E             (11 T)
5765      1B2B          F2  1B25          LD   A,E           ( 5 T)
5766      1B2E          7B              CPL  E             ( 5 T)
5767      1B2F          2F              LD   E,A           ( 5 T)
5768      1B30          5F              RET  C             (11 T)
5769      1B31          C9              CNTFUL:       ;
5770      1B32          C9              ;Load 255
5771      1B32          0D              DEC  C             ;Count full cycle
5772      1B32          0D              RET  C             ;
5773      1B33          C9              CNTFUL:       ;
5774      1B34          C9              ;Count full cycle
5775      1B34          C9              CNTFUL:       ;
5776      1B34          CD  046F          CALL  BREAKX
5777      1B34          CD  046F          CNTFUL:       ;
5778      1B34          CD  046F          CNTFUL:       ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE    61-1
-CASET- Cassette drivers stuff

      5779  1B37   D8      RET    C           ;Aborted
      5780  1B38   DB A2   IN     A,(PSG,DR)  ;Get cassette
      5781  1B3A   07      RLCA
      5782  1B3B   38 F7   JR     C,CNTFUL
      5783  1B3D   1E 00   LD     E,0         ;No
      5784  1B3F   CD 1B23  CALL   CNTHL0
      5785  1B42   C3 1B25  JP     CNTHL1
      5786          SUBTTL - BIO - OUTDO routine

```

(MSX ROM BASIC BIOS) Macro-80
- BIO - OUTDO routine

```

5787      1B45          OUTDO:
5788      1B45          ; OUTDO:                                ; OUTDO ( RST 18H )
5789          ; Prints char in [A], to either terminal or disk
5790          ; or printer depending on the flags:
5791          ;   PRTFLG if non-zero print to printer
5792          ;   PTRFL if non-zero print to disk file pointed
5793          ; to by PTRFL
5794          ;
5795          ;
5796          ;
5797      1B45          F5          PUSH    AF        ;Save character
5798      1B46          CD FEE4     CALL    H.OUTD
5799      1B49          CD 145F     CALL    ISFLIO
5800      1B4C          28 08      JR     Z,LPTCOD
5801      1B4E          F1          POP     AF        ;Doing I/O to file?
5802      1B4F          DD 21 6C48   LD     IX,FILOUL
5803      1B53          C3 01FF     JP     CALBAS
5804          ;
5805      1B56          LPTCOD:    LD     A,(PRTFLG) ;Output to printer?
5806      1B56          3A F416     OR     A
5807      1B59          B7          JR     Z,TTYCHR ;None, output to console
5808      1B5A          28 5F      LD     A,(RAWPRT) ;Print in "RAW" mode?
5809      1B5C          3A F418     AND    A
5810      1B5F          A7          JR     NZ,LPTCH1 ;Yes, send char to printer
5811      1B60          20 49      POP     AF        ;restore char
5812      1B62          F1          ;
5813          ;
5814      1B63          1B63      OUTDLP:   PUSH    AF        ;OUTDLP:
5815      1B63          F5          ;NTBK2:
5816          ;
5817      1B64          ;

```

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   62-1
- BIO - OUTDO routine                      3.44    01-Jan-85

      5818  1B64    FE 09          CP      9
      5819  1B66    20 0E          JR      NZ,NOTABL
      5820
      5821  1B68    3E 20          ;MORSPL: LD      A, ' '
      5822  1B68    CD 1B63          OUTDLP
      5823  1B6A    3A F415          LD      A,(LPTPOS)
      5824  1B6D    E6 07          AND     7
      5825  1B70    20 F4          JR      NZ,MORSPL
      5826  1B72    F1             POP     AF
      5827  1B74    C9             RET
      5828  1B75
      5829
      5830  1B76    D6 0D          ;NOTABL: SUB    0DH
      5831  1B76    28 0A          JR      Z,ZERLP1
      5832  1B78    38 0B          JR      C,LPTCH0
      5833  1B7A
      5834
      5835  1B7C    FE 13          CP      " "-13
      5836  1B7E    38 07          JR      C,LPTCH0
      5837  1B80    3A F415          LD      A,(LPTPOS)
      5838  1B83    3C             INC
      5839
      5840  1B84    32 F415          ;ZERLP1: LD      (LPTPOS),A
      5841  1B84
      5842
      5843  1B87    3A F417          ;LPTCH0: LD      A,(NTMSXP)
      5844  1B87    A7             AND     A
      5845  1B8A    28 1E          JR      Z,LPTCH1
      5846  1B8B    F1             POP     AF
      5847  1B8D    CD 089D          CALL   CNVCHR
      5848

```

;TAB? ;Print a space
;No ;Get current LPOS
;At TAB stop?
;No, back for more space
;Discard character
;Check if CR. If so load a zero
;It is, clear LPTPOS and send CR
;Code is 0..0CH, just send .
;without modify LPTPOS
;See if control character
;Code is 0EH..1FH, ditto
;Get LPOS

```

( MSX ROM BASIC BIOS ) Macro-80          PAGE   62-2
- BIO - OUTDO routine                      3.44    01-Jan-85

      5849    1B91    D0          RET      NC      ;Yep
      5850    1B92    20 23     JR      NZ,MAPSPC
      5851    1B94    A7          AND      A
      5852    1B95    F2 1BAC    JP      P,LPTCHR
      5853    1B98    FE 86      CP      86H
      5854    1B9A    38 1B      JR      C,MAPSPC
      5855    1B9C    FE A0      CP      0A0H
      5856    1B9E    30 04      JR      NC,NTHIRA
      5857    1BA0    C6 20      ADD     A,' '
      5858    1BA2    18 08      JR      LPTCHR
      5859    1BA4    FE E0      NTHIRA: CP      0E0H
      5860    1BA4    FE E0      JR      C,LPTCHR
      5861    1BA6    38 04      SUB     '
      5862    1BA8    D6 20      DB      38H
      5863    1BAA    38          LPTCH1: POP    AF
      5864    1BAB    F1          ;Restore char
      5865    1BAB    F1          ;HIRAGANA(part 2)?
      5866    1BAC    CD 085D    LPTCHR: CALL   LPTOUT
      5867    1BAC    D0          RET      NC      ;Send character out
      5868    1BAC    DD 21 73B2    LD      IX,DIOERR
      5869    1BAF    C3 01FF    JP      CALBAS
      ;Sent success ful
      5870    1BB0    DD 21 73B2    MAPSPC: LD      A,' '
      5871    1BB4    C3 01FF    JR      LPTCHR
      5872    1BB7    3E 20      TTYCHR: LD      A,' '
      5873    1BB7    18 F1      ;Output to console
      5874    1BB9    18 F1      ;Get the character
      5875    1BBB
      5876
      5877
      5878
      5879    1 BBBB   F1

```

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(MSX ROM BASIC BIOS) Macro-80			3.44	01-Jan-85	PAGE
- BIO -	OUTDO routine				62-3
5880	1BBC	C3 08BC	JP	CHPUT	
5881			SUBTTL	-MSXCHR-	MSX character set

(MSX ROM BASIC BIOS) Macro-80
-MSXCHR- MSX character set

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	CGTABL:		
5882			
5883	1BBF	00 00 00 00	DB 00H,00H,00H,00H,00H,00H,00H,00H
5884	1BBF	00 00 00 00	DB 00H,7EH,42H,7EH,42H,7EH,42H
5885	1BC3	00 00 00 00	DB 82H,00H,10H,92H,54H,10H,28H
5886	1BC6	00 7E 42 7E	DB 44H,82H,00H,12H,14H,0F8H,14H
5887	1BCA	42 7E 42	DB 34H,52H,92H,00H,10H,10H,0FEH
5888	1BCD	82 00 10 92	DB 10H,38H,54H,92H,00H,10H,28H
5889	1BD1	54 10 28	DB 0A2H,0EAH,06H,00H,28H,44H,82H
5890	1BD4	44 82 00 12	DB 5CH,0EAH,6CH,0C8H,50H,00H,7CH
5891	1BD8	14 F8 14	
5892	1BDB	34 52 92 00	
5893	1BDF	10 10 FE	
5894	1BE2	10 38 54 92	
5895	1BE6	00 10 28	
5896	1BE9	7C 92 38 54	
5897	1BED	FE 00 10	
5898	1BFO	10 10 7C 10	
5899	1BF4	10 FE 00	
5900	1BF7	7E 42 42 7E	
5901	1BFB	42 42 7E	
5902	1BFE	00 40 7E 48	
5903	1C02	3C 28 7E	
5904	1C05	08 00 FE 92	
5905	1C09	92 FE 82	
5906	1C0C	82 86 00 04	
5907	1C10	EE A4 EF	
5908	1C13	A2 EA 06 00	
5909	1C17	28 44 82	
5910	1C1A	3C 14 24 4C	
5911	1C1E	00 28 C8	
5912	1C21	5C EA 6C C8	

(MSX ROM BASIC BIOS) Macro-80
-MSXCHR- MSX character set

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5913	1C25	50 00 7C	DB	20H,7CH,44H,7CH,44H,7CH,00H
5914	1C28	20 7C 44 7C	DB	0CH,70H,10H,0FEH,10H,10H,10H
5915	1C2C	44 7C 00	DB	00H,7EH,10H,1EH,12H,22H,44H
5916	1C2F	0C 70 10 FE	DB	08H,00H,00H,7CH,28H,28H,28H
5917	1C33	10 10 10	DB	4EH,00H,00H,10H,10H,10H,0FFH
5918	1C36	00 7E 10 1E	DB	00H,00H,00H,00H,00H,00H,00H
5919	1C3A	12 22 44	DB	0FFH,10H,10H,10H,10H,10H,10H
5920	1C3D	08 00 00 7C	DB	10H,0FOH,10H,10H,10H,10H,10H
5921	1C41	28 28 28	DB	10H,10H,10H,0FFH,10H,10H,10H
5922	1C44	4E 00 00 10	DB	10H,10H,10H,10H,10H,10H,10H
5923	1C48	10 10 FF	DB	10H,10H,10H,10H,10H,10H,10H
5924	1C4B	00 00 00 00	DB	10H,10H,10H,10H,10H,10H,10H
5925	1C4F	00 00 00	DB	10H,10H,10H,10H,10H,10H,10H
5926	1C52	FF 10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5927	1C56	10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5928	1C59	10 F0 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5929	1C5D	10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5930	1C60	10 10 1F 10	DB	10H,10H,10H,10H,10H,10H,10H
5931	1C64	10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5932	1C67	10 10 10 FF	DB	10H,10H,10H,10H,10H,10H,10H
5933	1C6B	10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5934	1C6E	10 10 10	DB	10H,10H,10H,10H,10H,10H,10H
5935	1C72	10 10 10	DB	10H,10H,00H,00H,00H,00H,00H
5936	1C75	10 10 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5937	1C79	00 FF 00	DB	00H,00H,00H,00H,00H,00H,00H
5938	1C7C	00 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5939	1C80	00 00 1F	DB	10H,10H,10H,10H,10H,10H,10H
5940	1C83	10 10 10 10	DB	0FOH,10H,10H,10H,10H,10H,10H
5941	1C87	00 00 00 00	DB	0FOH,10H,10H,10H,10H,10H,10H
5942	1C8A	F0 10 10 10	DB	0FOH,10H,10H,10H,10H,10H,10H
5943	1C8E	10 10 10 10	DB	0FOH,10H,10H,10H,10H,10H,10H

(MSX ROM BASIC BIOS) Macro-80
-MSXCHR- MSX character set

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5944	1C91	10 1F 00 00	DB	10H,1FH,00H,00H,00H,10H
5945	1C95	00 00 10	DB	10H,10H,0FOH,00H,00H,00H
5946	1C98	10 10 F0 00	DB	81H,42H,24H,18H,18H,24H,42H
5947	1C9C	00 00 00	DB	81H,10H,7CH,10H,10H,28H,44H
5948	1C9F	81 42 24 18	DB	82H,00H,10H,10H,0FEH,92H,0FEH
5949	1CA3	18 24 42	DB	10H,10H,00H,00H,00H,00H
5950	1CA6	81 10 7C 10	DB	92H,10H,30H,00H,00H,00H
5951	1CAA	10 28 44	DB	20H,00H,00H,00H,00H,00H
5952	1CAD	82 00 10 10	DB	00H,00H,00H,00H,00H,00H
5953	1CBL	FE 92 FE	DB	00H,00H,00H,00H,00H,00H
5954	1CB4	10 10 00 10	DB	00H,00H,00H,00H,00H,00H
5955	1CB8	10 54 54	DB	00H,00H,00H,00H,00H,00H
5956	1CBB	92 10 30 00	DB	00H,00H,00H,00H,00H,00H
5957	1CBF	00 00 00	DB	00H,00H,00H,00H,00H,00H
5958	1CC2	00 00 00	DB	00H,00H,00H,00H,00H,00H
5959	1CC6	00 20 20	DB	00H,00H,00H,00H,00H,00H
5960	1CC9	20 20 00	DB	00H,00H,00H,00H,00H,00H
5961	1CDD	20 00 50	DB	00H,50H,00H,00H,00H,00H
5962	1CD0	50 50 00	DB	00H,00H,00H,00H,00H,00H
5963	1CD4	00 00 00	DB	00H,00H,00H,00H,00H,00H
5964	1CD7	50 50 F8 50	DB	50H,50H,0F8H,50H,0F8H,50H,50H
5965	1CDB	F8 50 50	DB	00H,20H,78H,0A0H,70H,28H,0FOH
5966	1CDE	00 20 78 A0	DB	20H,00H,0C0H,0C8H,10H,20H,40H
5967	1CE2	70 28 F0	DB	98H,18H,00H,40H,0A0H,40H,0A8H
5968	1CE5	20 00 C0 C8	DB	90H,98H,60H,00H,10H,20H,40H
5969	1CE9	10 20 40	DB	00H,00H,00H,00H,00H,00H,00H
5970	1CEC	98 18 00 40	DB	00H,00H,00H,00H,00H,00H,00H
5971	1CF0	A0 40 A8	DB	00H,00H,00H,00H,00H,00H,00H
5972	1CF3	90 98 60 00	DB	00H,00H,00H,00H,00H,00H,00H
5973	1CF7	10 20 40	DB	00H,00H,00H,00H,00H,00H,00H
5974	1CFA	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H

(MSX ROM BASIC BIOS) Macro-80
-MSXCHR- MSX character set

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5975	1CFE	00 10 20		
5976	1D01	40 40 40 20	DB	40H,40H,40H,20H,10H,00H,40H
5977	1D05	10 00 40	DB	20H,10H,10H,20H,40H,00H
5978	1D08	20 10 10 10	DB	20H,0A8H,70H,20H,70H,0A8H,20H
5979	1D0C	20 40 00	DB	00H,00H,20H,20H,0F8H,20H
5980	1D0F	20 A8 70 20	DB	00H,00H,00H,00H,00H,00H,00H
5981	1D13	70 A8 20	DB	00H,00H,00H,00H,00H,00H,00H
5982	1D16	00 00 20 20	DB	00H,00H,00H,00H,00H,00H,00H
5983	1D1A	F8 20 20	DB	00H,00H,00H,00H,00H,00H,00H
5984	1D1D	00 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5985	1D21	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5986	1D24	20 20 40 00	DB	20H,20H,40H,00H,00H,00H,78H
5987	1D28	00 00 78	DB	00H,00H,00H,00H,00H,00H,00H
5988	1D2B	00 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5989	1D2F	00 00 00	DB	00H,00H,60H,60H,00H,00H,00H
5990	1D32	00 00 60 60	DB	00H,00H,00H,00H,00H,00H,00H
5991	1D36	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
5992	1D39	08 10 20 40	DB	08H,10H,20H,40H,80H,00H,70H
5993	1D3D	80 00 70	DB	88H,98H,0A8H,0C8H,88H,70H,00H
5994	1D40	88 98 A8 C8	DB	00H,70H,88H,08H,10H,60H,80H
5995	1D44	88 70 00	DB	0F8H,00H,70H,88H,08H,30H,08H
5996	1D47	20 60 A0 20	DB	88H,70H,00H,10H,30H,50H,90H
5997	1D4B	20 20 F8	DB	0F8H,10H,10H,00H,0F8H,80H,0E0H
5998	1D4E	00 70 88 08	DB	
5999	1D52	10 60 80	DB	
6000	1D55	F8 00 70 88	DB	
6001	1D59	08 30 08	DB	
6002	1D5C	88 70 00 10	DB	
6003	1D60	30 50 90	DB	
6004	1D63	F8 10 10 00	DB	
6005	1D67	F8 80 E0	DB	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-4
-MSXCHR-	MSX character set				
6006	1D6A	10 08 10 E0	DB	10H,08H,10H,0E0H,00H,30H,40H	
6007	1D6E	00 30 40	DB	80H,0F0H,88H,88H,70H,00H,0F8H	
6008	1D71	80 F0 88 88	DB	88H,10H,20H,20H,20H,20H,00H	
6009	1D75	70 00 F8	DB	70H,88H,88H,70H,88H,88H,70H	
6010	1D78	88 10 20 20	DB	00H,00H,00H,00H,00H,00H,00H	
6011	1D7C	20 20 00	DB	00H,00H,00H,00H,00H,00H,00H	
6012	1D7F	70 88 88 70	DB	00H,00H,00H,00H,00H,00H,00H	
6013	1D83	88 88 70	DB	00H,00H,00H,00H,00H,00H,00H	
6014	1D86	00 70 88 88	DB	00H,70H,88H,88H,78H,08H,10H	
6015	1D8A	78 08 10	DB	00H,00H,00H,00H,00H,00H,00H	
6016	1D8D	60 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6017	1D91	20 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6018	1D94	20 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6019	1D98	00 20 00	DB	00H,20H,20H,40H,18H,30H,60H	
6020	1D9B	00 20 20 40	DB	00H,00H,00H,00H,00H,00H,00H	
6021	1D9F	18 30 60	DB	00H,00H,00H,00H,00H,00H,00H	
6022	1DA2	C0 60 30 18	DB	00H,00H,00H,00H,00H,00H,00H	
6023	1DA6	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6024	1DA9	F8 00 F8 00	DB	00H,00H,00H,00H,00H,00H,00H	
6025	1DAD	00 00 C0	DB	00H,00H,00H,00H,00H,00H,00H	
6026	1DB0	60 30 18 30	DB	00H,00H,00H,00H,00H,00H,00H	
6027	1DB4	60 C0 00	DB	00H,00H,00H,00H,00H,00H,00H	
6028	1DB7	70 88 08 10	DB	00H,00H,00H,00H,00H,00H,00H	
6029	1DBB	20 00 20	DB	00H,00H,00H,00H,00H,00H,00H	
6030	1DBE	00 70 88 08	DB	00H,00H,00H,00H,00H,00H,00H	
6031	1DC2	68 A8 A8	DB	00H,00H,00H,00H,00H,00H,00H	
6032	1DC5	70 00 20 50	DB	00H,00H,00H,00H,00H,00H,00H	
6033	1DC9	88 88 F8	DB	00H,00H,00H,00H,00H,00H,00H	
6034	1DCC	88 88 00 F0	DB	00H,00H,00H,00H,00H,00H,00H	
6035	1DD0	48 48 70	DB	00H,00H,00H,00H,00H,00H,00H	
6036	1DD3	48 48 F0 00	DB	00H,00H,00H,00H,00H,00H,00H	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-5
-MSXCHR-	MSX character set				
6037	1DD7	30 48 80		DB	80H,80H,48H,30H,00H,0E0H,50H
6038	1DDA	80 80 48 30			
6039	1DDE	00 E0 50		DB	48H,48H,48H,50H,0E0H,00H,0F8H
6040	1DEF	48 48 48 50			
6041	1DE5	E0 00 F8			
6042	1DE8	80 80 F0 80		DB	80H,80H,0F0H,80H,80H,0F8H,00H
6043	1DEC	80 F8 00			
6044	1DEF	F8 80 80 F0		DB	0F8H,80H,80H,0F0H,80H,80H,80H
6045	1DF3	80 80 80			
6046	1DF6	00 70 88 80		DB	00H,70H,88H,80H,0B8H,88H,88H
6047	1DFA	B8 88 88			
6048	1DFD	70 00 88 88		DB	70H,00H,88H,88H,88H,0F8H,88H
6049	1E01	88 F8 88			
6050	1E04	88 88 00 70		DB	88H,88H,00H,70H,20H,20H,20H
6051	1E08	20 20 20			
6052	1E0B	20 20 70 00		DB	20H,20H,70H,00H,38H,10H,10H
6053	1E0F	38 10 10			
6054	1E12	10 90 90 60		DB	10H,90H,90H,60H,00H,88H,90H
6055	1E16	00 88 90			
6056	1E19	A0 C0 A0 90		DB	0A0H,0C0H,0A0H,90H,88H,00H,80H
6057	1E1D	88 00 80			
6058	1E20	80 80 80		DB	80H,80H,80H,80H,0F8H,00H
6059	1E24	80 F8 00			
6060	1E27	88 D8 A8 A8		DB	88H,0D8H,0A8H,0A8H,88H,88H,88H
6061	1E2B	88 88 88			
6062	1E2E	00 88 C8 C8		DB	00H,88H,0C8H,0C8H,0A8H,98H,98H
6063	1E32	A8 98 98			
6064	1E35	88 00 70 88		DB	88H,00H,70H,88H,88H,88H,88H
6065	1E39	88 88 88			
6066	1E3C	88 70 00 F0		DB	88H,70H,00H,0F0H,88H,88H,0F0H
6067	1E40	88 88 F0			

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-6
-MSXCHR-	MSX character set				
6068	1E43	80 80 80 00	DB	80H,80H,80H,00H,70H,88H,88H	
6069	1E47	70 88 88	DB	88H,0A8H,90H,68H,00H,0F0H,88H	
6070	1E4A	88 A8 90 68	DB	88H,0F0H,0A0H,90H,88H,00H,70H	
6071	1E4E	00 F0 88	DB	88H,0F0H,70H,10H,10H,10H,10H	
6072	1E51	88 F0 A0 90	DB	00H,88H,50H,20H,70H,20H,70H	
6073	1E55	88 00 70 00	DB	10H,70H,00H,20H,50H,88H,00H	
6074	1E58	88 80 70 08	DB	88H,80H,70H,08H,88H,70H,00H	
6075	1E5C	88 70 00	DB	0F8H,20H,20H,20H,20H,20H,20H	
6076	1E5F	F8 20 20 20	DB	00H,88H,88H,88H,88H,88H,88H	
6077	1E63	20 20 20 20	DB	70H,00H,88H,88H,88H,88H,50H	
6078	1E66	00 88 88 88	DB	0A8H,0D8H,88H,00H,88H,88H,50H	
6079	1E6A	88 88 88 88	DB	50H,20H,00H,88H,88H,88H,0A8H	
6080	1E6D	70 00 88 88	DB	20H,50H,88H,88H,00H,88H,88H	
6081	1E71	88 88 50 00	DB	88H,70H,20H,20H,00H,0F8H	
6082	1E74	50 20 00 88	DB	08H,10H,20H,40H,40H,40H,70H	
6083	1E78	88 88 A8	DB	00H,88H,50H,20H,70H,20H,70H	
6084	1E7B	A8 D8 00 00	DB	10H,70H,00H,20H,50H,88H,00H	
6085	1E7F	88 88 50 00	DB	20H,00H,70H,10H,10H,10H,10H	
6086	1E82	20 50 88 88	DB	10H,40H,40H,40H,40H,40H,70H	
6087	1E86	00 88 88 88	DB	00H,88H,50H,20H,70H,20H,70H	
6088	1E89	88 70 20 20	DB	10H,70H,00H,20H,50H,88H,00H	
6089	1E8D	20 00 F8	DB	20H,00H,70H,10H,10H,10H,10H	
6090	1E90	08 10 20 40	DB	10H,40H,40H,40H,40H,40H,70H	
6091	1E94	80 F8 00 00	DB	00H,88H,50H,20H,70H,20H,70H	
6092	1E97	70 40 40 40	DB	10H,70H,00H,20H,50H,88H,00H	
6093	1E9B	40 40 70 70	DB	20H,00H,70H,10H,10H,10H,10H	
6094	1E9E	00 88 50 20	DB	10H,70H,00H,20H,50H,88H,00H	
6095	1EA2	70 20 70 70	DB	10H,70H,00H,20H,50H,88H,00H	
6096	1EA5	20 00 70 10	DB	10H,70H,00H,20H,50H,88H,00H	
6097	1EA9	10 10 10 10	DB	10H,70H,00H,20H,50H,88H,00H	
6098	1EAC	10 70 00 20	DB	10H,70H,00H,20H,50H,88H,00H	

(MSX ROM BASIC BIOS) Macro-80
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6099	1EB0	50 88 00	DB	00H,00H,00H,00H,00H,00H,00H
6100	1EB3	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
6101	1EB7	00 00 00	DB	00H,00H,00H,0F8H,00H,40H,20H
6102	1EBA	00 00 00	DB	10H,00H,00H,00H,00H,00H,00H
6103	1EBE	00 40 20	DB	00H,70H,08H,78H,88H,78H,00H
6104	1EC1	10 00 00	DB	80H,80H,0B0H,0C8H,88H,0C8H,0B0H
6105	1EC5	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
6106	1EC8	00 70 08	DB	00H,00H,00H,00H,00H,00H,00H
6107	1ECC	88 78 00	DB	00H,00H,00H,00H,00H,00H,00H
6108	1ECF	80 80 B0	DB	00H,00H,00H,00H,00H,00H,00H
6109	1ED3	88 C8 B0	DB	00H,00H,00H,00H,00H,00H,00H
6110	1ED6	00 00 70	DB	00H,00H,00H,00H,00H,00H,00H
6111	1EDA	88 80 88	DB	00H,00H,08H,08H,68H,98H,88H
6112	1EDD	70 00 08	DB	00H,00H,00H,00H,00H,00H,00H
6113	1EE1	68 98 88	DB	00H,00H,00H,00H,00H,00H,00H
6114	1EE4	98 68 00	DB	00H,00H,00H,00H,00H,00H,00H
6115	1EE8	00 70 88	DB	00H,00H,00H,00H,00H,00H,00H
6116	1EEB	F8 80 70	DB	00H,00H,00H,00H,00H,00H,00H
6117	1EEF	10 28 20	DB	00H,00H,00H,00H,00H,00H,00H
6118	1EF2	F8 20 20	DB	00H,00H,00H,00H,00H,00H,00H
6119	1EF6	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H
6120	1EF9	68 98 68	DB	00H,00H,00H,00H,00H,00H,00H
6121	1EFD	08 70 80	DB	00H,00H,00H,00H,00H,00H,00H
6122	1FO0	80 F0 88	DB	00H,00H,00H,00H,00H,00H,00H
6123	1FO4	88 88 00	DB	00H,00H,00H,00H,00H,00H,00H
6124	1FO7	20 00 60	DB	00H,00H,00H,00H,00H,00H,00H
6125	1FOB	20 20 70	DB	00H,00H,00H,00H,00H,00H,00H
6126	1FOE	00 10 00	DB	00H,00H,00H,00H,00H,00H,00H
6127	1F12	10 10 10	DB	00H,00H,00H,00H,00H,00H,00H
6128	1F15	90 60 40	DB	00H,00H,00H,00H,00H,00H,00H
6129	1F19	48 50 60	DB	00H,00H,00H,00H,00H,00H,00H

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-8
-MSXCHR- MSX character set					
6130	1F1C	50 48 00 60	DB	50H,48H,00H,60H,20H,20H,20H	
6131	1F20	20 20 20	DB	20H,20H,70H,00H,00H,00H,0D0H	
6132	1F23	20 20 70 00	DB		
6133	1F27	00 00 D0	DB	0A8H,0A8H,0A8H,0A8H,00H,00H,00H	
6134	1F2A	A8 A8 A8 A8	DB		
6135	1F2E	00 00 00	DB		
6136	1F31	B0 C8 88 88	DB	0B0H,0C8H,88H,88H,88H,00H,00H	
6137	1F35	88 00 00	DB		
6138	1F38	00 70 88 88	DB	00H,70H,88H,88H,88H,70H,00H	
6139	1F3C	88 70 00	DB		
6140	1F3F	00 00 B0 C8	DB	00H,00H,0B0H,0C8H,0C8H,0B0H,80H	
6141	1F43	C8 B0 80	DB		
6142	1F46	80 00 00 68	DB	80H,00H,00H,68H,98H,98H,68H	
6143	1F4A	98 98 68	DB		
6144	1F4D	08 08 00 00	DB	08H,08H,00H,00H,0B0H,0C8H,80H	
6145	1F51	B0 C8 80	DB		
6146	1F54	80 80 00 00	DB	80H,80H,00H,00H,00H,78H,78H,80H	
6147	1F58	00 78 80	DB		
6148	1F5B	F0 08 F0 00	DB	0F0H,08H,0F0H,00H,40H,40H,0FOH	
6149	1F5F	40 40 F0	DB		
6150	1F62	40 40 48 30	DB	40H,40H,48H,30H,00H,00H,00H	
6151	1F66	00 00 00	DB		
6152	1F69	90 90 90	DB	90H,90H,90H,68H,00H,00H	
6153	1F6D	68 00 00	DB		
6154	1F70	00 88 88 88	DB	00H,88H,88H,88H,50H,20H,00H	
6155	1F74	50 20 00	DB		
6156	1F77	00 00 88 A8	DB	00H,00H,88H,0A8H,0A8H,0A8H,50H	
6157	1F7B	A8 A8 50	DB		
6158	1F7E	00 00 00 88	DB	00H,00H,00H,88H,50H,20H,50H	
6159	1F82	50 20 50	DB		
6160	1F85	88 00 00 00	DB	88H,00H,00H,00H,88H,88H,98H	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-9
-MSXCHR-	MSX character set				
6161	1F89	88	88	98	
6162	1F8C	68	08	70	00
6163	1F90	00	F8	10	
6164	1F93	20	40	F8	00
6165	1F97	18	20	20	
6166	1F9A	40	20	20	18
6167	1F9E	00	20	20	
6168	1FA1	20	00	20	20
6169	1FA5	20	00	C0	
6170	1FA8	20	20	10	20
6171	1FAC	20	C0	00	
6172	1FAF	40	A8	10	00
6173	1FB3	00	00	00	
6174	1FB6	00	00	00	00
6175	1FBA	00	00	00	
6176	1FBF	00	00	10	38
6177	1FC1	7C	FE	FE	
6178	1FC4	38	7C	00	6C
6179	1FC8	FE	FE	FE	
6180	1FCB	7C	38	10	00
6181	1FCF	38	38	FE	
6182	1FD2	FE	D6	10	7C
6183	1FD6	00	10	38	
6184	1FD9	7C	FE	7C	38
6185	1FDD	10	00	00	
6186	1FE0	78	84	84	84
6187	1FE4	84	78	00	
6188	1FE7	00	78	FC	FC
6189	1FEB	FC	FC	78	
6190	1FEE	00	40	FE	48
6191	1FF2	70	48	82	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-10
-MSXCHR-	MSX character set				
6192	1FF5	7C 00 00 00	DB	7CH,00H,00H,00H,10H,7EH,3CH	
6193	1FF9	10 7E 3C	DB	5AH,34H,00H,00H,00H,40H,42H	
6194	1FFC	5A 34 00 00	DB	42H,52H,20H,00H,00H,00H,1CH	
6195	2000	00 40 42	DB	1CH,22H,02H,0CH,00H,00H,00H	
6196	2003	42 52 20 00	DB	18H,7EH,18H,30H,6EH,00H,00H	
6197	2007	00 00 1C	DB	00H,00H,28H,7CH,2AH,22H,24H	
6198	200A	1C 22 02 0C	DB	00H,12H,7EH,3CH,52H,34H,00H	
6199	200E	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6200	2011	18 7E 18 30	DB	02H,02H,1CH,00H,00H,00H,00H	
6201	2015	6E 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6202	2018	00 12 7E 3C	DB	00H,00H,00H,00H,00H,00H,00H	
6203	201C	52 34 00	DB	00H,00H,00H,00H,00H,00H,00H	
6204	201F	00 00 28 7C	DB	00H,00H,00H,00H,00H,00H,00H	
6205	2023	2A 22 24	DB	00H,00H,00H,00H,00H,00H,00H	
6206	2026	00 00 00 08	DB	00H,00H,00H,00H,00H,00H,00H	
6207	202A	5C 6A 0C	DB	00H,00H,00H,00H,00H,00H,00H	
6208	202D	30 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6209	2031	08 0E 38	DB	00H,00H,00H,00H,00H,00H,00H	
6210	2034	4C 3A 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6211	2038	00 00 3C	DB	00H,00H,00H,00H,00H,00H,00H	
6212	203B	02 02 1C 00	DB	00H,00H,00H,00H,00H,00H,00H	
6213	203F	00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6214	2042	00 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6215	2046	00 20 FE	DB	00H,00H,00H,00H,00H,00H,00H	
6216	2049	20 7C AA B2	DB	00H,00H,00H,00H,00H,00H,00H	
6217	204D	64 00 00	DB	00H,00H,00H,00H,00H,00H,00H	
6218	2050	80 82 82	DB	00H,00H,00H,00H,00H,00H,00H	
6219	2054	90 60 00	DB	00H,00H,00H,00H,00H,00H,00H	
6220	2057	1C 00 7C 02	DB	00H,00H,00H,00H,00H,00H,00H	
6221	205B	02 04 18	DB	00H,00H,00H,00H,00H,00H,00H	
6222	205E	00 38 00 FE	DB	00H,00H,00H,00H,00H,00H,00H	

(MSX ROM	BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-11
-MSXCHR-	MSX character set					
6223	2062	08 30 50				
6224	2065	9E 00 20 FA	DB	9EH,00H,20H,0FAH,22H,7CH,0A2H		
6225	2069	22 7C A2	DB	0A2H,4CH,00H,40H,44H,0F2H,4AH		
6226	206C	A2 4C 00 40	DB	48H,88H,30H,00H,10H,0FCH,08H		
6227	2070	44 F2 4A	DB			
6228	2073	48 88 30 00	DB			
6229	2077	10 FC 08	DB			
6230	207A	3E 04 80 7C	DB	3EH,04H,80H,7CH,00H,18H,18H		
6231	207E	00 18 18	DB			
6232	2081	30 60 30	DB	30H,60H,60H,30H,18H,00H,04H		
6233	2085	18 00 04	DB			
6234	2088	84 BE 84	DB	84H,0BEH,84H,84H,84H,48H,00H		
6235	208C	84 48 00	DB			
6236	208F	00 FC 02 00	DB	00H,0FCHEH,02H,00H,40H,80H,7EH		
6237	2093	40 80 7E	DB			
6238	2096	00 10 16 F8	DB	00H,10H,16H,0F8H,08H,7CH,80H		
6239	209A	08 7C 80	DB			
6240	209D	78 00 80 80	DB	78H,00H,80H,80H,80H,80H,84H		
6241	20A1	80 80 84	DB			
6242	20A4	88 70 00 08	DB	88H,70H,00H,08H,0FEH,08H,38H		
6243	20A8	FE 08 38	DB			
6244	20AB	48 38 08 00	DB	48H,38H,08H,00H,04H,44H,0FEH		
6245	20AF	04 44 FE	DB			
6246	20B2	44 44 40 3E	DB	44H,44H,40H,3EH,00H,64H,28H		
6247	20B6	00 64 28	DB			
6248	20B9	30 FE 20 40	DB	30H,0FEH,20H,40H,3CH,00H,00H		
6249	20BD	3C 00 00	DB			
6250	20C0	00 00 00 00	DB	00H,00H,00H,00H,00H,00H,00H		
6251	20C4	00 00 00	DB			
6252	20C7	00 00 00	DB	00H,00H,00H,00H,60H,90H,60H		
6253	20CB	60 90 60	DB			

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-12
-MSXCHR-	MSX character set				
6254	20CE	00 38 20 20	DB	00H, 38H, 20H, 20H, 00H, 00H	
6255	20D2	20 00 00	DB	00H, 00H, 00H, 00H, 20H, 20H	
6256	20D5	00 00 00 00	DB	00H, 00H, 00H, 00H, 00H, 00H	
6257	20D9	00 20 20	DB	20H, 0E0H, 00H, 00H, 00H, 00H	
6258	20DC	20 E0 00 00	DB	80H, 40H, 20H, 00H, 00H, 00H	
6259	20E0	00 00 00 00	DB	30H, 30H, 00H, 00H, 00H, 08H	
6260	20E3	80 40 20 00	DB	00H, 0F0H, 10H, 00H, 00H, 00H	
6261	20E7	00 00 00 00	DB	00H, 0F0H, 10H, 00H, 00H, 00H	
6262	20EA	30 30 00 00	DB	00H, 0F0H, 10H, 00H, 00H, 00H	
6263	20EE	00 F8 08 08	DB	00H, 0F0H, 10H, 00H, 00H, 00H	
6264	20F1	F8 08 10 20	DB	00H, 0F0H, 10H, 00H, 00H, 00H	
6265	20F5	40 00 00 00	DB	00H, 0F0H, 10H, 60H, 40H, 80H, 00H	
6266	20F8	00 F0 10 60	DB	00H, 0F0H, 10H, 60H, 40H, 80H, 00H	
6267	20FC	40 80 00 00	DB	00H, 10H, 20H, 60H, 0A0H, 20H, 20H	
6268	20FF	00 10 20 60	DB	00H, 0F0H, 00H, 0F0H, 90H, 10H, 20H	
6269	2103	A0 20 20	DB	00H, 00H, 20H, 0F0H, 90H, 10H, 20H	
6270	2106	00 00 20 F0	DB	00H, 00H, 20H, 0F0H, 90H, 10H, 20H	
6271	210A	90 10 20	DB	40H, 00H, 00H, 0F0H, 20H, 20H	
6272	210D	40 00 00 00	DB	0A0H, 0A0H, 20H, 00H, 00H, 40H, 0F8H	
6273	2111	F0 20 20	DB	20H, 0F0H, 00H, 00H, 20H, 0F0H, 60H	
6274	2114	20 F0 00 00	DB	48H, 50H, 40H, 40H, 00H, 00H, 00H	
6275	2118	20 F0 60	DB	70H, 10H, 10H, 0F8H, 00H, 00H	
6276	211B	A0 A0 20 00	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6277	211F	00 40 F8	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6278	2122	48 50 40 40	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6279	2126	00 00 00 00	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6280	2129	70 10 10 10	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6281	212D	F8 00 00 00	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6282	2130	00 F0 10 F0	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6283	2134	10 F0 00 00	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	
6284	2137	00 00 A8 A8	DB	00H, 0F0H, 10H, 0F0H, 10H, 0F0H, 00H	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-13
-MSXCHR-	MSX character set				
6285	213B	08 10 20		DB	00H,00H,00H,00H,0F8H,00H,00H
6286	213E	00 00 00		DB	00H,00H,0F8H,08H,28H,30H,20H
6287	2142	F8 00		DB	20H,40H,00H,08H,10H,20H,60H
6288	2145	00 00 F8 08		DB	0A0H,20H,20H,00H,20H,0F8H,88H
6289	2149	28 30 20		DB	88H,08H,10H,20H,00H,00H,0F8H
6290	214C	20 40 00 08		DB	0F8H,10H,30H,50H,90H,10H,00H
6291	2150	10 20 60		DB	20H,0F8H,28H,28H,48H,48H,88H
6292	2153	A0 20 20 00		DB	50H,50H,10H,20H,00H,00H,0F8H,00H
6293	2157	20 F8 88		DB	0C0H,08H,0C8H,08H,10H,0E0H,00H
6294	215A	88 08 10 20		DB	
6295	215E	00 00 F8		DB	
6296	2161	20 20 20 20		DB	
6297	2165	F8 00 10		DB	
6298	2168	F8 10 30 50		DB	
6299	216C	90 10 00		DB	
6300	216F	20 F8 28 28		DB	
6301	2173	28 48 88		DB	
6302	2176	00 20 F8 20		DB	
6303	217A	F8 20 20		DB	
6304	217D	20 00 78 48		DB	
6305	2181	88 08 08		DB	
6306	2184	10 20 00 40		DB	
6307	2188	78 50 90		DB	
6308	218B	10 10 20 00		DB	
6309	218F	00 F8 08		DB	
6310	2192	08 08 F8		DB	
6311	2196	00 50 F8		DB	
6312	2199	50 50 10 10		DB	
6313	219D	20 00 00		DB	
6314	21A0	C0 08 C8 08		DB	
6315	21A4	10 E0 00		DB	

(MSX ROM	BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-14
-MSXCHR-	MSX character set					
6316	21A7	00 F8 08 10	DB	00H,0F8H,08H,10H,20H,50H,88H		
6317	21AB	20 50 88	DB	00H,40H,0F8H,48H,50H,40H,40H		
6318	21AE	00 40 F8 48	DB	38H,00H,88H,88H,48H,08H,10H		
6319	21B2	50 40 40	DB	20H,40H,00H,78H,48H,78H,88H		
6320	21B5	38 00 88 88	DB	08H,10H,20H,00H,10H,0E0H,20H		
6321	21B9	48 08 10	DB	0A8H,08H,08H,10H,20H,00H,70H		
6322	21BC	20 40 00 78	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6323	21C0	48 78 88	DB	040H,040H,040H,040H,040H,040H		
6324	21C3	08 10 20 00	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6325	21C7	10 E0 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6326	21CA	F8 20 20 40	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6327	21CE	00 A8 A8	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6328	21D1	A8 08 08 10	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6329	21D5	20 00 70	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6330	21D8	00 F8 20 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6331	21DC	20 40 00	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6332	21DF	40 40 60 50	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6333	21E3	48 40 40	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6334	21E6	00 20 F8 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6335	21EA	20 20 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6336	21ED	40 00 00 70	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6337	21F1	00 00 00	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6338	21F4	00 F8 00 00	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6339	21F8	F8 08 D0	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6340	21FB	20 50 88 00	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6341	21FF	20 F8 08	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6342	2202	30 E8 20 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6343	2206	00 08 08	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6344	2209	08 10 20 40	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6345	220D	80 00 20	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		
6346	2210	10 48 48 48	DB	00H,0F8H,00H,0F8H,00H,0F8H,00H		

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(MSX ROM BASIC BIOS)	-MSXCHR-	MSX character set	3.44			
6347	2214	48 88 00				
6348	2217	80 80 F8 80	DB	80H,80H,0F8H,80H,80H,80H,78H		
6349	221B	80 80 78	DB	00H,0F8H,08H,08H,08H,10H,20H		
6350	221E	00 F8 08 08	DB	40H,00H,00H,40H,0A0H,10H,08H		
6351	2222	08 10 20	DB	08H,00H,00H,20H,0F8H,20H,20H		
6352	2225	40 00 40	DB	0A8H,0A8H,20H,00H,00H,0F8H,08H		
6353	2229	A0 10 08	DB	08H,50H,20H,10H,00H,0F0H,00H		
6354	222C	08 00 20	DB	08H,08H,08H,50H,20H,0F8H,48H		
6355	2230	F8 20 20	DB	00H,00H,00H,00H,00H,00H,00H		
6356	2233	A8 20 00	DB	00H,00H,00H,00H,00H,00H,00H		
6357	2237	00 F8 08	DB	00H,00H,00H,00H,00H,00H,00H		
6358	223A	08 50 20	DB	00H,00H,00H,00H,00H,00H,00H		
6359	223E	00 F0 00	DB	00H,00H,00H,00H,00H,00H,00H		
6360	2241	60 00 00	DB	00H,00H,00H,00H,00H,00H,00H		
6361	2245	08 00 10	DB	00H,00H,00H,00H,00H,00H,00H		
6362	2248	20 40 80	DB	00H,00H,00H,00H,00H,00H,00H		
6363	224C	88 F8 00	DB	00H,00H,00H,00H,00H,00H,00H		
6364	224F	08 08 08	DB	00H,00H,00H,00H,00H,00H,00H		
6365	2253	20 50 80	DB	00H,00H,00H,00H,00H,00H,00H		
6366	2256	00 78 20	DB	00H,00H,00H,00H,00H,00H,00H		
6367	225A	20 20 20	DB	00H,00H,00H,00H,00H,00H,00H		
6368	225D	18 00 40	DB	00H,00H,00H,00H,00H,00H,00H		
6369	2261	48 48 50	DB	00H,00H,00H,00H,00H,00H,00H		
6370	2264	40 40 00	DB	00H,00H,00H,00H,00H,00H,00H		
6371	2268	70 10 10	DB	00H,00H,00H,00H,00H,00H,00H		
6372	226B	10 10 F8 00	DB	00H,00H,00H,00H,00H,00H,00H		
6373	226F	00 F8 08	DB	00H,00H,00H,00H,00H,00H,00H		
6374	2272	F8 08 08	DB	00H,00H,00H,00H,00H,00H,00H		
6375	2276	00 70 00	DB	00H,00H,00H,00H,00H,00H,00H		
6376	2279	F8 08 10	DB	00H,00H,00H,00H,00H,00H,00H		
6377	227D	20 00 48	DB	00H,00H,00H,00H,00H,00H,00H		

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-16
-MSXCHR-	MSX character set				
6378	2280	48 48 48 48	DB	48H,48H,48H,48H,10H,20H,00H	
6379	2284	10 20 00	DB	10H,50H,50H,50H,50H,58H,90H	
6380	2287	10 50 50 50	DB	00H,40H,40H,40H,48H,48H,50H	
6381	228B	50 58 90	DB	04H,38H,00H,00H,0F8H,88H,80H	
6382	228E	00 40 40 40	DB	30H,40H,40H,3EH,00H,24H,0F2H	
6383	2292	48 48 50	DB	48H,48H,9CH,0AAH,10H,00H,80H	
6384	2295	60 00 00 F8	DB	60H,00H,00H,0F8H,88H,88H,88H	
6385	2299	88 88 88	DB	88H,0F8H,00H,0F8H,88H,88H,08H	
6386	229C	88 F8 00 F8	DB	08H,10H,20H,00H,00H,0C0H,00H	
6387	22A0	88 88 08	DB	08H,08H,10H,0EOH,00H,90H,48H	
6388	22A3	08 10 20 00	DB	90H,60H,00H,00H,00H,00H,00H	
6389	22A7	00 C0 00	DB	04H,22C6,00H,00H,00H,00H,00H	
6390	22AA	08 08 10 E0	DB	04H,22D4,00H,00H,00H,00H,00H	
6391	22AE	00 90 48	DB	04H,22D8,00H,00H,00H,00H,00H	
6392	22B1	00 00 00	DB	04H,22DB,00H,00H,00H,00H,00H	
6393	22B5	00 00 60	DB	04H,22DF,00H,00H,00H,00H,00H	
6394	22B8	90 60 00 00	DB	04H,22E2,00H,00H,00H,00H,00H	
6395	22BC	00 00 00	DB	04H,22E6,00H,00H,00H,00H,00H	
6396	22BF	40 FE 40 5E	DB	04H,22E9,00H,00H,00H,00H,00H	
6397	22C3	80 A0 9E	DB	04H,22F2,00H,00H,00H,00H,00H	
6398	22C6	00 20 FE 40	DB	04H,22F6,00H,00H,00H,00H,00H	
6399	22CA	F8 04 04	DB	04H,22F8,00H,00H,00H,00H,00H	
6400	22CD	78 00 00	DB	04H,22FA,00H,00H,00H,00H,00H	
6401	22D1	FC 02 02	DB	04H,22FB,00H,00H,00H,00H,00H	
6402	22D4	04 38 00 00	DB	04H,22FD,00H,00H,00H,00H,00H	
6403	22D8	FE 0C 30	DB	04H,22FE,00H,00H,00H,00H,00H	
6404	22DB	40 40 38 00	DB	04H,22FF,00H,00H,00H,00H,00H	
6405	22DF	10 12 1C	DB	04H,22E2,00H,00H,00H,00H,00H	
6406	22E2	30 40 40 3E	DB	04H,22E6,00H,00H,00H,00H,00H	
6407	22E6	00 24 F2	DB	04H,22E9,00H,00H,00H,00H,00H	
6408	22E9	48 48 9C AA	DB	04H,22F2,00H,00H,00H,00H,00H	

(MSX ROM BASIC BIOS)	Macro-80	3.44	01-Jan-85	PAGE	63-17
-MSXCHR-	MSX character set				
6409	22ED	10 00 80		DB	9EH,80H,80H,0A0H,0BEH,0C0H,00H
6410	22F0	9E 80 80 A0			
6411	22F4	BE C0 00		DB	44H,4CH,7AH,0AAH,0A6H,0AAH,6CH
6412	22F7	44 4C 7A AA			
6413	22FB	A6 AA 6C		DB	00H,40H,0ECH,52H,62H,0CEH,4AH
6414	22FE	00 40 EC 52			
6415	2302	62 CE 4A		DB	0A2H,4CH,00H,04H,0BEH,84H,84H
6416	2305	4C 00 00 38		DB	4CH,00H,00H,38H,54H,92H,0A2H
6417	2309	54 92 A2			
6418	230C	A2 4C 00 04		DB	0A2H,4CH,00H,04H,0BEH,84H,84H
6419	2310	BE 84 84			
6420	2313	9E A4 5C 00		DB	9EH,0A4H,5CH,00H,08H,4CH,0C6H
6421	2317	08 4C C6			
6422	231A	46 44 44 38		DB	46H,44H,44H,38H,00H,20H,18H
6423	231E	00 20 18			
6424	2321	20 16 8A CA		DB	20H,16H,8AH,0CAH,18H,00H,00H
6425	2325	18 00 00			
6426	2328	20 70 D8 8C		DB	20H,70H,0D8H,8CH,06H,02H,00H
6427	232C	06 02 00			
6428	232F	3E 84 BE 84		DB	3EH,84H,0BEH,84H,9CH,0A6H,18H
6429	2333	9C A6 18			
6430	2336	00 08 7E 08		DB	00H,08H,7EH,08H,7EH,38H,4CH
6431	233A	7E 38 4C			
6432	233D	3A 00 E0 24		DB	3AH,00H,0E0H,24H,24H,7EH,0A4H
6433	2341	24 7E A4			
6434	2344	A4 68 00 20		DB	0A4H,68H,00H,20H,0FCH,24H,62H
6435	2348	FC 24 62			
6436	234B	A0 62 3C 00		DB	0A0H,62H,3CH,00H,04H,44H,7CH
6437	234F	04 44 7C			
6438	2352	C6 AA 92 64		DB	0C6H,0AAH,92H,64H,00H,20H,20H
6439	2356	00 20 20			

(MSX ROM BASIC BIOS) Macro-80
 -MSXCHR- MSX character set

		3.44	01-Jan-85	PAGE	63-18
6440	2359	78 20 78 22		DB	78H,20H,78H,22H,1CH,00H,00H
6441	235D	1C 00 00		DB	48H,0FCH,4AH,42H,4CH,40H,00H
6442	2360	48 FC 4A 42		DB	08H,0BCH,0CAH,8AH,0BCH,08H,30H
6443	2364	4C 40 00		DB	00H,08H,08H,0EH,08H,78H,8CH
6444	2367	08 BC CA 8A		DB	72H,00H,38H,84H,80H,0FCH,0C2H
6445	236B	BC 08 30		DB	02H,38H,00H,00H,42H,42H,42H,42H
6446	236E	00 08 08 OE		DB	0DCH,62H,92H,7CH,00H,20H,2CH
6447	2372	08 78 8C		DB	0F4H,24H,64H,0E4H,26H,00H,7CH
6448	2375	72 00 38 84		DB	18H,20H,5CH,82H,02H,7CH,00H
6449	2379	80 FC C2		DB	00H,10H,30H,20H,70H,48H,0CEH
6450	237C	02 38 00 00		DB	84H,00H,00H,00H,00H,00H,00H,00H
6451	2380	42 42 42		DB	SUBTTL - MSXINL, Screen editor - Line input and function character
6452	2383	62 04 18 00		DB	00H,00H,00H,00H,00H,00H,00H,00H
6453	2387	7C 08 30		DB	
6454	238A	DC 62 92 7C		DB	
6455	238E	00 20 2C		DB	
6456	2391	F4 24 64 E4		DB	
6457	2395	26 00 7C		DB	
6458	2398	18 20 5C 82		DB	
6459	239C	02 7C 00		DB	
6460	239F	40 60 DC 62		DB	
6461	23A3	42 C2 5C		DB	
6462	23A6	00 10 30 20		DB	
6463	23AA	70 48 CF		DB	
6464	23AD	84 00 00 00		DB	
6465	23B1	00 00 00 00		DB	
6466	23B4	00 00 00 00		DB	
6467	23B8	00 00 00 00		DB	
6468	23BB	00 00 00 00		DB	
6469					

(MSX ROM BASIC BIOS) Macro-80
- MSXINL, Screen editor - Line input and function character

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```
6470 6471 23BF          PINLIN:  
6472          ;  
6473          ; Main entry point  
6474          ;  
6475 23BF  CD FDBB    CALL H.PINL  
6476 23C2  3A F6AA    LD A,(AUTFLG) ; During AUTO mode?  
6477 23C5  A7          AND A  
6478 23C6  20 0D      JR NZ,INLIN ; Yes, then fake INLIN to prevent 0 from  
6479          ; deleting line number  
6480 23C8  2E 00      LD L,0  
6481 23CA  18 14      JR INLIN  
6482 23CC          ;  
6483          ; Output question mark then get input  
6484          ;  
6485 23CC  CD FDE0    CALL H.QINL  
6486 23CF  3E 3F      LD A,'?'  
6487 23D1  DF          RST 18H  
6488 23D2  3E 20      LD A,' '  
6489 23D4  DF          RST 18H  
6490 23D5          INLIN:  
6491 23D5  CD FDE5    CALL H.INLI  
6492 23D8  2A F3DC    LD HL,(CSRY)  
6493 23DB  2D          DEC L  
6494 23DC  C4 0C29    CALL NZ,TERMIN ; Terminate previous line  
6495 23DF  2C          INC L  
6496 23E0          INLIN1:  
6497 23E0  22 FBCA    LD (FSTPOS),HL ; Mark first position  
6498 23E3  AF          XOR A  
6499 23E4  32 FC9B    LD (INTFLG),A  
6500          ;
```

```

( MSX ROM BASIC BIOS ) Macro-80          3.44    01-Jan-85      PAGE   64-1
- MSXINL, Screen editor - Line input and function character

6501  23E7          CD 10CB          INLIN2:     CALL    CHGET
6502  23E7          CD 10CB          LD      HL,SCITBL-2
6503  23EA          21 2437          LD      C,0BH      ;SCI Max
6504  23ED          0E 0B          CD 0919          LD      INDJMP   ;Do functions
6505  23EF          CD 0919          CALL
6506  23F2          F5              PUSH   AF
6507  23F3          C4 23FF          CALL   NZ,INLOUT
6508  23F6          F1              POP    AF      ;Output a character
6509  23F7          30  EE          JR     NC,INLIN2   ;Not a terminator
6510
6511          ;                                ; return to BASIC (break or CR)
6512          ;
6513  23F9          21  F55D          LD      HL,BUFMIN
6514  23FC          C8              RET    Z      ;Cnt-C, return with carry set
6515  23FD          3F              CCF
6516  23FE          C9              RETURN:  RET    ;No, return carry clear
6517

```

(MSX ROM BASIC BIOS) Macro-80
 - MSXINL, Screen editor - Line input and function character

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```

6518      23FF          ;INOUT:
6519      23FF          ;;
6520      23FF          F5      PUSH    AF      ;Save character to output
6521      2400          FE 09  CP      9       ;TAB?
6522      2400          FE 0F  JR      NZ,OUTNTB ;Nope
6523      2402          20 0F  POP     AF      ;Discard stack
6524      2404          F1      OUTTAB: LD      A,' '   ;Map to space
6525      2405          3E 20  CALL    INLOUT
6526      2405          CD 23FF  LD      A,(CSRX) ;Make it zero based.
6527      2407          240A  F3DD  DEC    A       ;Reached TAB stop?
6528      240D          3D      AND    7       ;Not yet, continue...
6529      240D          240A  F3DD  LD      A,(CSRX)
6530      240E          E6 07  AND    NZ,OUTTAB ;Restore character
6531      2410          20 F3  RET
6532      2412          C9      OUTNTB: LD      A,' '   ;points insert mode flag
6533      2413          F1      POP     HL,INSLFG ;Graphic header byte?
6534      2413          F1      POP     1       ;Yes, send as is
6535      2414          21 FCA8  JR      Z,INLOT0 ;control char?
6536      2414          21 FCA8  CP      ,'
6537      2417          FE 01  CP      1       ;branch if so. - Reset insert mode
6538      2419          28 0B  JR      Z,INLOT0 ;save char to output
6539      241B          FE 20  CP      ,'
6540      241D          38 09  JR      C,INLOT1 ;get insert mode flag
6541      241F          F5      PUSH    AF      ;test
6542      2420          7E      LD      A,(HL) ;if insert mode, make room to insert
6543      2421          A7      AND    A       ;restore char to output
6544      2422          C4 24F2  CALL    NZ,INSERT ;output char
6545      2425          F1      POP     AF      ;output char
6546      2426          DF      INLOT0: RST    18H
6547      2426          C9      RET
6548      2427          C9

```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85
- MSXTNL, Screen editor - Line input and function character

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```
6549 2428      INLOTL:  
6550          ;  
6551 2428 36 00          LD    (HL),0      ;reset insert mode  
6552 242A DF          RST   18H      ;send this control char  
6553 242B 3E          DB    3EH  
6554 242C          SETINS: DB    3EH      ;Set insert mode and exit  
6555 242C 3E          SETOVM:  
6556 242D AF          XOR   A         ;Set overwrite mode  
6557 242D F5          PUSH  AF  
6558 242E          F5          XOR   A         ;Set overwrite mode  
6559 242F CD 0A2E          PUSH  AF  
6560 2432 F1          CALL  CKERCS  
6561 2433 32 FCAA          POP   AF  
6562 2436 C3 09E1          LD    (CSTYLE),A  
                          JP    CRDPFC
```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44   01-Jan-85    PAGE   66
- MSXINL, Screen editor - Line input and function character

6563          2439          SCITBL:
6564          2439          ; SCITBL:
6565          243A          ; ; table of function characters
6566          243C          ; ; table of function characters
6567          243D          ; ; table of function characters
6568          243E          ; ; table of function characters
6569          243F          ; ; table of function characters
6570          2440          ; ; table of function characters
6571          2441          ; ; table of function characters
6572          2442          ; ; table of function characters
6573          2443          ; ; table of function characters
6574          2444          ; ; table of function characters
6575          2445          ; ; table of function characters
6576          2446          ; ; table of function characters
6577          2447          ; ; table of function characters
6578          2448          ; ; table of function characters
6579          2449          ; ; table of function characters
6580          244B          ; ; table of function characters
6581          244C          ; ; table of function characters
6582          244E          ; ; table of function characters
6583          244F          ; ; table of function characters
6584          2451          ; ; table of function characters
6585          2452          ; ; table of function characters
6586          2454          ; ; table of function characters
6587          2455          ; ; table of function characters
6588          2457          ; ; table of function characters
6589          2458          ; ; table of function characters
6590          2459          ; ; table of function characters

; Delete previous char
DB    08H
DW    DELETE
;Toggle insert flag
DB    12H
DW    TGLINS
;Escape
DB    1BH
DW    RETURN
;Back word
DB    02H
DW    LBCKWD
;Next word
DB    06H
DW    LNXTWD
;Erase to end of line
DB    05H
DW    TRUNC
;Abort
DB    03H
DW    LBREAK
;Carriage return
DB    0DH
DW    LCRRET
;Delete whole line
DB    15H
DW    LERASE
;Delete character at cursor
DB    7FH
DW    LDELNX
SUBTL - MSXINL, Screen editor - Process special characters

```

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85 PAGE 67
 - MSXINL, Screen editor - Process special characters

```

6591    245A          LCRRET:           ;;;;;;;;;;;;;;;;
6592    6593          ;;;;
6594    ;;;;
6595    ; Carriage return ;
6596    ;;;;
6597    245A          CD 266C          ;L=Line number of first visual
6598    245D          3A F6AA          ;L=Line number of first visual
6599    2460          A7               ;During AUTO mode?
6600    2461          28 02           ;NO
6601    2463          26 01           ;Always get from top of line during AUTO mode
6602    2465          E5               NOTAUT:
6603    2465          E5               NOTAUT:           PUSH   HL
6604    6605          ; Put logical starting at L into BUF
6605    ;;;;
6606    ;;;;
6607    2466          CD 0A2E          CKERCS
6608    2469          E1               POP    HL
6609    246A          11 F55E          ;Line buffer pointer
6610    246D          06 FE           DE,BUF
6611    246F          2D               LD    B,0FEH
6612    2470          2C               DEC   L
6613    2470          2C               LCR1:           INC    L
6614    2471          2C               LCR2:           INC    L
6615    2471          D5               PUSH   DE
6616    2471          D5               PUSH   BC
6617    2472          C5               CALL   GETVRM
6618    2473          CD 0BD8          CALL   POP    BC
6619    2476          C1               POP    DE
6620    2477          D1               AND   A
6621    2478          A7               ;Save buffer pointer
6622          ;Save buffer count
6623          ;Get current character in Acc
6624          ;Restore buffer count
6625          ;Restore buffer pointer
6626          ;Null?

```

(MSX ROM BASIC BIOS) Macro-80 PAGE 67-1

- MSXINL, Screen editor - Process special characters

```

 6622 2479 28 14   JR Z,LCRNUL      ;Yes, ignore this
 6623 247B FE 20   CP '                   ;Special graphic character?
 6624 247D 30 0B   JR NC,LCRNRN        ;No, proceed normally
 6625 247F 05     DEC B                 ;Decrement BUF size counter before storing
 6626 2480 28 1D   JR Z,LBLKSP       ;At end of BUF, so ignore this
 6627 2482 4F     LD C,'A             ;Store header byte for graphic symbol
 6628 2483 3E 01   LD A,1              ;A,1
 6629 2485 12     LD (DE),A          ;(DE),A
 6630 2486 13     INC DE             ;DE
 6631 2487 79     LD A,C              ;A,C
 6632 2488 C6 40   ADD A,'@         ;A,'@
 6633 248A          LCRNRM:        LD (DE),A          ;Store byte in buffer
 6634 248A 12     INC DE             ;Bump buffer pointer
 6635 248B 13     DEC B              ;Decrement BUF size counter
 6636 248C 05     JR Z,LBLKSP       ;At end of BUF
 6637 248D 28 10   LCRNUL:          INC H              ;Next column
 6638 248F 24     LD A,(LINLEN)    ;Max column reached?
 6639 248F          LCRNRM:        LD H              ;;
 6640 2490 3A F3B0      ;Max column reached?
 6641 2493 BC     CP H              ;;
 6642 2494 30 DB     JR NC,LCR2      ;Not yet
 6643 2496 D5     PUSH DE           ;Save buffer pointer
 6644 2497 CD 0C1D      ;Is this line terminated?
 6645 249A D1     CALL GETTRM     ;Restore buffer pointer
 6646 249B 26 01     POP DE           ;Assume not, start from top of next line
 6647 249D 28 D1     LD H,1            ;No
 6648 249F          JR Z,LCRI       ;;
 6649          ; Suppress trailing blanks, [DE]=last+1
 6650          ;;
 6651          DEC DE             ;Back up buffer pointer
 6652 249F 1B

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE   67-2
- MSXINL, Screen editor - Process special characters

6653  24A0  1A          LD      A,(DE)      ;Get stored character
6654  24A1  FE 20       CP      '           ;Is it space?
6655  24A3  28 FA       JR      Z,LBLKSP    ;Yes, ignore this
6656  24A5  E5          PUSH   HL
6657  24A6  D5          PUSH   DE
6658  24A7  CD 09E1     CALL   CKDPCS
6659  24A8  D1          POP    DE
6660  24AB  E1          POP    HL
6661
6662 ; Terminate
6663
6664  24AC  13          INC    DE      ;Point past last valid character
6665  24AD  AF          XOR    A       ;Load terminator
6666  24AE  12          LD     (DE),A    ;Put it in BUF
6667  24AF  FAKERCR:   FAKECR: LD     A,0DH    ;Load character to echo to console
6668  24AF  3E 0D       AND    A       ;Reset Z-flag, (say not break)
6669  24B1  A7          LNXTLN: LD     AF      ;Save this flag
6670  24B2  F5          PUSH   TERMIN    ;Save current cursor position
6671  24B2  CD 0C29     CALL   POSIT    ;Move cursor to start of next line
6672  24B3  CD 088E     CALL   POSIT    ;Clear possible INSFLG
6673  24B6  3E 0A       LD     A,0AH    ;Restore flags
6674  24B9  DF          RST    18H    ;Set carry indicating end of input
6675  24BB  AF          XOR    A       ;Discard return address (XRA A;RET)
6676  24BC  AF          LD     (INSFLG),A  ;If break, Z flag is set
6677  24BD  32 FCA8     POP    AF
6678  24C0  F1          SCF
6679  24C1  37          POP    HL
6680  24C2  E1          RET
6681  24C3  C9          LBREKO: ;
6682  24C4
6683

```

{ MSX ROM BASIC BIOS } Macro-80 3.44 01-Jan-85
 - MSXINL, Screen editor - Process special characters

PAGE 67-3

```

6684      ; Control-C input
6685      ; Bump line counter
6686      2C          ; Bump line counter
6687      24C4        ; Control-C input
6688      24C5        ; Line terminated?
6689      24C8        ; No, check next line
6690      24CA        ; Set to overwrite mode
6691      24CD        AF          ; Load 0 in Acc, and set z flag
6692      24CE        32 F55E    LD (BUF),A ;Say no character in BUF
6693      24D1        26 01      LD H,1     ;Set to first column
6694      24D3        E5          PUSH HL   ;Save cursor position
6695      24D4        CD 04BD    CALL GICINI ;Initialize sound chip and queue
6696      24D7        CD 0454    CALL CKSTTP ;Check if STOP trap is active or not
6697      24DA        E1          POP HL
6698      24DB        38 D2      JR C,FAKECR ;Yes, fake CR
6699      24DD        3A FBBL    LD A,(BASROM) ;Executing BASIC program in ROM?
6700      24E0        A7          AND A
6701      24E1        20 CC      JR NZ,FAKECR ;Yes, fake CR
6702      24E3        18 CD      JR LMXTLN

```

(MSX ROM BASIC BIOS) Macro-80
- MSXINL, Screen editor - Process special characters

```

6703      24E5          TGLINS:
6704          ; Toggle insert mode flag
6705          ; Get current insert flag
6706          LD A,(HL)
6707          LD HL,INSFLG
6708          24E5          FCA8
6709          24E8          7E
6710          24E9          EE FF
6711          24EB          77
6712          24EC          CA 242D
6713          24EF          C3 242C
6714          24F2          INSERT:
6715          ; Insert a blank
6716          ; Erase cursor before operation
6717          CD 0A2E
6718          24F2          2A F3DC
6719          24F5          0E 20
6720          24F8          INSL:
6721          24FA          E5
6722          24FB          INS2:
6723          24FB          C5
6724          24FB          CD 0BD8
6725          24FC          DL
6726          24FF          C5
6727          2500          4B
6728          2501          CD 0BE6
6729          2502          C1
6730          2505          C5
6731          2506          3A F3B0
6732          2509          24
6733          250A          BC

;Toggle insert mode flag
;Get current insert flag
;Toggle insert status and affect Z flag
;Set to overwrite mode
;Set to insert mode
;Insert a blank
;Load raw code for space
;Save current cursor position
;Save previous character
;Get current character in C
;Restore previous character in [E]
;Save current character
;C=previous character
;Put it on screen
;Restore current character in C
;Check if end of line
;Bump column counter
;End of line?
```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85    PAGE   68-1
- MSXINL, Screen editor - Process special characters

6734  250B    7A          LD      A,D      ;Get current attribute in Acc
6735  250C    30 ED       JR      NC,INS2  ;If not, continue till end of line
6736
6737           ; Now we just finished a line, code of character wrapped to next
6738           ; line is held in [C].
6739           ;
6740  250E    E1          POP     HL      ;Restore current cursor position
6741  250F    CD 0C1D     CALL    GETTRM ;Is this line terminated?
6742  2512    28 37       JR      Z,INS6  ;Line not terminated on this visual
6743           ;
6744           ; The current line is terminated. A check must be made to
6745           ; determine if a wrapped character is a space, or we're inserting
6746           ; at the end-of-line. If so, we have to open a next line to
6747           ; insert.
6748           ;
6749  2514    79          LD      A,C      ;Move last character to A for comparison
6750  2515    FE 20       CP      ,        ;Save the condition
6751  2517    F5          PUSH    AF      ;No, open next line
6752  2518    20 0A       JR      NZ,INS3 ;Are we trying to insert at the EOL?
6753  251A    3A F3B0     LD      A,(LINLEN) ;Yes, open next line
6754  251D    BC          CP      H      ;Discard stack
6755  251E    28 04       JR      Z,INS3 ;Display cursor again
6756  2520    F1          POP     AF      ;CKDPCS
6757  2521    C3 09E1     JP      CKDPCS
6758  2524    INS3:      ;Unterminate this line
6759
6760  2524    CD 0C2A     CALL    UNTERM ;Go to next row
6761  2527    2C          INC     L      ;Save character code
6762  2528    C5          PUSH    BC      ;Save position of character in operation
6763  2529    E5          PUSH    HL      ;Bottom of screen?
6764  252A    CD 0C32     CALL    GETLEN

```

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( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE   68-2
- MSXINL, Screen editor - Process special characters

6765  252D    BD          CP      L
6766  252E    38 05      JR      C,INS4
6767          ; scroll down starting at line L
6768          ;
6769  2530    CD 0AB7    CALL   INSLNO
6770  2533    18 0F      JR      INSS5
6771  2535          ; Insert a blank line there
6772          ;
6773          ;
6774          ;
6775          ;
6776  2535    21 F3DC    LD      HL,CSRY
6777  2538    35          DEC    (HL)
6778  2539    20 01      JR      NZ,INS45
6779  253B    34          INC    (HL)
6780  253C          ; scroll up
6781  253C    2E 01      LD      L,1
6782  253E    CD 0A88    CALL   DELINO
6783  2541    E1          POP    HL
6784  2542    2D          DEC    L
6785  2543    E5          PUSH   HL
6786  2544          ; scroll up
6787  2544    E1          POP    HL
6788  2545    C1          POP    BC
6789  2546    F1          POP    AF
6790  2547    CA 09E1    JP      Z,CKDPCS
6791          ; If we were trying to insert at the
6792  254A    2D          DEC    L
6793  254B          ; end-of-line, nothing else to do
6794          ; Cancel next 'INR L'
6795          ; Not end of logical line, pass character to next line

```

{ MSX ROM BASIC BIOS } Macro-80 3.44 01-Jan-85 PAGE 68-3
- MSXINL, Screen editor - Process special characters

6796		;			
6797	254B	2C		INC	L
6798	254C	26 01		LD	H,1
6799	254E	18 AA		JR	INS1

;Bump row counter
;Start from first column
;Pass character to next line

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85

- MSXINL, Screen editor - Process special characters

```

6800      2550          L,DELNX:
6801      2550          ; Delete current character
6802          ; Delete current character
6803          ; Delete current character
6804          ; Delete current character
6805      2550          3A, F3B0
6806      2553          BC
6807      2554          20, 05
6808      2556          CD, OC1D
6809      2559          20, 3A
6810      255B          L,DELX1:
6811      255B          3E, 1C
6812      255D          DF
6813      255E          2A, F3DC
6814      2561          DELETE:
6815          ; Delete previous character
6816          ; Delete previous character
6817          ; Delete previous character
6818      2561          E5
6819      2562          CD, 0A2E
6820      2565          E1
6821      2566          25
6822      2567          C2, 257A
6823      256A          24
6824      256B          E5
6825      256C          2D
6826      256D          28, 0A
6827      256F          3A, F3B0
6828      2572          67
6829      2573          CD, OC1D
6830      2576          20, 01

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```

;At rightmost position?
;No
;Is this a terminated line?
;Yes , place a space there.
;Move cursor right
;Fall into 'delete prev . character'
;Are we at top of line?
;No
;Save current cursor position
;Look a line above
;At top of screen
;Is previous line terminated?
;Yes

(MSX ROM BASIC BIOS)		Macro-80		3.44		01-Jan-85	
- MSXINL, Screen editor -		Process special characters					
6831	2578	E3		EX		(SP), HL	
6832	2579		DELET1:				
6833	2579	E1		POP		HL	
6834	257A		DELET2:				
6835	257A	22	F3DC	LD		(CSRY), HL	
6836	257D		DELET3:				
6837	257D	3A	F3B0	LD		A, (LINLEN)	
6838	2580	BC		CP		H	
6839	2581	28	12	JR		Z, DELET5	
6840	2583	24		INC		H	
6841	2584		DELET4:				
6842	2584	CD	0BD8	CALL		GETVRM	
6843	2587	25		DEC		H	
6844	2588	CD	0BE6	CALL		PUTVRM	
6845	258B	24		INC		H	
6846	258C	24		INC		H	
6847	258D	3A	F3B0	LD		A, (LINLEN)	
6848	2590	3C		INC		A	
6849	2591	BC		CP		H	
6850	2592	20	F0	JR		NZ, DELET4	
6851	2594	25		DEC		H	
6852	2595		DELET5:				
6853	2595	0E	20	LD		C, ' '	
6854	2597	CD	0BE6	CALL		PUTVRM	
6855	259A	CD	0C1D	CALL		GETTRM	
6856	259D	C2	09E1	JP		NZ, CKDPCS	
6857	25A0	E5		PUSH		HL	
6858	25A1	2C		INC		L	
6859	25A2	26	01	LD		H, 1	
6860	25A4	CD	0BD8	CALL		GETVRM	
6861	25A7			(SP), HL			

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85 PAGE 69-2
- MSXINL, Screen editor - Process special characters

6862	25A8	CD 0BE6	CALL	PUTVRM
6863	25AB	E1	POP	HL
6864	25AC	18 CF	JR	DELETE3

;Put at last position last line

(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85
 - MSXINI, Screen editor - Process special characters

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```

6865      25AE          LERASE:           ; Erase logical line
6866      25B1          CD 0A2E          CKERCS
6867      25B2          CD 266C          GTFRST
6868      25B3          CD 2F3DC         ;Set L=first visual this logical line
6869      25B4          22 F3DC         (CSRY),HL
6870      25B5          18 05          LD    TRUNC1
6871      25B6          JR   TRUNC1
6872      25B7          ; Truncate logical line
6873      25B8          ; Truncate logical line
6874      25B9          E5              PUSH  HL
6875      25B9          CD 0A2E          CALL  CKERCS
6876      25BD          EL              POP   HL
6877      25BE          ; Truncate logical line
6878      25B9          E5              PUSH  HL
6879      25BA          CD 0A2E          CALL  CKERCS
6880      25BD          EL              POP   HL
6881      25BE          ; Truncate logical line
6882      25BE          CD 0C1D          CALL  GETTRM
6883      25C1          F5              PUSH  AF
6884      25C2          CD 0AEE          CALL  EOL
6885      25C5          F1              POP   AF
6886      25C6          20 05          JR   NZ,DPCSW
6887      25C8          26 01          LD   H,1
6888      25CA          2C              INC   L
6889      25CB          18 F1          JR   TRUNC1
6890      25CD          DPCSW:        ; Yes
6891      25CD          CD 09E1          CALL  CKDPCS
6892      25D0          AF              XOR   A
6893      25D1          32 FCA8         LD   (INSFLG),A
6894      25D4          C3 242D         JP   SETOWW

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( MSX ROM BASIC BIOS ) Macro-80   3.44   01-Jan-85   PAGE    70-1
- MSX1NL, Screen editor - Process special characters

6896  25D7          LAPPND:
6897          ; Append to current line
6898          ;
6899          ; Append to current line
6900  25D7  CD 0A2E          CALL    CKERCS      ;Erase cursor
6901  25DA  2A F3DC          LD      HL,(CSRy)  ;Get current cursor position
6902  25DD  2D               DEC    L
6903  25DE  LAP1:           INC    L          ;Line terminated?
6904  25DE  2C               INC    CALL    GETTRM    ;Line terminated?
6905  25DF  CD 0C1D          CALL    Z,LAP1     ;No, look at next line
6906  25E2  28 FA             JR    A,(LINLEN) ;Space?
6907  25E4  3A F3B0          LD    H,A        ;Yes, skip this
6908  25E7  67               LD    H,A
6909  25E8  24               INC    H
6910  25E9  LAP2:           DEC    H          ;Reached start of line?
6911  25E9  25               DEC    Z,LAP3     ;Yes
6912  25EA  28 07           CD    0BD8      ;GetVRM
6913  25EC  25EF              CALL    CP        ' '
6914  25EF  FE 20           CD    0A5B      ADVCUR
6915  25F1  28 F6           CALL    DPCSW     ;Re-display cursor
6916  25F3  LAP3:           CALL    Z,LAP2     ;Advance cursor to point to end of line
6917  25F3  25F6  18 D5       JR    LNXTWD:   ;Still in word?
6918          ; Move to next word
6919  25F8          ; Move to next word
6920
6921
6922          ; Move to next word
6923  25F8  CD 0A2E          CALL    CKERCS      ;Still in word?
6924  25FB  CD 2634          CALL    PRVCHK
6925  25FE          LNWL:   CALL    NXTCHK
6926  25FE  CD 2624          CALL    NXTCHK

```

```

( MSX ROM BASIC BIOS ) Macro-80      3.44    01-Jan-85      PAGE    70-2
- MSXINI, Screen editor - Process special characters

6927  2601  28 CA          JR      Z,DPCSW
6928  2603  38 F9          JR      C,LNW1
6929  2605  CD 2624        L.NW2:   CALL   NXTCHK
6930  2605  2608  C3          JR      Z,DPCSW
6931  2608  28 C3          JR      NC,LNW2
6932  260A  30 F9          JR      DPCSW
6933  260C  18 BF          JR      LBCKWD:
6934  260E
6935 ; Move to previous word
6936 ;
6937  260E  CD 0A2E        L.BWL:   CALL   CKERCS
6938  2611
6939  2611  CD 2634        CALL   PRVCHK
6940  2611  2614  B7          JR      Z,DPCSW
6941  2614  28 B7          JR      NC,LBW1
6942  2616  30 F9          JR      LBW2:
6943  2618
6944  2618  CD 2634        CALL   PRVCHK
6945  261B  28 B0          JR      Z,DPCSW
6946  261D  38 F9          JR      C,LBW2
6947  261F  CD 0A5B        CALL   ADVCUR
6948  2622  18 A9          JR      DPCSW
6949  2624  NXTCHK:
6950 ; Move right and check
6951 ;
6952 ;
6953  2624  2A F3DC        LD      HL,(CSR)
6954  2627  CD 0A5B        CALL   ADVCUR
6955  262A  CD 0C32        CALL   GETLEN
6956  262D  5F              LD      E,A
6957  262E  3A F3B0        LD      A,(LINLEN)

```

;Get current cursor position
;Advance cursor
;Get an actual height of screen
;[D],[E] hold the dead end position

(MSX ROM BASIC BIOS) Macro-80
 - MSXINL, Screen editor - Process special characters

```

 3.44   01-Jan-85    PAGE    70-3
 6958  2631    57      LD      D,A
 6959  2632    18 09    PRVCHK: JR
 6960  2634          ; Move left and check
 6961          ; PRVCK1:
 6962          ; Move left and check
 6963          ; PRVCHK:
 6964  2634    2A F3DC    LD      HL, (CSR)
 6965  2637    CD 0A4C    CALL   BS
 6966  263A    11 0101    LD      DE,0101H
 6967  263D          PRVCK1:          ;Get current cursor position
 6968          ;Regress cursor
 6969          ;[D],[E] hold the dead end position
 6970          ; Check current character
 6971          ; Carry set if the character is regarded as separator
 6972  263D    2A F3DC    LD      HL, (CSR)          ;Get updated cursor position
 6973  2640    E7      RST    20H          ;Reached dead end?
 6974  2641    C8      RET    Z           ;Yes, return with Z flag
 6975  2642    11 2668    LD      DE,RESZRO          ;Jump to RESZRO when done
 6976  2645    D5      PUSH   DE
 6977  2646    CD 0BD8    CALL   GETVRM          ;Get ASCII code of character at [H],[L]
 6978  2649    FE 30      CP    '0'          ;Set carry if "0".."9"
 6979  264B    3F      CCF   RET    NC
 6980  264C    D0      CP    ':'          ;Set carry if ":"..":"
 6981  264D    FE 3A      RET    C
 6982  264F    D8      CP    'A'          ;Set carry if "A".."Z"
 6983  2650    FE 41      RET    NC
 6984  2652    3F      CCF   RET    'Z'+1
 6985  2653    DO      CP    C
 6986  2654    FE 5B      RET    NC
 6987  2656    D8      CP    'a'          ;Set carry if "a".."z"
 6988  2657    FE 61      CP    ''

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( MSX ROM BASIC BIOS ) Macro-80          3.44      01-Jan-85      PAGE    70-4
- MSXTNL, Screen editor - Process special characters

6989  2659  3F          CCF      NC
6990  265A  D0          RET      'z '+1
6991  265B  FE 7B       CP      C
6992  265D  D8          RET      ;Check for Hiragana (86H)
6993  265E  FE 86       CP      86H
6994  2660  3F          CCF      NC
6995  2661  D0          RET      NC
6996  2662  FE A0       CP      0A0H
6997  2664  D8          RET      C
6998  2665  FE A6       CP      0A6H
6999  2667  3F          CCF      NC
7000  2668  3E 00       RESZRO: LD      A,0      ;Reset Z flag without affecting C flag
7001  2668  3C          INC      A
7002  266A  C9          INC      RET
7003  266B  C9          INC      RET
7004
7005
7006
7007  266C  2D          GTFRST: DEC      L      ;Look a line just above
7008  266C  2D          JR      Z,GTFS1   ;If we're at top of screen, all done
7009  266D  28 05       CD      OC1D   ;Get terminator
7010  266F  28 F8       CALL    Z,GTFRST ;More to get above in this logical
7011  2672  28 F8       GTFS1:  INC      L      ;L=1ine number of first visual
7012  2674  2C          INC      LD      A,(FSTPOS) ;Get first line
7013  2674  2C          LD      CP      L      ;Same?
7014  2675  3A FBCA    JR      H,1      ;Assume not
7015  2678  BD          LD      NZ      HL,(FSTPOS) ;Good assumption
7016  2679  26 01       RET      LD      HL,(FSTPOS) ;Get first line and column
7017  267B  C0          RET      RET
7018  267C  2A FBCA    C9
7019  267F

```

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(MSX ROM BASIC BIOS) Macro-80 3.44 01-Jan-85 PAGE 70-5
- MSXINL, Screen editor - Process special characters

7020 END

MSX BIOS CROSS REFERENCE

(MSX BASIC ROM BIOS) Macro-80
 - BIOS CROSS REFERENCE LISTING -

PAGE XREF - 1

258

ACTION	1#	2664	3518#			
ADVCUR	1#	1930	2166#	6917	6947	6954
ALPJMP	1#	2892#	3041			
ASCPCT1	1#	5236				
ASCPCT2	1#	5238				
ATRBAS	1#	1163	1255	1296	1387	1437
ATRBYT	1#	4407	4725	4756	5113	5168
AUTFLG	1#	6476	6599			
BAKCLR	1#	1574	1584	1660	1684	
BASROM	1#	923	2571	6699		
BDRCLR	1#	1690				
BEEP	1#	170	1914	3485#		
BEGIN	30#					
BIT0	1#	5517	5523#			
BIT1	1#	5516	5519	5520	5533#	
BIT1OT	1#	5494	5535	5542	5544#	
BITOUT	1#	5511	5530	5552#		
BRDADR	1#	5259	5282	5377	5427	
BREAKX	1#	167	1008#	1733	5500	5521
BS	1#	1916	1932	2144#	2297	6965
BUF	1#	6610	6692			
BUFEND	1#	2087	2391	2478	2497	
BUFMIN	1#	6513				
CALATR	1#	136	1430#			
CALBAS	1#	252	363#	2768	5803	5871
CALESI	1#	412	419#			
CALLF	1#	90	366#			
CALPAT	1#	135	1413#			
CALSLT	1#	57	365	404#	437	
CAPST	1#	3055	3193	3275		
CGCAP1	1#	3201	3203#	11159	1471	2083
CGPBAS	1#	1140				

(MSX BASIC ROM BIOS) Macro-80
- BIOS CROSS REFERENCE LISTING -

PAGE XREF - 2

259

CGPNT	1#	1473	1474	1521	1525				
CGSND1	1#	3247	3249#						
CGTABL	1#	40	5883#						
CHCLTX	1#	1650	1677#						
CHGBD1	1#	1687	1691#						
CHGBDR	1#	1570	1583	1652	1688#				
CHGCAP	1#	237	3198#						
CHGCLR	1#	119	1141	1164	1644#				
CHGET	1#	157	3403#	6502					
CHGET1	1#	3414#	3416						
CHGET2	1#	3412	3418#						
CHGET3	1#	3422	3424#						
CHGMOD	1#	118	1704	1714#					
CHGSND	1#	238	3244#						
CHKBUF	1#	2799#	2823						
CHKCHG	1#	5300	5313	5385	5401#				
CHK EOC	1#	3980	4003#						
CHKMOD	1#	4442	4460	4520	4533#	4589	4683	4736	4833
4859		4883	4898	4925	5070	5247	5268	5370	
CHKRAM	1#	31	681#						
CHK SCR	1#	1544	1700	1820	2071	2126	2451#	2813	
CHPLP1	1#	1732#	1736						
CHPLP2	1#	1738#	1753						
CHPUT	1#	158	1813#	5880					
CHPUT1	1#	1825	1837#	2195					
CHPUT3	1#	1842	1850#						
CHRGT R	1#	51							
CHSNS	1#	156	2807#	3411	3415				
CHSNS1	1#	2814	2822#						
CKCNTC	1#	169	3431#						
CKDPC0	1#	943	2051#	3413					
CKDPCS	1#	1826	2059#	6562	6658	6757	6790	6856	6892

(MSX BASIC ROM BIOS) Macro-80
 - BIOS CROSS REFERENCE LISTING -

PAGE XREF - 3

260

CKERC0	1#	953	2106#	3417					
CKERC5	1#	1822	2114#	6559	6608	6718	6819	6870	6879
CKRM05	1#	717#	778						
CKRML0	1#	732#	774						
CKRML5	1#	725	729	737#					
CKRM20	1#	739#	748	750					
CKRM25	1#	746	751#						
CKRM30	1#	758	766#						
CKRM35	1#	769	775#						
CKRM50	1#	801#	854						
CKRM55	1#	810#	850						
CKRM60	1#	805	812#						
CKRM65	1#	814#	823	827					
CKRM70	1#	821	828#						
CKRM75	1#	835	843#						
CKRM80	1#	846	851#						
CKSTTIP	1#	963	983#	6696					
CLICKW	1#	3241#	3243						
CLIKFL	1#	2718	3234	3238					
CLIKSW	1#	3231							
CLOC	1#	4614	4652	4663	4672	4843	4858	4882	4897
	5048								
CLPRIM	1#	418							
CLRSPR	1#	126	1372#						
CLRTX1	1#	1555	1557#						
CLRTX2	1#	1563#	1566						
CLRTXT	1#	1142	1165	1547#	1924	1941	1943		
CLS	1#	171	1705#						
CLSHRS	1#	1265	1545	1568#					
CLSMLT	1#	1314	1546	1581#					
CLSPR2	1#	1389#	1411						
CLSPR3	1#	1403	1407#						

(MSX BASIC ROM BIOS) Macro-80
- BIOS CROSS REFERENCE LISTING -

PAGE XREF - 4

261

CLSSUB	1#	1542#	1711				
CMASK	1#	4598	4634	4662	4671	4846	4992
CNSDFG	1#	2363	2450	2594	2818		
CNTFUL	1#	5585	5616	5774#	5782		
CNTHL0	1#	5708	5757#	5784			
CNTHL1	1#	5759#	5765	5785			
CNTHLF	1#	5678	5681	5709	5749#		
CNTPUT	1#	1849	1869#				
CNTTBL	1#	1912#					
CNVCH1	1#	1797#	1807				
CNVCH2	1#	1795	1800#				
CNVCH3	1#	1791	1803#				
CNVCHR	1#	161	1781#	1839	2421	4397	5848
CODSAV	1#	2076	2129				
CR	1#	1926	2206#	2221	2257		
CRTCNT	1#	2355	2376	2439	2596		
CSAVEA	1#	5303	5321	5353			
CSAVEM	1#	5304	5322	5354			
CSDLY1	1#	3504	3506#	3515			
CSHOME	1#	1560	1922	1965	2201#		
CSRSW	1#	2049	2055	2063	2110	2118	
CSRX	1#	1827	2004	2196	6528		
CSRY	1#	1851	1901	2073	2128		
CSTYLE	1#	2041	2089	2183	2375	6493	6719
CTWOF1	1#	5455#	5459				
CURLIN	1#	995	3116				
DATAR	1#	5665#	5670				
DATA0	1#	5671#	5676				
DATA1	1#	5679#	5685	5687			
DATA1L	1#	5694#	5711				
DATAW		5502#					

(MSX BASIC ROM BIOS) Macro-80
 - BIOS CROSS REFERENCE LISTING -

PAGE XREF - 5

262

DATAW1	1#	5514#	5518
DCOMPR	1#	59	4146#
DELETE1	1#	6826	6830
DELETE2	1#	6822	6834#
DELETE3	1#	6836#	6864
DELETE4	1#	6841#	6850
DELETE5	1#	6809	6839
DELETE6	1#	6569	6814#
DELLNO	1#	1868	2222#
DELLN1	1#	2240#	2249
DIOERR	1#	5870	
DISSC1	1#	1175	1182#
DISSCR	1#	108	1131
DLN	1953	2215#	
DOWN	1#	1864	1936
DOWN1	1#	2180	2185#
DOWNC	1#	216	4876#
DPCSW	1#	6886	6890#
DSFKCL	1#	2395#	2398
DSPCS1	1#	2091	2093#
DSPCSR	1#	2058	2066#
DSPFK1	1#	2386	2389#
DSPFK2	1#	2413#	2437
DSPFK4	1#	2405#	2408
DSPFK5	1#	2417#	2422
DSPFK6	1#	2425	2428#
DSPFK8	1#	2423	2426#
DSPFKE	1#	2403	2411
DSPFNK	1#	175	2366#
DWNCL0	1#	4869	4873
EASYTB	1#	2937#	3163
ELN	1#	1949	2226
		2250	2263
		2289	2301#
		2356	

(MSX BASIC ROM BIOS) Macro-80
 - BIOS CROSS REFERENCE LISTING -

PAGE XREF - 6

263

EMSITB	1124#									
ENASCR	1#	109	1145	1169#	1268	1317				
ENASLT	1#	61	476#	498	976					
ENESLT	1#	478	484#							
ENSTOP	1#	2761								
ENTESC	1#	1928	1982#							
ZOCCHK	1#	4011#	4014							
EOL	1#	1945	2308#	2334	6884					
EOP	1#	1947	2327#	2342						
ERACSR	1#	2113	2121#							
ERAPNK	1#	174	2346#							
ERASPR	1#	1167	1266	1315	1384#					
EREOL1	1#	2319#	2325							
ESCCNT	1#	1843	1984							
ESCTBL	1#	1939#	1992							
EXABOL	1#	964	971#							
EXCABO	1#	936	960#							
EXPTEBL	1#	364	880	974						
FAKECR	1#	6667#	6698	6701						
FETCHC	1#	220	4418	4656#	4681	4737	4789			
4969	4979	5073	5302	5318	5352					
FLOU1	1#	5802								
FILVRM	1#	115	1383	1559	1575	1580	1664#			
FKTABL	1#	4071								
FLPMOT	1#	4043	4052#							
FLVRL1	1#	1667#	1674							
FNKDEF	4075#									
FNKFLG	1#	3093								
FNKINT	1#	3097	3114#							
FNKSBB	1#	173	1567	2359#						
FNKSTR	1#	2384	2387	3104	4070					
FNKSWI	1#	2390	2815							

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			Macro-80
FORCLR	1#	1385	1655
FORMAT	1#	246	4201#
FSTPOS	1#	2236	6498
GENCLK	1#	3218	3230#
GETLL1	1#	2464	2476#
GETLN	1#	2243	2282
GET8B	1#	2086	2458#
GETBAK	1#	4293	4321#
GETLEN	1#	2007	2177
GETPAT	1#	1506#	4405
GETPNT	1#	1005	1027
GETPTR	1#	4263	4291
GETQ	1#	3678	4287#
GETTRM	1#	2231	2269
	6905	7010	
GETVC1	1#	1096	4168
GETVC2	1#	250	4169#
GETVCL	1#	4190#	4193
GETVCP	1#	249	3547
GETVCX	1#	4188	4194#
GETVRM	1#	2075	2501#
GETYPR	1#	63	
GICINI	1#	1083#	1109
GICINI	1#	146	1056#
GORSET	1#	2000	2031#
GOSET	1#	1998	2020#
GPRTO5	1#	4399	4404#
GPRTO10	1#	4416#	4440
GPRTO20	1#	4422#	4431
GPRTO30	1#	4428	4432#
GPRTO40	1#	4437	4441#
GPRTO50	1#	4444	4448#

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			Macro-80
GPRT60	1#	4447	4452#
GPRT70	1#	4462	4465#
GPRT80	1#	4468	4470#
GRPACX	1#	4410	4443
GRPACY	1#	4408	4461
GRPATR	1#	1254	4471
GRPCGP	1#	1576	4612
GRPCOL	1#	1573	4862
GRPCR	1#	4401	4901
GRPDIF	1#	4688	4446
GRPHED	1#	1787	4451
GRPNAM	1#	1256	4456#
GRPPAT	1#	1252	5115
GRPPRT	1#	138	5153
GRPTAB	1#	4389#	5202
GSPADI	1#	3365	3377#
GSPSIZ	1#	1422	1425#
GTASPC	1#	137	1420
GTFRST	1#	228	1440#
GTFST1	1#	6598	5232#
GTPAD	1#	7009	6871
GTPADO	1#	7012#	7007#
GTPAT1	1#	186	7011
GTPDL	1#	3867#	
GTPDP1	1#	3893	
GTRROW8	1#	3898	
GTSTCK	1#	3891#	
GTTRIG	1#	3726#	
H.CHGE	1#	3410	3804
H.CHPU	1#	1819	
H.DSPC	1#	2070	
H.DSPF	1#	2370	

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H.ERAAC	1#	2125		
H.ERAF	1#	2350		
H.FORM	1#	4203		
H.INIP	1#	1470		
H.INLI	1#	6492		
H.ISFL	1#	4139		
H.KEYC	1#	2993		
H.KEYI	1#	2621		
H.KYEAA	1#	3160		
H.LPTO	1#	1730		
H.LPTS	1#	1759		
H.NMI	1#	4061		
H.OUTD	1#	5798		
H.PHYD	1#	4199		
H.PINL	1#	6475		
H.QINL	1#	6486		
H.TIMI	1#	2625		
H.TOTE	1#	1703		
HEADER	1#	5485		
HIGH	1#	5551		
HRSSCL	1#	4521	4526#	
HRZMOV	1#	4811	4841#	
HRZMV1	1#	4791	4807	4837
ILN	1#	1951	2251#	4845#
INDJMP	1#	1889#	1897	6505
INESC	1#	1846	1987#	
INESC1	1#	1989	1995#	
INESC2	1#	2005	2009#	
INGI	1#	1051	3476#	3723
INIFNK	1#	99	4065#	
INIGR1	1#	1260#	1263	1264
INIGRP	1#	129	1245#	1722

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INIML1	1 #	1300#	1313
INIML2	1 #	1302#	1310
INIML3	1 #	1305#	1308
INIMLT	1 #	130	1286# 1723
INIPAT	1 #	1143	1166 1466#
INIPT1	1 #	1477#	1490
INIT	1 #	919	
INIT32	1 #	128	1146# 1720
INITIO	1 #	98	1038#
INITQ	1 #	1088	4328#
INITXT	1 #	127	1127# 1719
INLIN	1 #	164	6478 6491#
INLIN1	1 #	6481	6497#
INLIN2	1 #	6501#	6509
INLOTO	1 #	6538	6546#
INLOT1	1 #	6540	6549#
INLOUT	1 #	6507	6519# 6527
INS1	1 #	6721#	6799
INS2	1 #	6723#	6735
INS3	1 #	6752	6755 6758#
INS4	6766	6772#	
INS45	1 #	6778	6780#
INS5	1 #	6771	6786#
INS6	1 #	6742	6793#
INSERT	1 #	6544	6714#
INSLFG	1 #	6536	6677 6708 6894
INSLNO	1 #	2258#	6770
INSLNL	1 #	2279#	2288
INTCNT	1 #	2638	2647
INTFLG	1 #	927	944 3217 3419 6500
INTRET	1 #	2624	2672 2723 2731#
INTVAL	1 #	2645	

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KY1CNT	2900	2914#		
KY1NOM	2902	2903#		
KY1SFC	2899	2924#		
KY1SFT	2901	2908#		
KYALP	1#	2865	3034#	
KYANY1	1#	2844#	2852	
KYC1TB	1#	2898#	3063	
KYCLAO	1#	2999	3005#	
KYCLAS	1#	2995	3007#	3017
KYCLS	2881	3150#		
KYCOD1	1#	2863	3061#	
KYEASY	1#	2867	2875	3156#
KYFNC1	1#	3086	3090#	
KYFNC2	1#	3098#	3120	
KYFNC3	1#	3107#	3113	
KYFUNC	1#	2873	3080#	
KYGRAP	1#	3001	3360#	
KYJTAB	1#	2859#	2992	
KYKANI	1#	3262	3268	3270#
KYKANA	1#	3004	3252#	
KYKLOK	1#	2871	3169#	
KYLOCK	1#	2869	3189#	
KYNUM	1#	2861	3018#	
KYSTCK	1#	3686	3696#	
KYSTOP	1#	2877	3206#	
KYSTP1	1#	3214	3216#	
LAP1	1#	6903#	6906	
LAP2	1#	6910#	6915	
LAP3	1#	6912	6916#	
LAPPND	1#	6579	6896#	
LBCKWD	1#	6575	6934#	
LBLKSP	1#	6626	6637	6648#
				6655

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LBREAK	1#	6583	6687#			
LBREK0	1#	6682#	6689			
LBWL	1#	6939#	6942			
LBW2	1#	6943#	6946			
LCR1	1#	6613#	6647			
LCR2	1#	6615#	6642			
LCRNFM	1#	6624	6633#			
LCRNUL	1#	6622	6638#			
LCRRET	1#	6585	6592#			
LDELNX	1#	6589	6801#			
LDELX1	1#	6807	6810#			
LDIMV1	1#	1457#	1464			
LDIRMV	1#	116	1452#	2479		
LDIRWM	1#	117	1493#	2498		
LDIVM1	1#	1497#	1504			
LEFT	1#	1963	2148	2153#		
LEFTC	1#	213	4828#			
LEFTC1	1#	4826	4838#			
LERASE	1#	6587	6866#			
LF	1#	1860#	1920			
LFTQ	1#	205	4347#			
LINL32	1#	1154				
LINL40	1#	1135				
LINLEN	1#	1136	1155	2003	2139	2323
	6731	6753	6805	6827	6837	2401
LINTTB	1#	1561				
LNWL	1#	6925#	6928			
LNW2	1#	6929#	6932			
LNXTLN	1#	6670#	6702			
LNXTWD	1#	6577	6919#			
LOC	1#	1955	1979#			
LOW	1#	5506	5529			
						6640
						2542
						2492
						2474
						2401
						6957

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LOWLIM	1#	5638	5663
LPT.DW	1#	623#	1740
LPT.SB	1#	624#	1055
LPT.ST	1#	625#	1742
LPTABO	1#	1734	1748#
LPTCH0	1#	5833	5836
LPTCH1	1#	5811	5846
LPTCHR	1#	5852	5858
LPTCOD	1#	5800	5805#
LPTOUT	1#	159	1726#
LPTPOS	1#	1751	5824
LPTSTT	1#	160	1735
MAPSPC	1#	5850	5854
MAPXYC	1#	219	4413
MDNC	1#	4884	5006
MHCMOV	1#	4966	4987#
MHZMV1	1#	4949	4960
MLFTC	1#	4834	4977#
MLFTC1	1#	4975	4983#
MLTATR	1#	1295	
MLTCGP	1#	1591	4650
MLTNAM	1#	1297	1333
MLTPAT	1#	1293	
MMPXY1	1#	4631	4633#
MMPXYC	1#	4590	4624#
MNSTCX	1#	5071	5221#
MORACT	1#	3577#	3596
MORSPL	1#	5821#	5826
MOTRON	1#	4045	4048#
MOTRWT	1#	5479#	5483
MREADC	1#	4684	4706#
MRGTC	1#	4804	4955#

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PDLPL1	1#	3840	3843#
PHYDIO	1#	244	4197#
PINLIN	1#	163	6471#
PLYCNT	1#	3660	5253#
PNTHRS	1#	5248	5242#
PNTINI	1#	229	5258#
PNTIRT	1#	5252	1821
POPALL	1#	1113	1830#
POSIT	1#	172	1766#
PPI.AR	1#	257#	292
	852	891	4118
PPI.AW	1#	258#	354
	786	802	863
PPI.BR	1#	615#	1016
PPI.CM	1#	619#	692
PPI.CR	1#	616#	1012
PPI.CW	1#	618#	696
PRTFLG	1#	5806	
PRVCHK	1#	6924	6944
PRVCK1	1#	6959	6967#
PSG.DR	1#	607#	3179
PSG.DW	1#	606#	3186
PSG.LW	1#	605#	3178
PSG.PA	1#	609#	3480
PSG.PB	1#	610#	3710
PTRFIL	1#	4141	
PUTLL1	1#	2486	2494#
PUTLLN	1#	2245	2284
PUT8B	1#	2102	2482#
PUTCHR	1#	2893	2894
PUTPNT	1#	1004	1026
PUTQ	1#	206	4259#

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				Macro-80
RIGHT	1#	1855	1961	2135#
RIGHTC	1#	212	4798#	2170
RSET10	1#	2038	5227	5390
RSLREG	1#	239	4116#	5418
RSTFL1	1#	3645#	3647	
RSTMOD	1#	1969	1976#	
RUBOUT	1#	1853	2293#	
RUNFLG	1#	3902	4023	5279
SAMEBG	1#	5170	5192#	
SAMEFG	1#	5176	5185	5198#
SAVSTK	1#	979		
SCALXY	1#	218	4411	4475#
SCANL	1#	231	5364#	
SCANL1	1#	5379#	5387	
SCANL2	1#	5384	5388#	
SCANL3	1#	5381	5391#	
SCANL4	1#	5334	5397#	
SCANR	1#	230	5261#	
SCANR1	1#	5284#	5293	
SCANR2	1#	5287	5296#	
SCANR3	1#	5306#	5314	
SCANR4	1#	5309	5312	5315#
SCIITBL	1#	6503	6564#	
SCLXOK	1#	4513	4518#	
SCLYOK	1#	4497	4502#	
SCNCNT	1#	2670		
SCRMOD	1#	1133	1152	1292
SELEXP	1#	301	342	486
SELPRM	1#	290	331	477
SETATTR	1#	224	4714#	544#
SETC	1#	226	4425	500#
SETCHK	1#	2352	2372	2446#

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SETGRP	1#	133	1267	1269#	1336
SETINS	1#	6554#	6713		
SETMLT	1#	134	1316	1318#	
SETMOD	1#	1967	1970#		
SETOWW	1#	6556#	6690	6712	6895
SETRD	1#	113	1454	1610	1630#
SETREG	1#	1337	1345	1347#	2505
SETRG1	1#	1340	1343	1349#	
SETRG2	1#	1357#	1360		
SETSCM	1#	1227	1244	1285	1335#
SETT32	1#	132	1168	1228#	
SETTRM	1#	1858	2584#		
SETTXT	1#	131	1144	1210#	
SETWRT	1#	114	1257	1298	
SFTKEY	1#	2382	2764	2816	
SLEXP1	1#	563#	566		
SLPRM1	1#	509#	513		
SLPRM2	1#	531#	533		
SLSTC1	1#	3713	3717#		
SLSTC2	1#	3716	3721#		
SLSTCK	1#	2678	2682	3687	3705#
SLTTBL	1#	430	492	915	917
SNSMAT	1#	242	4124#		
SSLTLP	1#	881#	887		
STATFL	1#	2631			
STCFSW	1#	2029	2048#		
STICK1	1#	3689#	3704		
STKTBL	1#	3688	3747#		
STMOT1	1#	4044#	4056		
STMOTR	1#	199	4041#		
STOCSR	1#	1866	2143	2165	2182#
STOP	1#	981			

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			Macro-80	
STOREC	1#	222	4435	4665#
STRTMS	1#	149	3651#	5323
STSTYL	1#	2027	2040#	5331
SULOP	1#	5649#	5652	
SYN05	1#	5577#	5589	5602
SYN10	1#	5583#	5606	
SYN11	1#	5597	5600#	
SYN20	1#	5608#		
SYN30	1#	5615#	5620	
SYNCHR	1#	46		
SYNCW1	1#	5486	5489#	
SYNLPI	1#	5493#	5499	
T32ATTR	1#	1162		
T32CGP	1#	1158		
T32COL	1#	1662		
T32NAM	1#	1156	1242	
T32PAT	1#	1160		
TAB	1#	1918	2190#	2199
TAPIN	1#	194	5659#	
TAPIOF	1#	195	5462#	
TAPION	1#	193	5568#	
TAPOFF	1#	198	5450#	
TAPOON	1#	196	5469#	
TAPOUT	1#	197	5501#	
TDOWNC	1#	217	4436	4850#
TERMIN	1#	2314	2579#	6495
TGLINS	1#	6571	6704#	6672
TIMOUT	1#	5762	5770#	
TLEFT	1#	4812#	5380	5415
TODEXT	1#	176	973	1696#
TRGFLG	1#	2694		
TRIG1	1#	3794	3796#	

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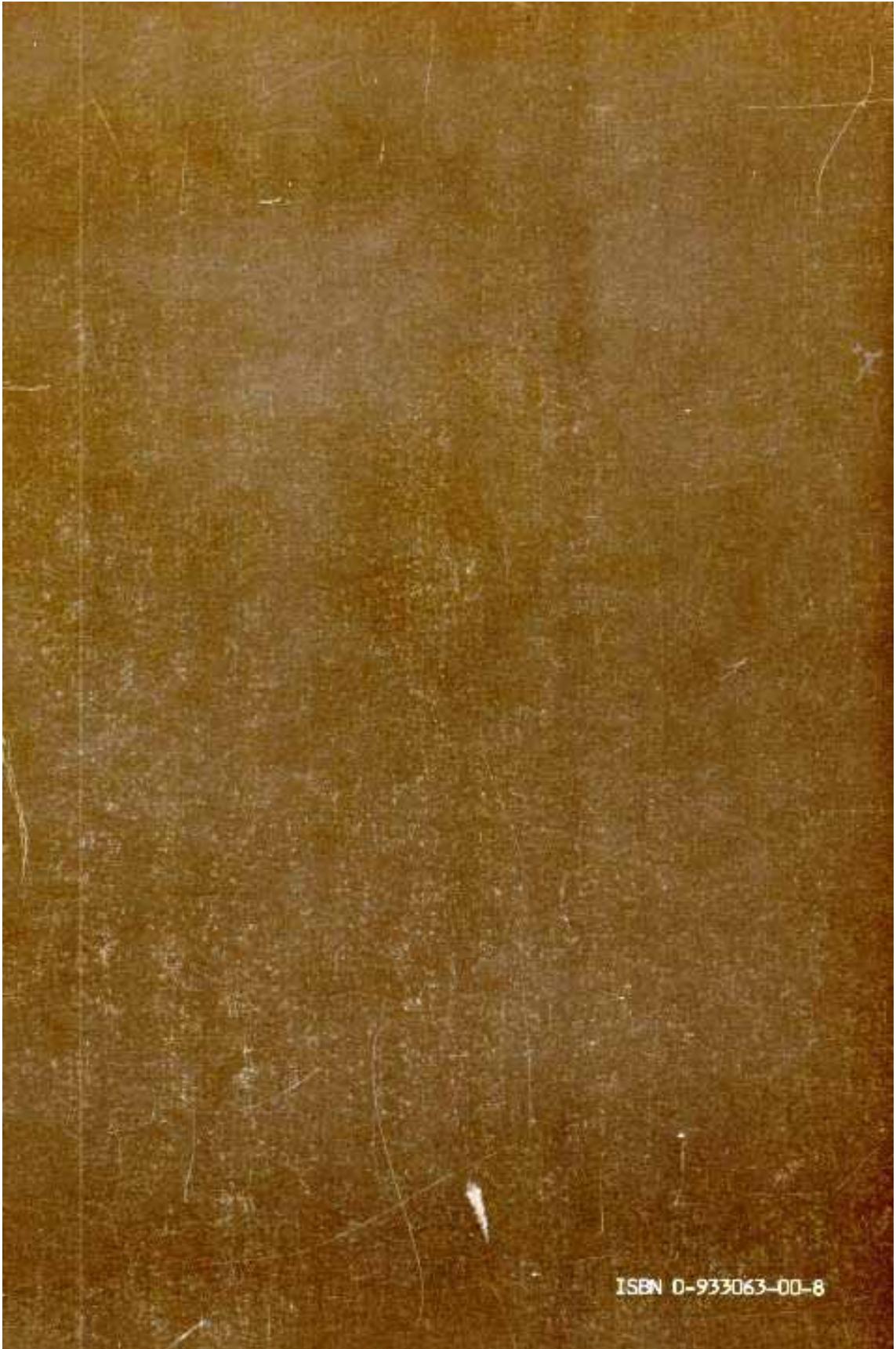
TRIG2	1#	3798#	3806	5292	5308	5346	5358	
TRIGHT	1#	4426	4781#	2643	2700	2703	2706	3121
TRPTBL	1#	2633	6581	6874#	6881#	6889	2712	
TRUNC	1#	6581	6873	3914	3921#	3940	3948	
TRUNC1	1#	6873	5808	5875#				
TRYAGN	1#	3914	1829	215	4890#			
TTYCHR	1#	5808	4595	4617#				
TTYPOS	1#	1829	215	4908				
TUPC	1#	4595	4908	3225				
TWOPWR	1#	4617#	6760	6760				
TXTCGP	1#	1139	1137	1934	1957	2159#		
TXTNAM	1#	1137	2582#	1934	4918#			
UNTERM	1#	2582#	214	4908	4911	4927#		
UP	1#	6760	4908	3225	3393#	3427		
UPC	1#	1934	4911	3225				
UPC10	1#	1957	4918#	3393#				
UPDATE	1#	2159#	4911	3427				
V.COLR	1#	2125	4918#					
VADDR	1#	2316	2473	2491	2504	2515	2521#	
VADDR1	1#	2543	2546#					
VADDR2	1#	2545	2550#					
VCBA	1#	600#	2550#					
VCBB	1#	3666	4184					
VCBC	1#	3667						
VDP.CW	1#	3668						
VDP.DRW	1#	597#	1197	1200	1623	1627	1638	
	2518	596#	1261	1306	1458	1485	1499	1641
VDP.SR	1#	598#	1262	1458	1485	1499	1604	1613
VOICAO	1#	1080	1485	1499	1604	1613	1669	2321
VOICEN	1#	4175	1499	1604	1613	1669	2321	2508
VOICOF	1#	3563	4889	4931#				
VRTMOV	1#	3632#	4889	4931#				

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VRTMVI	1#	4933	4935#			
WATINT	1#	945#	951			
WINWID	1#	5655	5724			
WORK1	1#	5326				
WORK2	1#	5325	5393			
WORK3	1#	5281	5376	5398	5409	
WRESED	1#	305	347#			
WRESLT	1#	332	339#			
WRPRIM	1#	338				
WRSLT	1#	53	329#	346		
WRTPSG	1#	147	1044	1050	1092	1112
	3591	3594	3608	3612	3625	3640
WRTVDP	1#	110	1186#	1218	1224	1236
	1379	1694				
WRTVRM	1#	112	1393	1397	1409	1595#
WSLREG	1#	240	4120#			
WTPTAB	1#	5201#	5212			
XEPER	1#	3601	3613#			
XGETQ	1#	3561	3573	3580	3587	3620
XNEGTV	1#	4508	4516#			
XPOSTV	1#	4506	4509#			
XVOL	1#	3583	3597#			
YNEGTV	1#	4492	4500#			
YPOSTV	1#	4490	4493#			
ZERLP1	1#	5832	5840#			



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