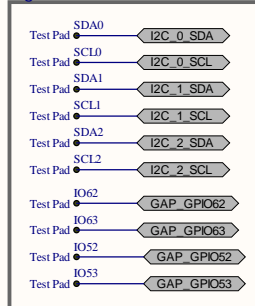
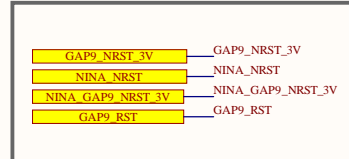


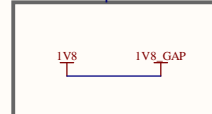
## Signal Test Pads



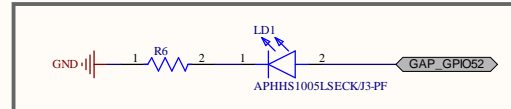
## Offsheet connections



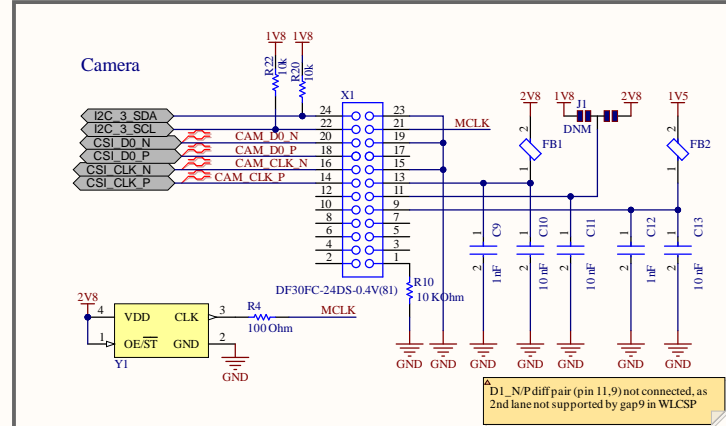
## Power Jumper



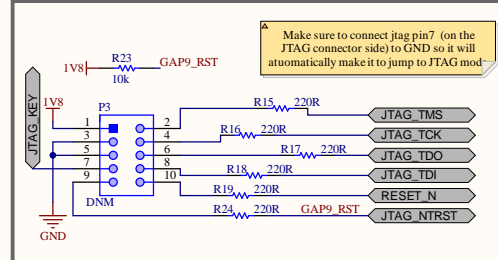
## GAP9 Leds



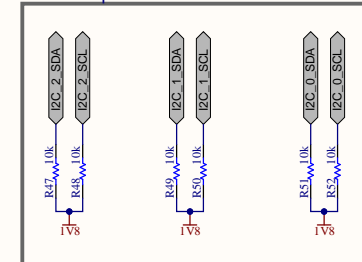
## Camera



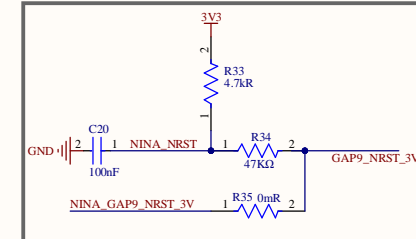
## JTAG



## I2C Pull-ups



## GAP9 -> Nina Reset



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Project:

**GAP9Shield**

Drawing number: 1

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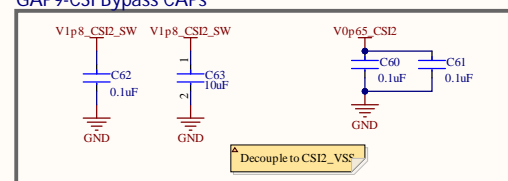
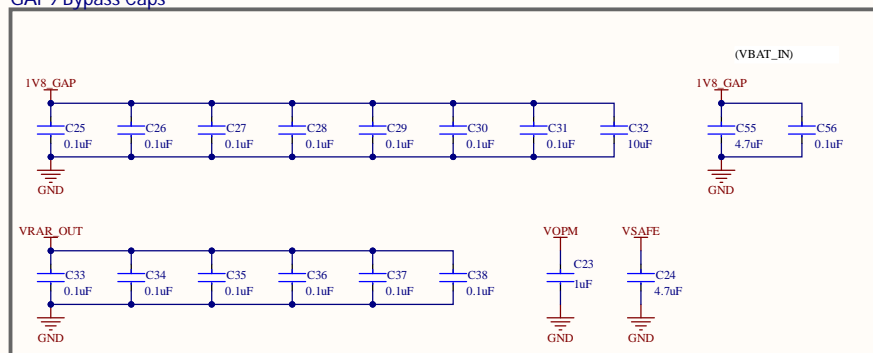
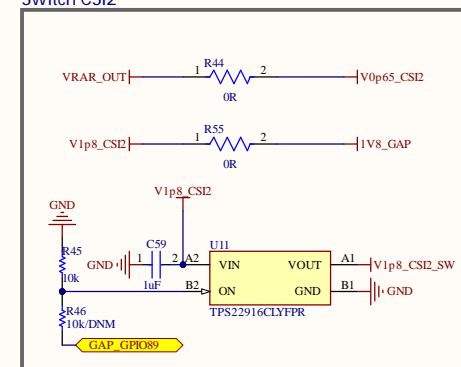
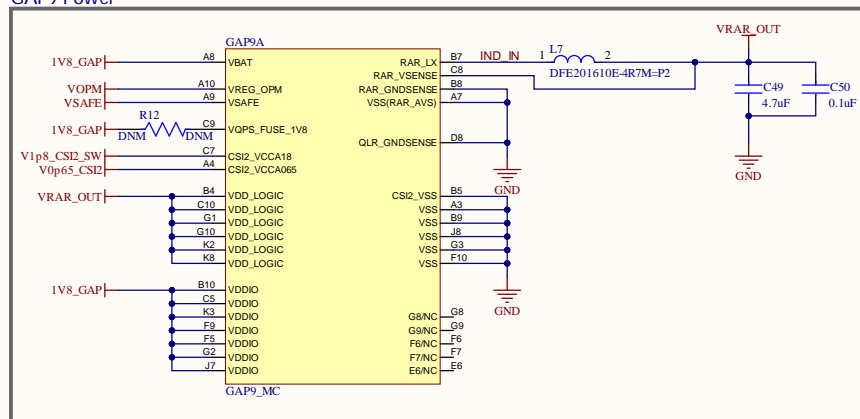
Format: Laboratory: Integrated Systems Laboratory

A3 Drawn by: Hanna Müller and Victor Kartsch

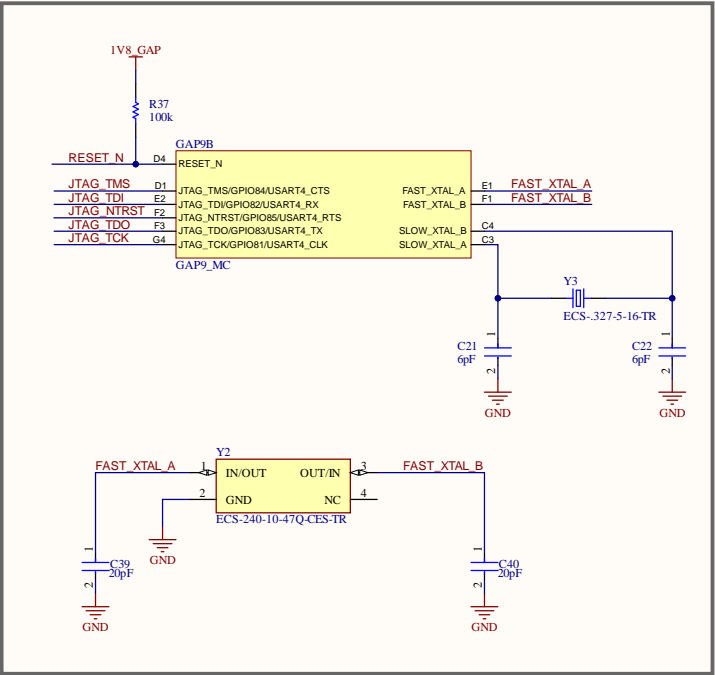
Sheet: 01\_GAP9\_INTERFACE.SchDoc

Page 1 of 9

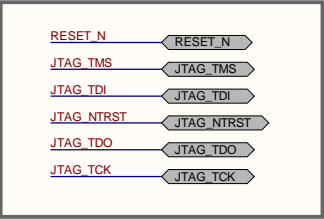
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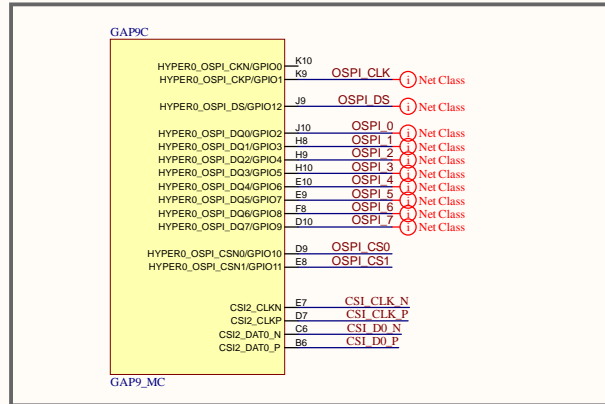
GAP9 XTALs



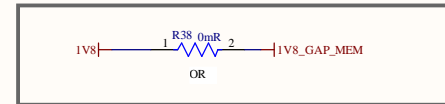
Offsheet Connections



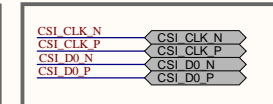
## GAP9 Memory Interface



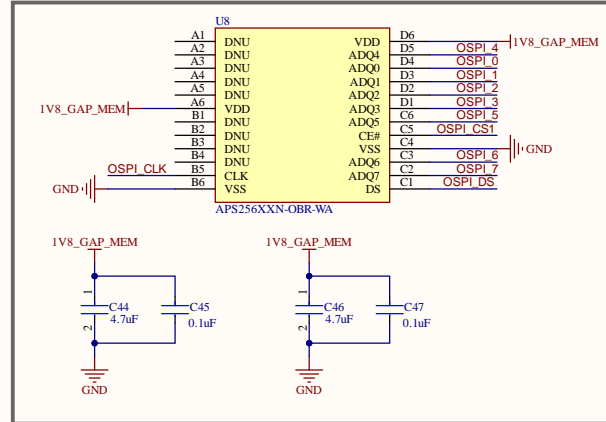
## GAP9 Supply Load Switch



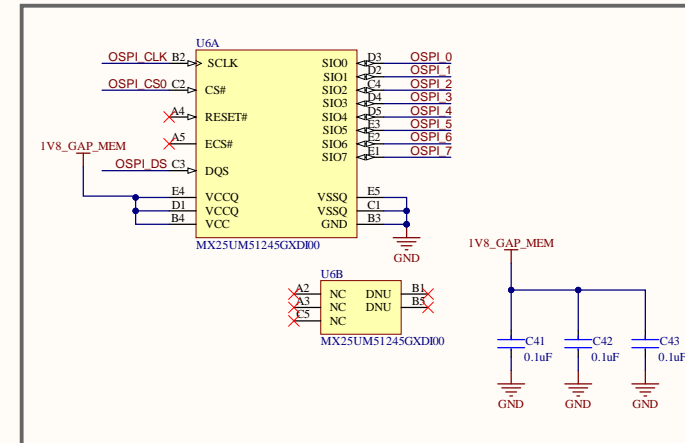
## Offsheet Connections



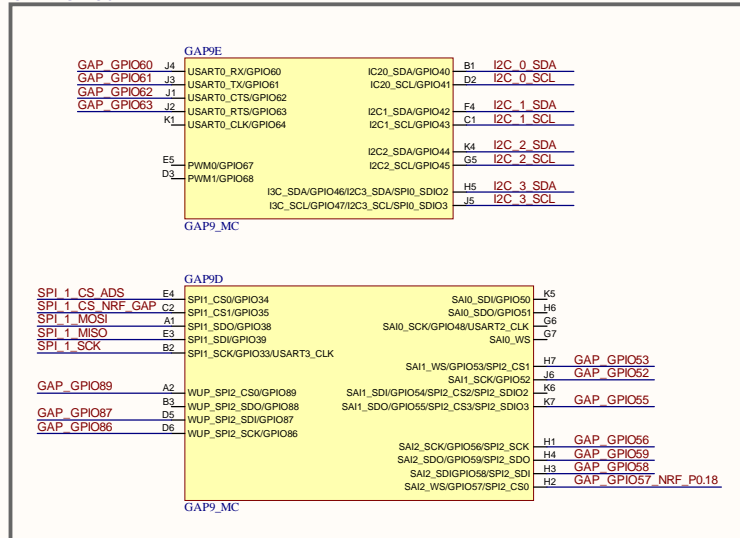
## GAP9 PSRAM



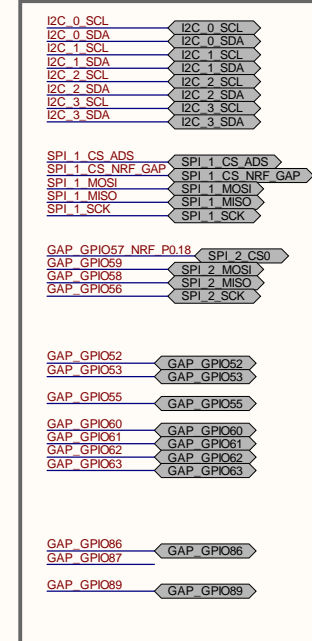
## GAP9 FLASH



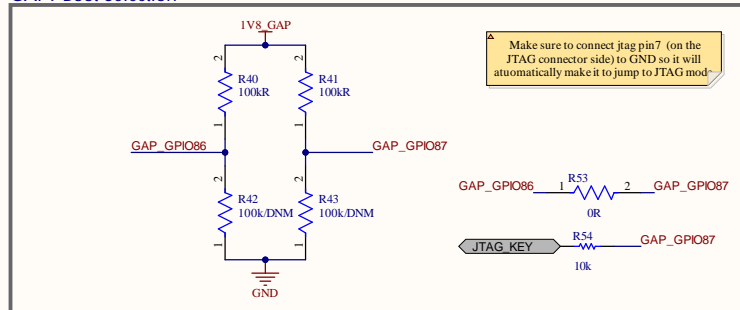
## GAP9 GPIOs



## Offsheet Connections



## GAP9 Boot Selection



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**GAP9Shield**

Drawing number: 5

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Sheet: 05\_GAP9\_CHIP\_GPIO.SchDoc

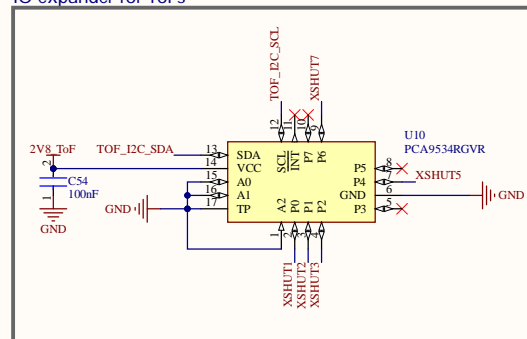
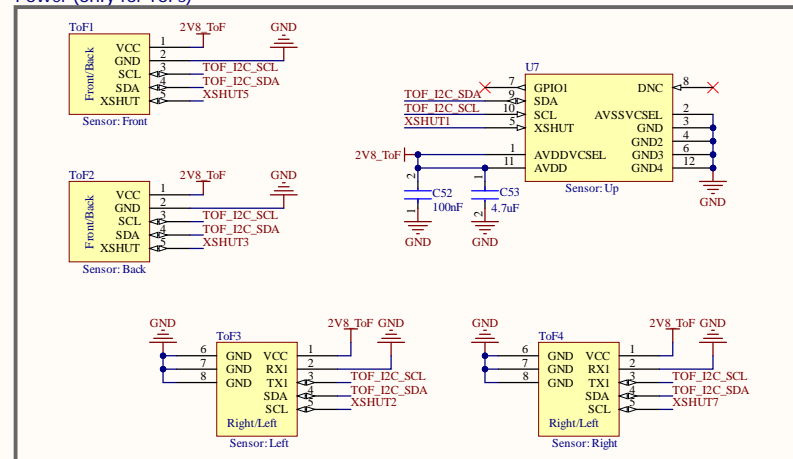
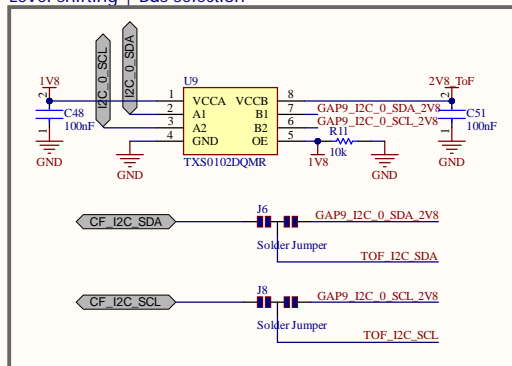
Date: 3/24/2025 2:23:05 PM

A3

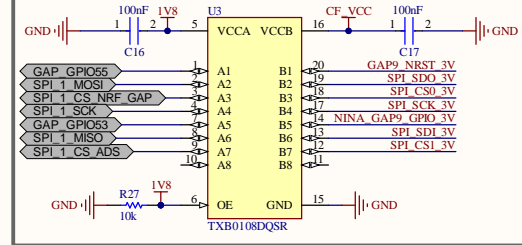
Drawn by: Hanna Müller and Victor Kartsch

Page 5 of 9

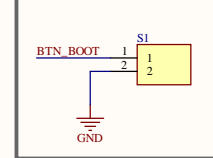
File: C:\deleteme\gap9-shield\hardware\gap9\_deck\_v205\_GAP9\_CHIP\_GPIO.SchDoc



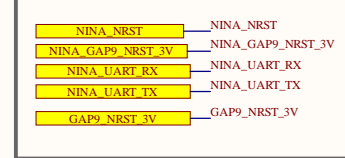
### Level Shifter GAP/NINA



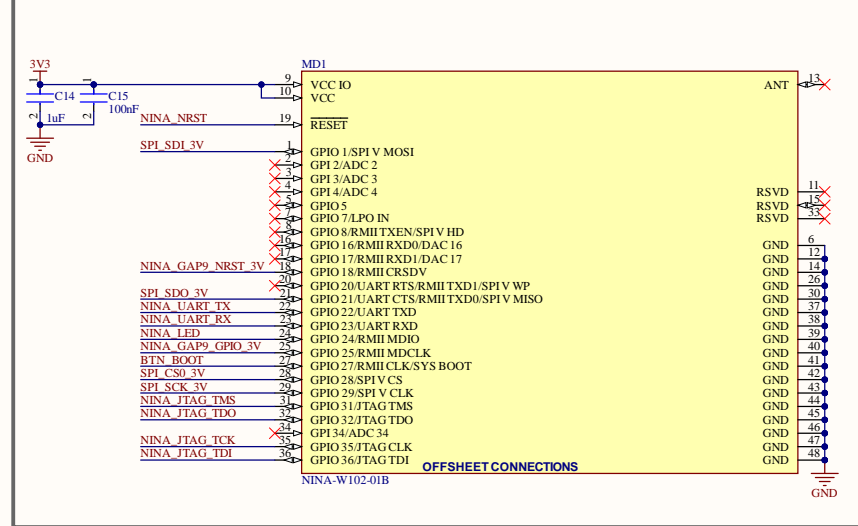
### BOOT Button



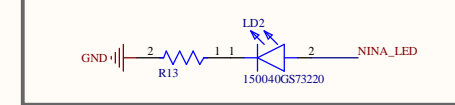
### Offsheet connections



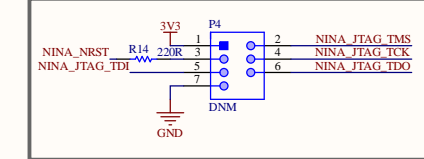
### NINA Module



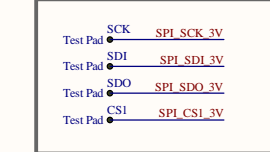
### NINA Leds



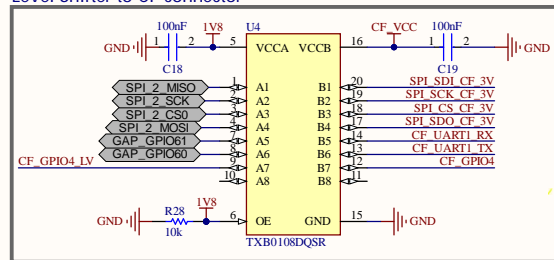
### JTAG



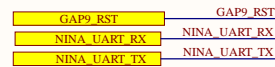
### Signal Test Pads



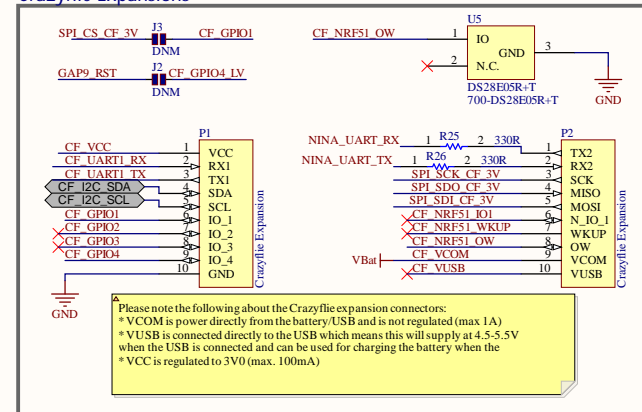
### Level Shifter to CF connector



### Offsheet connections



### Crazyflie Expansions



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**GAP9Shield**

Drawing number: 8

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Sheet: 08\_CF\_INTERFACE.SchDoc

Date: 3/24/2025 2:23:06 PM

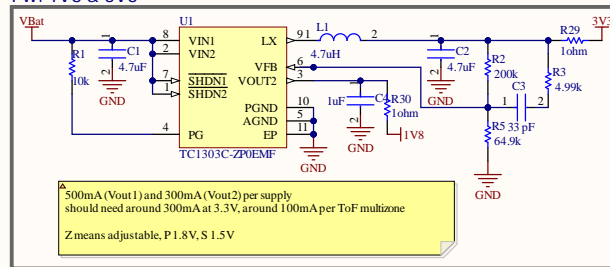
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Page 8 of 9

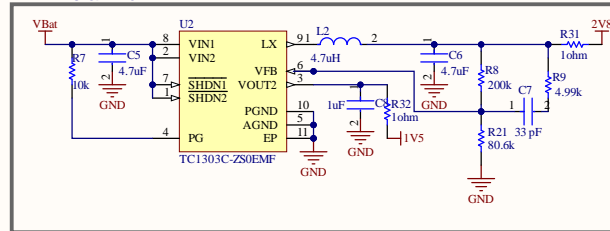
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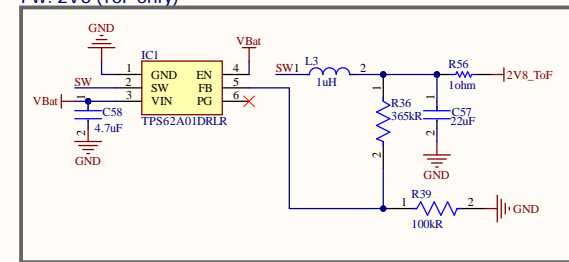
# PW: 1V8 & 3V3



# PW: 2V8 & 1V5



# PW: 2V8 (ToF only)



# Power Test Pads

