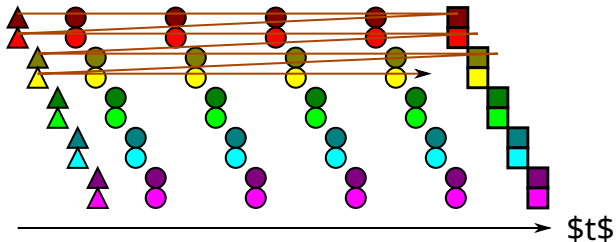


the issue order



the earliest cycle each instruction can be executed