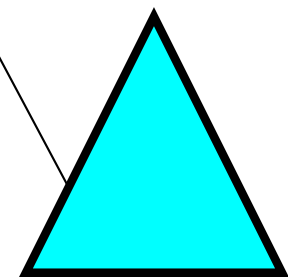
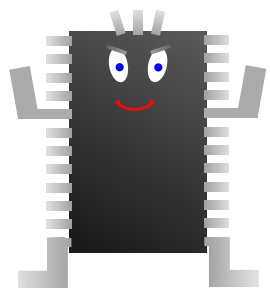


Initial cache states (different)



hit

miss

executed instructions \aka{\em (identical)}

