## Table 19-1. Architectural Performance Events

E۱	ent/		Umask		
Nı	JM.	Event Mask Mnemonic	Value	Description	Comment
30	Н	UnHalted Core Cycles	00H	Unhalted core cycles	
30	H	UnHalted Reference Cycles	01H	Unhalted reference cycles	Measures bus cycle <sup>1</sup>
CC	)Η	Instruction Retired	00H	Instruction retired	
26	H	LLC Reference	4FH	Longest latency cache references	
ZE	H	LLC Misses	41H	Longest latency cache misses	
CZ	ŀΗ	Branch Instruction Retired	00H	Branch instruction at retirement	
C5	5H	Branch Misses Retired	00H	Mispredicted Branch Instruction at retirement	