## Table 19-1. Architectural Performance Events

| Event Num. | Event Mask Mnemonic | Umask Value | Description | Comment |
|---|---|---|---|---|
| 3CH | UnHalted Core Cycles | 00H | Unhalted core cycles | |
| 3CH | UnHalted Reference Cycles | 01H | Unhalted reference cycles | Measures bus cycle[1] |
| C0H | Instruction Retired | 00H | Instruction retired | |