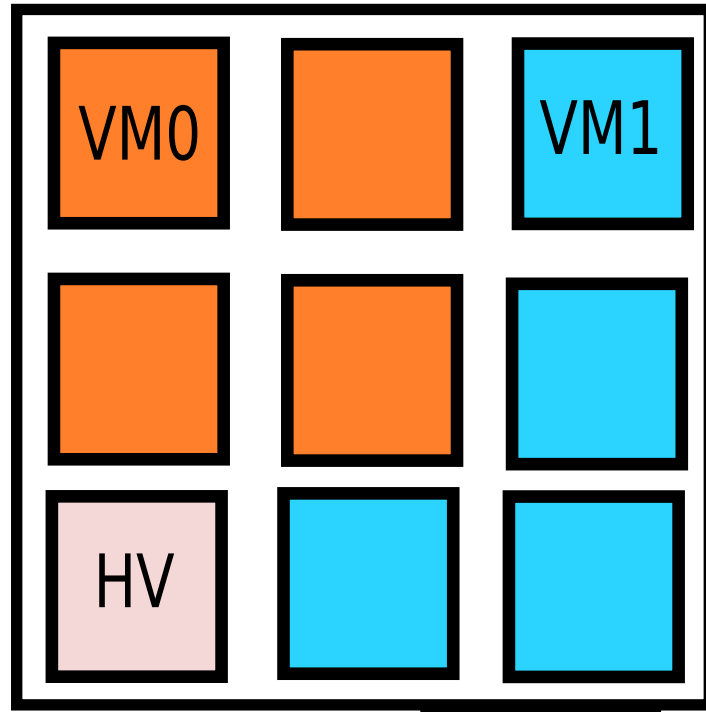
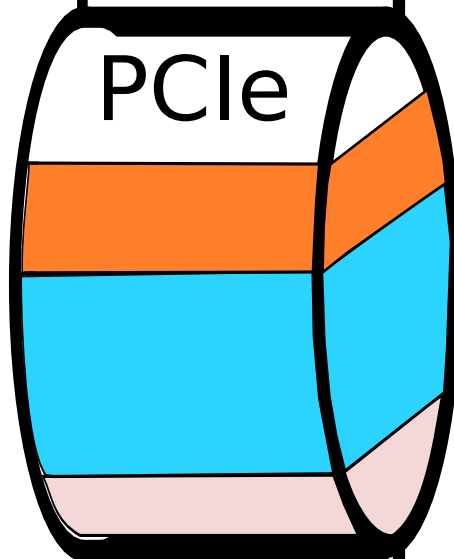
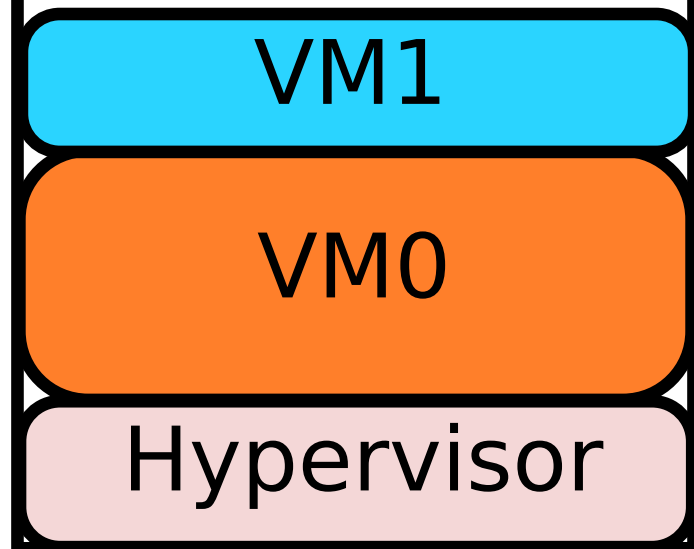


Host

Intel x86 Cores



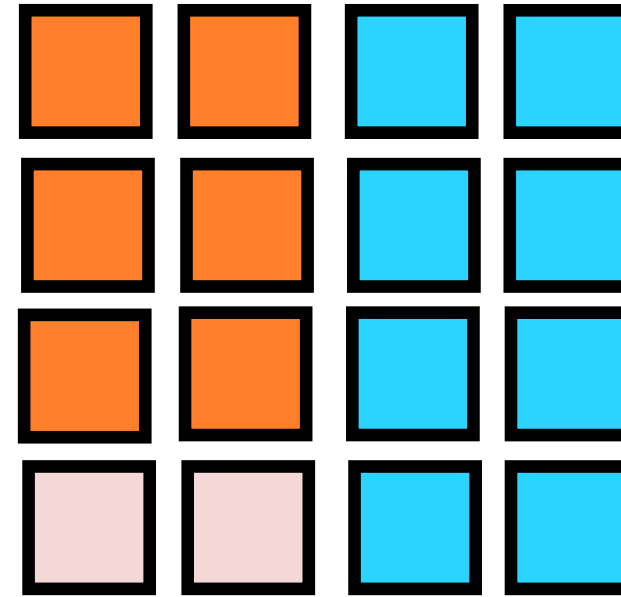
Host Memory



SmartNIC

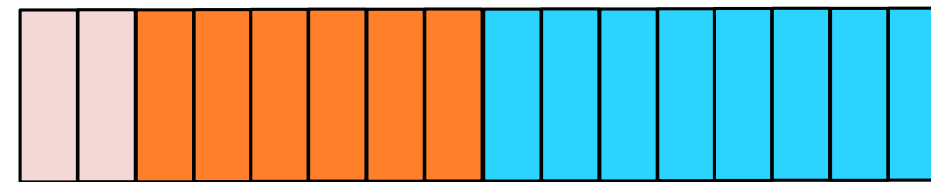
Core Partitioning (4.1)

MIPS Cores



Cache Striping (4.3)

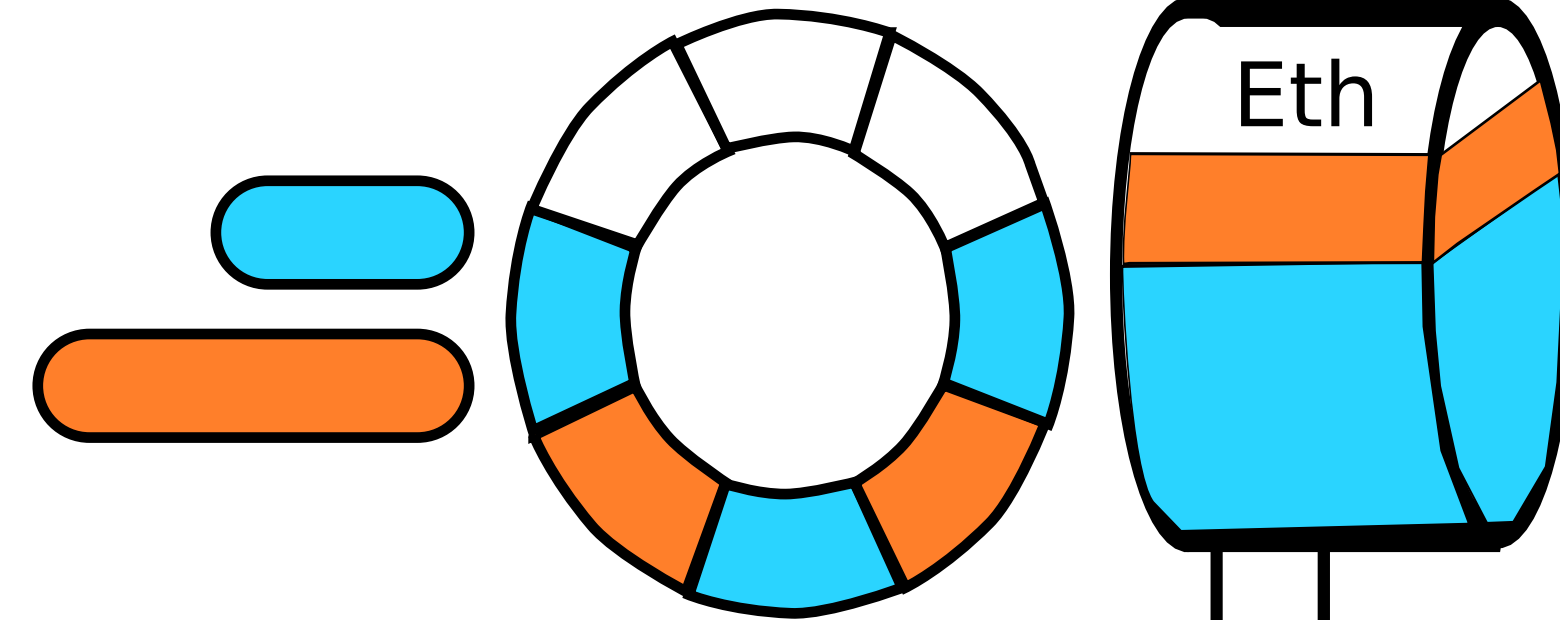
L2 Cache



Main Memory

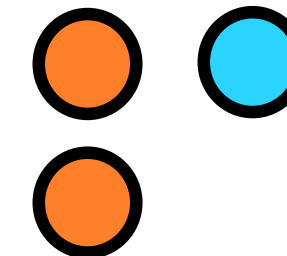
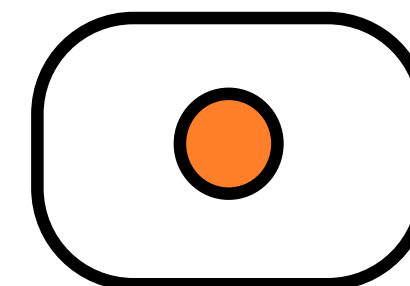


Traffic Scheduling (4.2)



Rate-limiting
coprocessors (4.4)

Security



Zip

