H Roll No. .....

TBC–402

B. C. A. (FOURTH SEMESTER)
MID SEMESTER
EXAMINATION, March, 2024

MICROPROCESSORS

Time : 1½ Hours

Maximum Marks : 50

Note : (i) Answer all the questions by choosing any ***one*** of the sub-questions.

(ii) Each sub-question carries 10 marks.

1. (a) What do you mean by the microprocessor ? Describe the evolution of Intel microprocessors in detail. (CO1)

OR

(b) Explain the functions of the following pins of 8085 : (CO1)

(i) ALE

(ii) HLDA and HOLD

P. T. O.

(iii) READY and Reset out

(iv) TRAP and INTR

(v) SOD and SID

2. (a) Explain the external system bus architecture of 8085 microprocessor with a diagram. (CO1/CO2)

OR

(b) Write a short notes on the following :

(i) Memory Mapped I/O

(ii) Peripheral I/O

(CO1/CO2)

3. (a) Explain all the general purpose register and special purpose register of 8085 microprocessor. (CO1/CO2)

OR

(b) What is the interfacing of 8085 microprocessor with memory ? What is the primary function of memory interfacing ? How many memory locations are there in 8085 ? (CO1/CO2)

4. (a) Explain the Internal Architecture of 8085 microprocessor in detail with a neat diagram. (CO1/CO2)

OR

(b) Explain the flag register of 8085 also discuss all five flags with suitable example. (CO1/CO2)

5. (a) If the last address of 1K memory is FBFFH, find the starting address. (CO2)

OR

(b) What do you mean by an addressing mode ? Explain all addressing modes of 8085 in detail. (CO2)