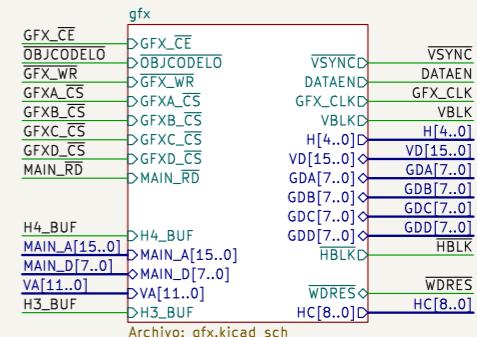
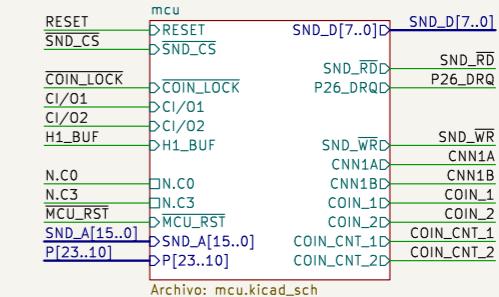
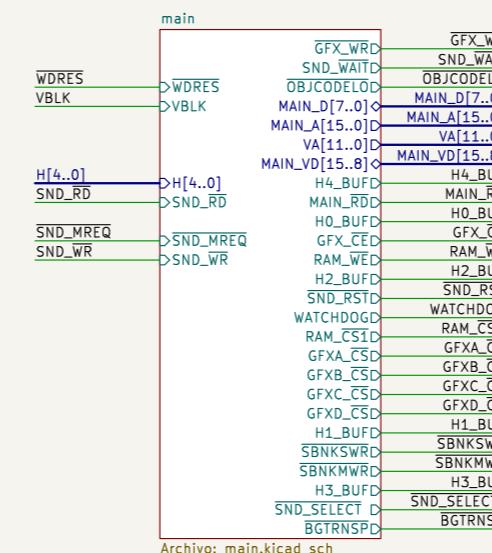
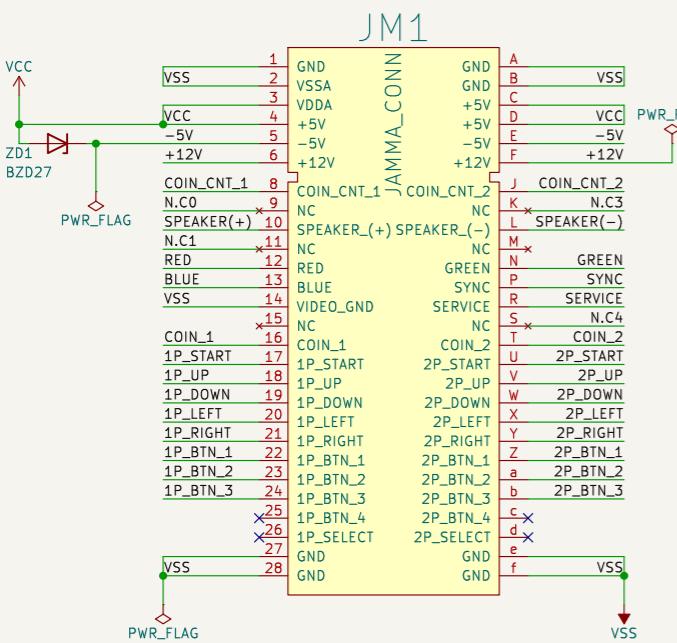
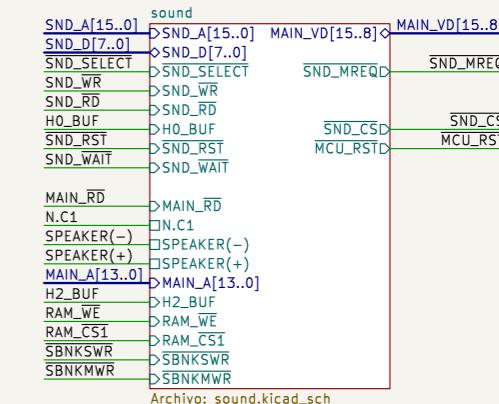
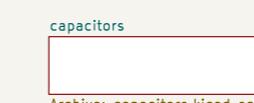
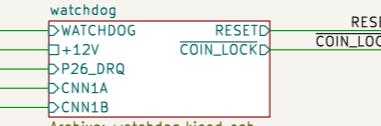
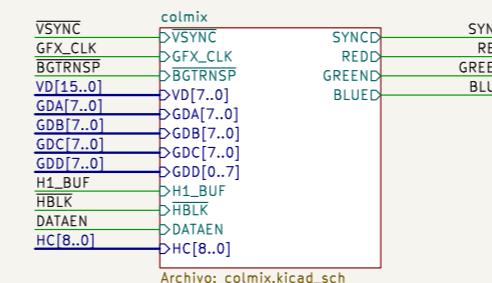
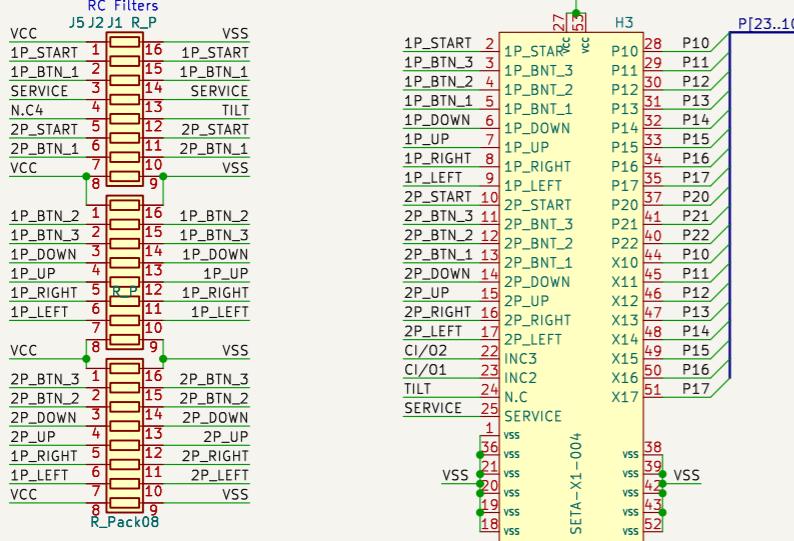


A



CABINET I/O



JOTEGO

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File: exterm.kicad_sch

Title: EXTERMINATIONSize: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cl 7.0.10-7.0.10-ubuntu20.04.1Rev: José Tejada G.
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1 2 3 4 5 6 7 8

A

B

C

D

E

F



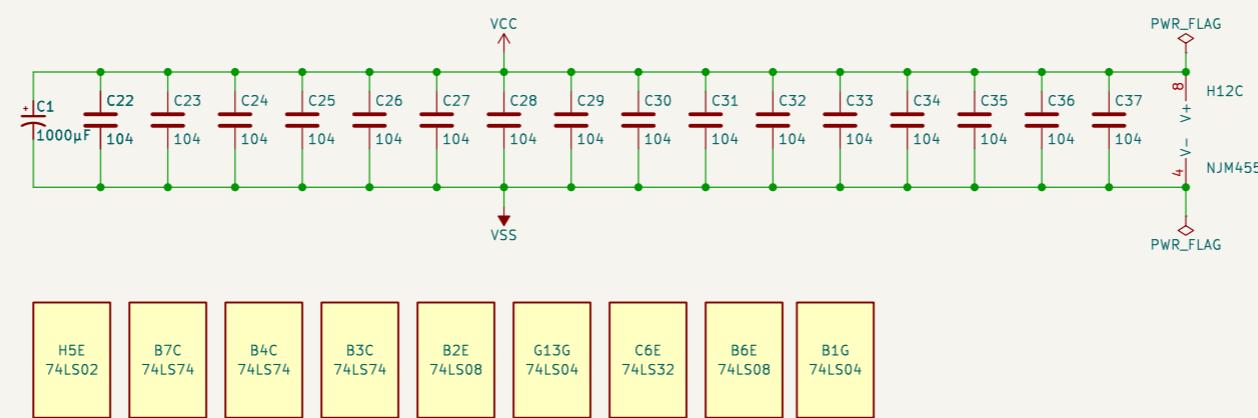
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Size: A3 | Date: 29/08/2022
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H5E
74LS02

B7C
74LS74

B4C
74LS74

B3C
74LS74

B2E
74LS08

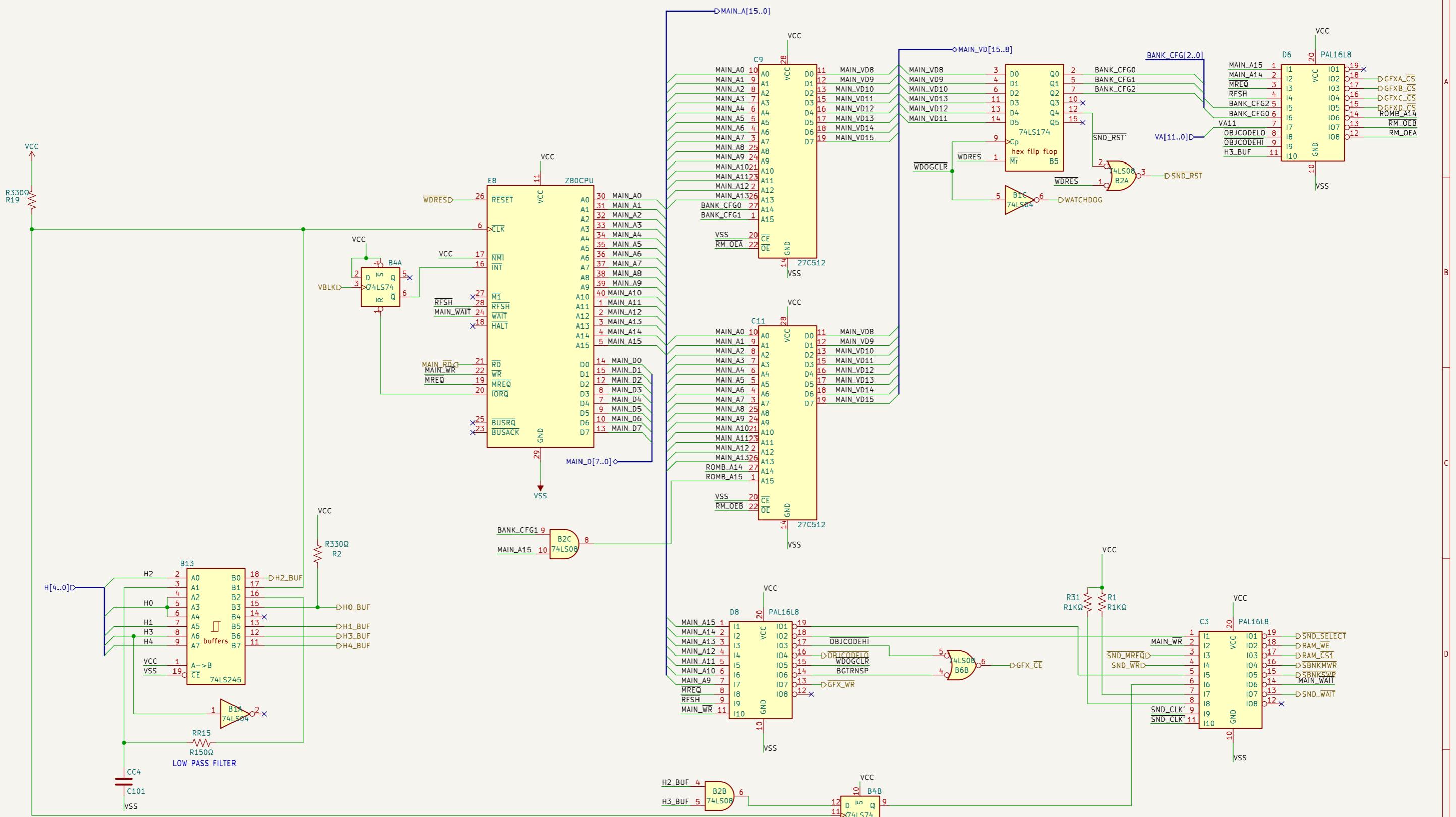
G13G
74LS04

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B6E
74LS08

B1G
74LS04

1 2 3 4 5 6 7 8



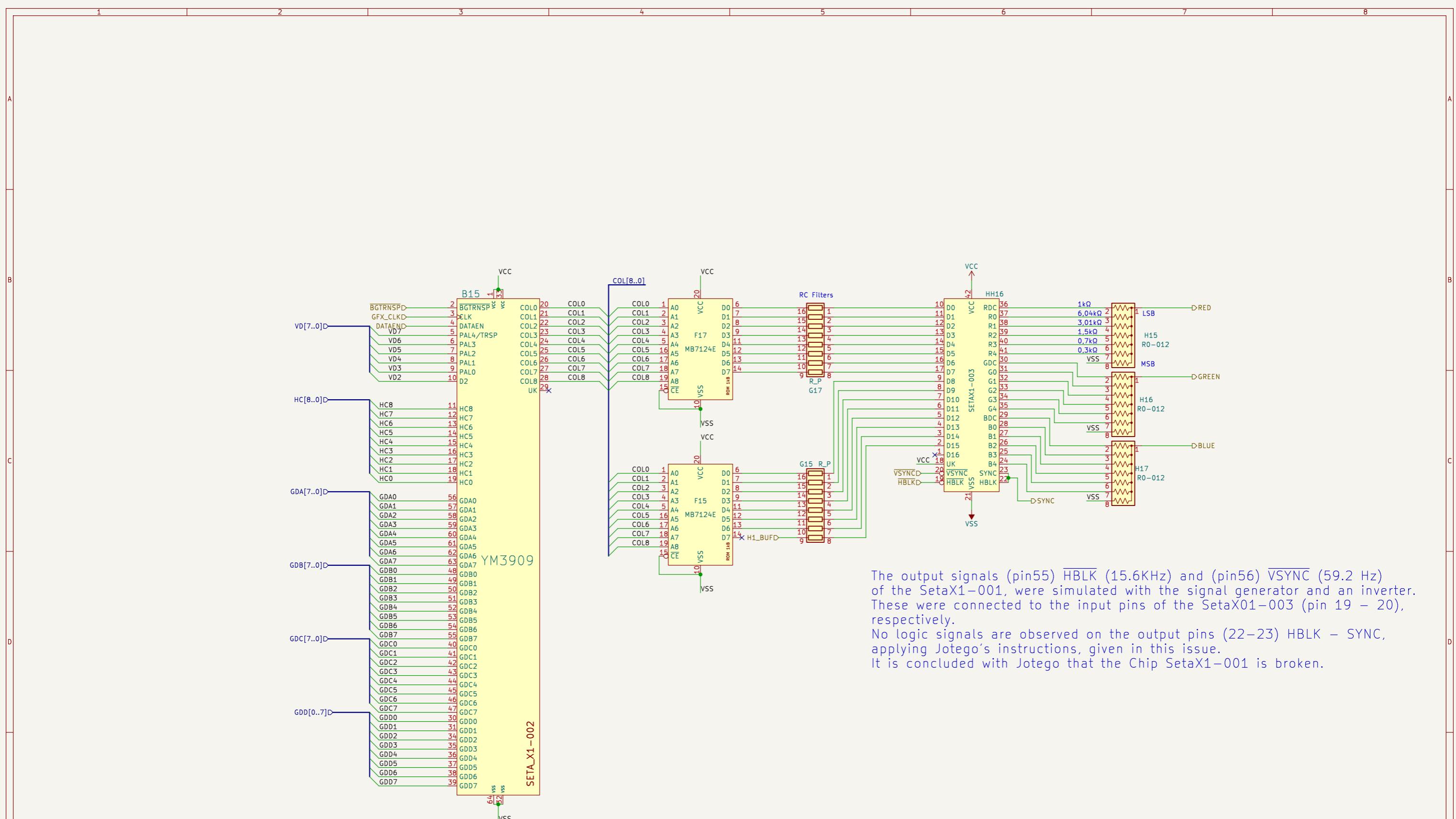
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Size: A3	Date: 29/08/2022	Rev: José Tejada G.
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1		Id: 3/10





The output signals (pin 55) **HBLK** (15.6KHz) and (pin 56) **VSYNC** (59.2 Hz) of the SetaX1-001, were simulated with the signal generator and an inverter. These were connected to the input pins of the SetaX01-003 (pin 19 – 20), respectively. No logic signals are observed on the output pins (22–23) HBLK – SYNC, applying Jotego's instructions, given in this issue. It is concluded with Jotego that the Chip SetaX1-001 is broken.

JOTEGO

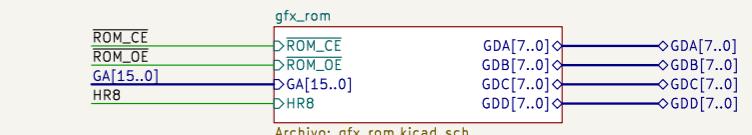
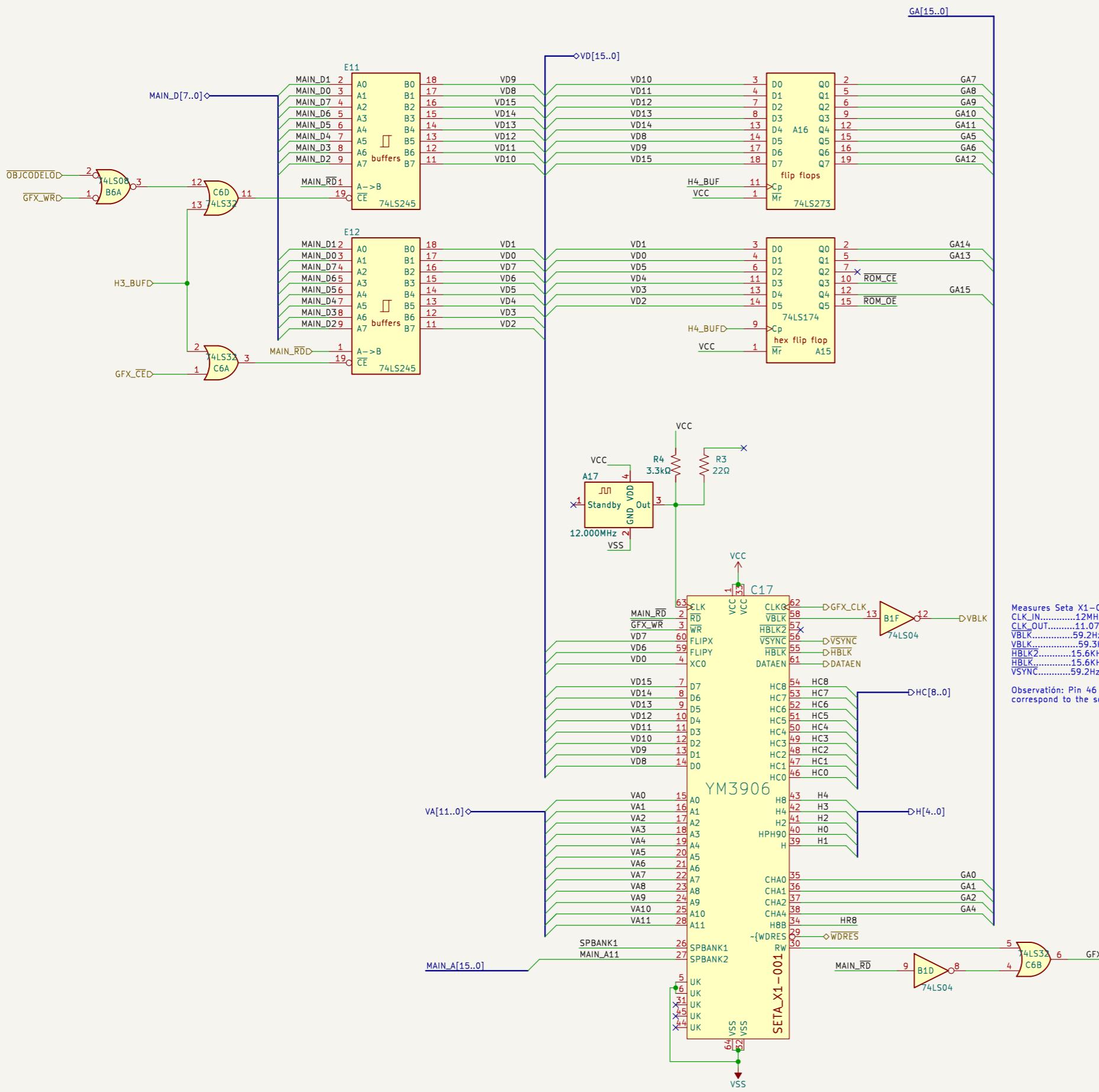
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Title: EXTERMINATION

Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1



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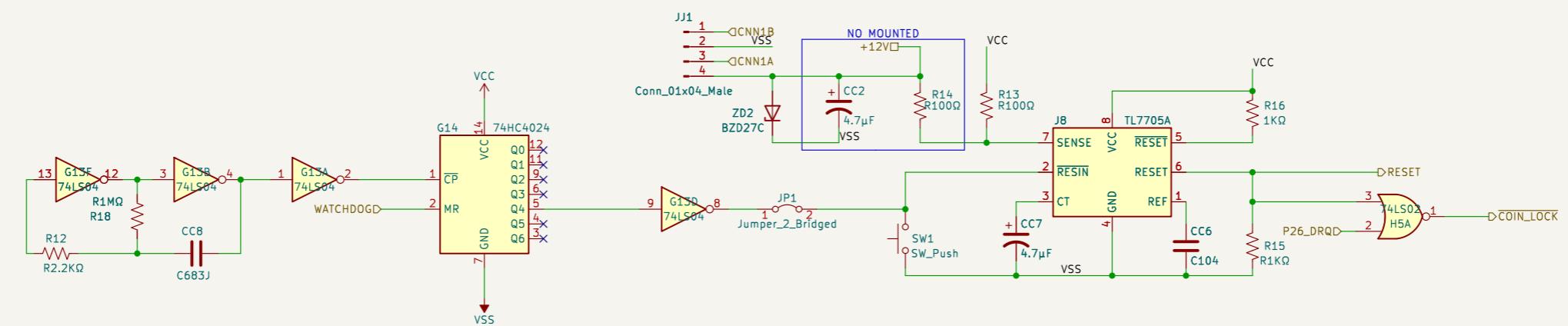
JOTEGO

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Title: EXTERMINATION

Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10~ubuntu20.04.1

The JT logo consists of a stylized lowercase 't' and 'j' in black, with a red outline.



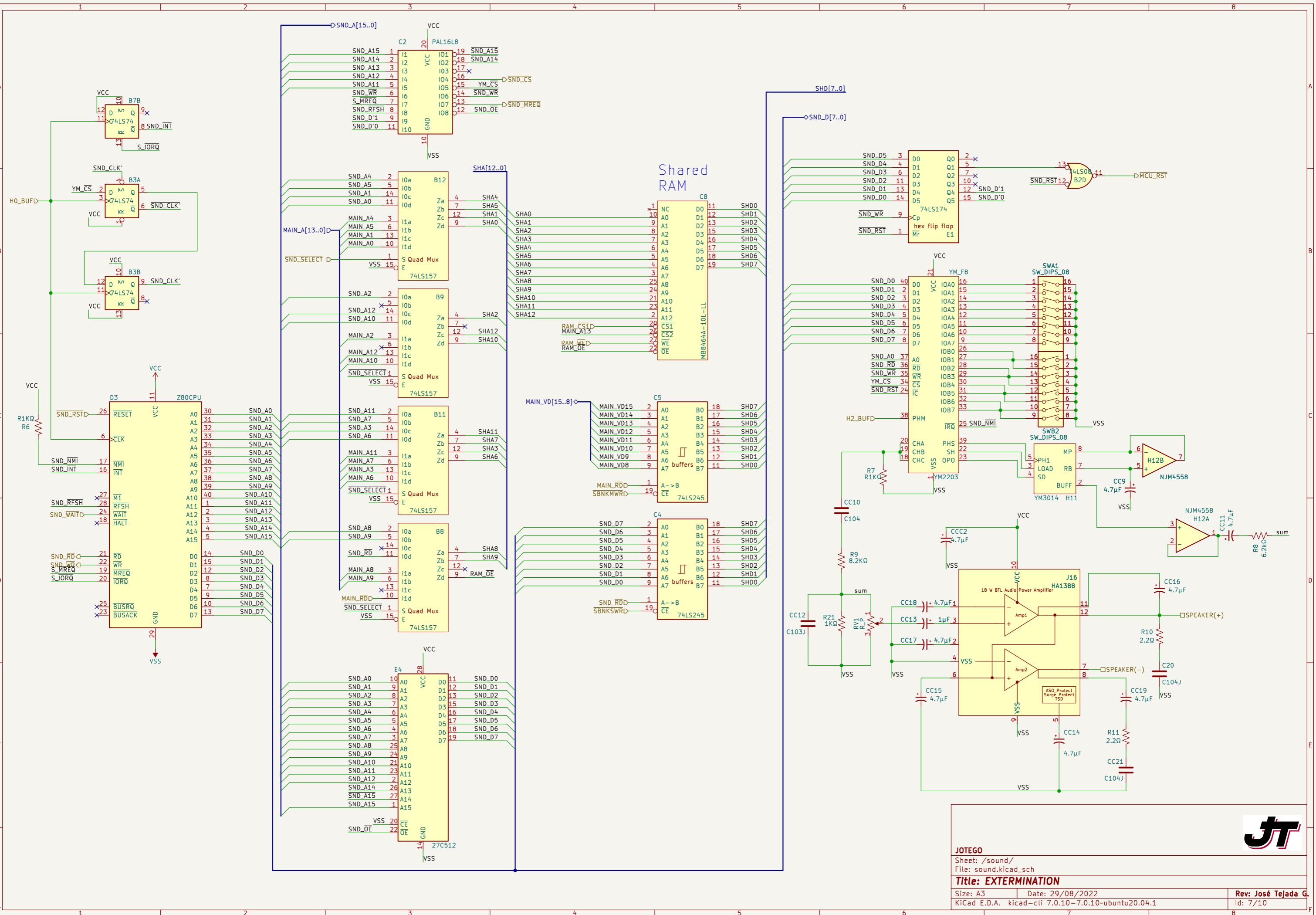
JOTEGO

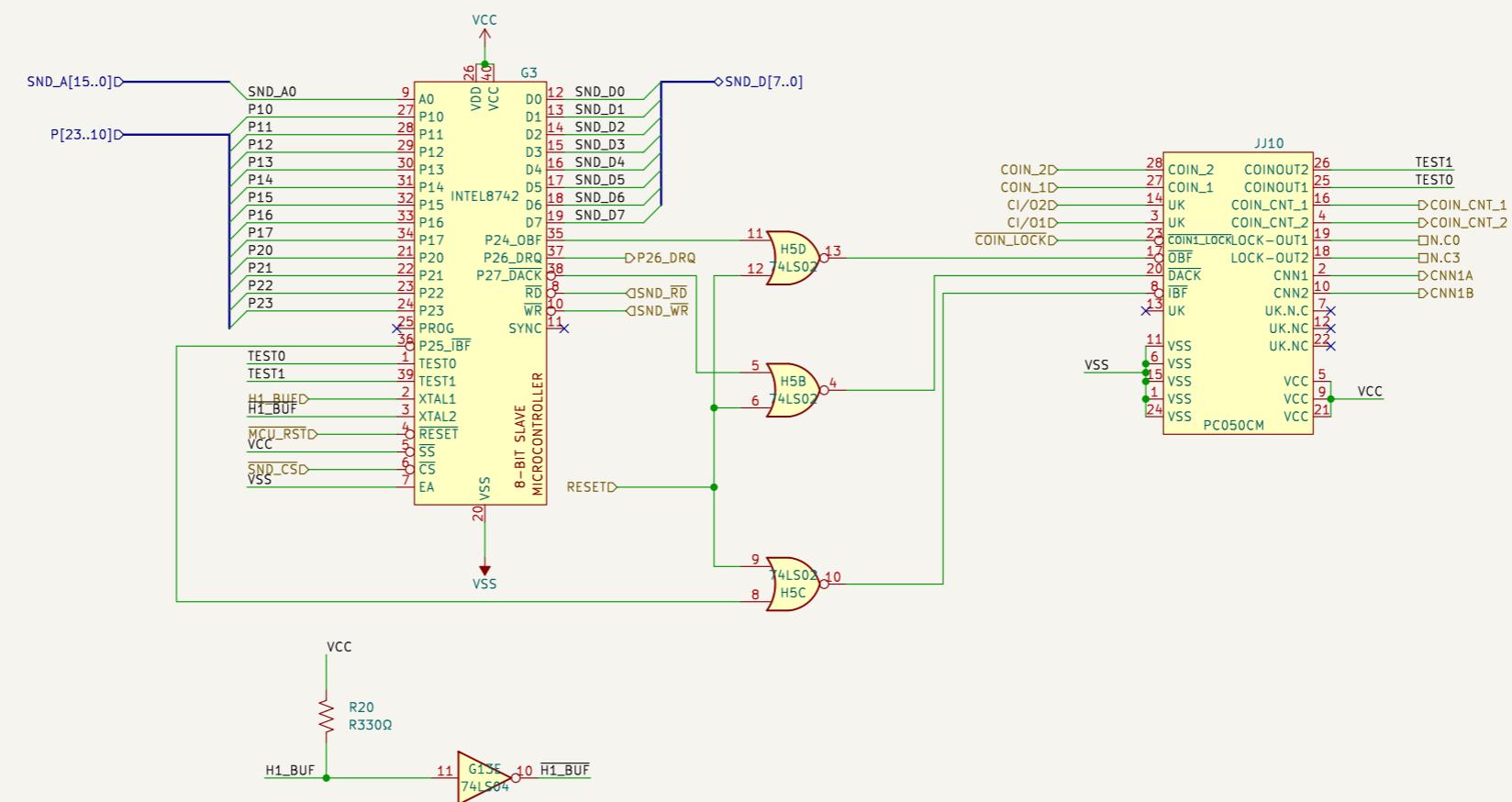
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Title: EXTERMINATION

Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10~ubuntu20.04.1





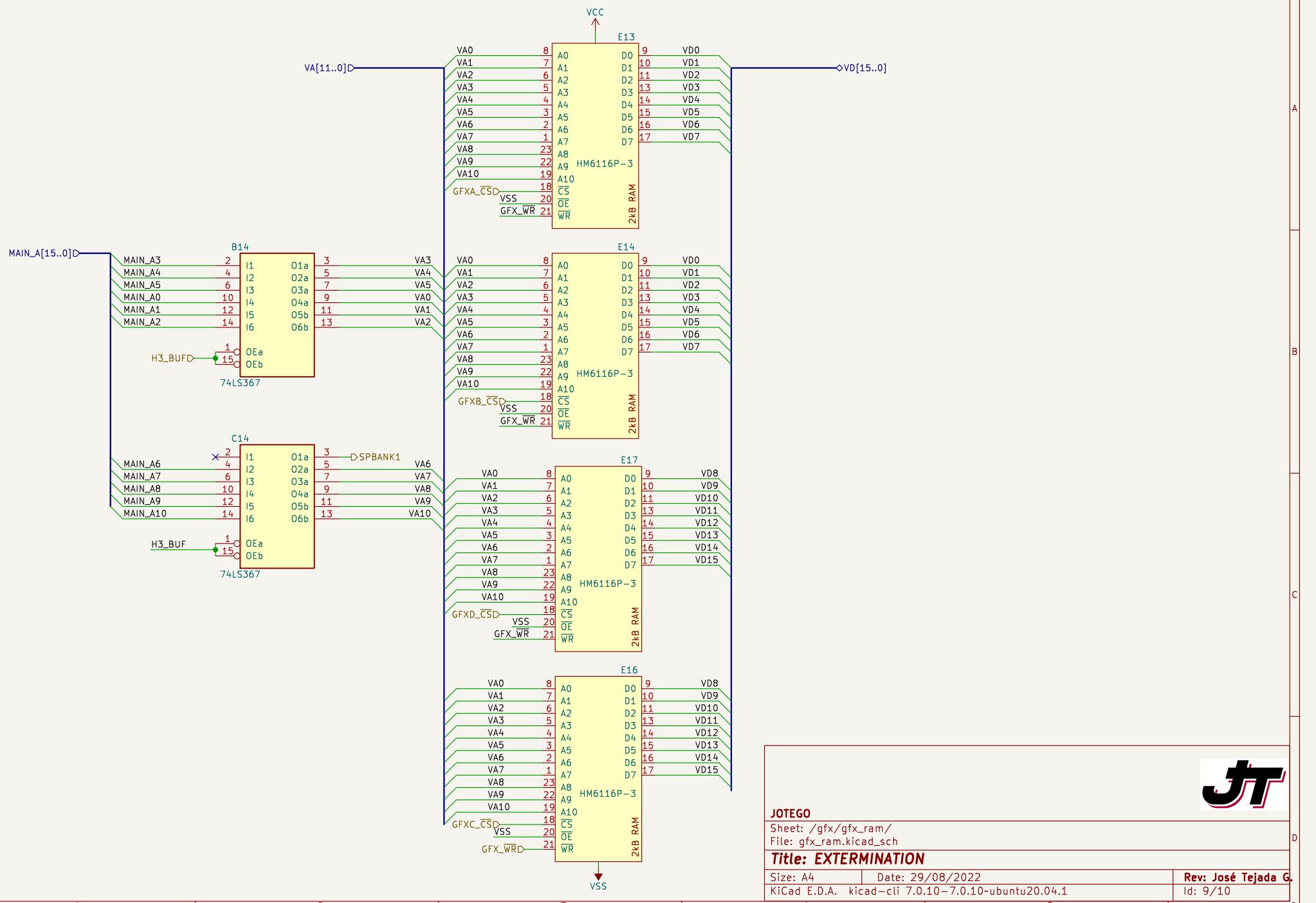


JOTEGO

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Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10~ubuntu20.04.1



JOTEGO

Sheet: /gfx/gfx_ram/
File: gfx_ram.kicad_sch

Title: EXTERMINATION

Size: A4 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1



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