

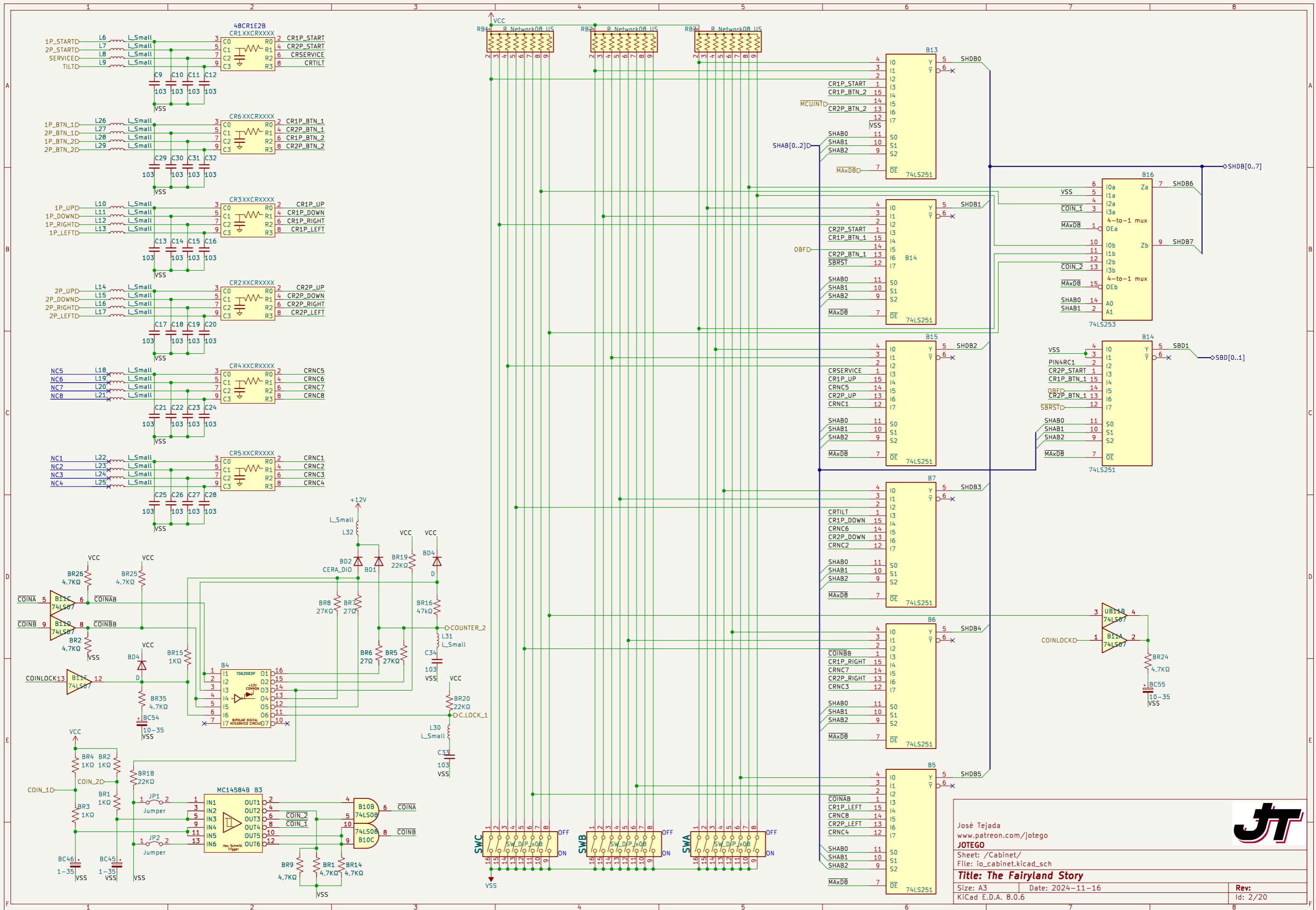
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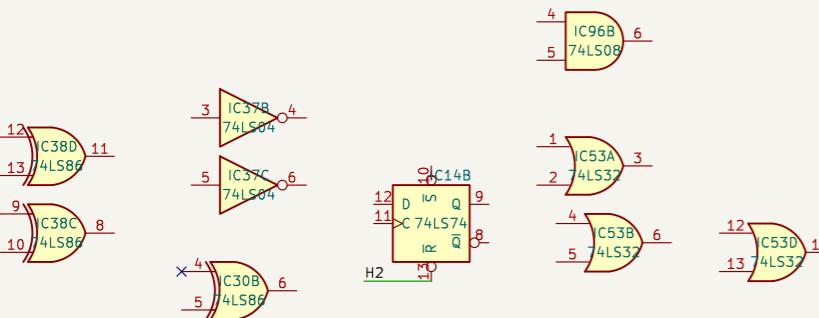
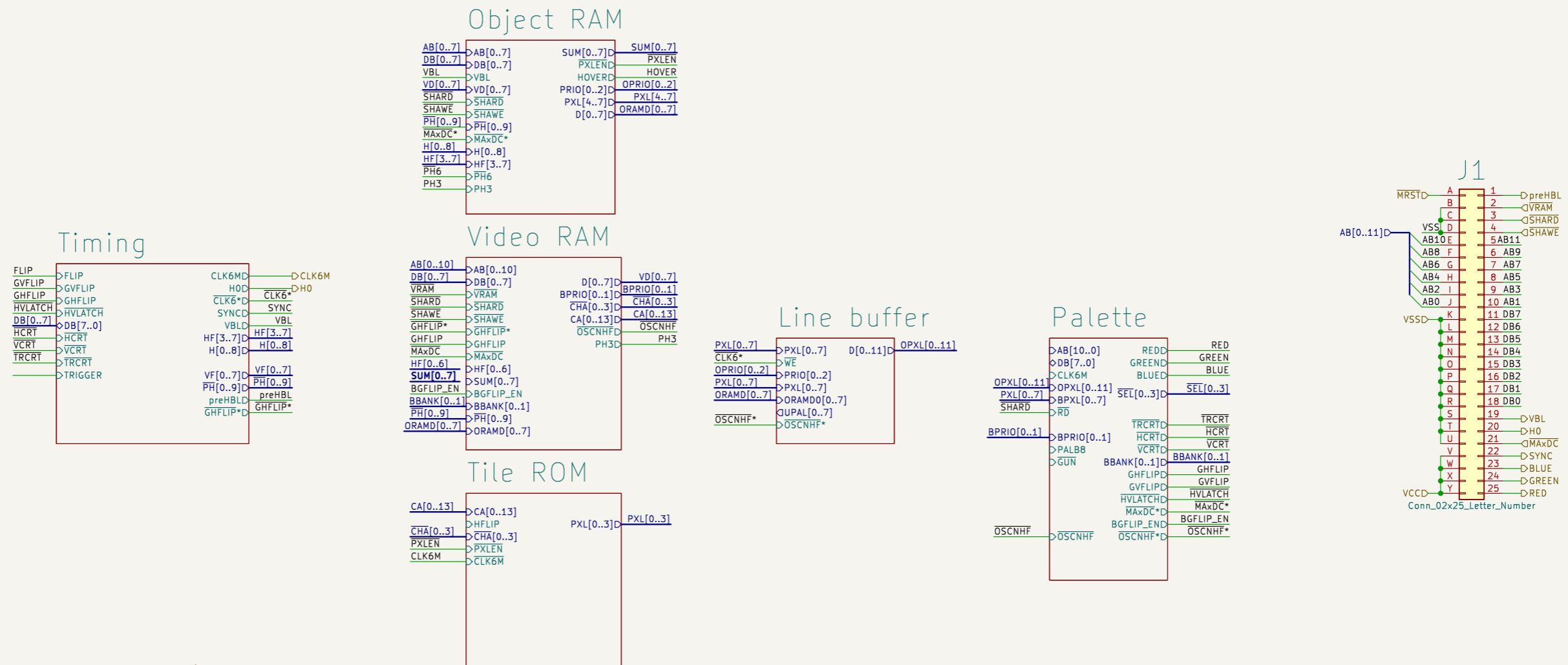
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Title: Fairyland.kicad_sch

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.6

Rev: Id: 1/20





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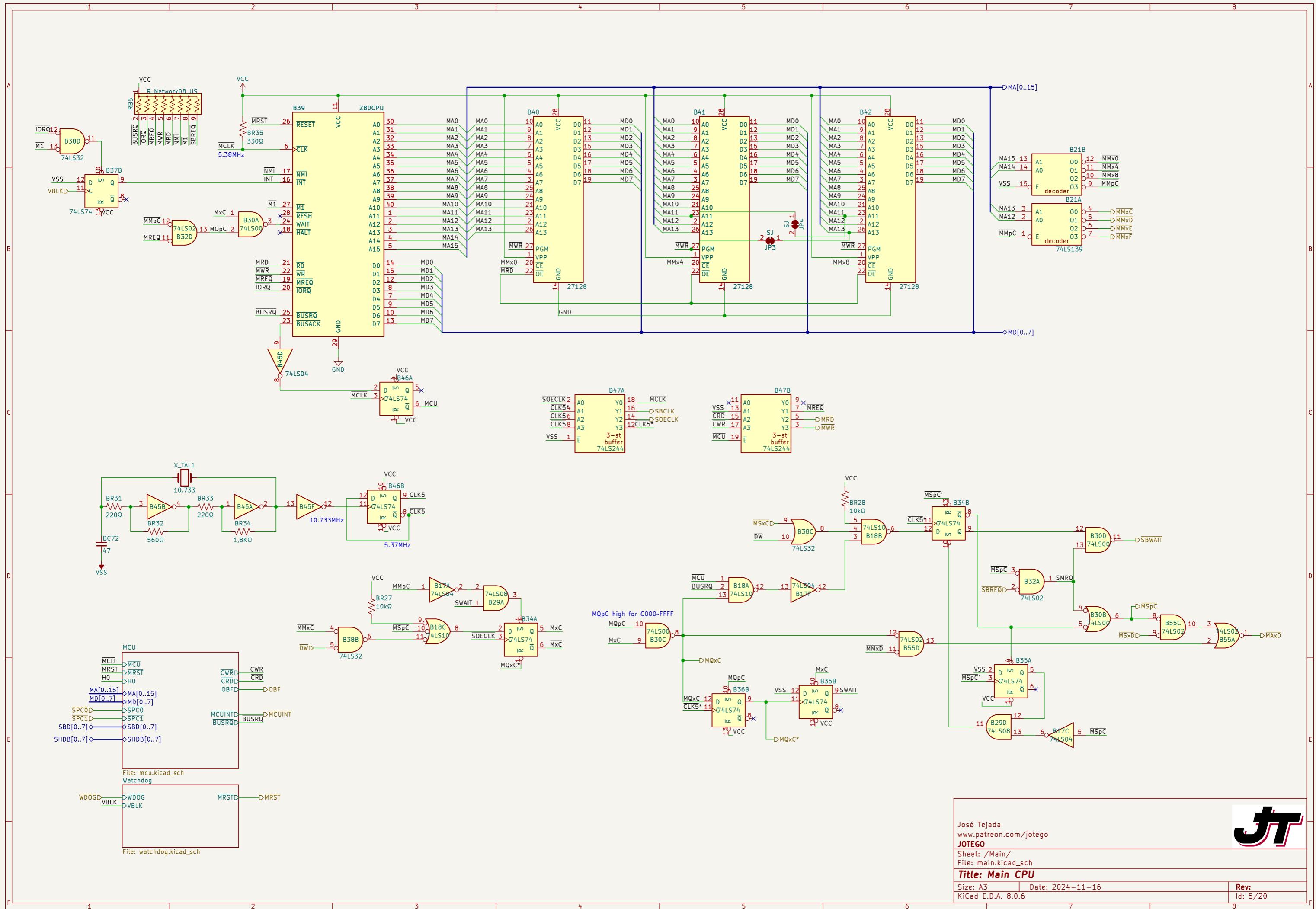
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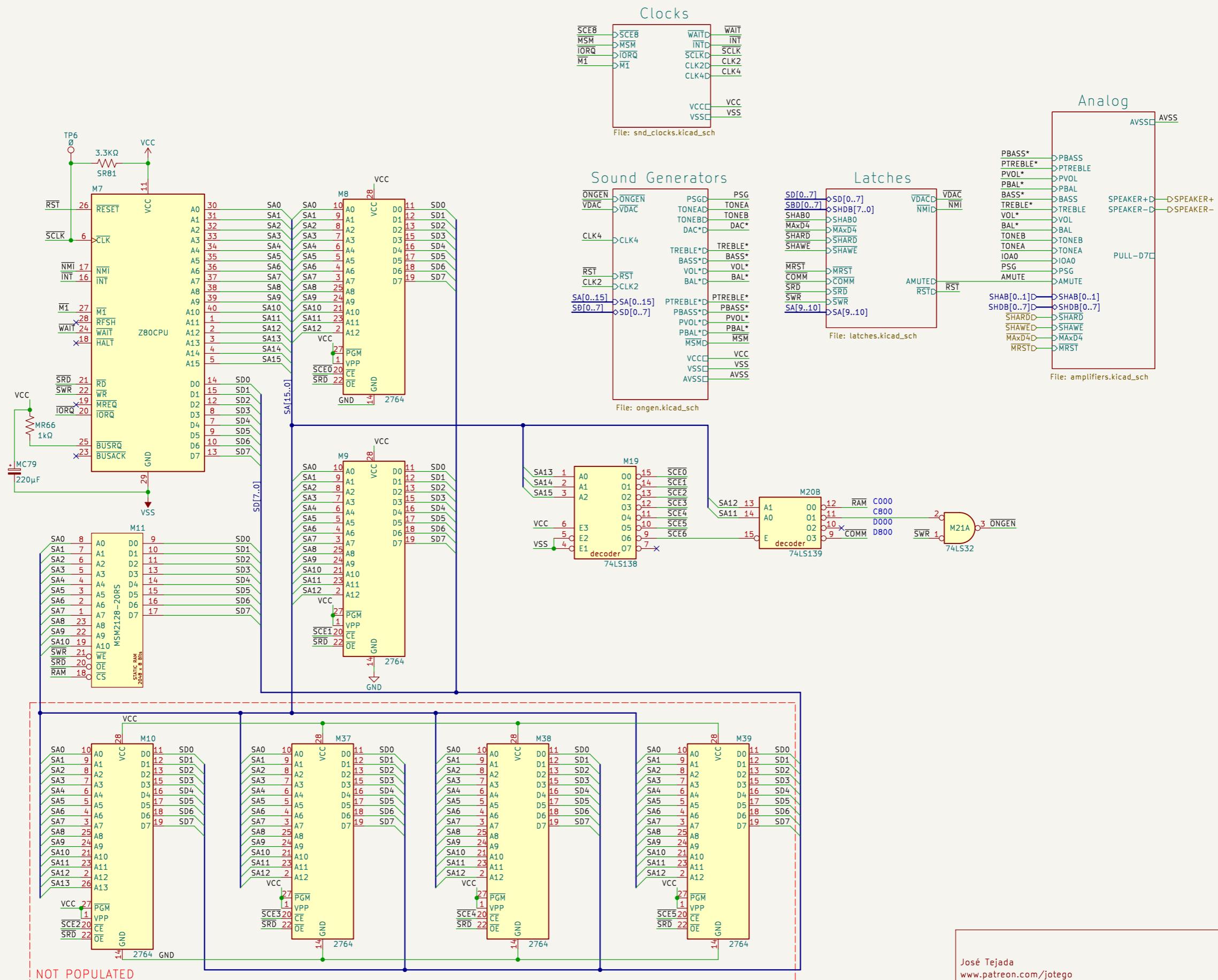
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Title: The Fairyl

Size: A3 Date: 2

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Sheet: /Sound/

File: sound.kicad_sch

Title: Sound Board

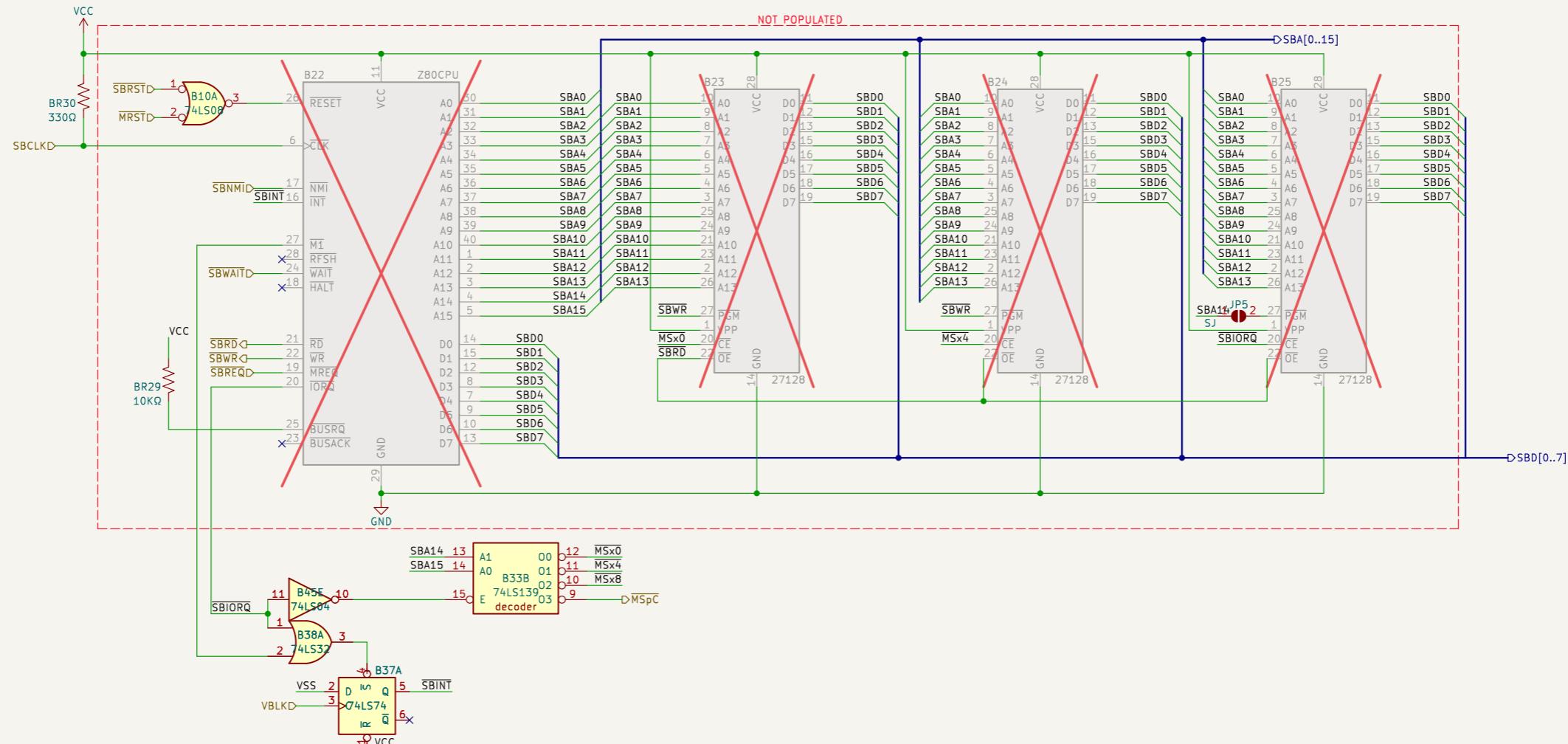
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For more information about the study, please contact Dr. John Smith at (555) 123-4567 or via email at john.smith@researchinstitute.org.

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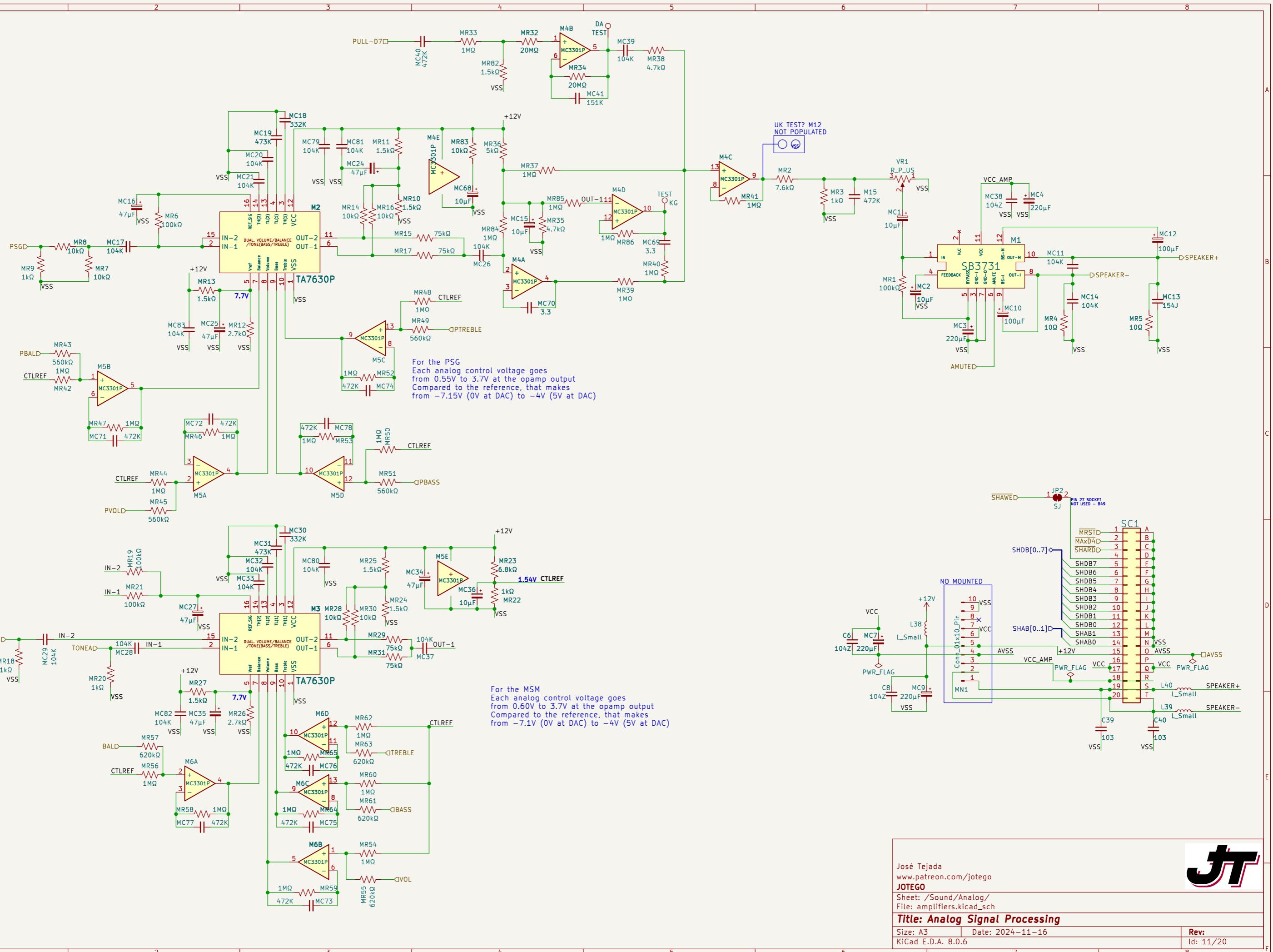
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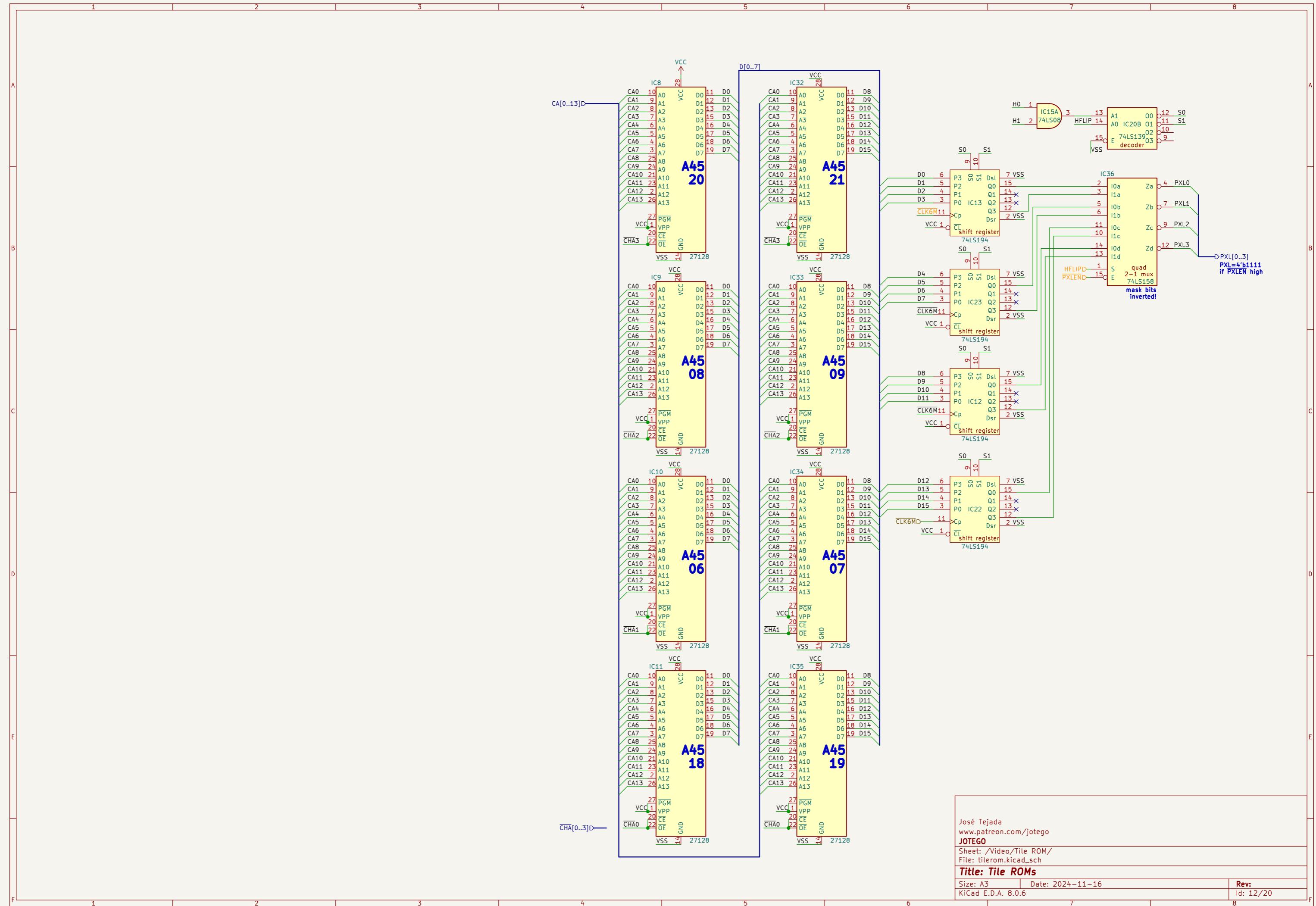
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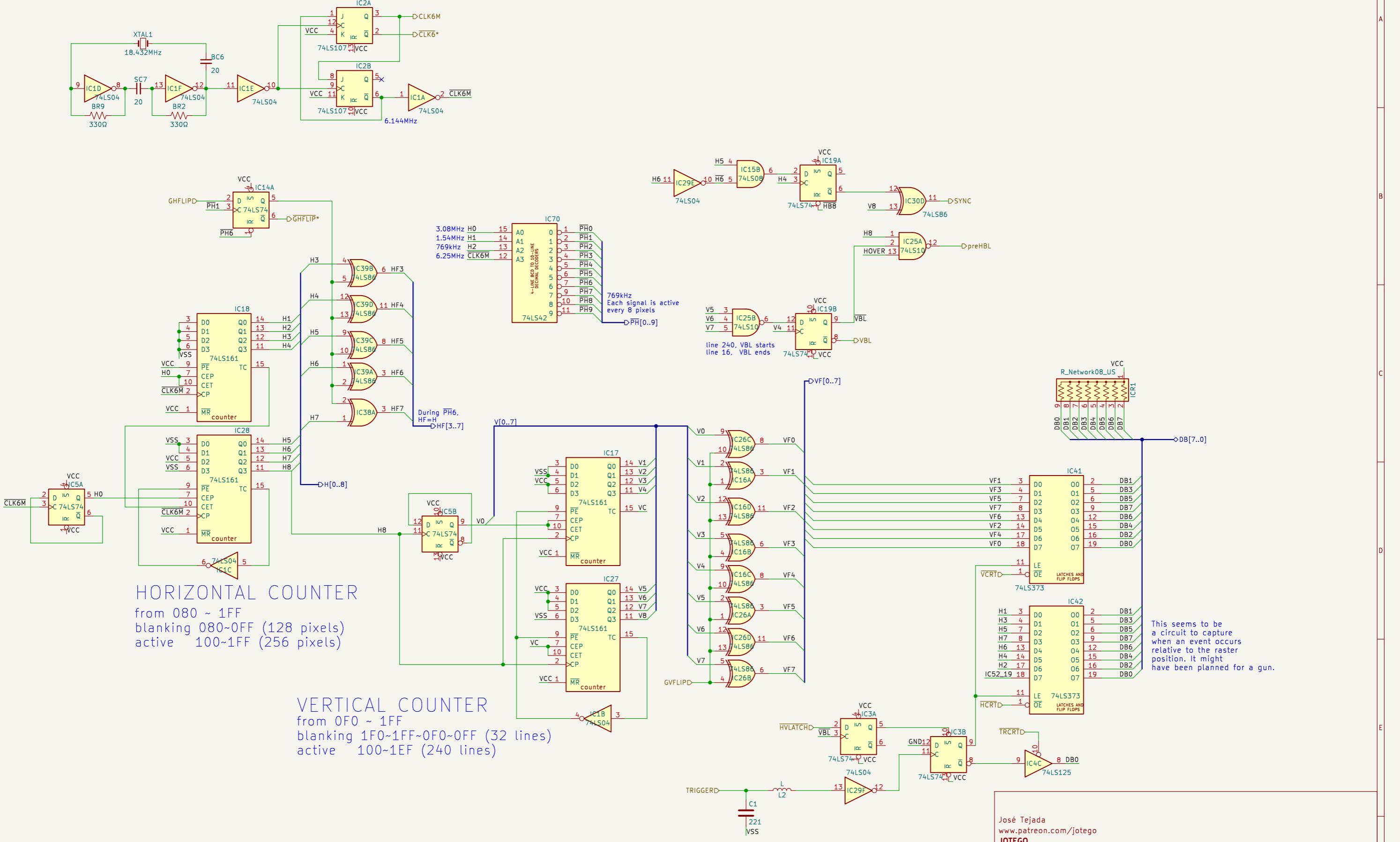
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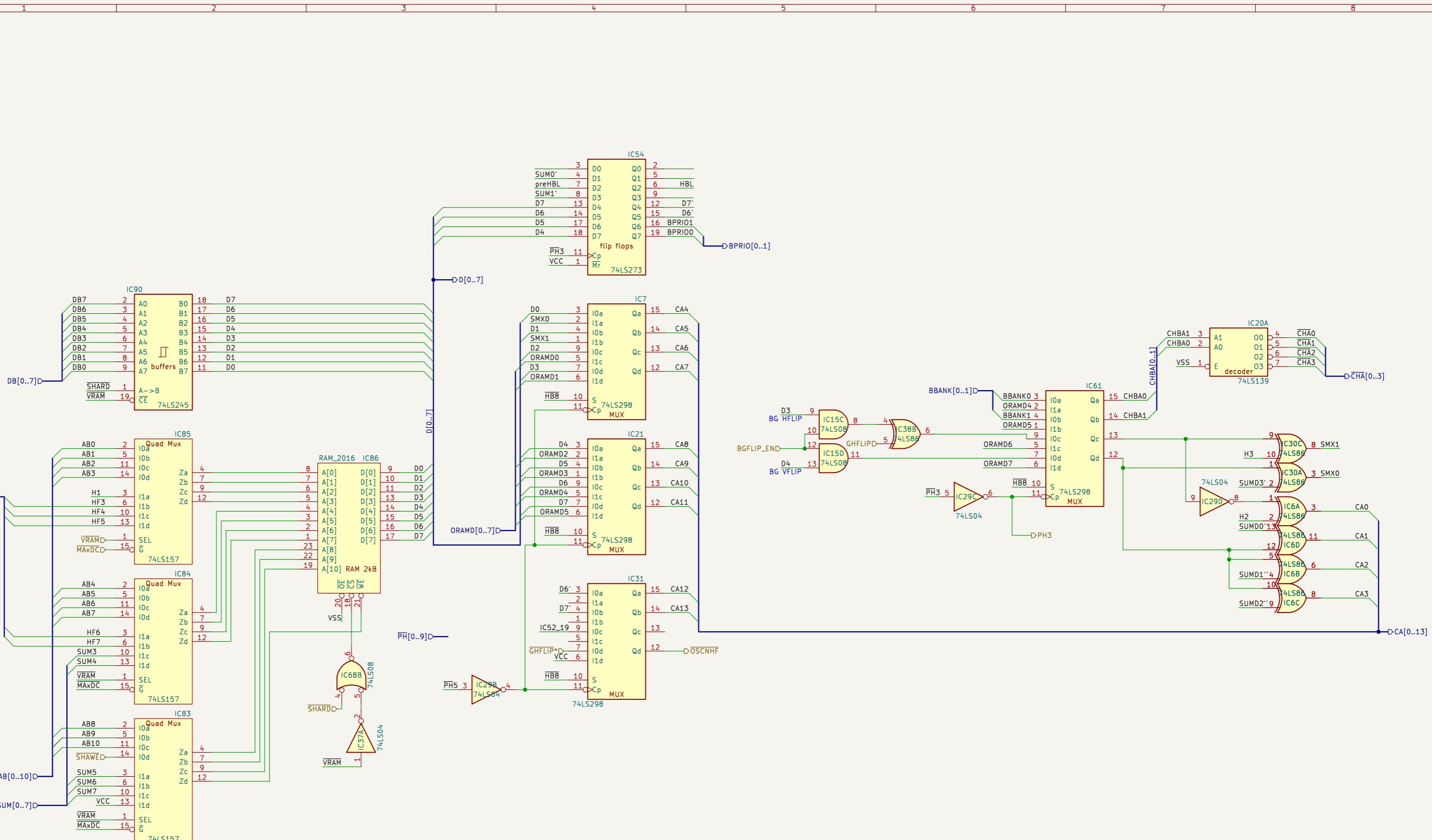
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KiCad E.D.A. 8.0.6

Rev: 10/20









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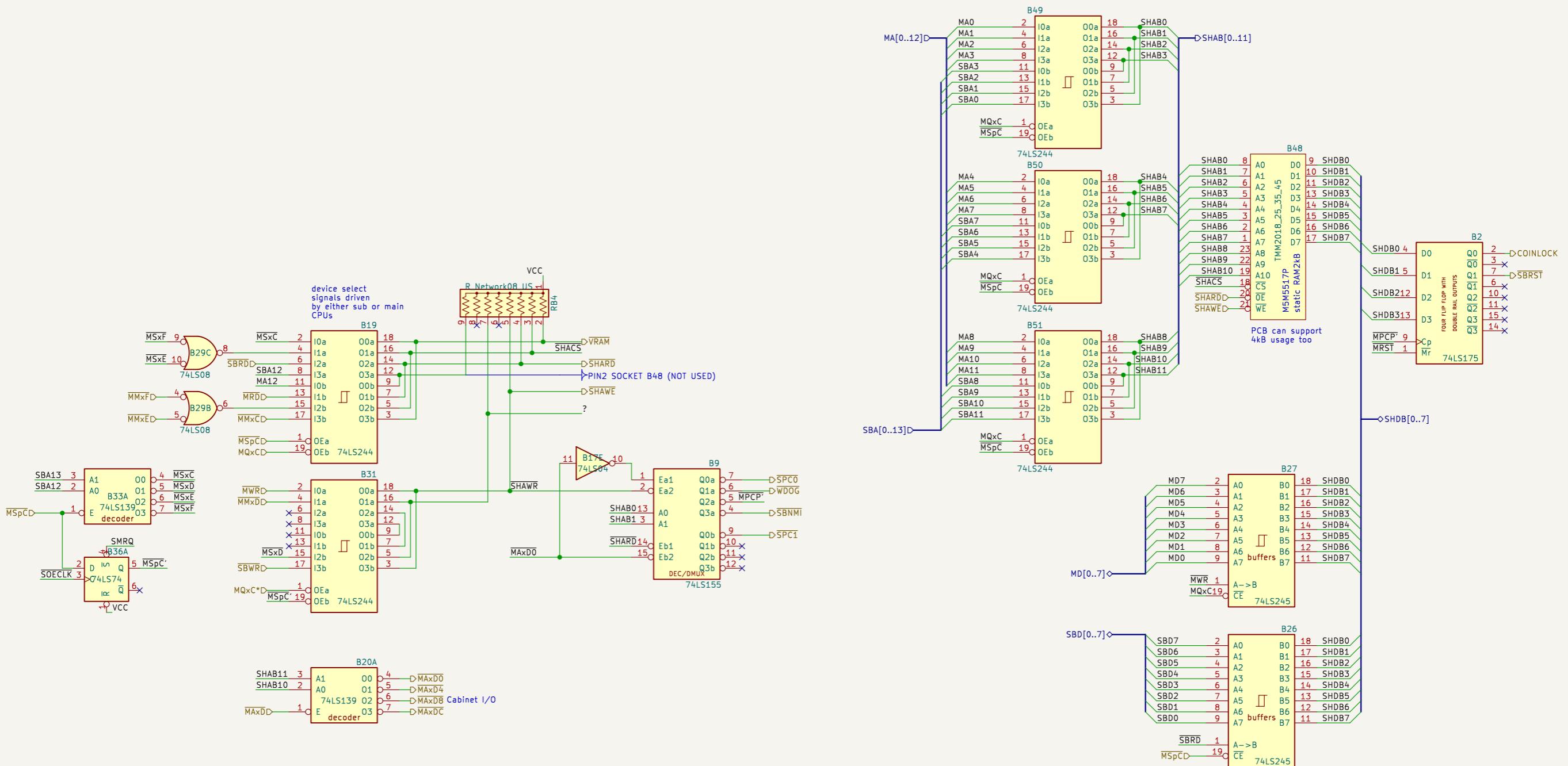
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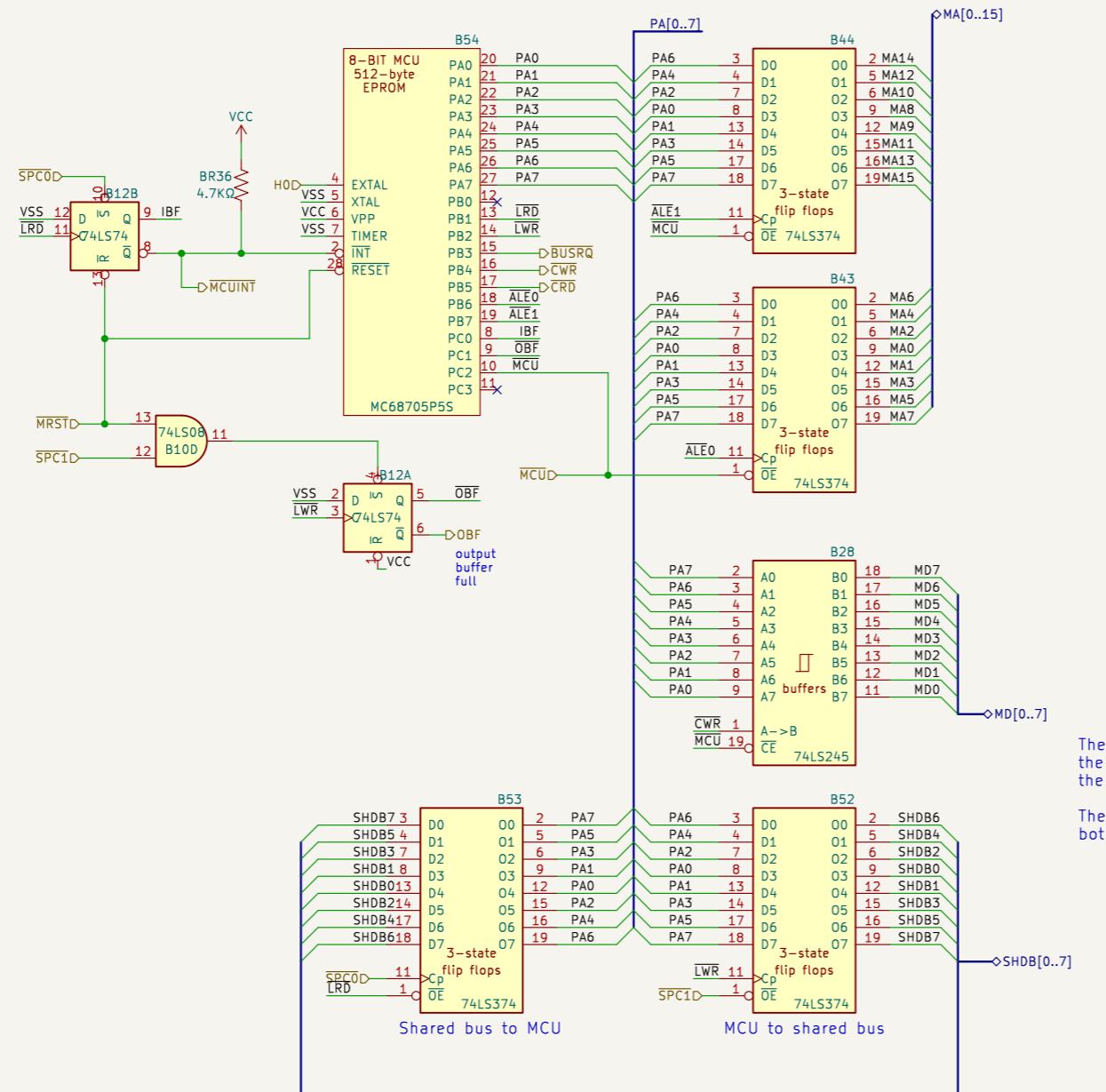
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File: vram.kicad_sch

Title: Background Video RAM

Size: A3 | Date: 2024-11-16
KiCad E.D.A. 8.0.6

Rev:
Id: 13/20





The MCU can take control of the share bus by requesting the main CPU bus

The MCU can communicate with both CPUs via latches B52 and B53

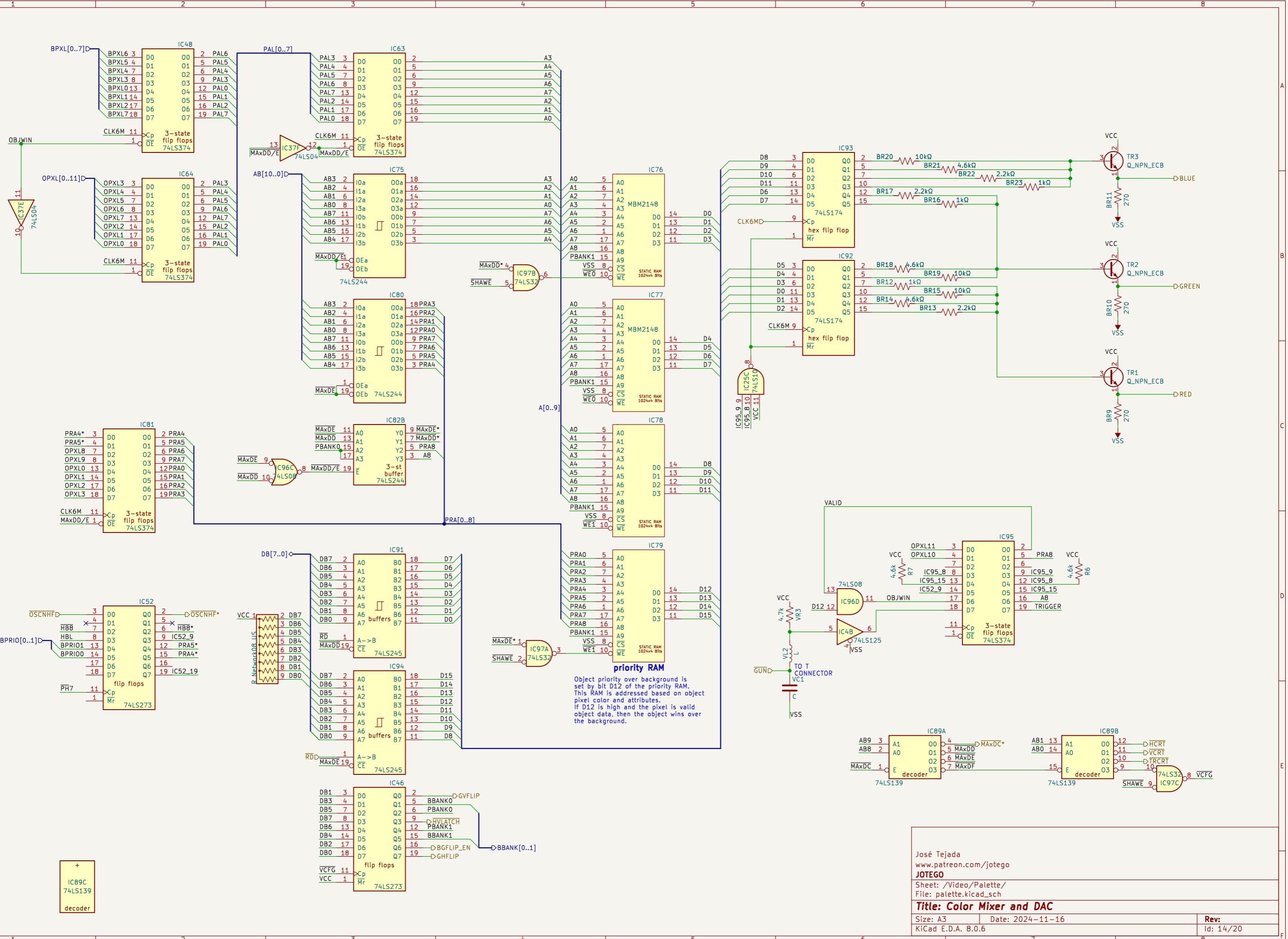
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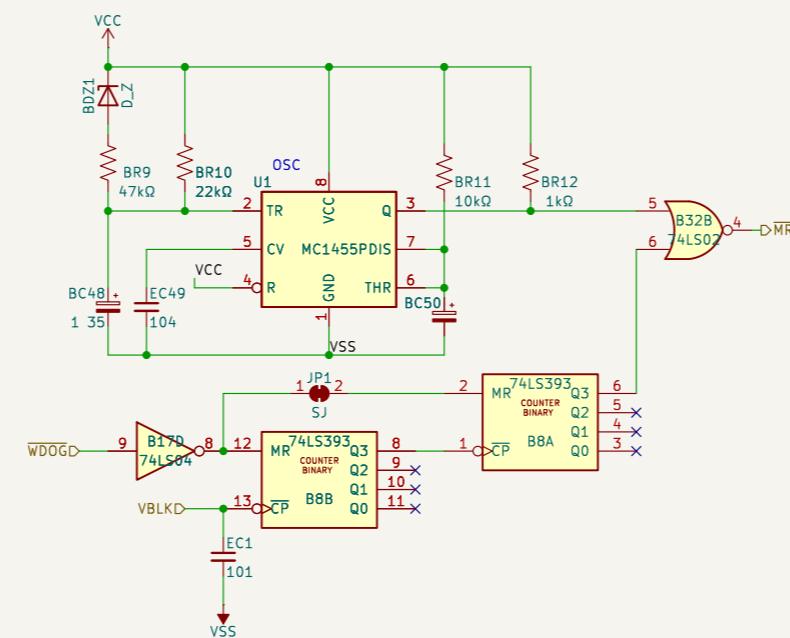
Sheet: /Main/MCU/
File: mcu.kicad_sch

Title: MCU

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.6

Rev: Id: 14/20





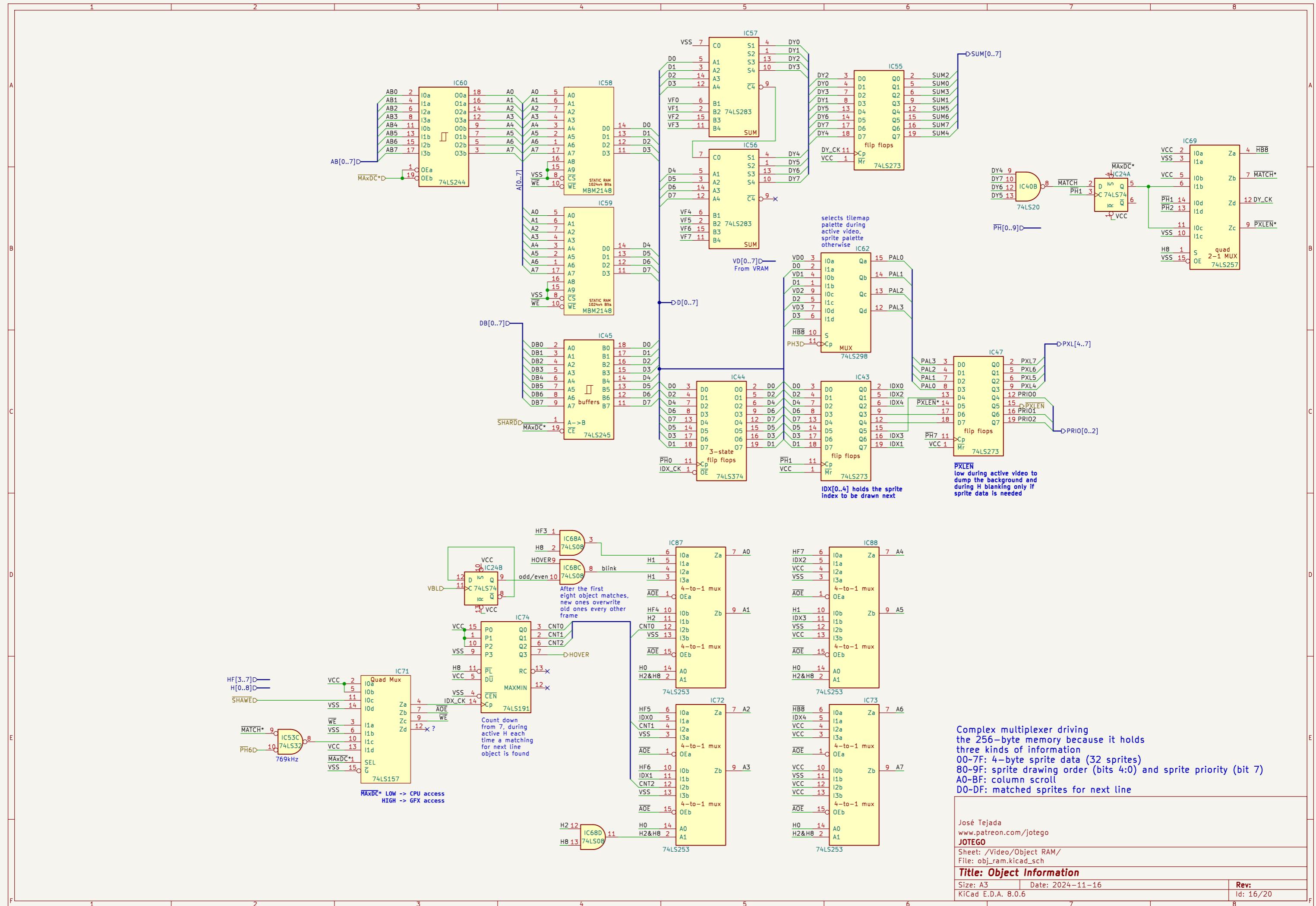
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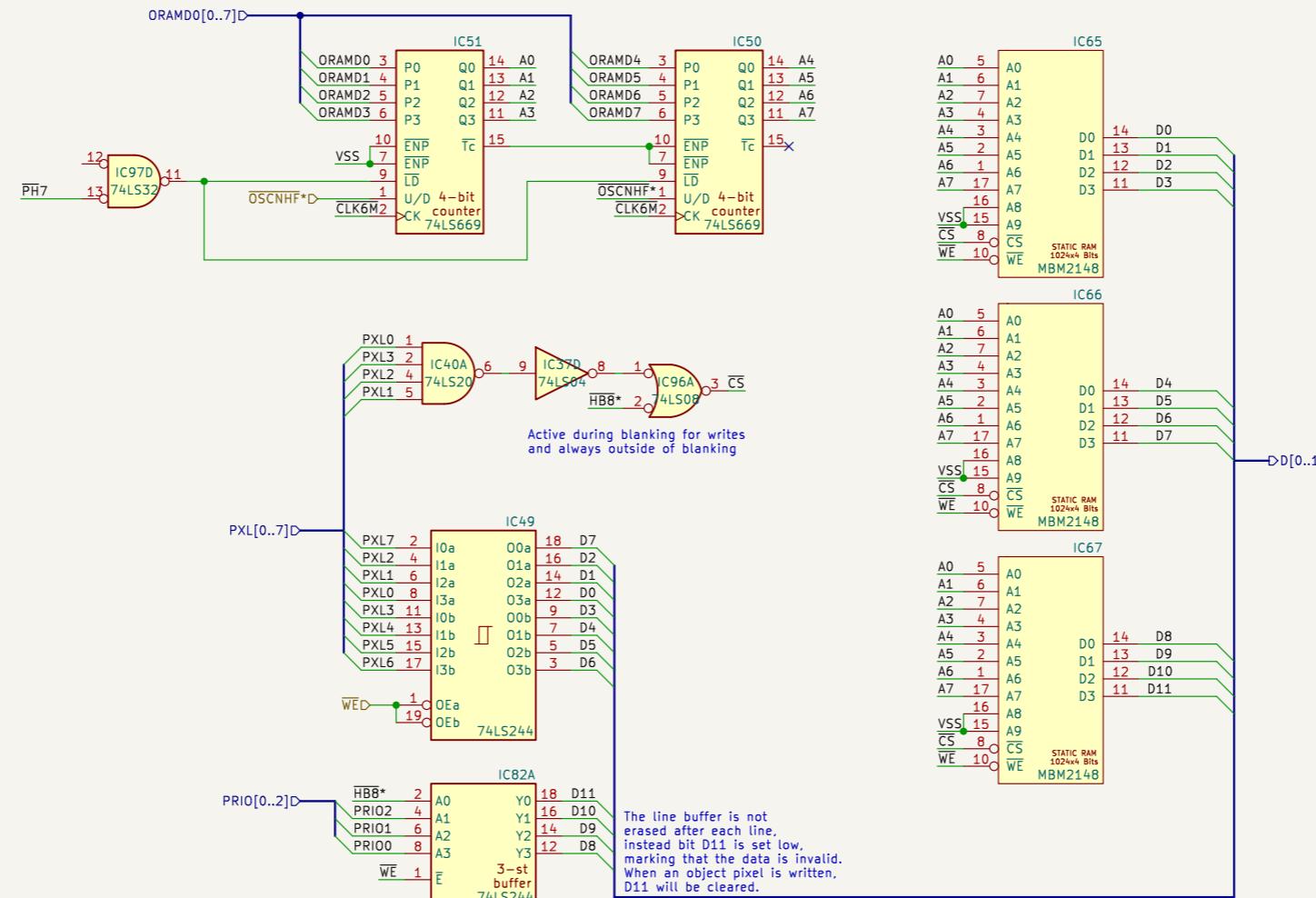
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KiCad E.D.A. 8.0.6

Rev:
Id: 15/20



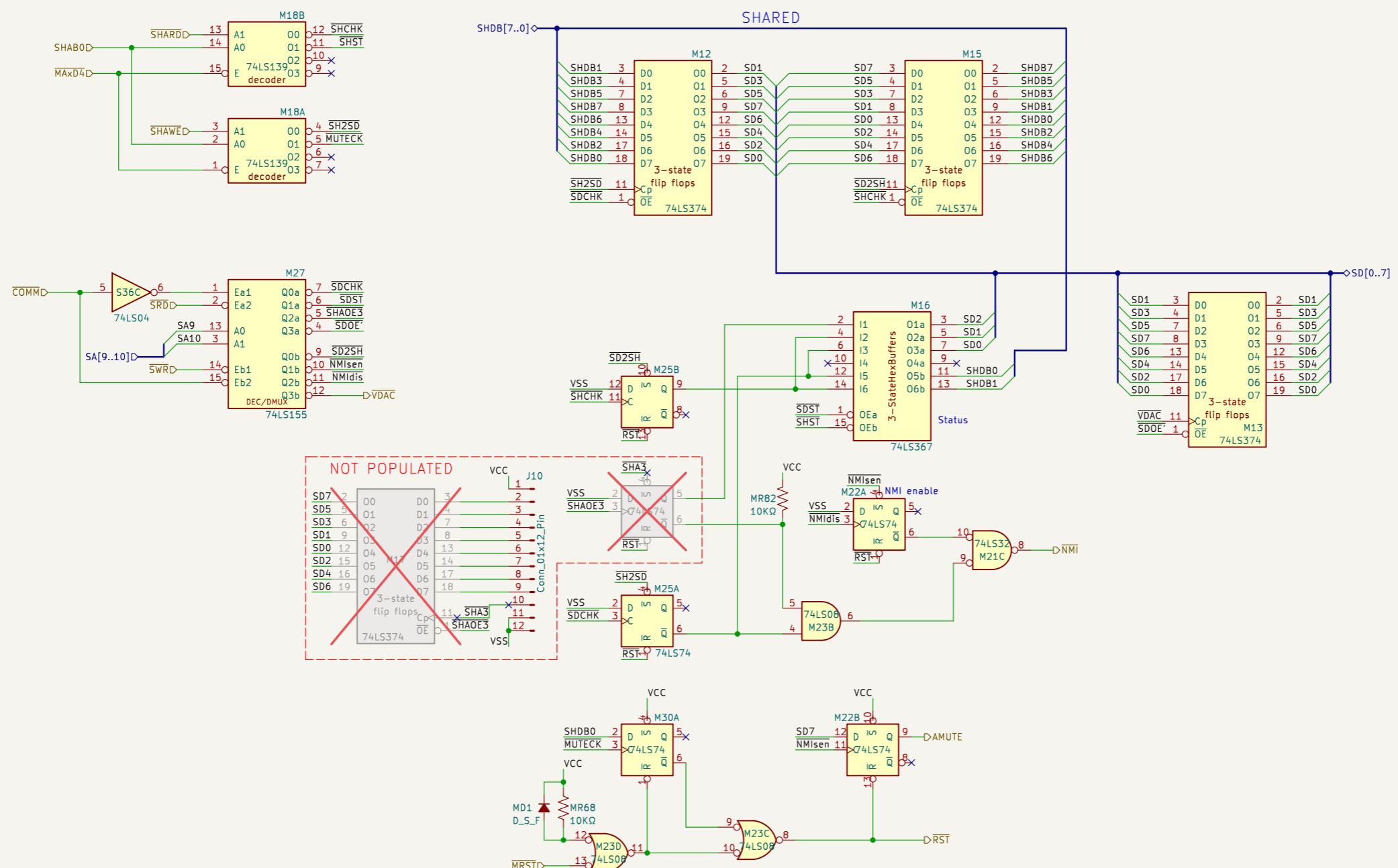


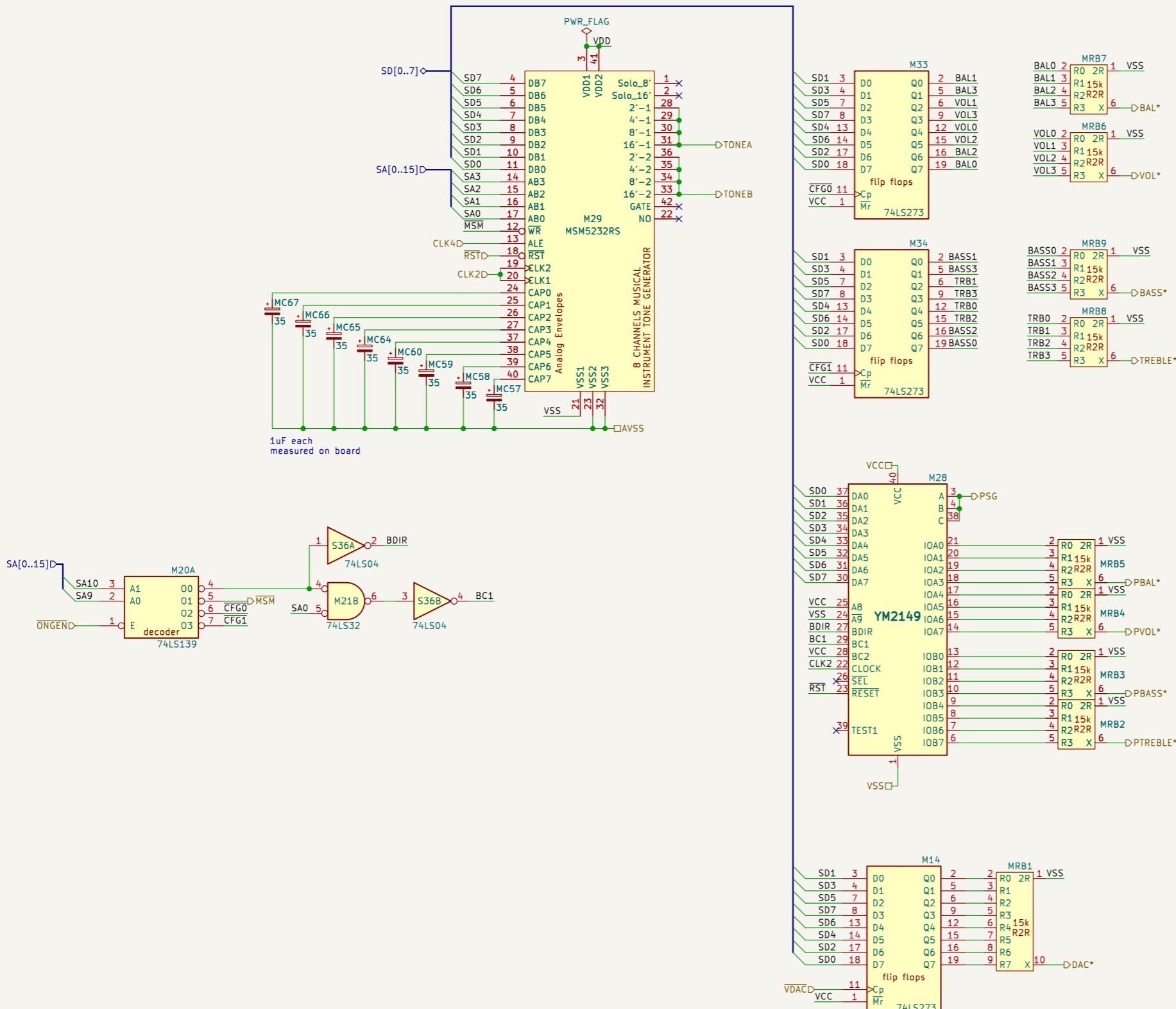
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Sheet: /Video/Line buffer/
File: linebuf.kicad_sch

Title: Single Line Video Buffer

Size: A3	Date: 2024-11-16
KiCad E.D.A. 8.0.6	Rev: 17/20





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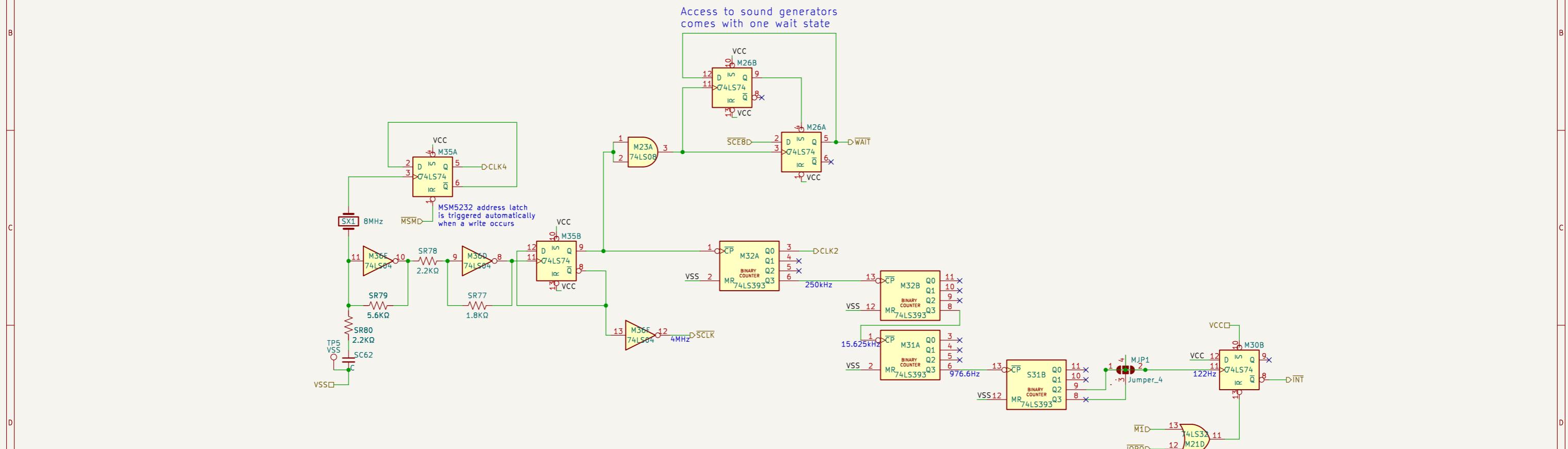
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Sheet: /Sound/Sound Generators/
File: ongen.kicad_sch

Title: Sound Generators

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.6

Rev:
Id: 19/20



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Sheet: /Sound/Clocks/
File: snd_clocks.kicad_sch

Title: Sound Clock Generation

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.6

Rev: Id: 20/20