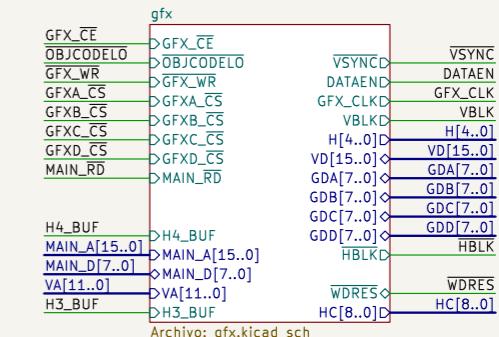
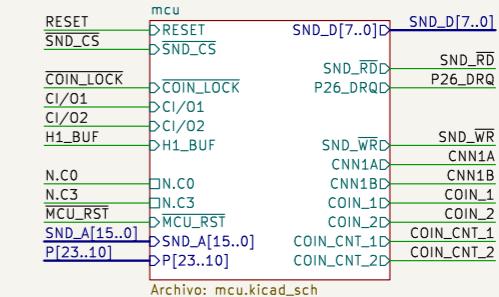
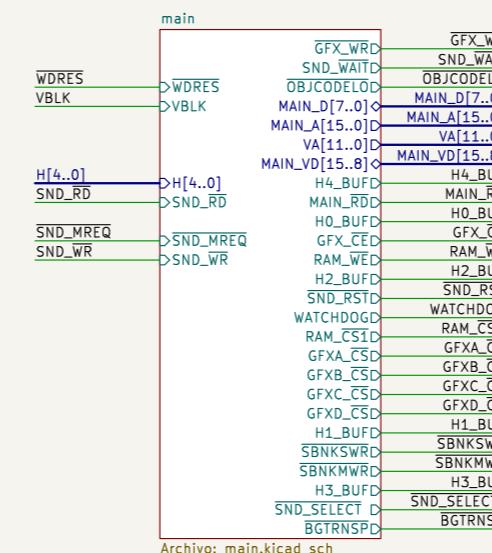
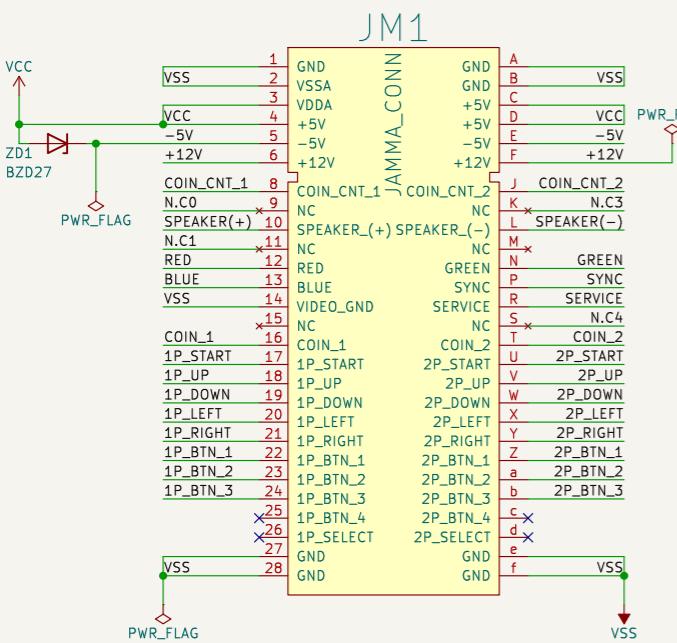
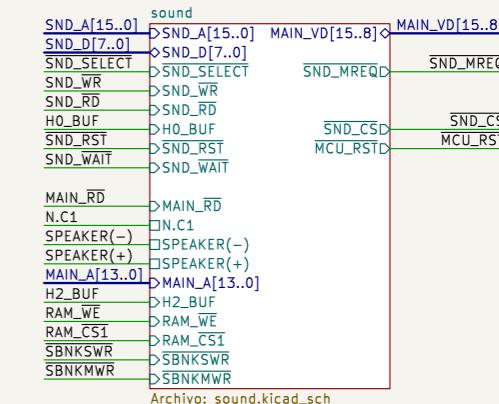
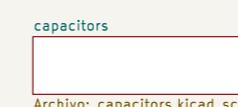
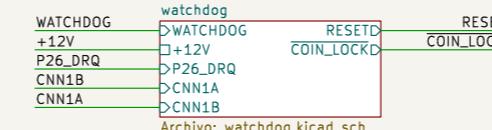
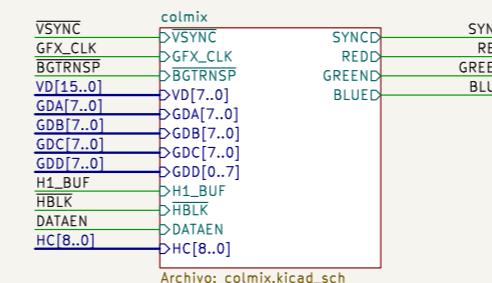
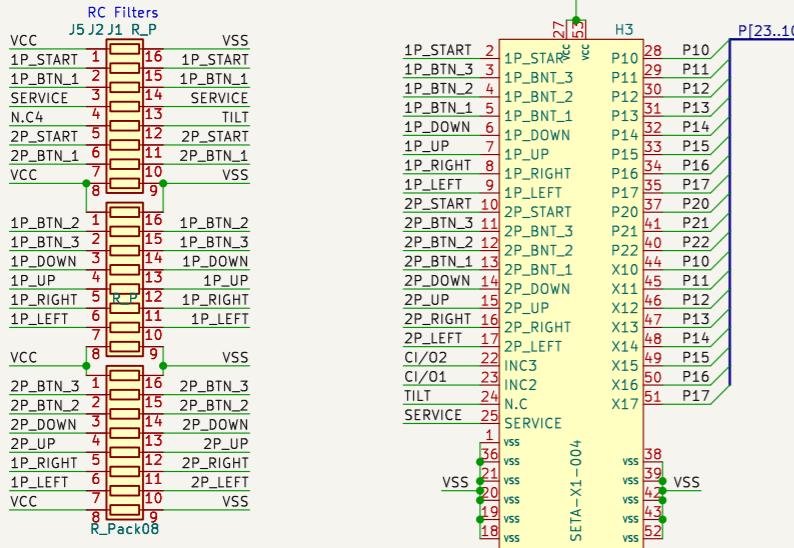


A



CABINET I/O



JOTEGO

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Title: EXTERMINATIONSize: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cl 7.0.10-7.0.10-ubuntu20.04.1Rev: José Tejada G.
Id: 1/10

1 2 3 4 5 6 7 8

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D

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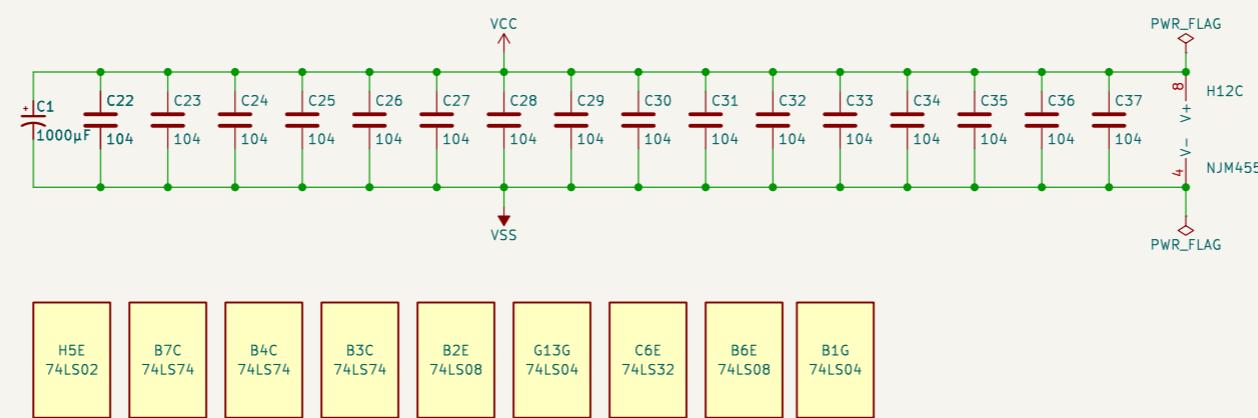
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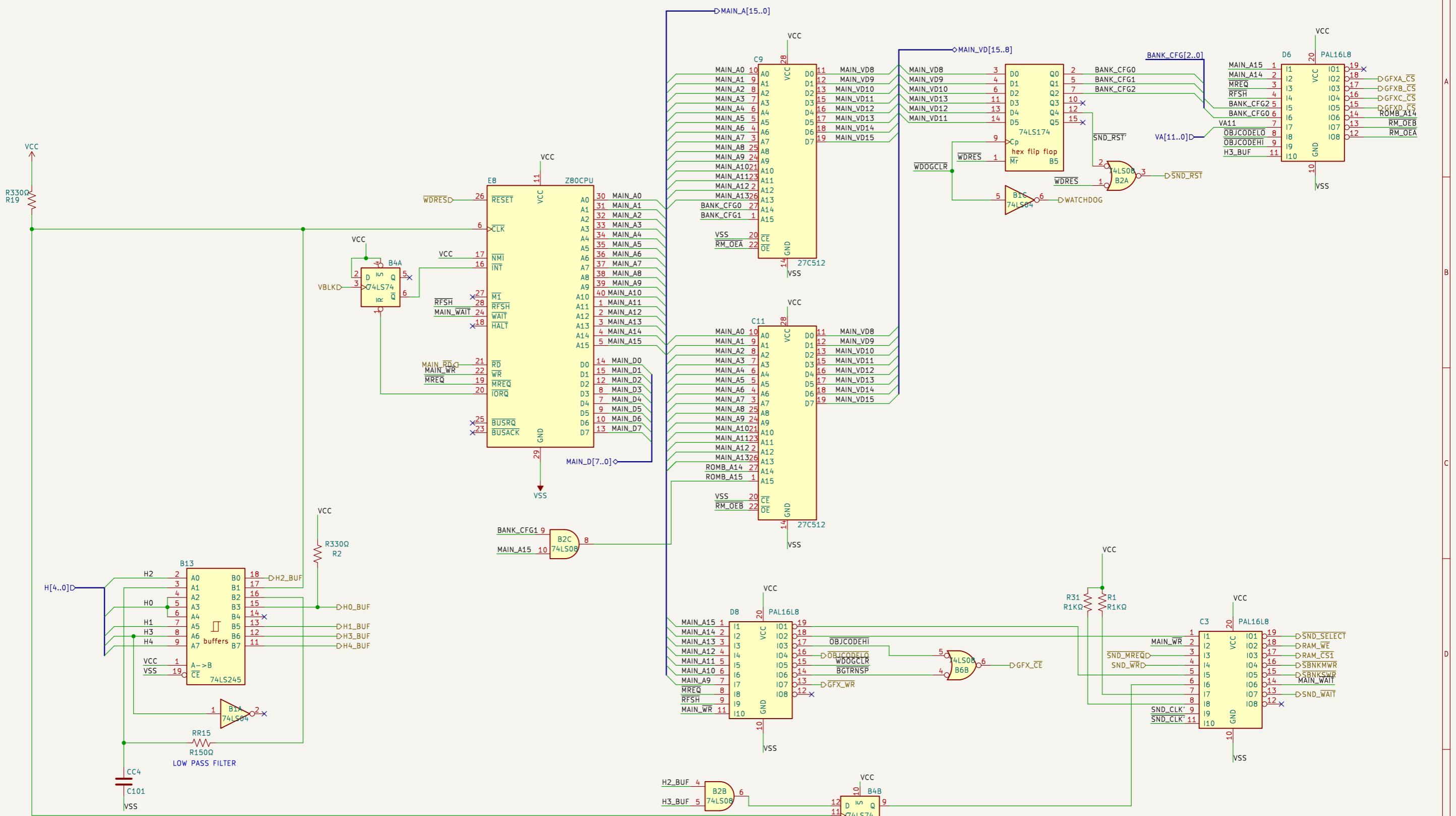
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Rev: José Tejada G. | Id: 2/10



1 2 3 4 5 6 7 8



JOTEGO

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KiCad E.D.A. kicad-cliv 7.0.10-7.0.10-ubuntu20.04.1		Id: 3/10



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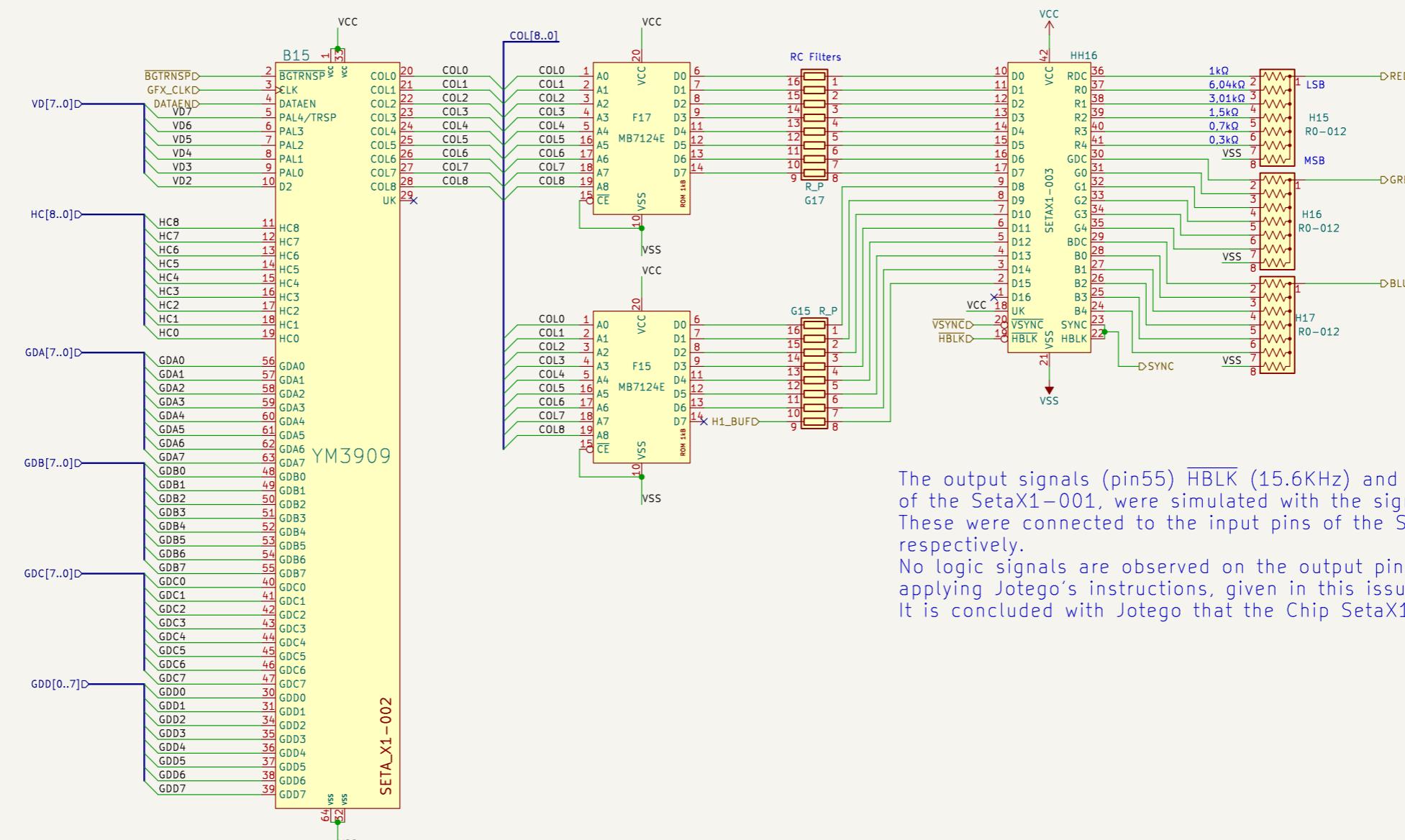
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The output signals (pin55) HBLK (15.6KHz) and (pin56) VSYNC (59.2 Hz) of the SetaX1-001, were simulated with the signal generator and an inverter. These were connected to the input pins of the Setax01-003 (pin 19 – 20), respectively.
 No logic signals are observed on the output pins (22–23) HBLK – SYNC, applying Jotego's instructions, given in this issue.
 It is concluded with Jotego that the Chip SetaX1-001 is broken.

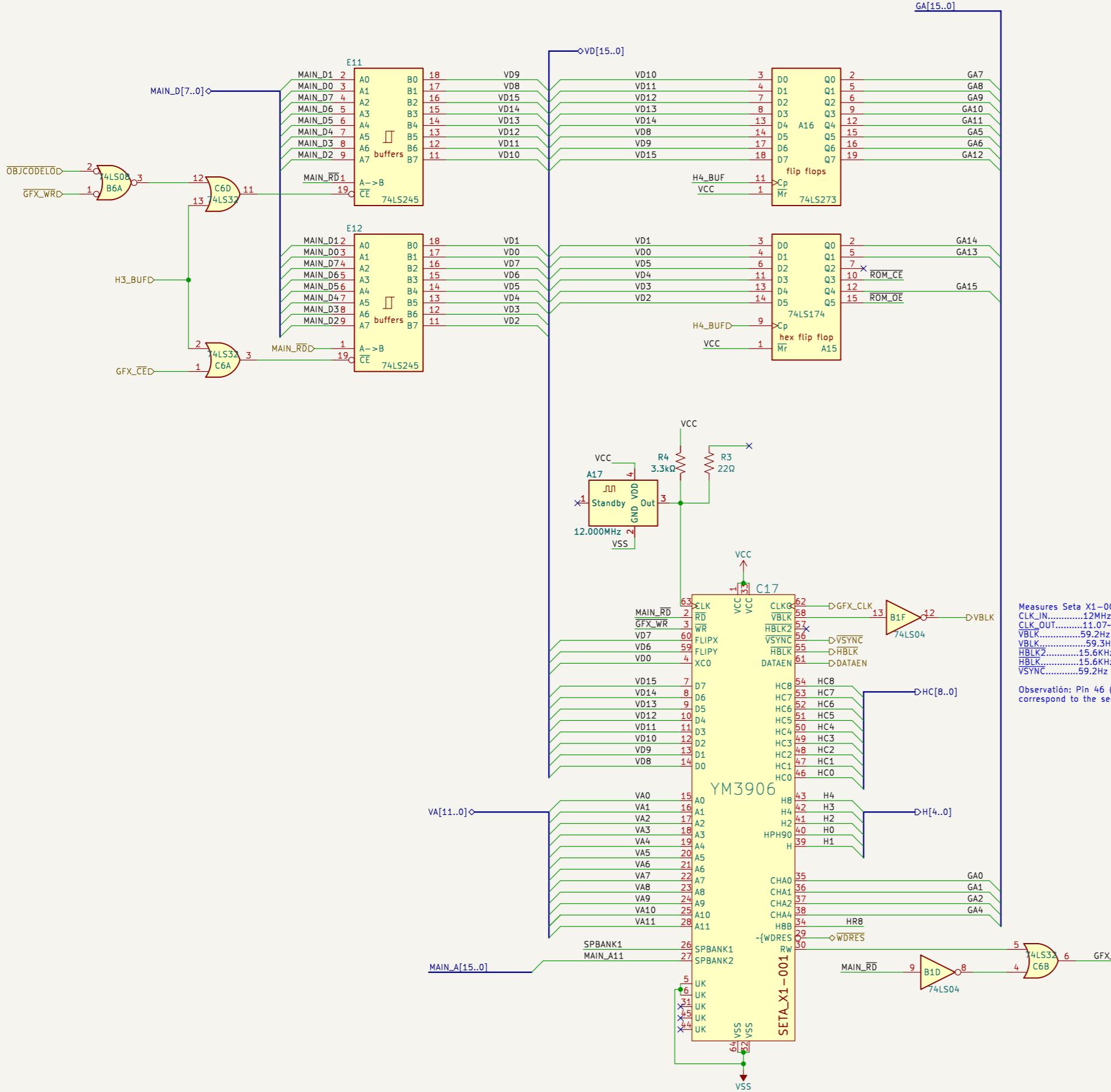

JOTEGO

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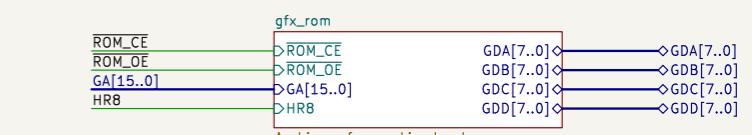
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 KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1

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Measures Seta X1-001:
 CLK_IN.....12MHz
 CLK_OUT.....11.07-12.01MHz
 VBLK.....59.2Hz
 VBLK.....59.3Hz
 HBLK2.....15.6kHz
 HBLK.....15.6kHz
 HBLK.....15.6kHz
 VSYNC.....59.2Hz

Observación: Pin 46 (HCO) does not correspond to the sequence of the HC[8..0] counter.



1 2 3 4 5 6 7 8

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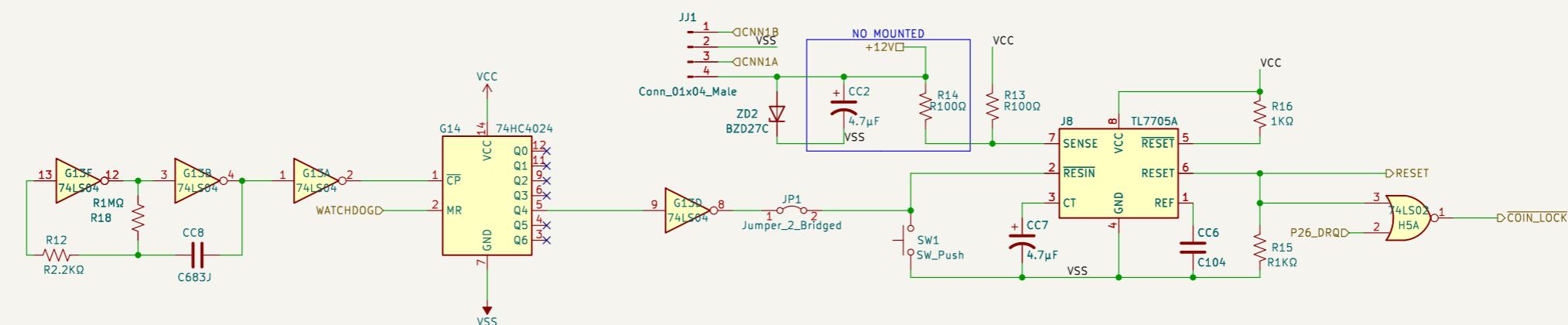
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JOTEGO

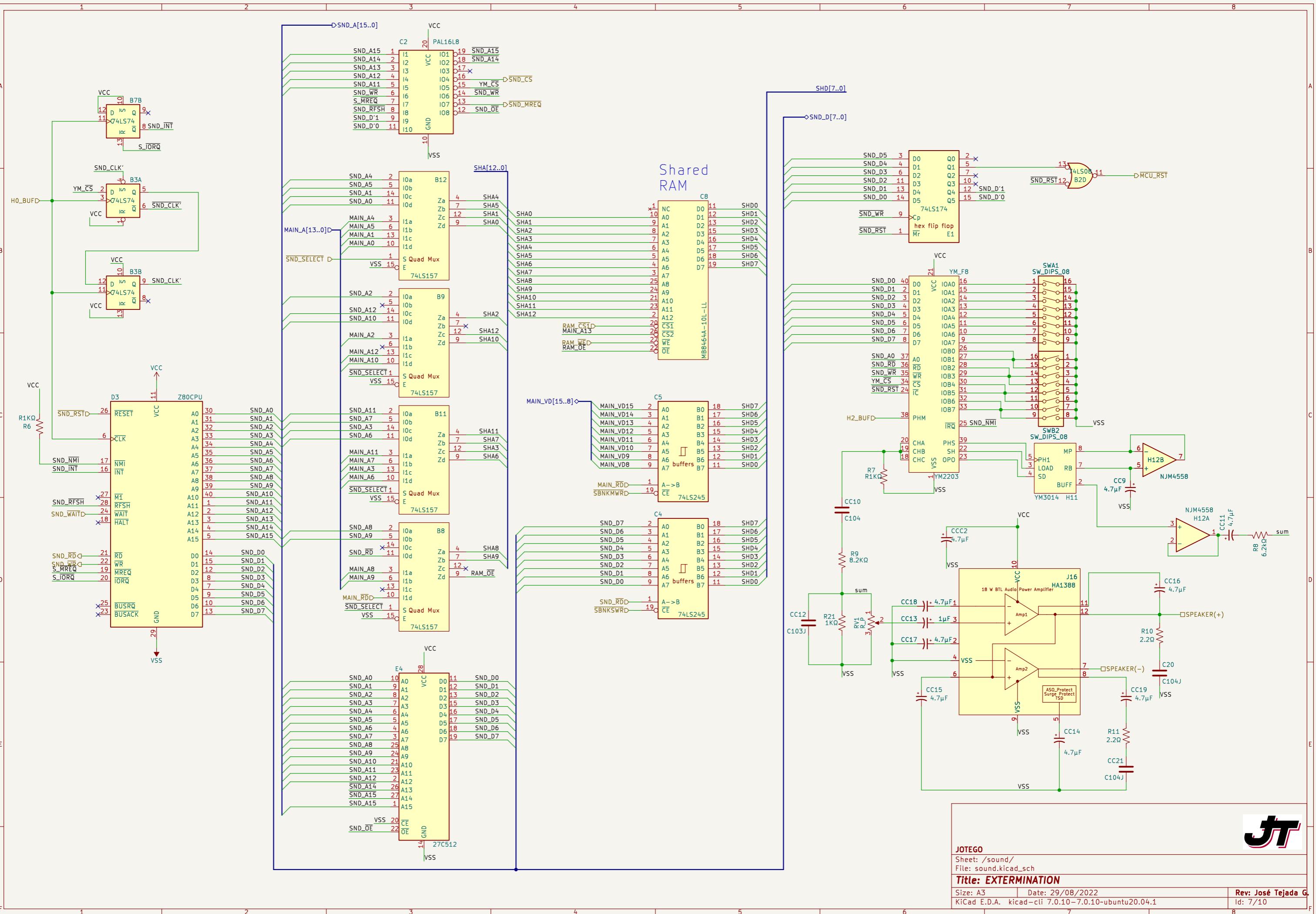
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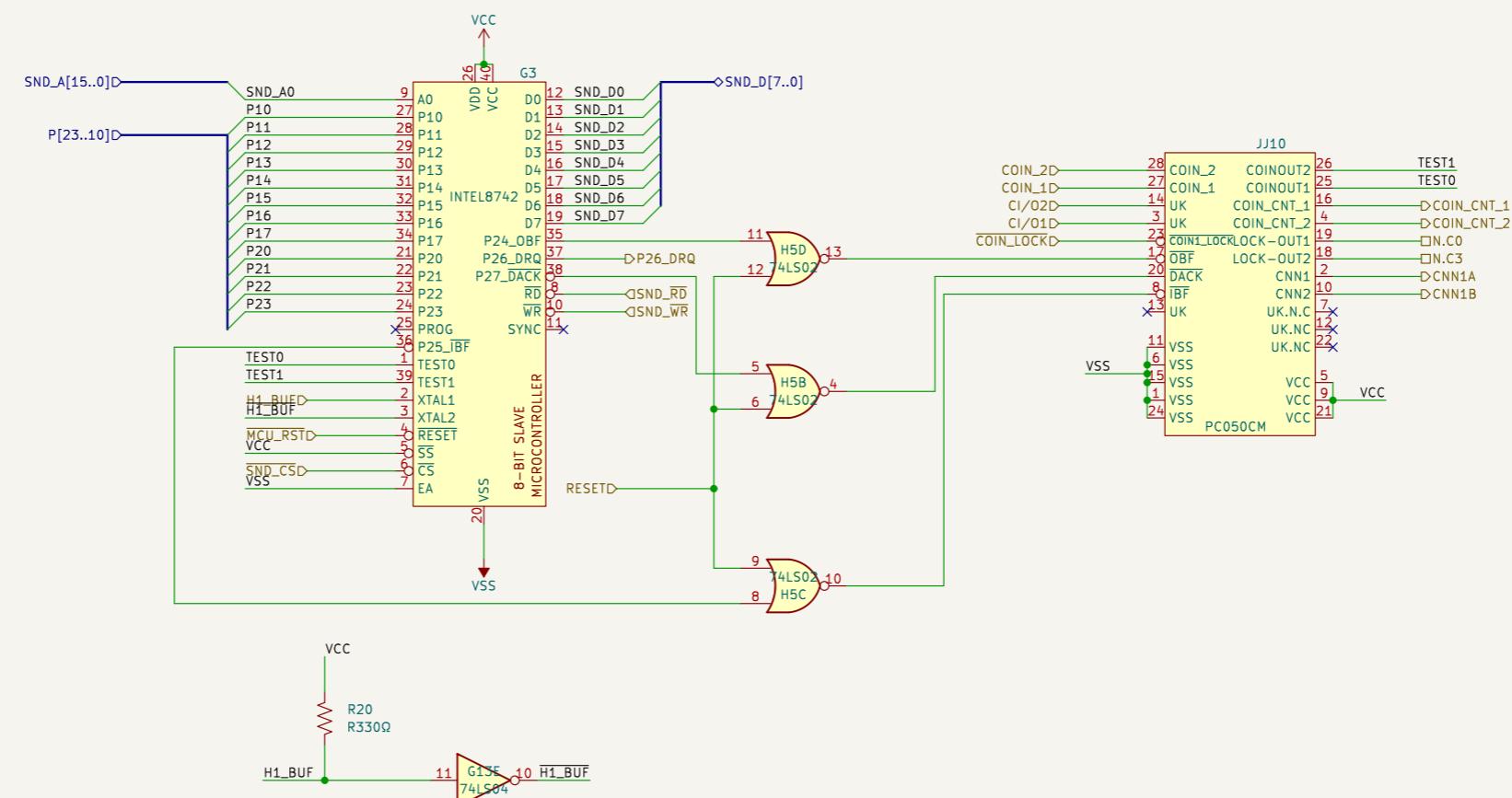
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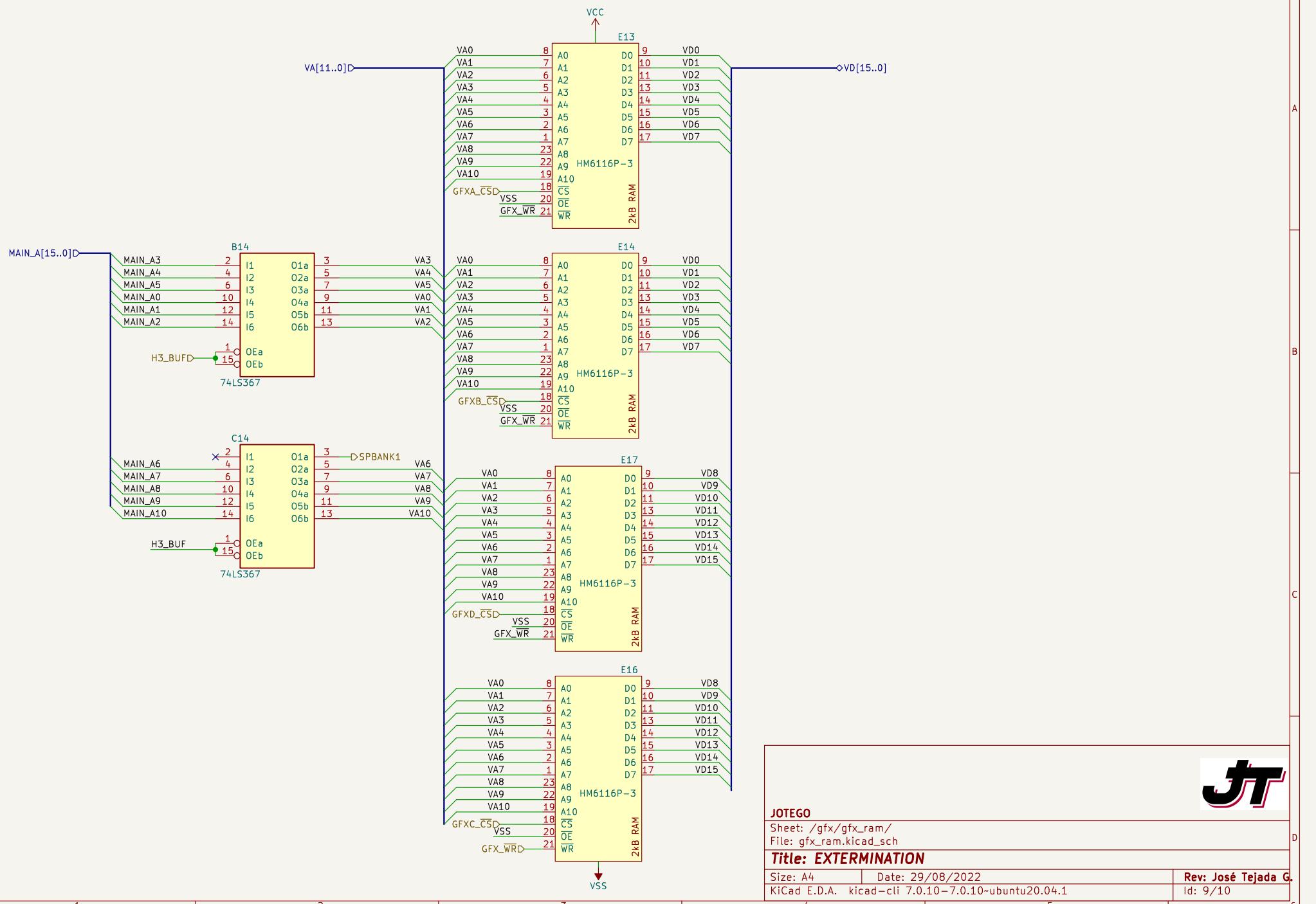
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JT

