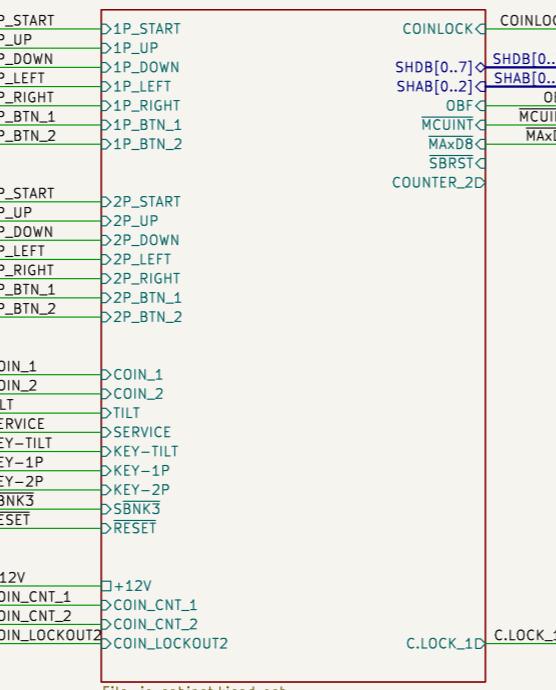
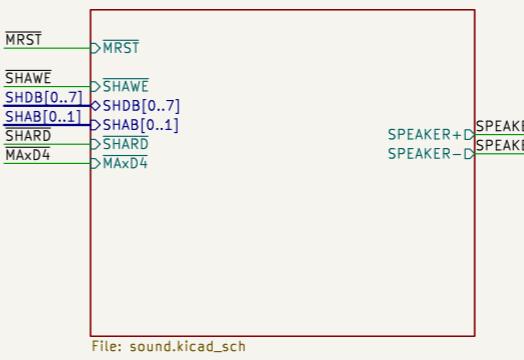


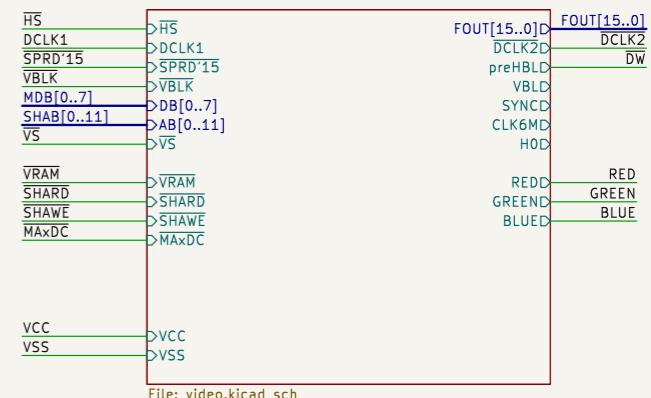
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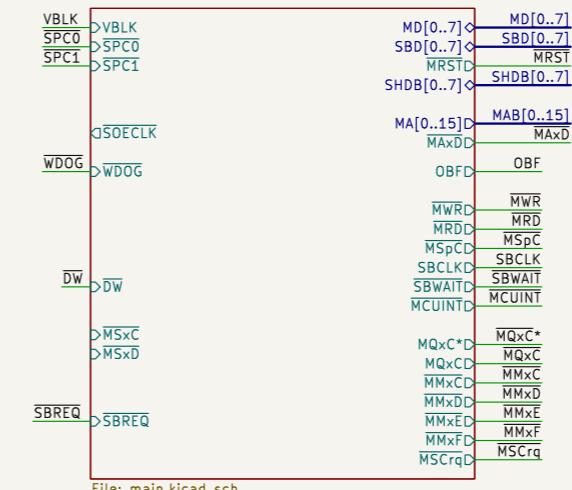
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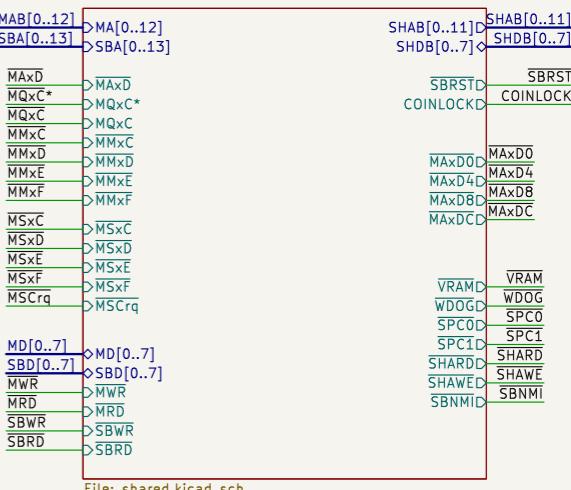
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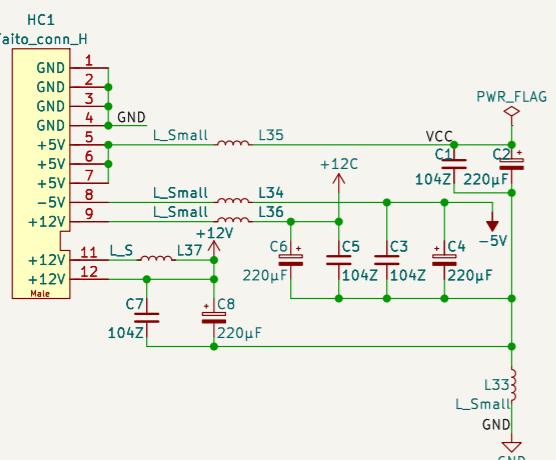
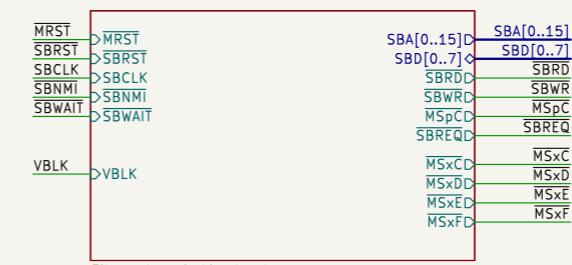
Main



Shared



Sub



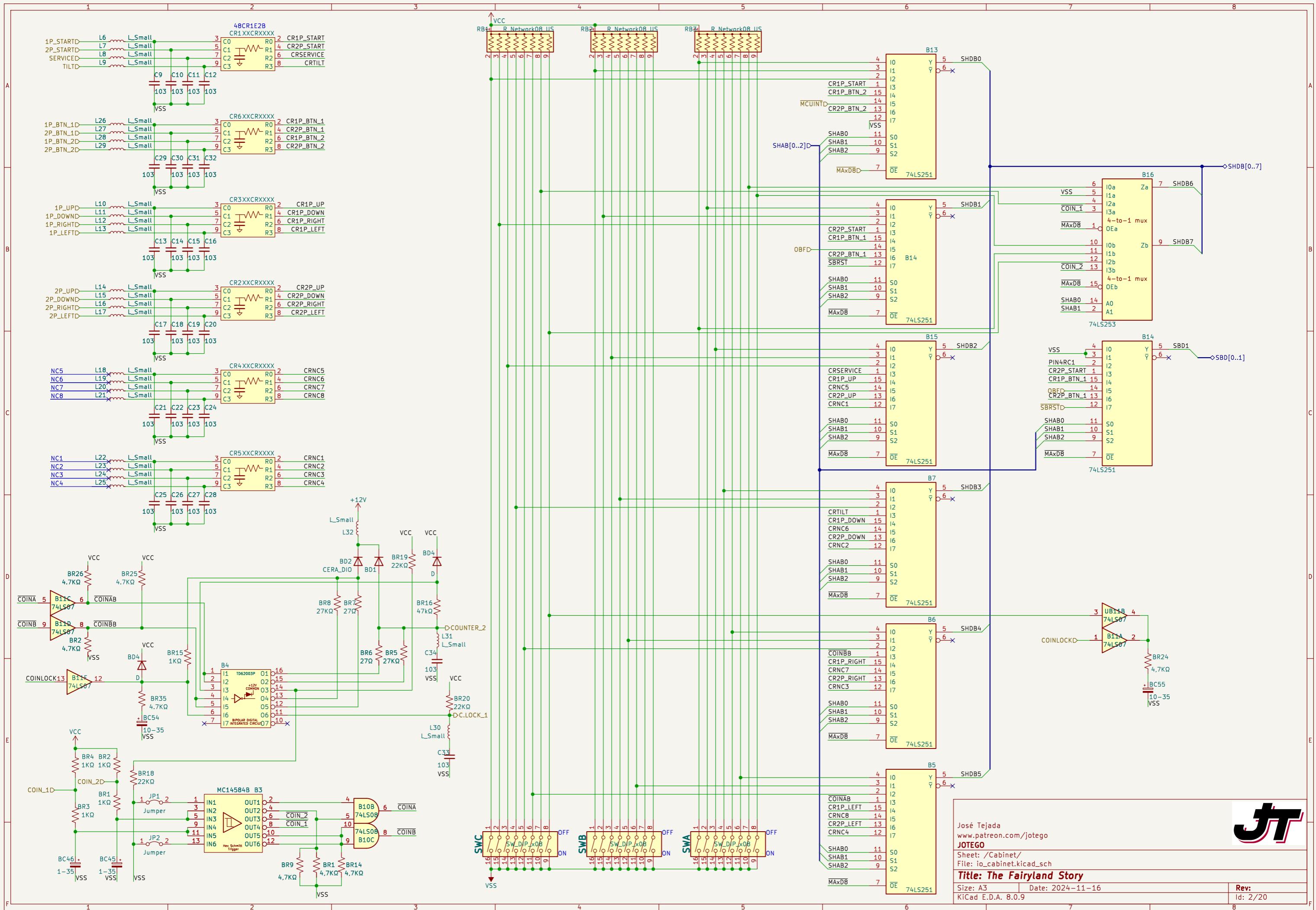
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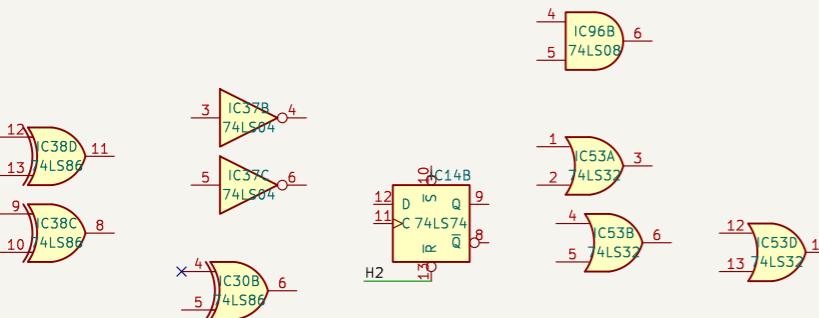
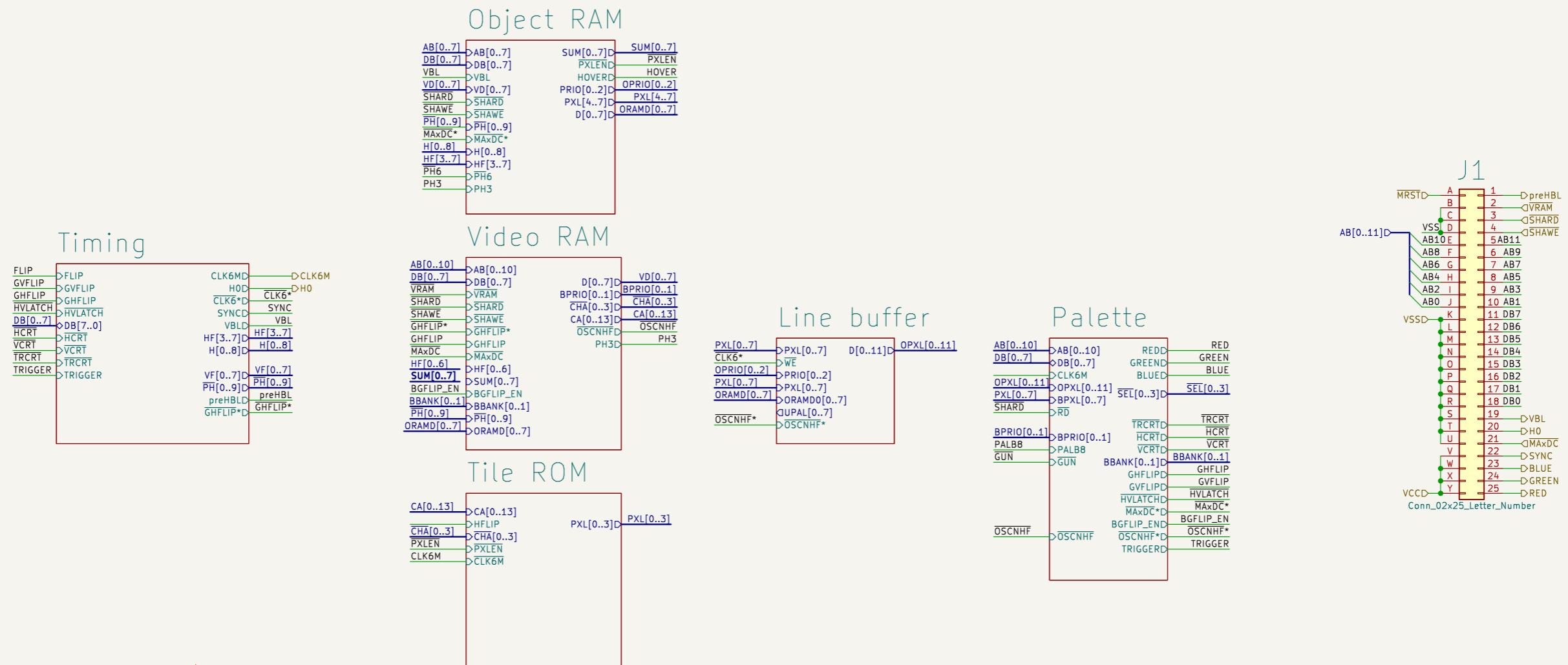
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Title: The Fairyland Story

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 1/20





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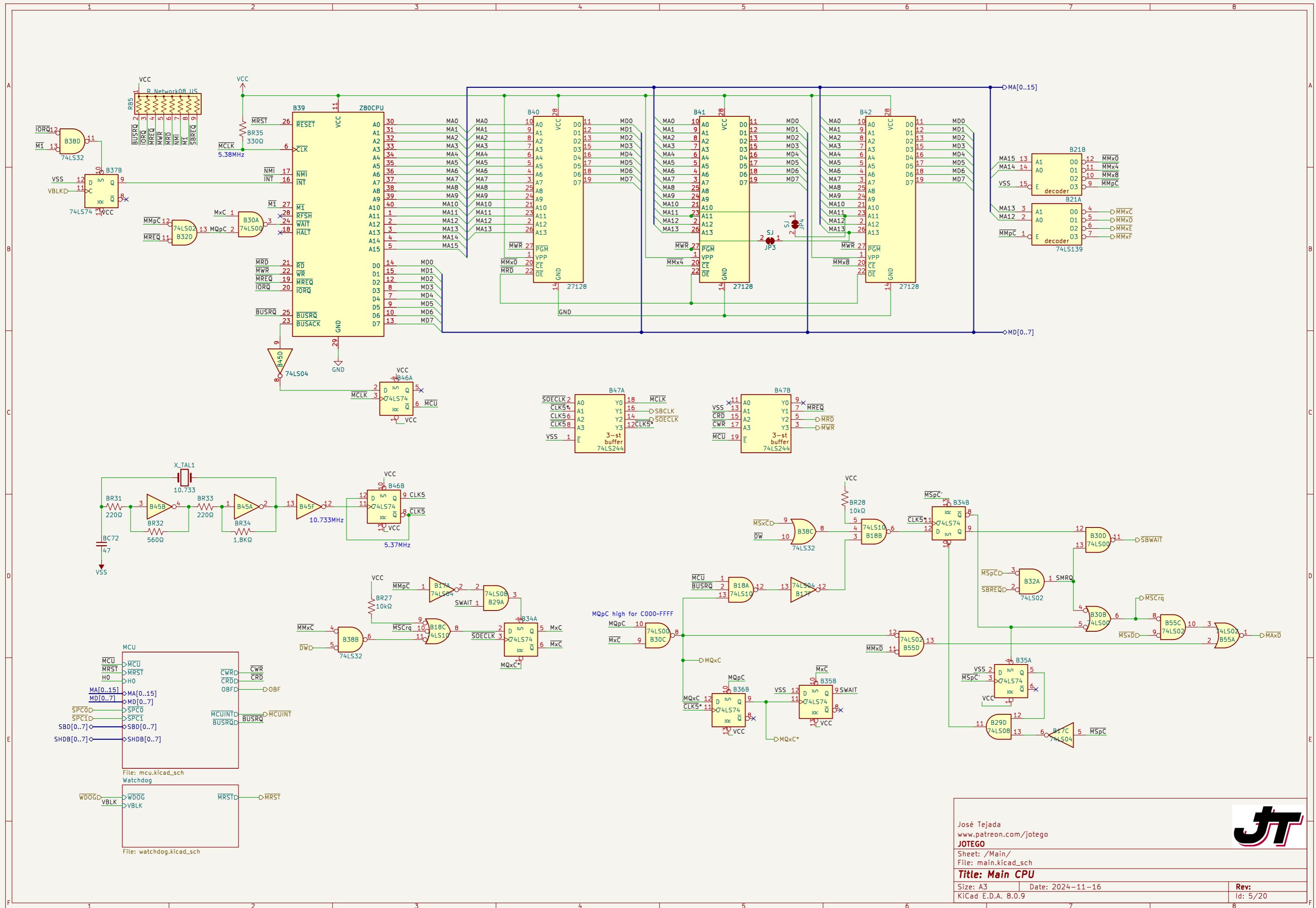
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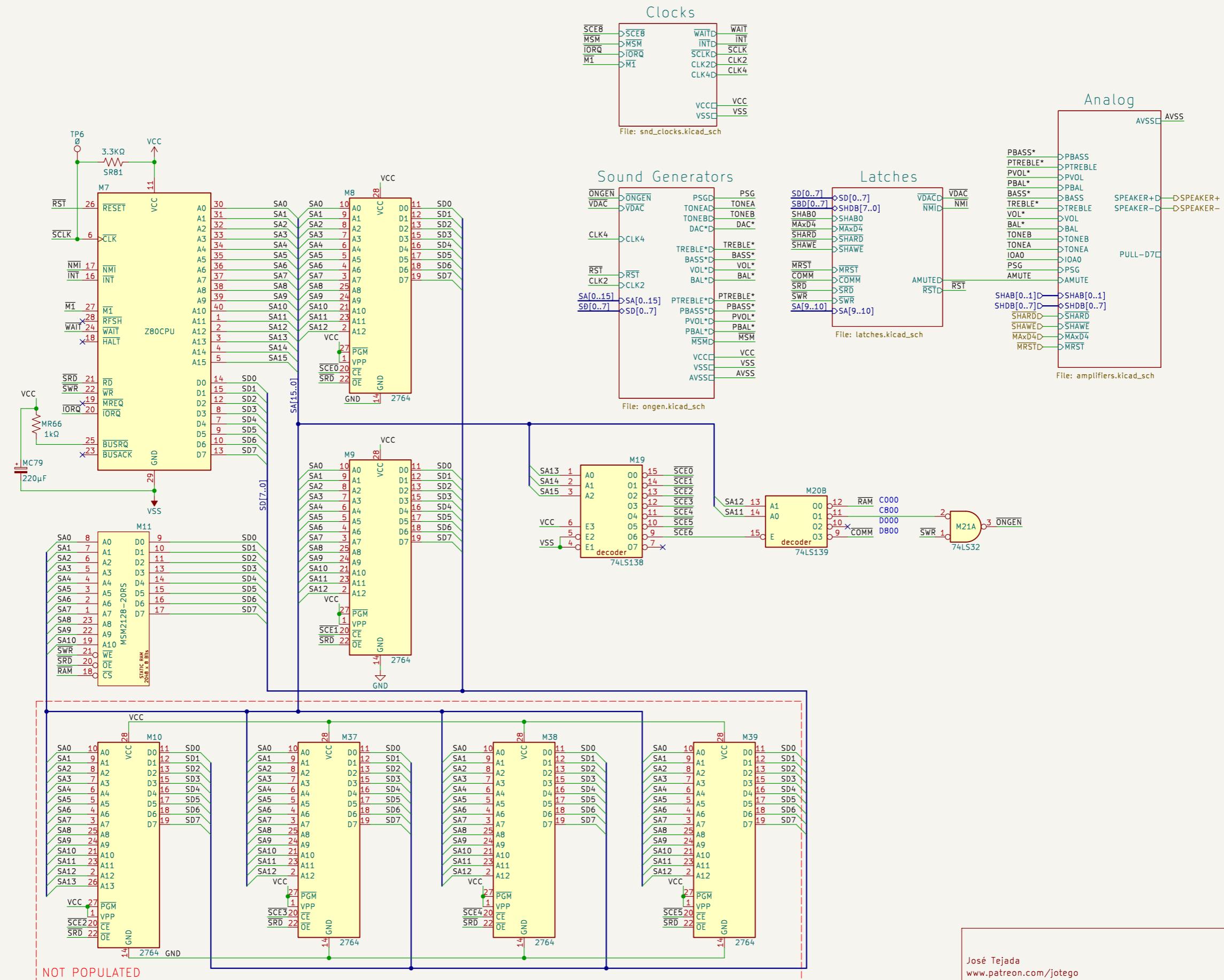
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Title: The Fairy

Size: A3 Date: 2

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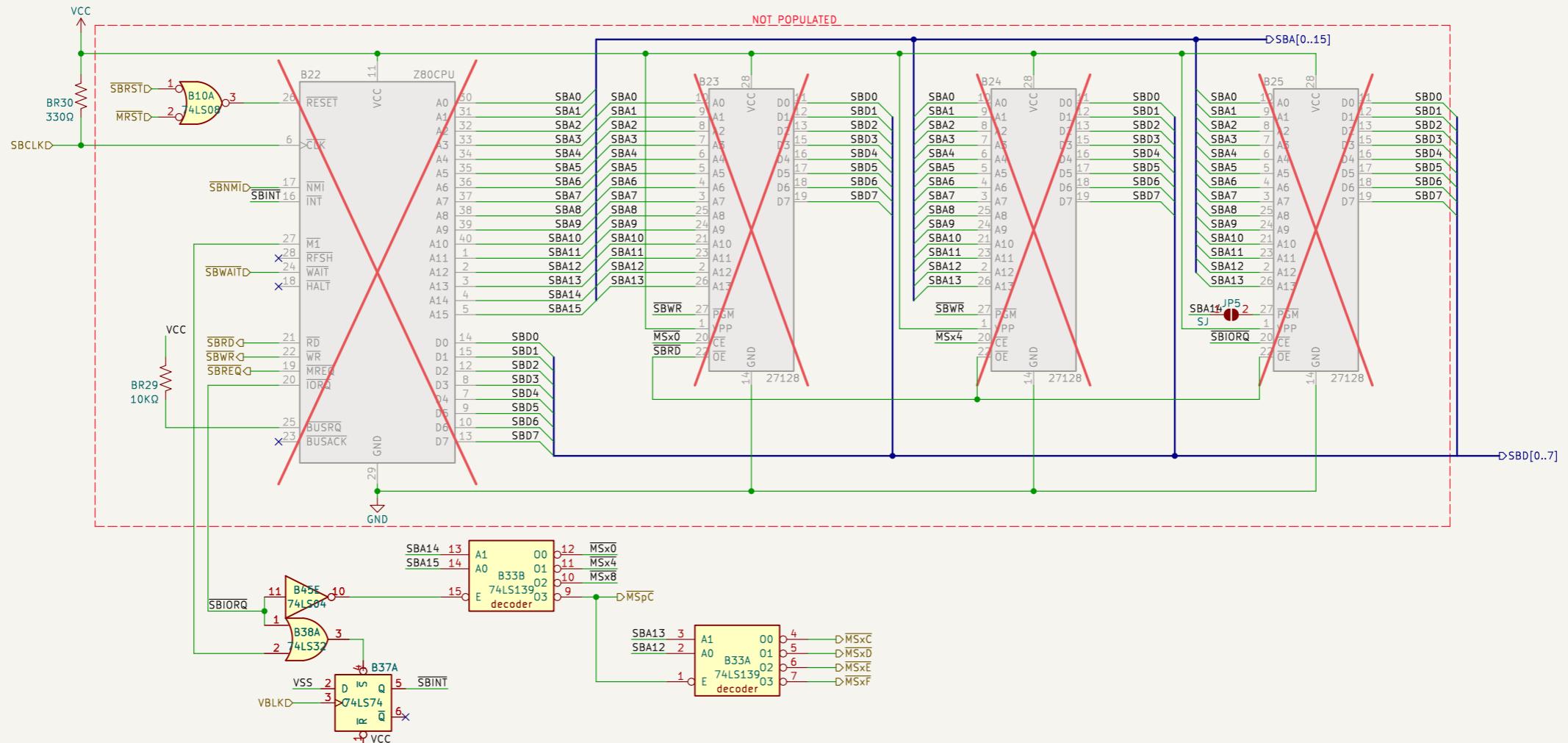
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Title: Sound Board

Size: A3 | Date: 2024-11-16
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Rev:
 Id: 7/20





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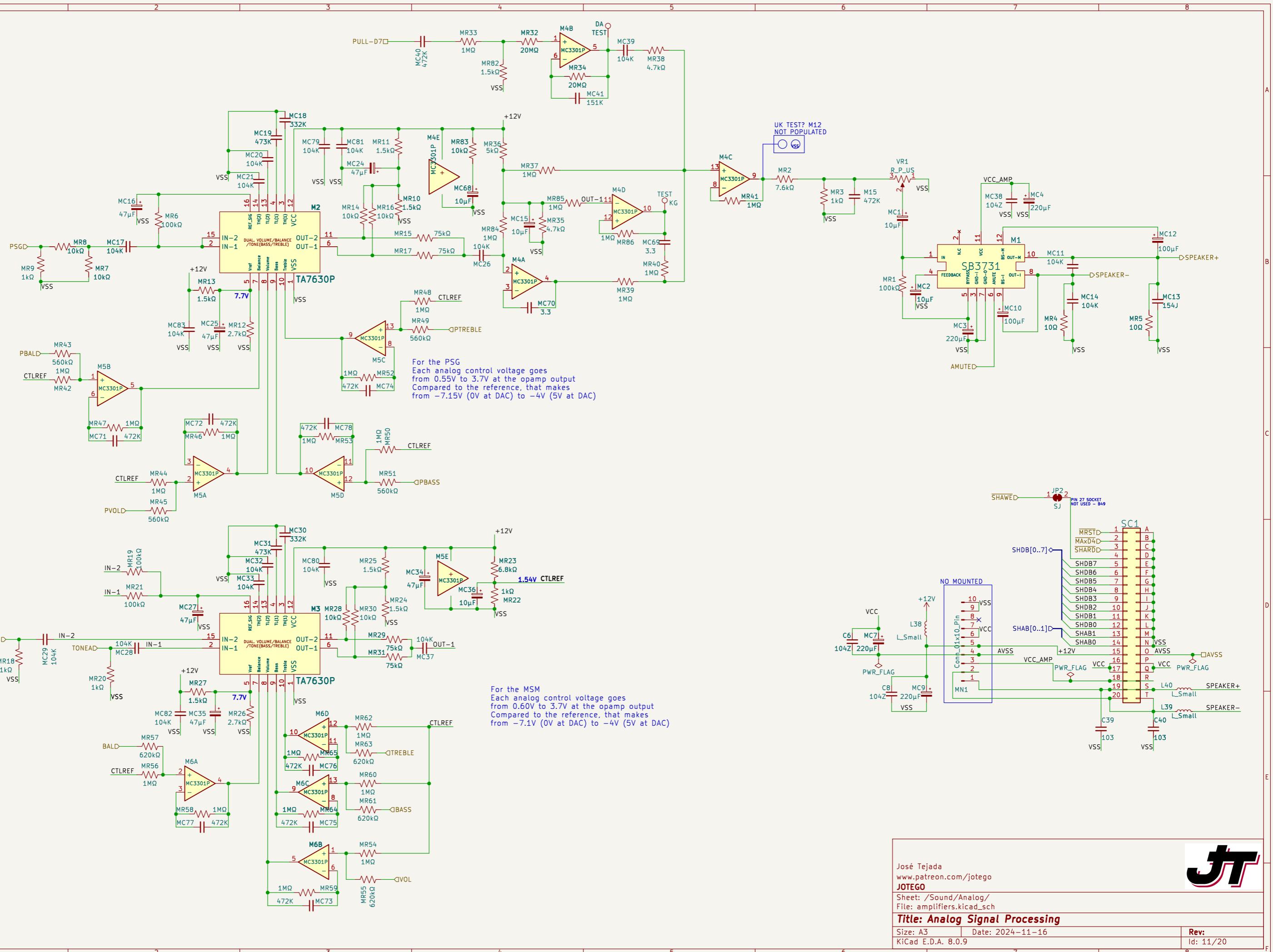
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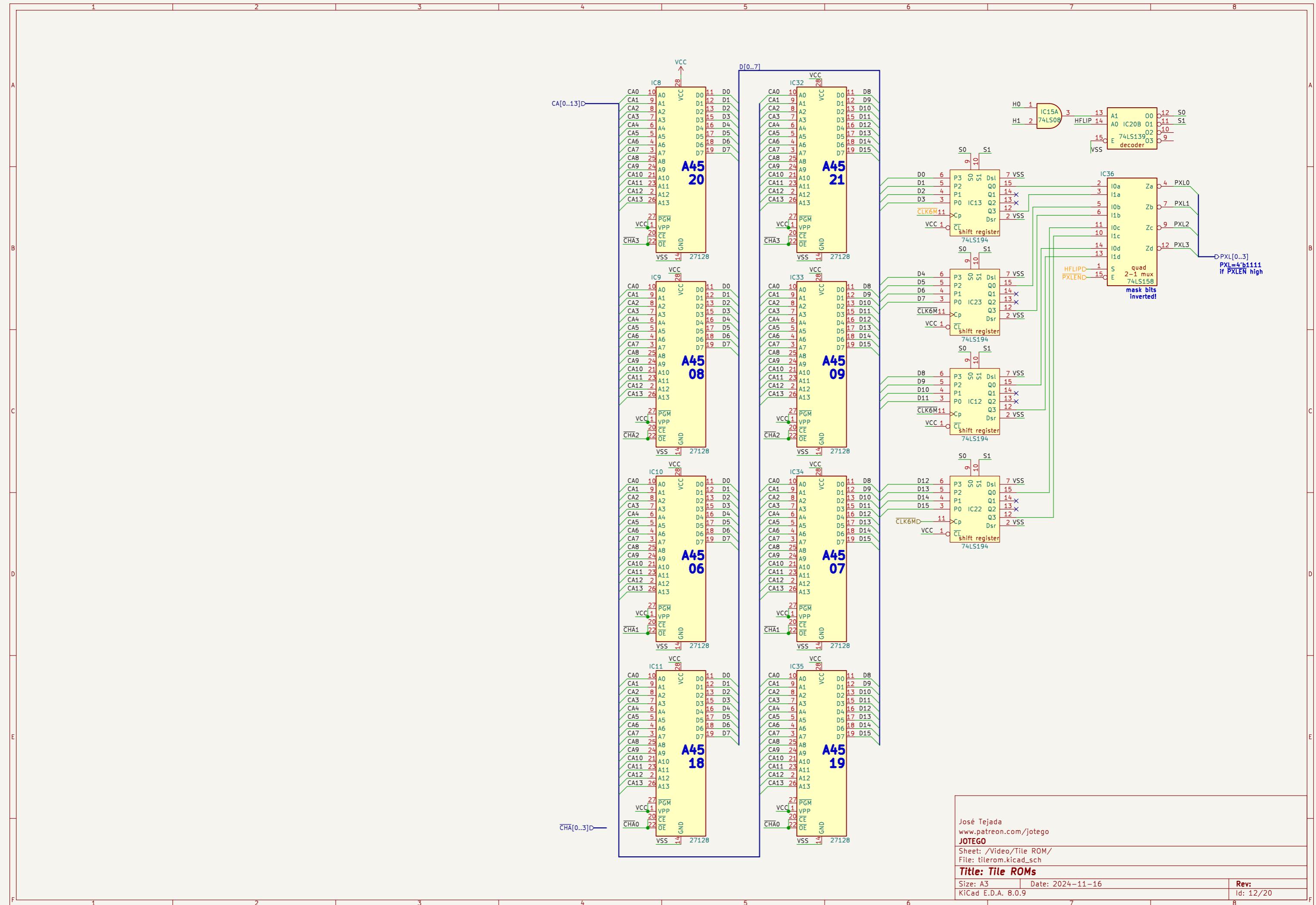
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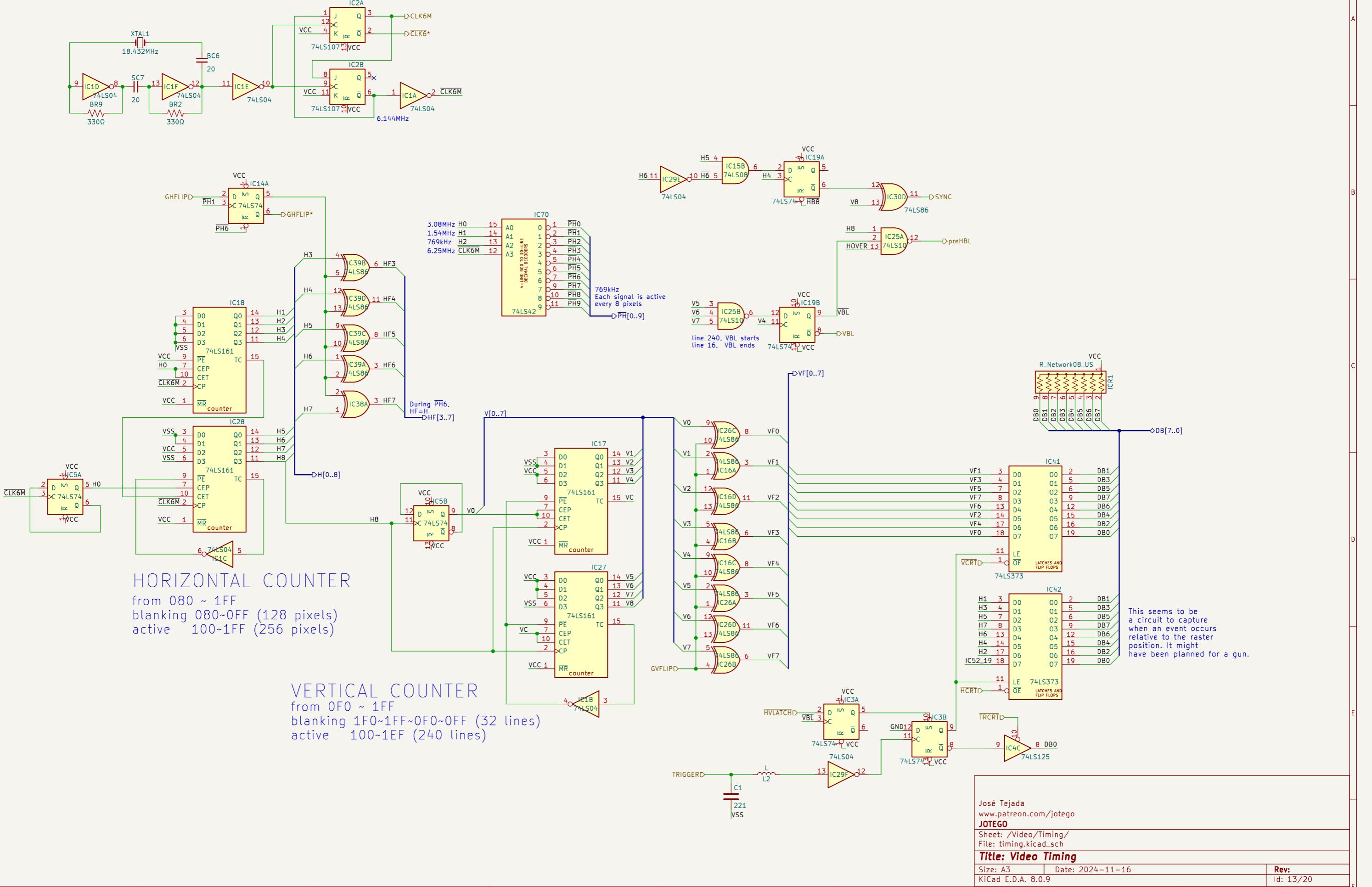
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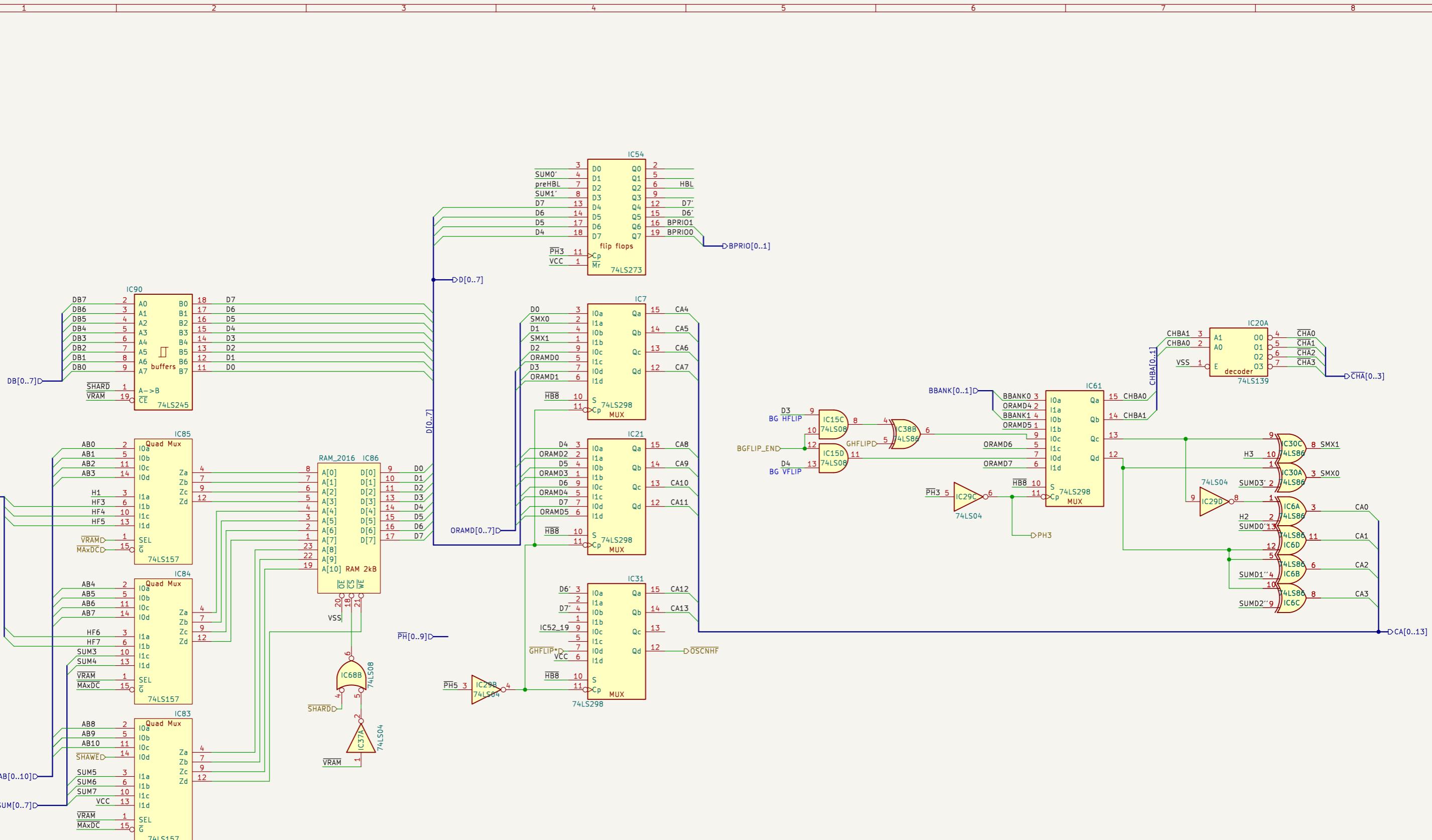
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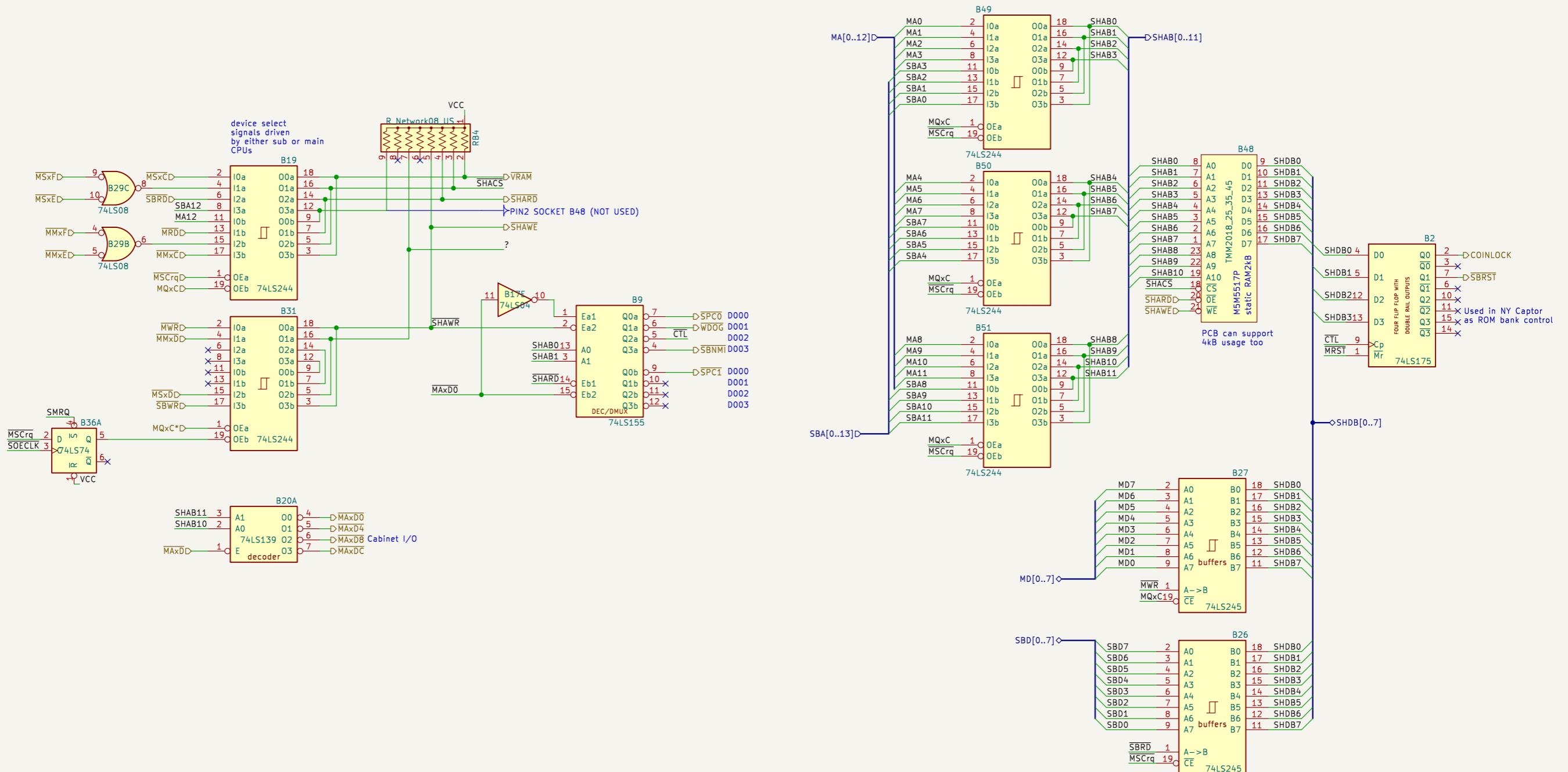
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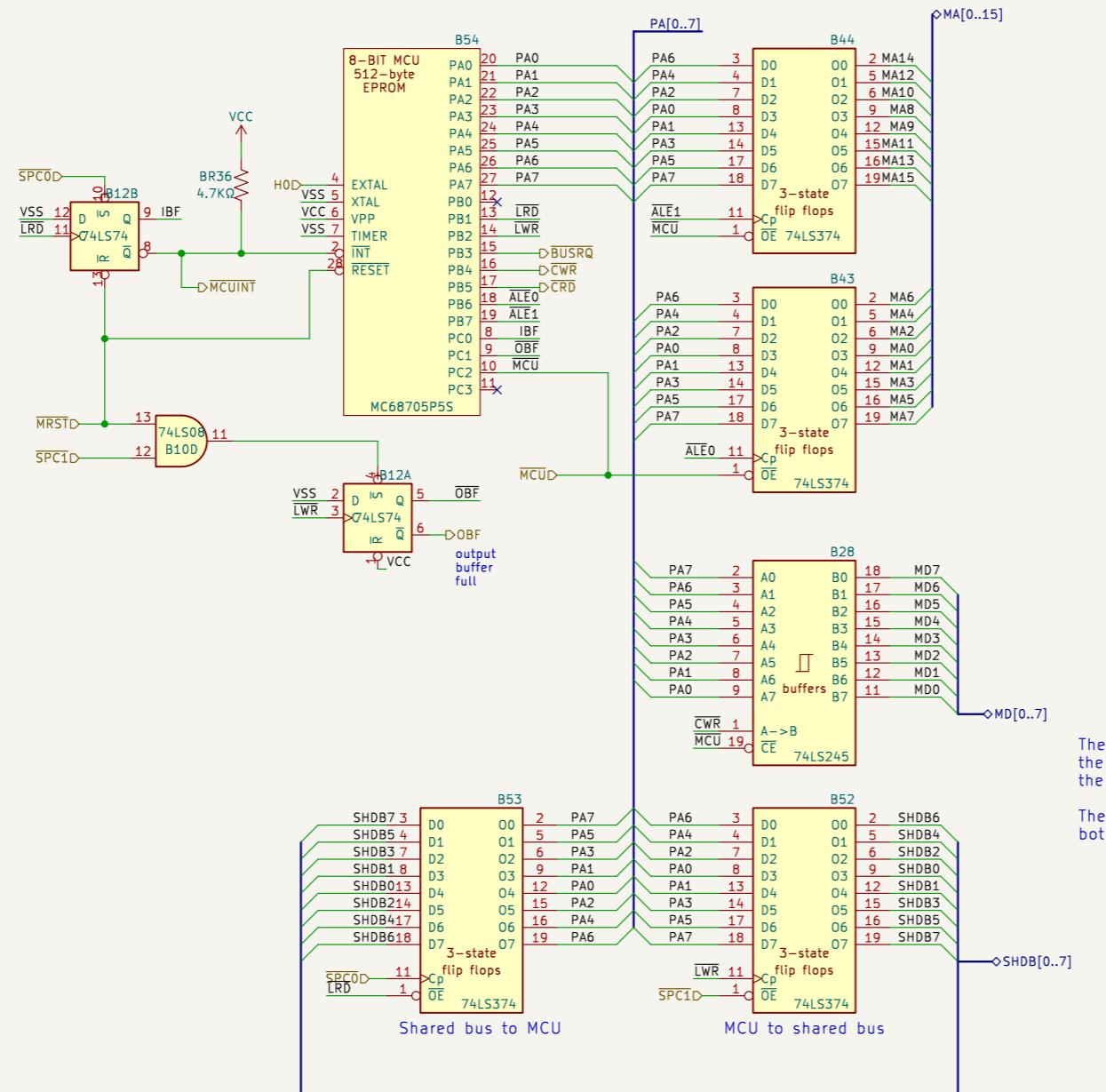












The MCU can take control of the share bus by requesting the main CPU bus

The MCU can communicate with both CPUs via latches B52 and B53

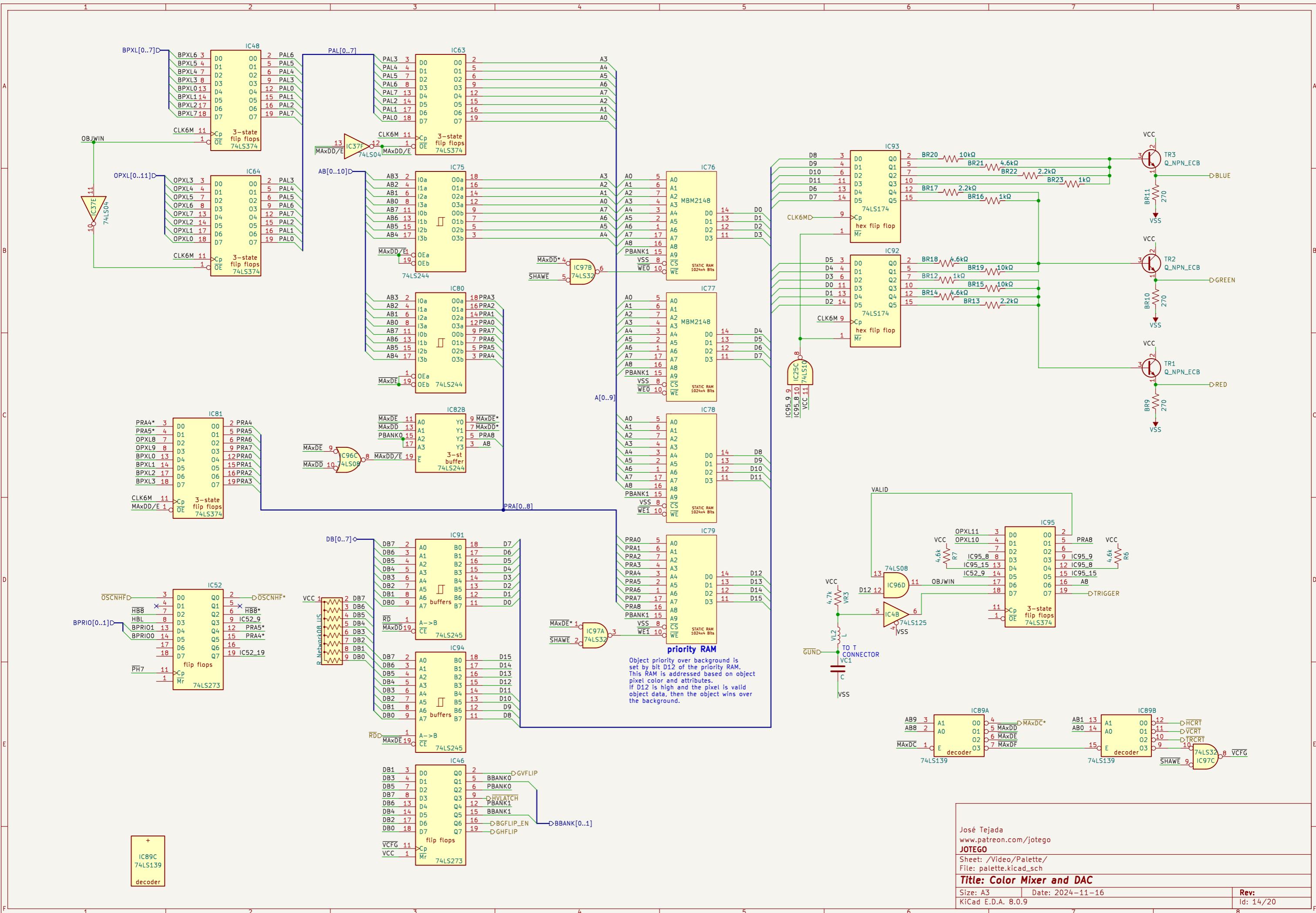
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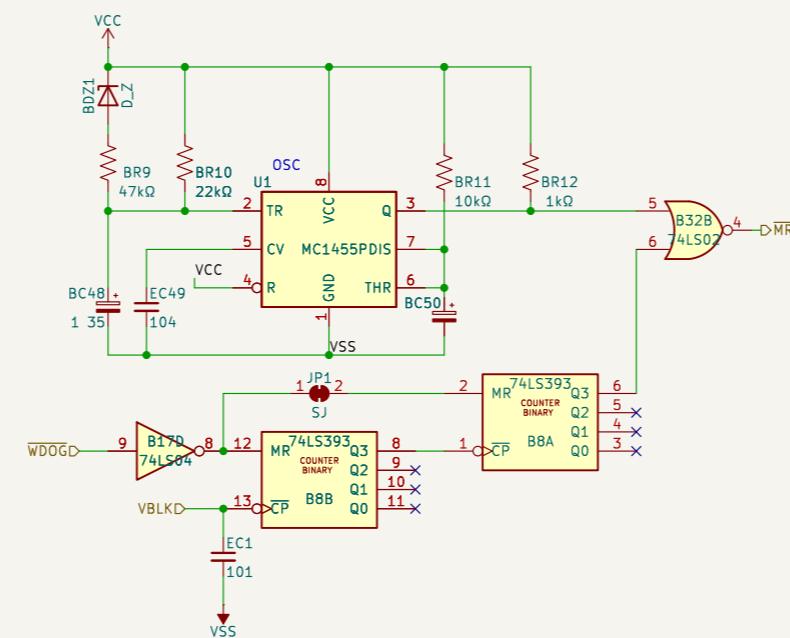
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Title: MCU

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev: Id: 14/20





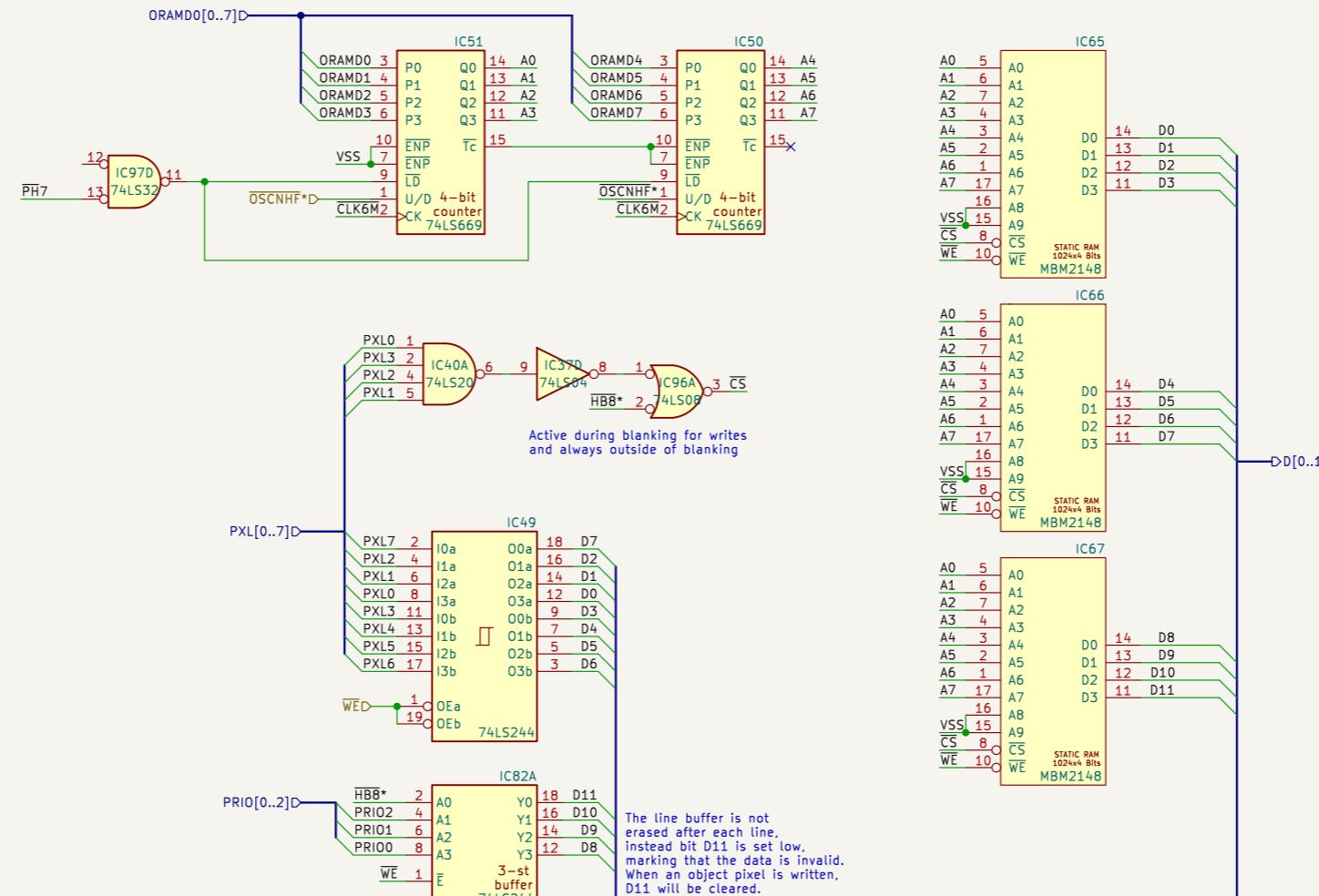
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Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 15/20

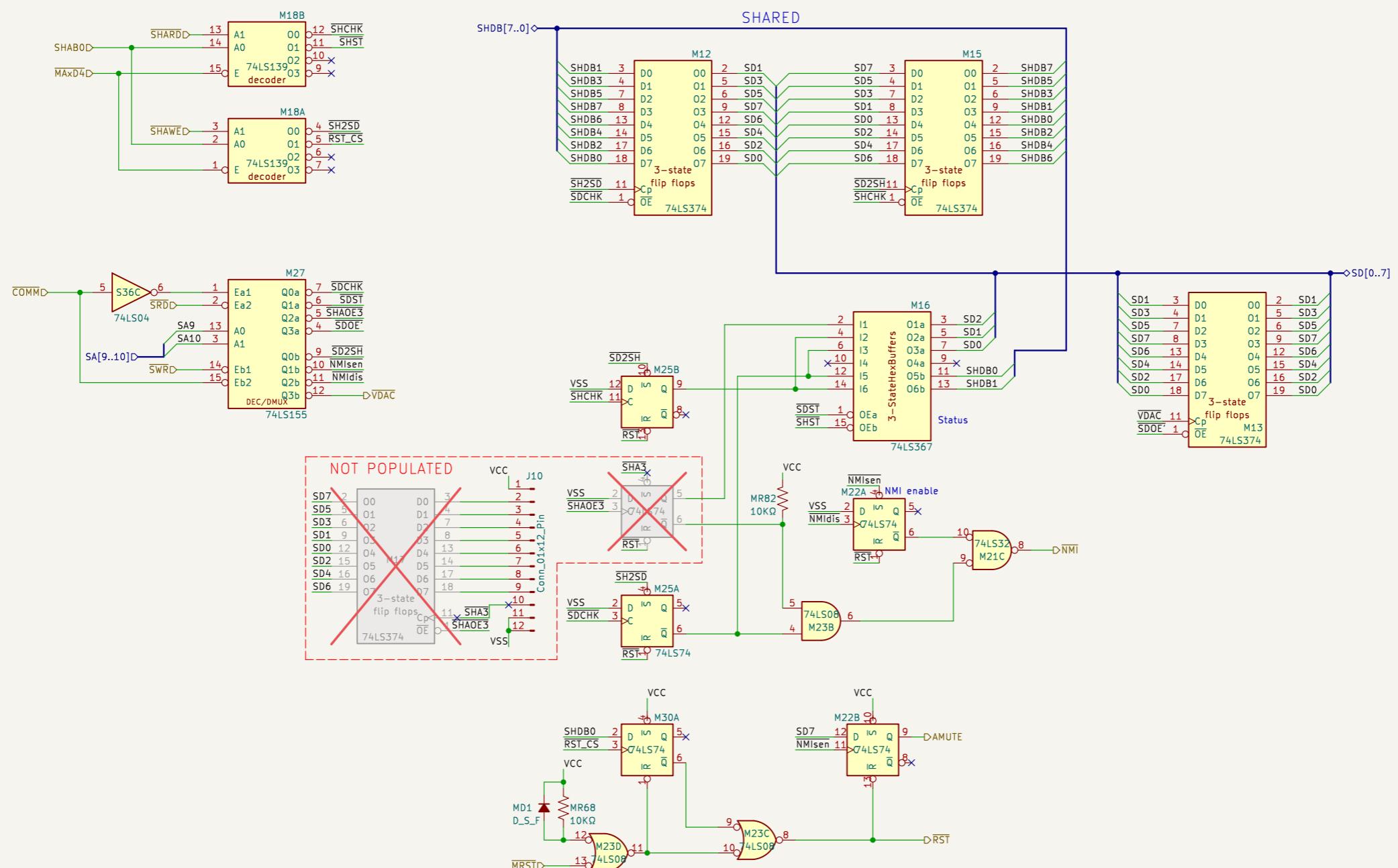


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Title: Single Line Video Buffer

Size: A3	Date: 2024-11-16
KiCad E.D.A. 8.0.9	Rev: 17/20



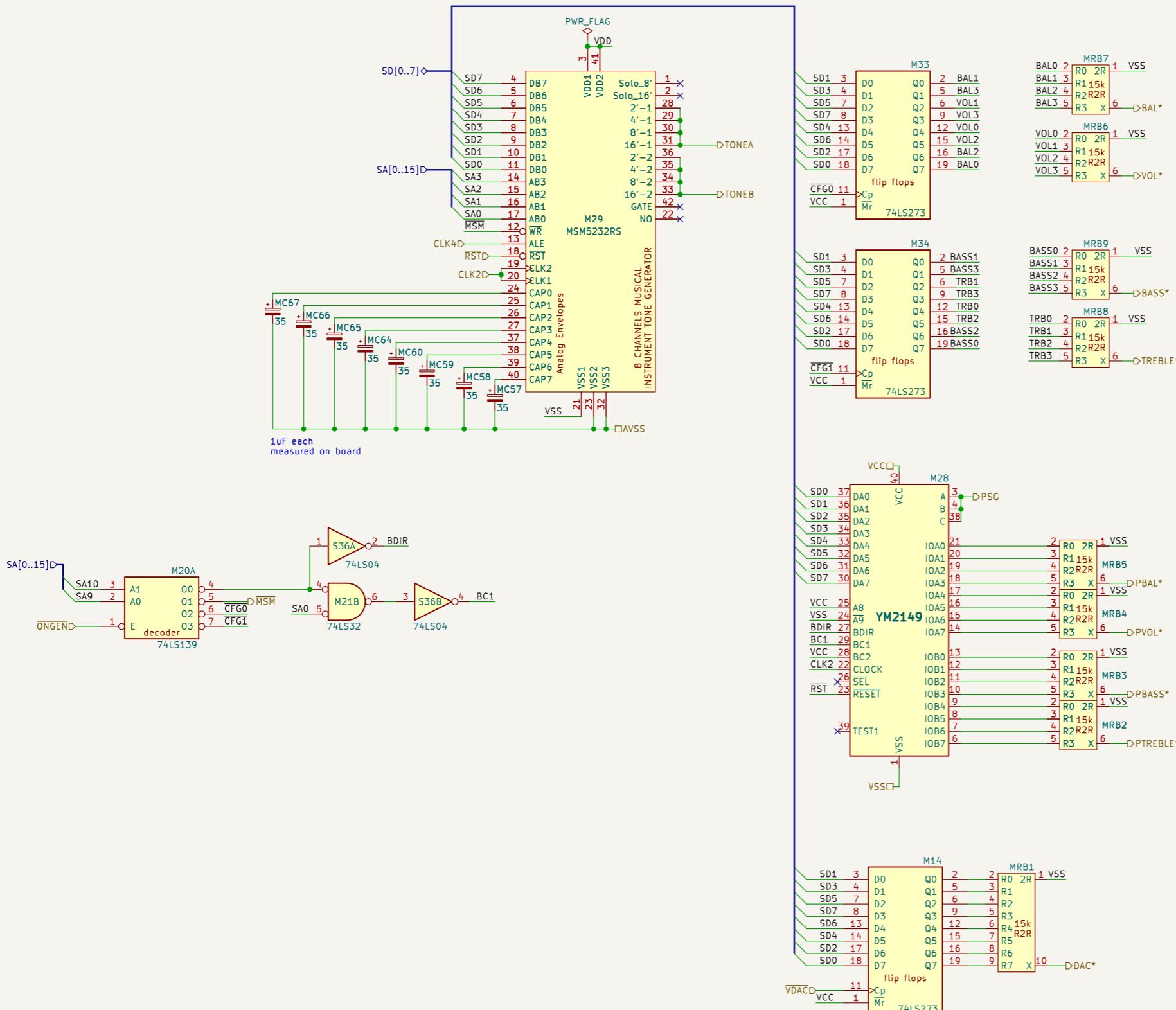
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Size: A3 | Date: 2024-11-16
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Sheet: /Sound/Sound Generators/
File: ongen.kicad_sch

Title: Sound Generators

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev: Id: 19/20

A

A

B

B

C

C

D

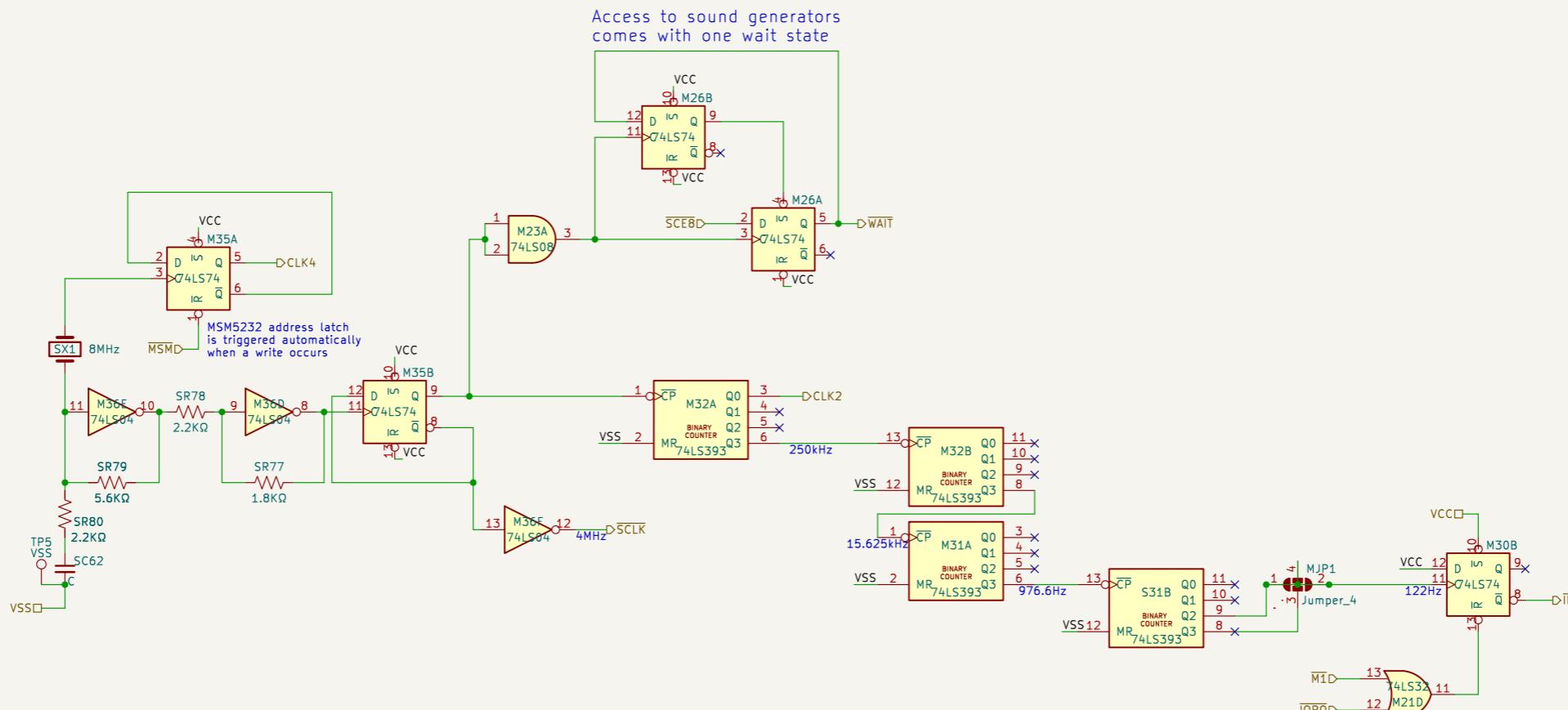
D

E

E

F

F



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File: snd_clocks.kicad_sch

Title: Sound Clock Generation

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 20/20