

A

B

C

D

E

A

B

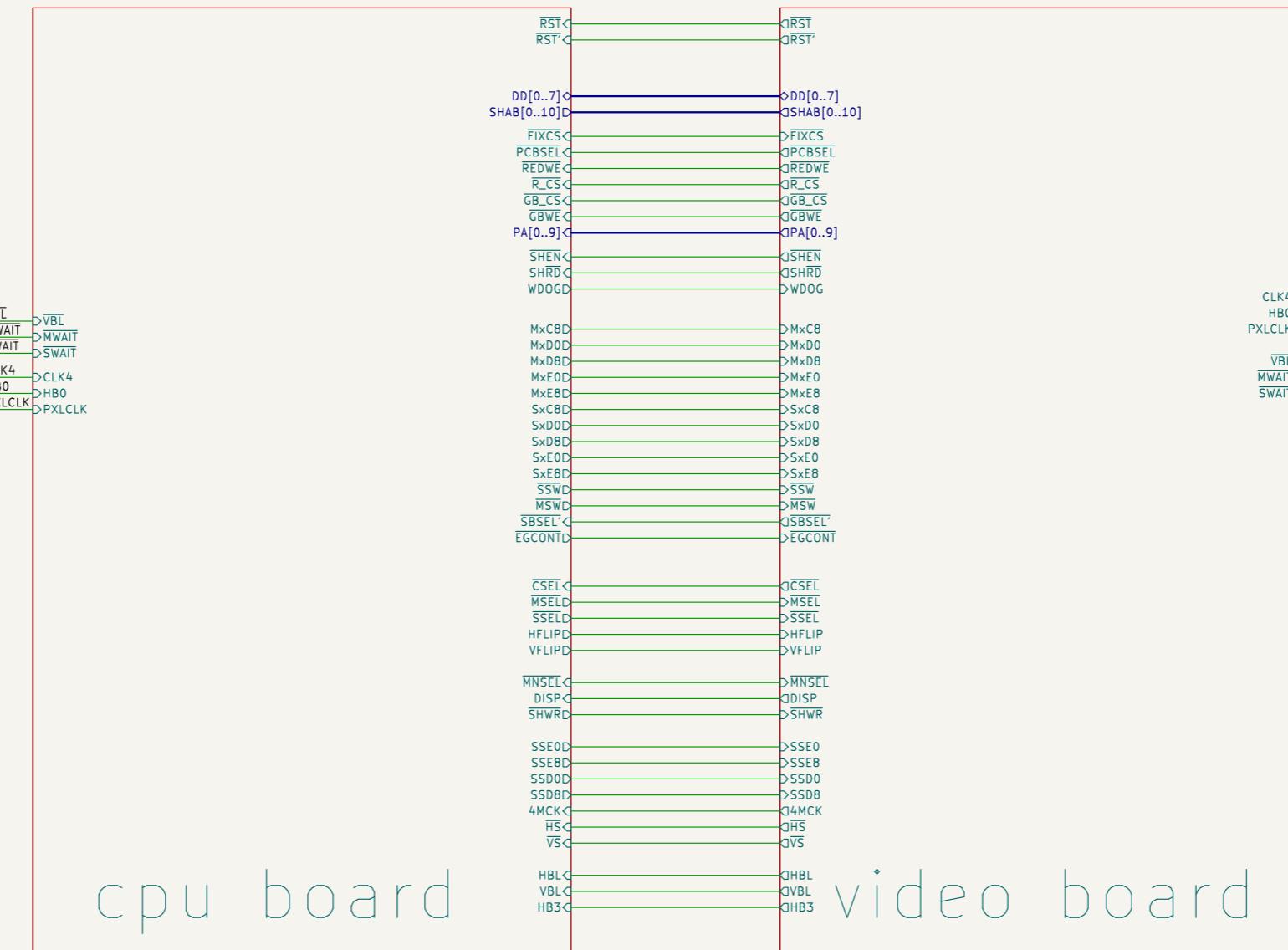
C

D

E

tehkanwch bootleg board

- original PCB has through-hole vias whereas the bootleg has filled vias
- original uses three custom packages from Mitsubishi with logic inside.
These were replaced by a small daughter board on the bootleg, serving the same functionality
- different edge connectors
- debug edge connector on video board not present on bootleg
- cabinet input logic partly missing and rewired on the bootleg to replace the trackball interface for a joystick one
- different power amplifier for sound without heat sink on the bootleg
- slightly different frequencies for crystal oscillators (cost reduction?)
- device reference markers (silkscreen) on bootleg have reversed order on bootleg



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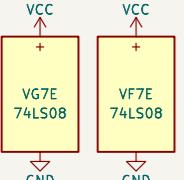
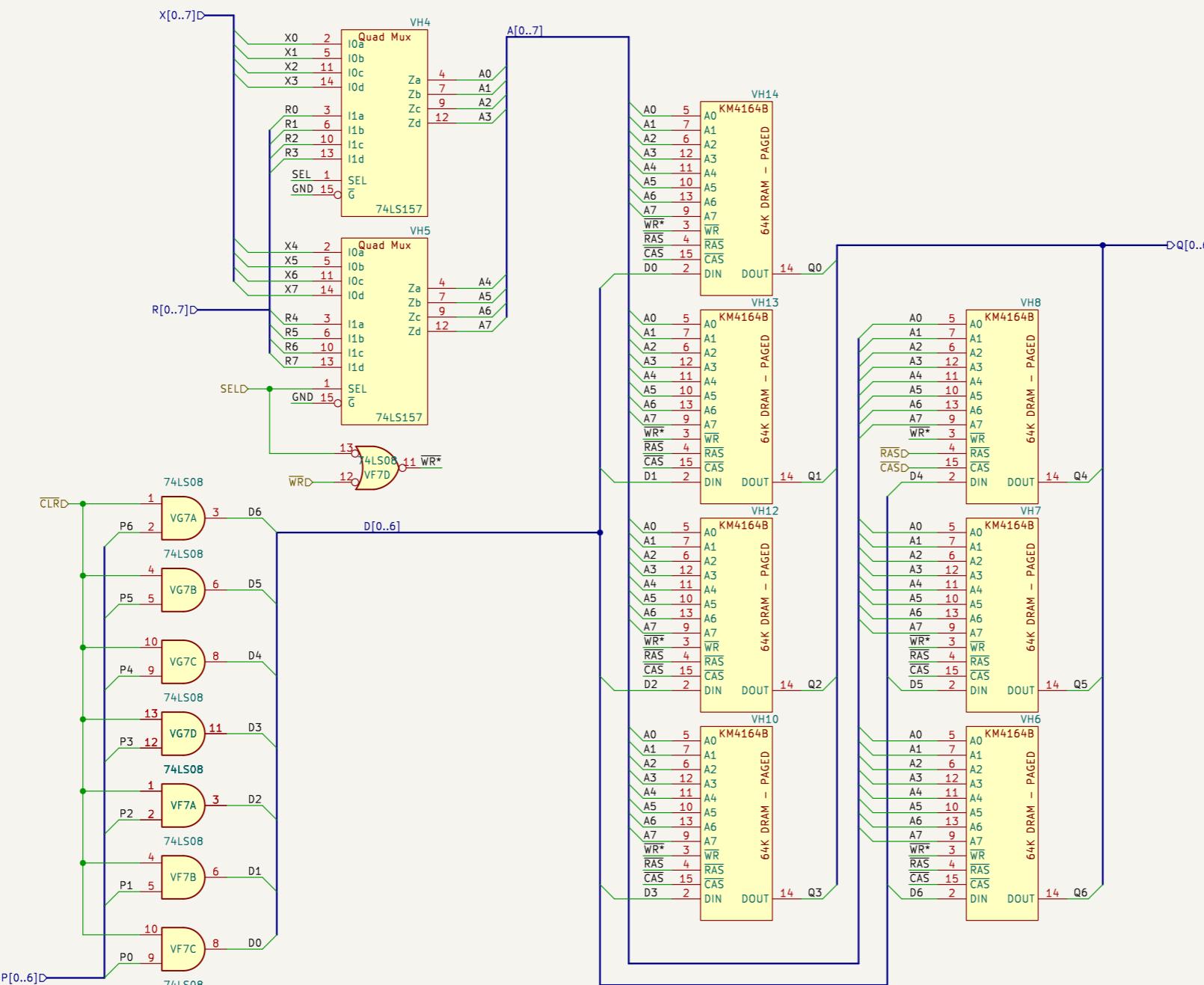
Sheet: / File: tehkanwch.kicad_sch

Title: TEHKAN WORLD CUP (tehkanwch bootleg)

Size: A3 Date: 2024-11-15

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Rev: 1/26



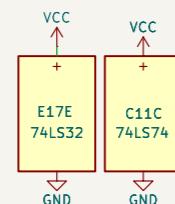
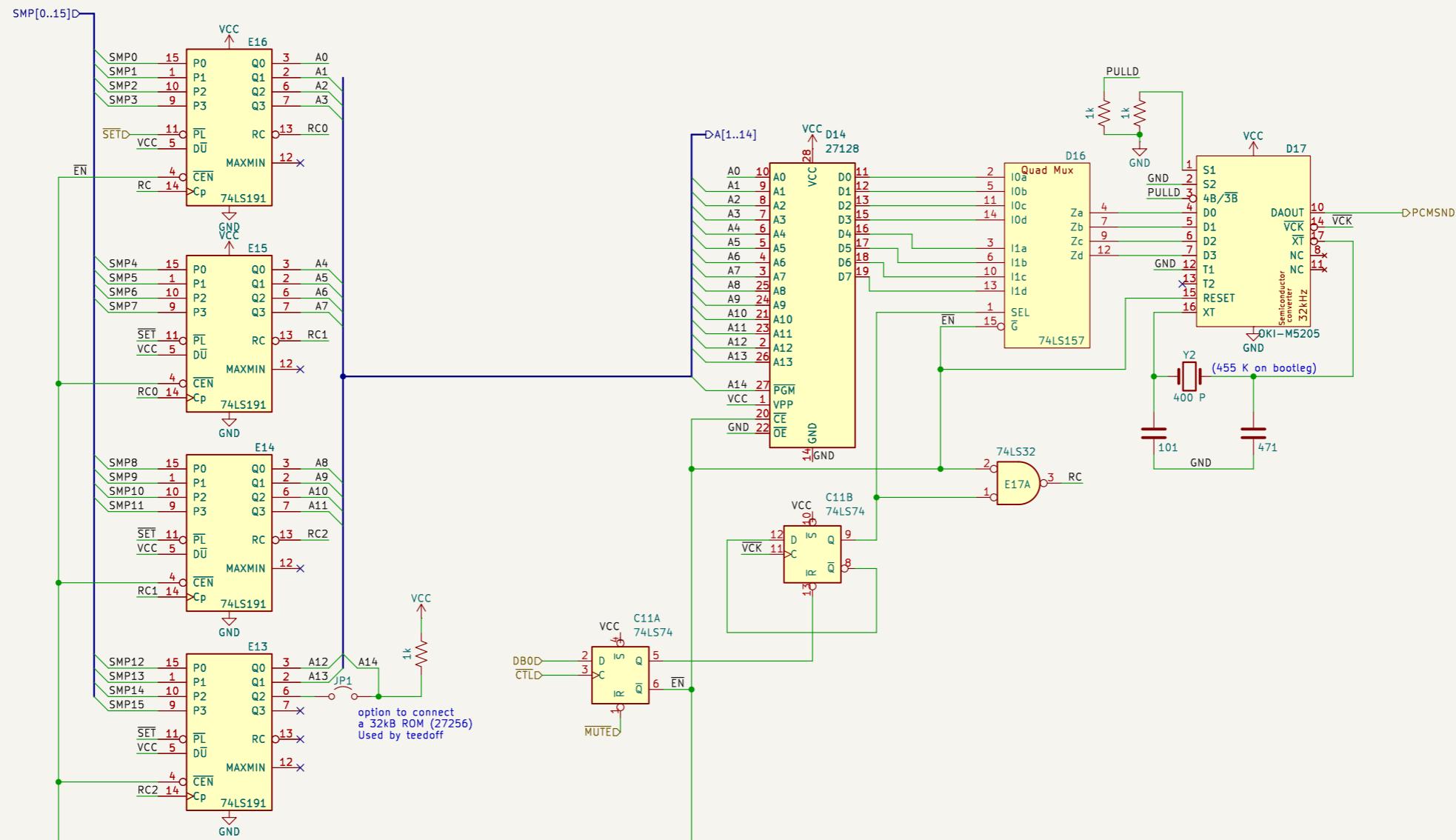
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Sheet: /video board/object/frame buffer B/
File: buffer.kicad_sch

Title: Line Buffer

Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

Rev: 2/26



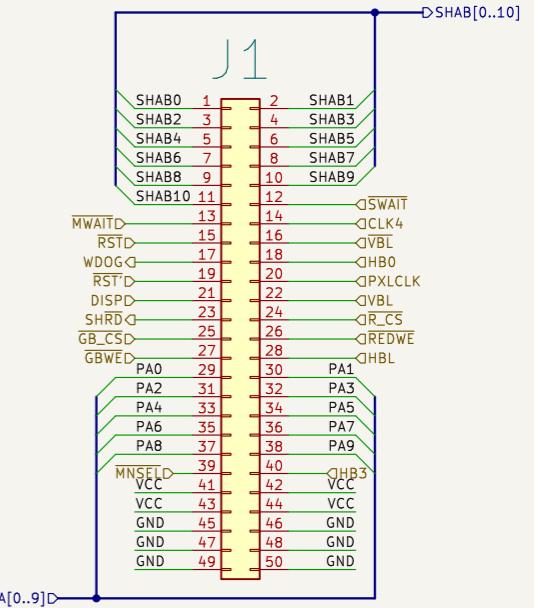
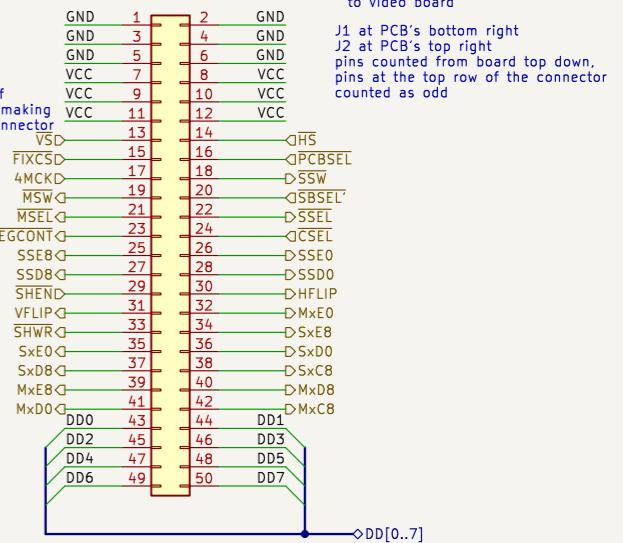
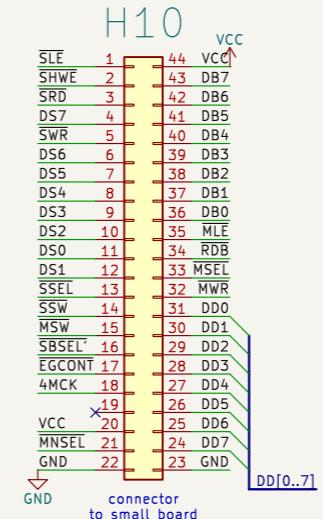
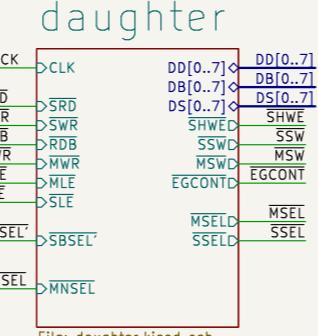
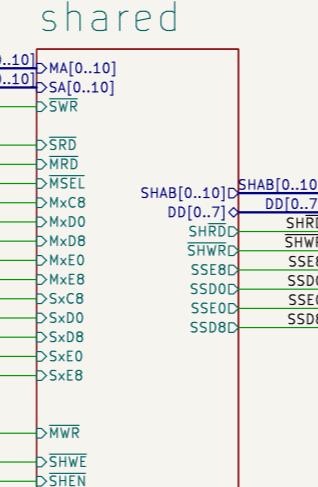
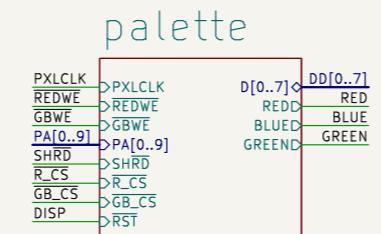
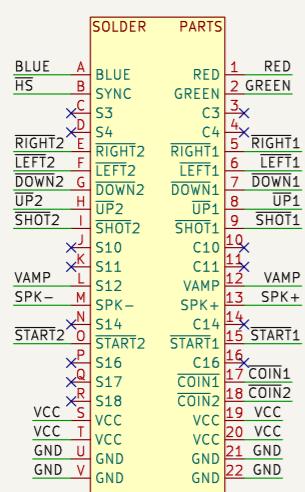
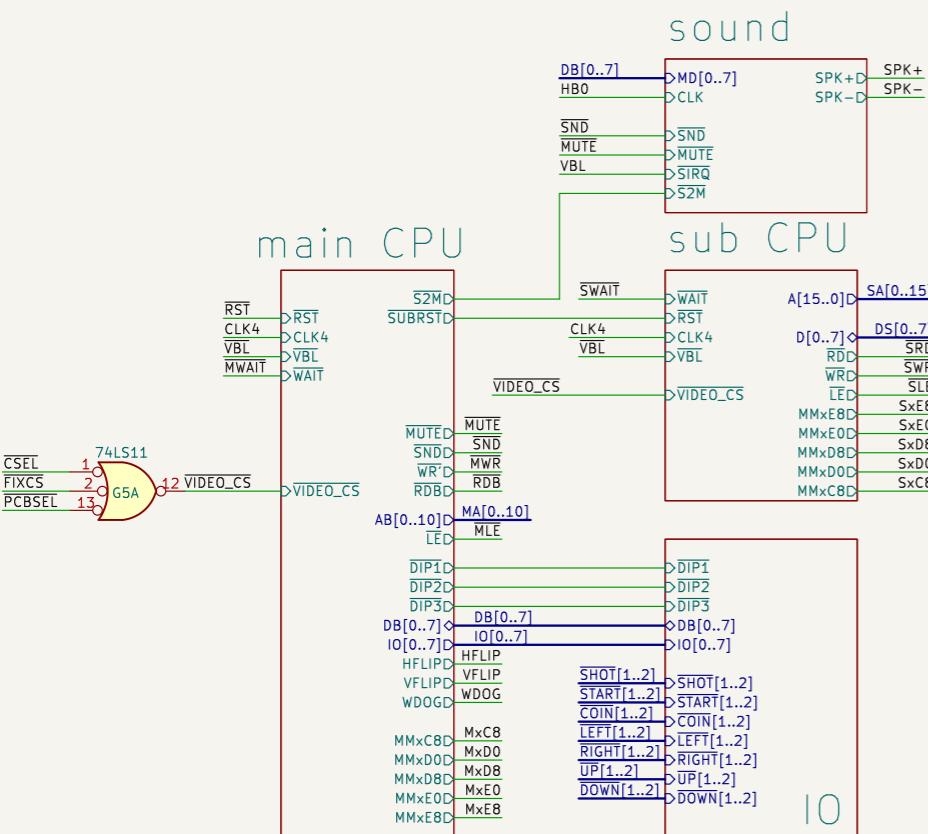
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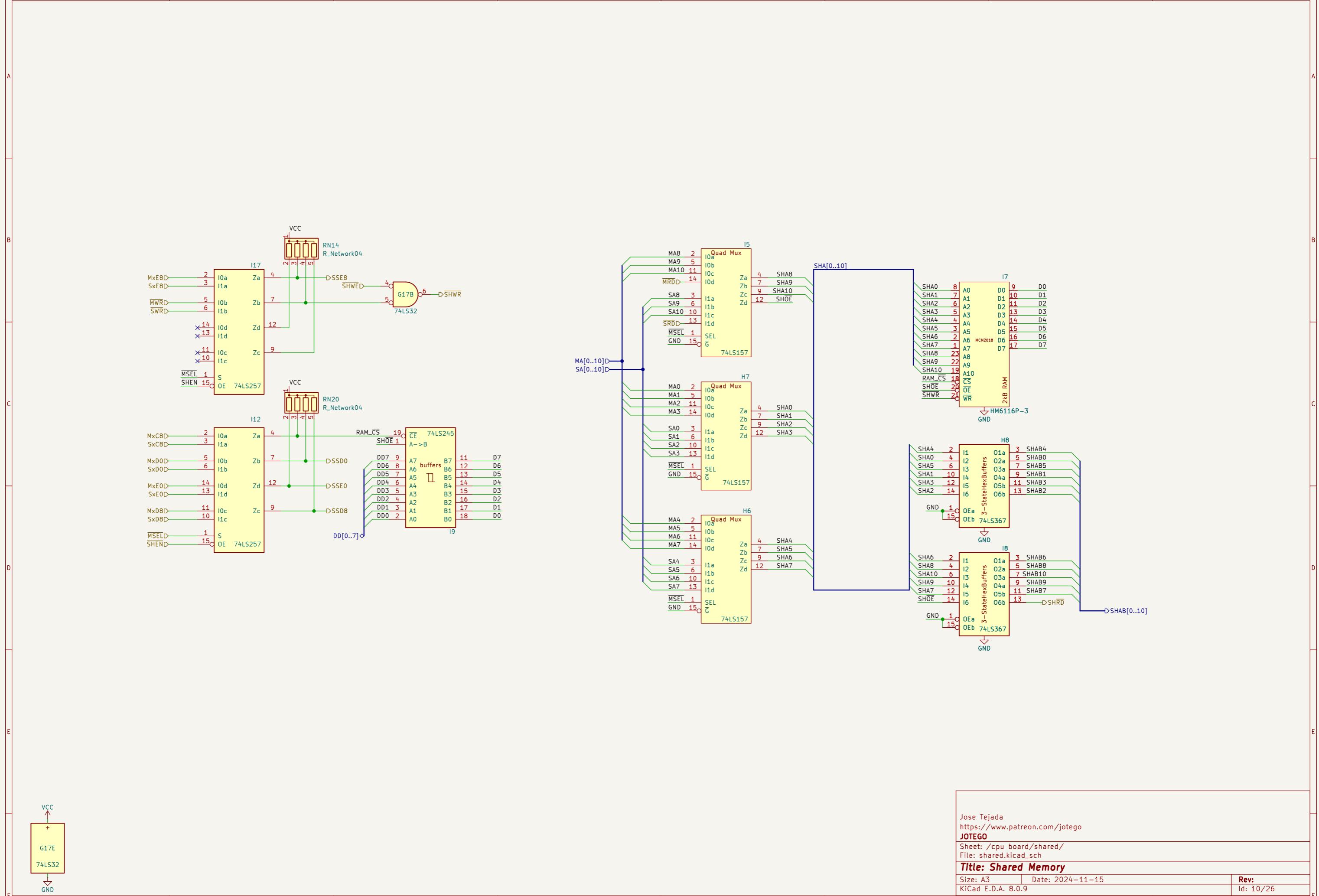
Sheet: /cpu_board/sound/PCM/
File: pcm.kicad_sch

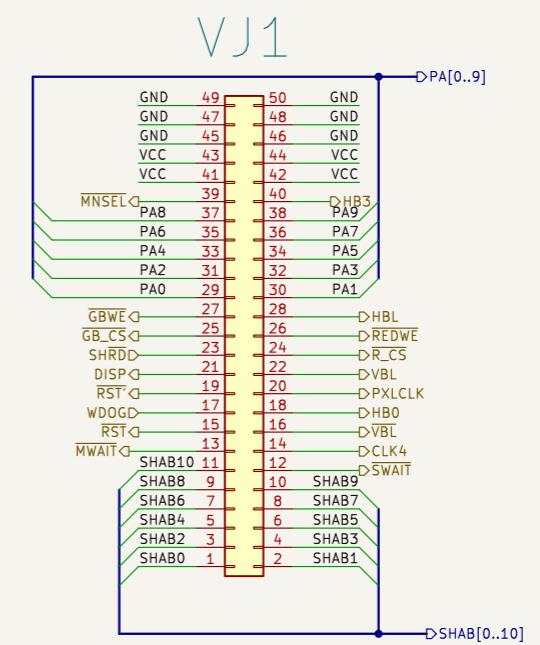
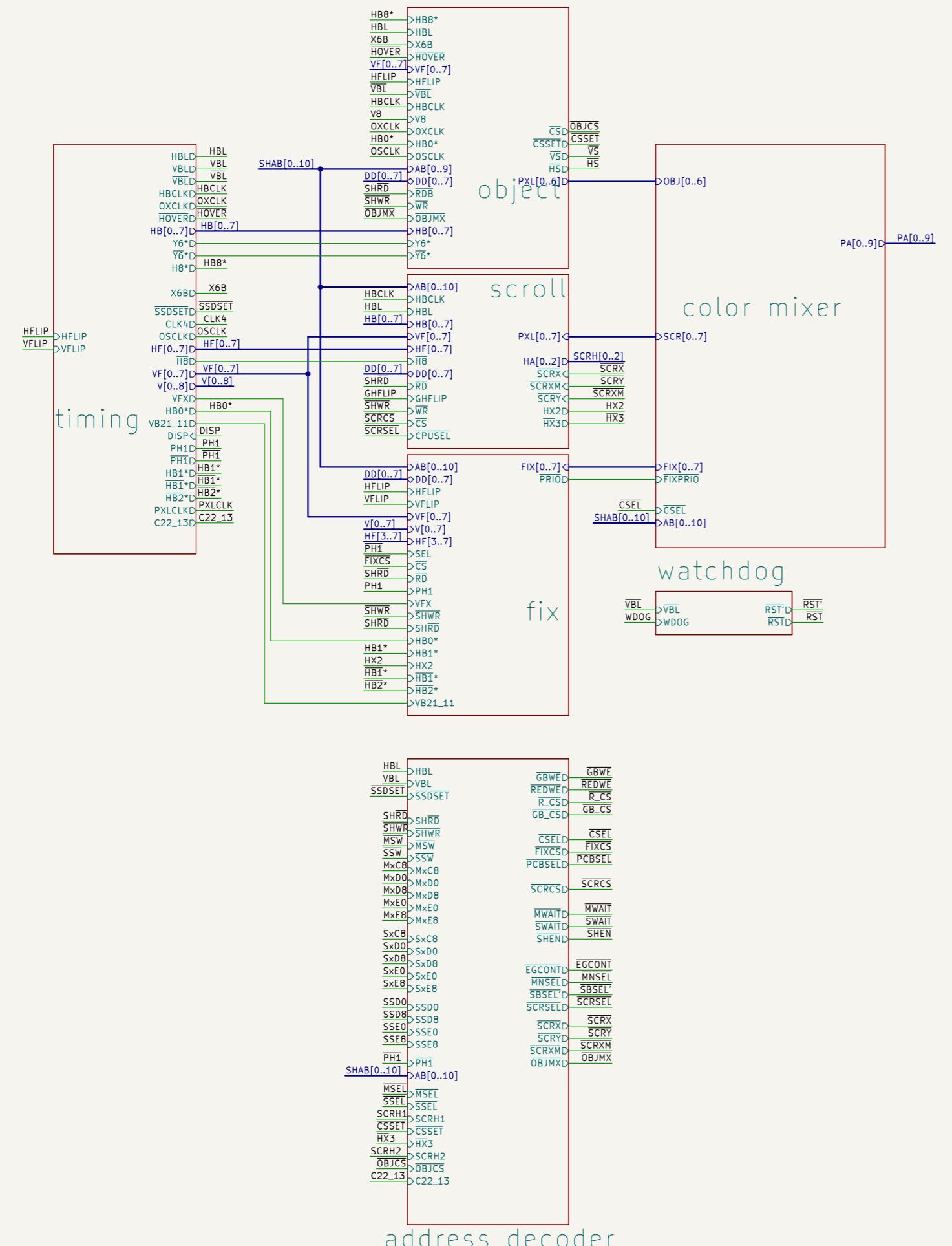
Title: PCM

Size: A3 Date: 2024-11-15
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Rev: Id: 5/26

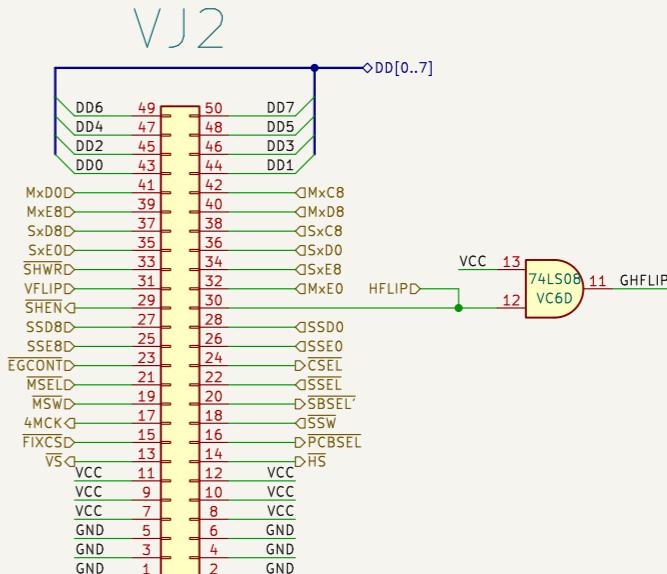






connectors
to CPU board

/J1 at PCB's top right
/J2 at PCB's bottom right
pins counted from board bottom up,
pins at the top row of the connector
counted as even



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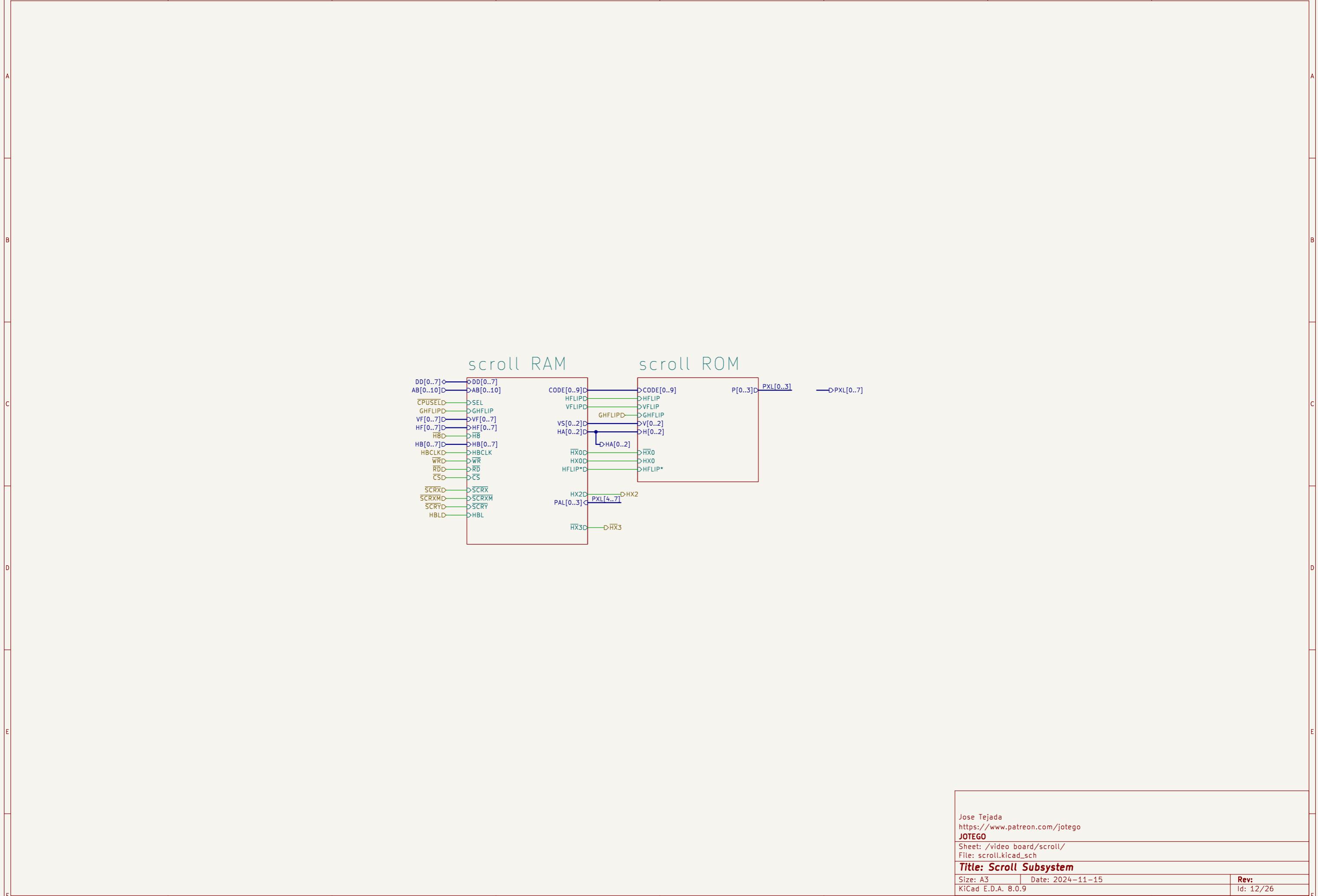
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Title: Video board (bottom board)

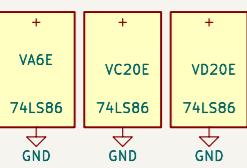
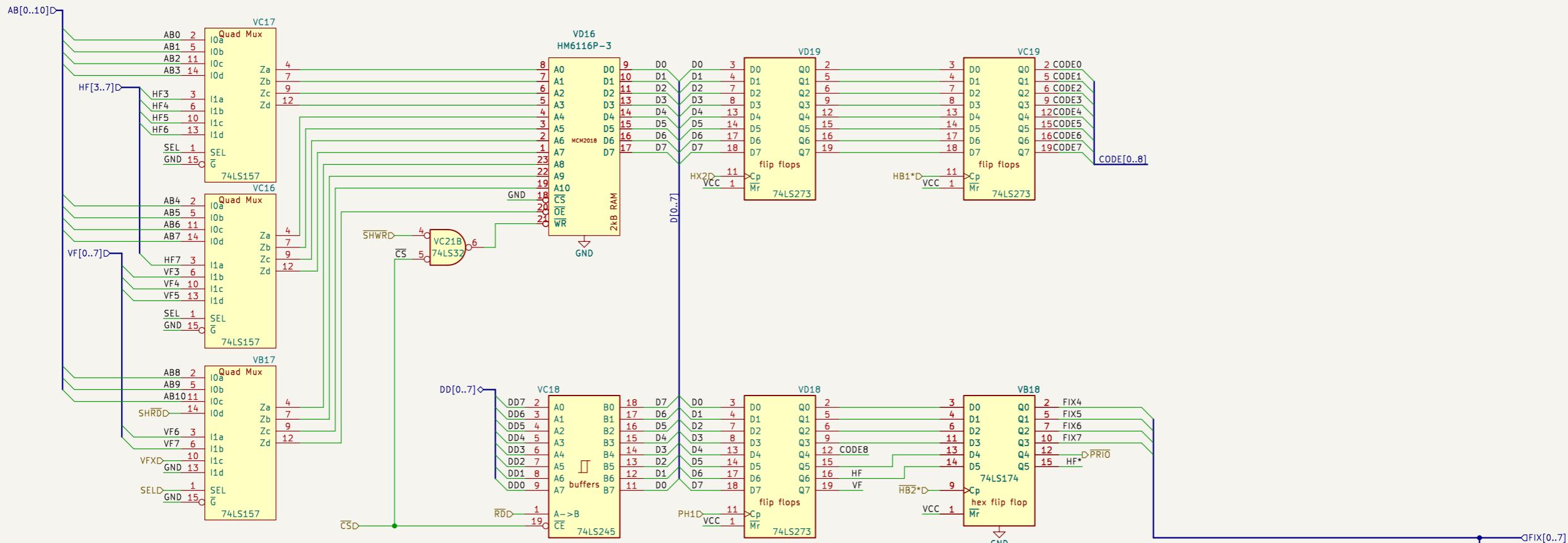
Size: A3 Date: 2024-11-15

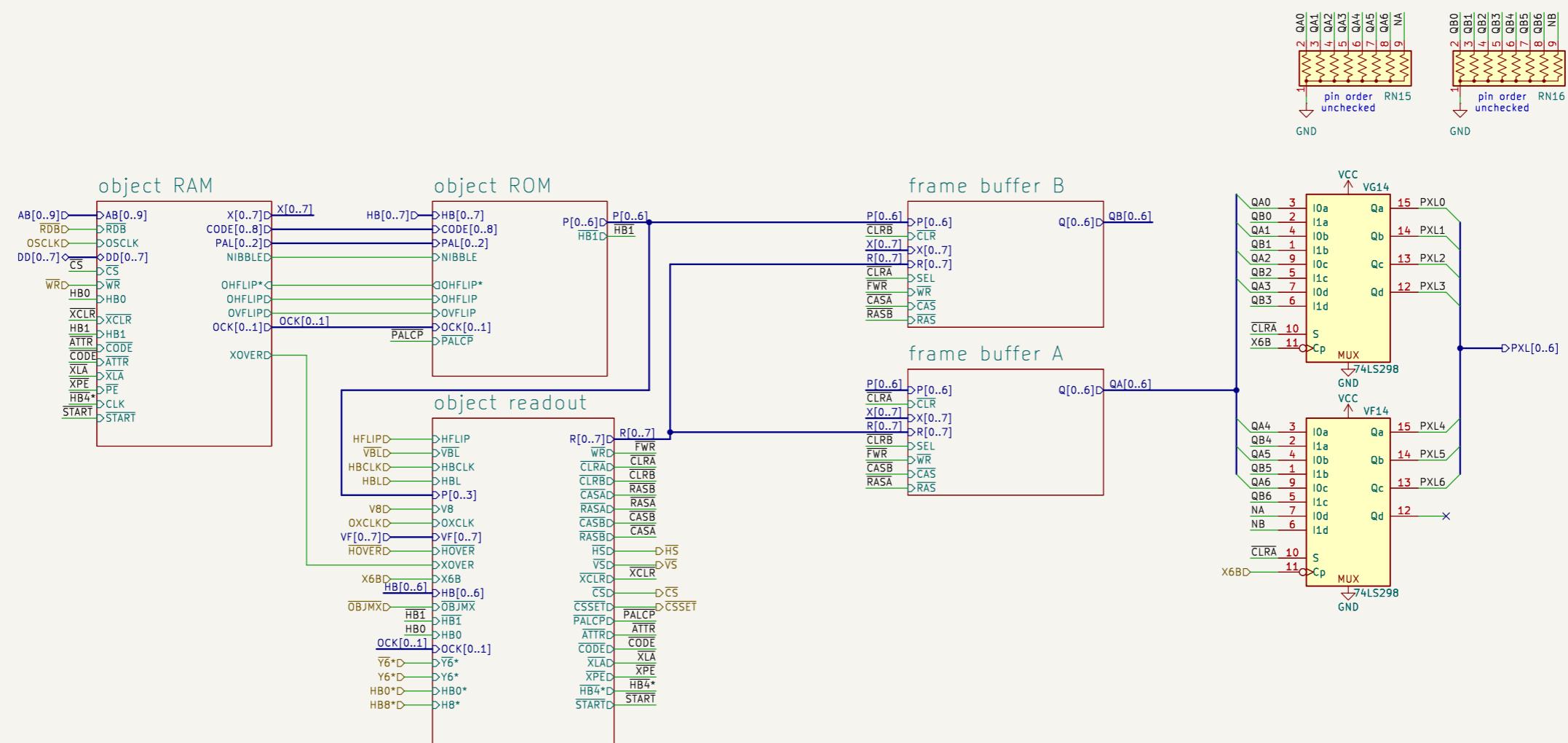
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7



A





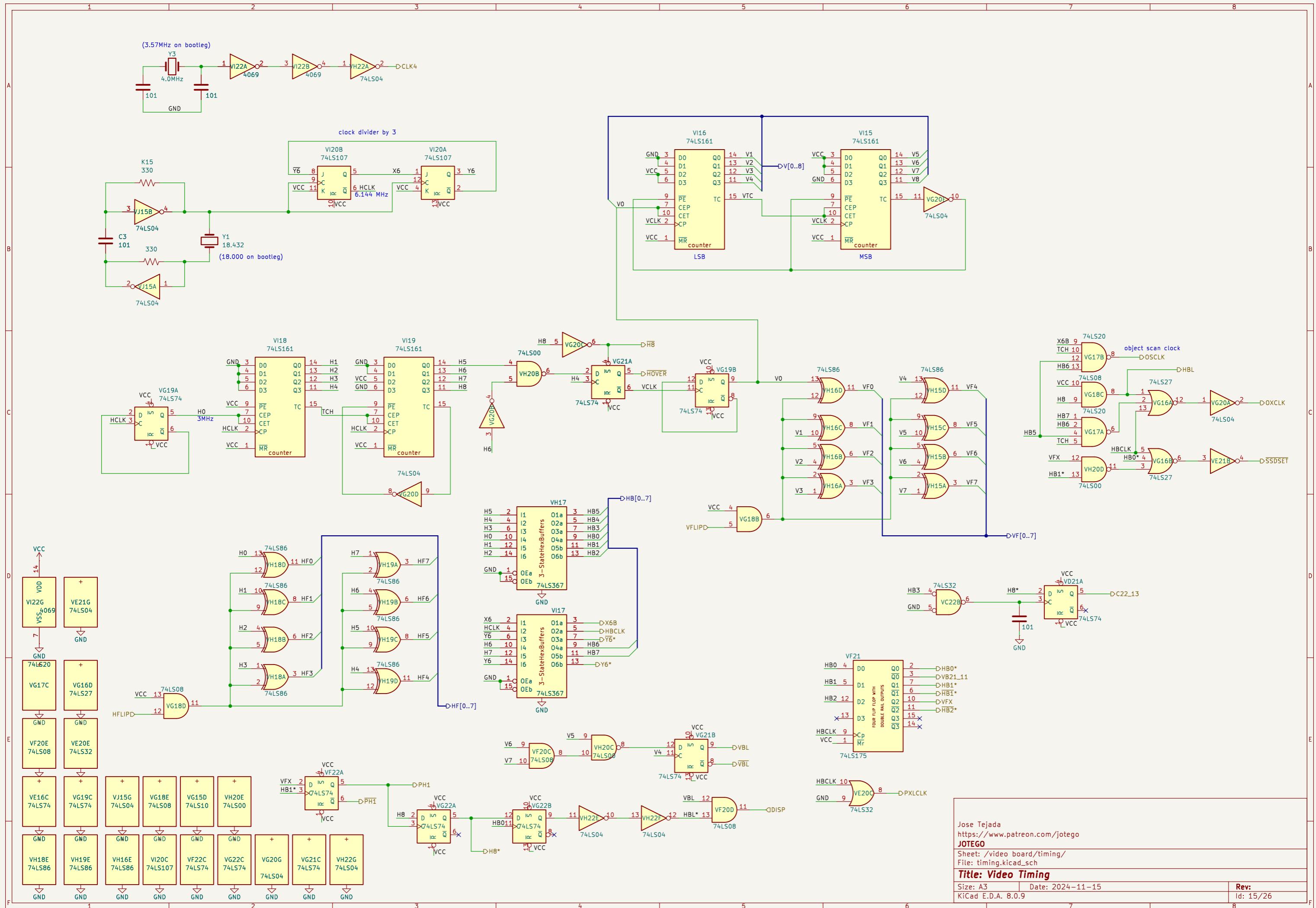
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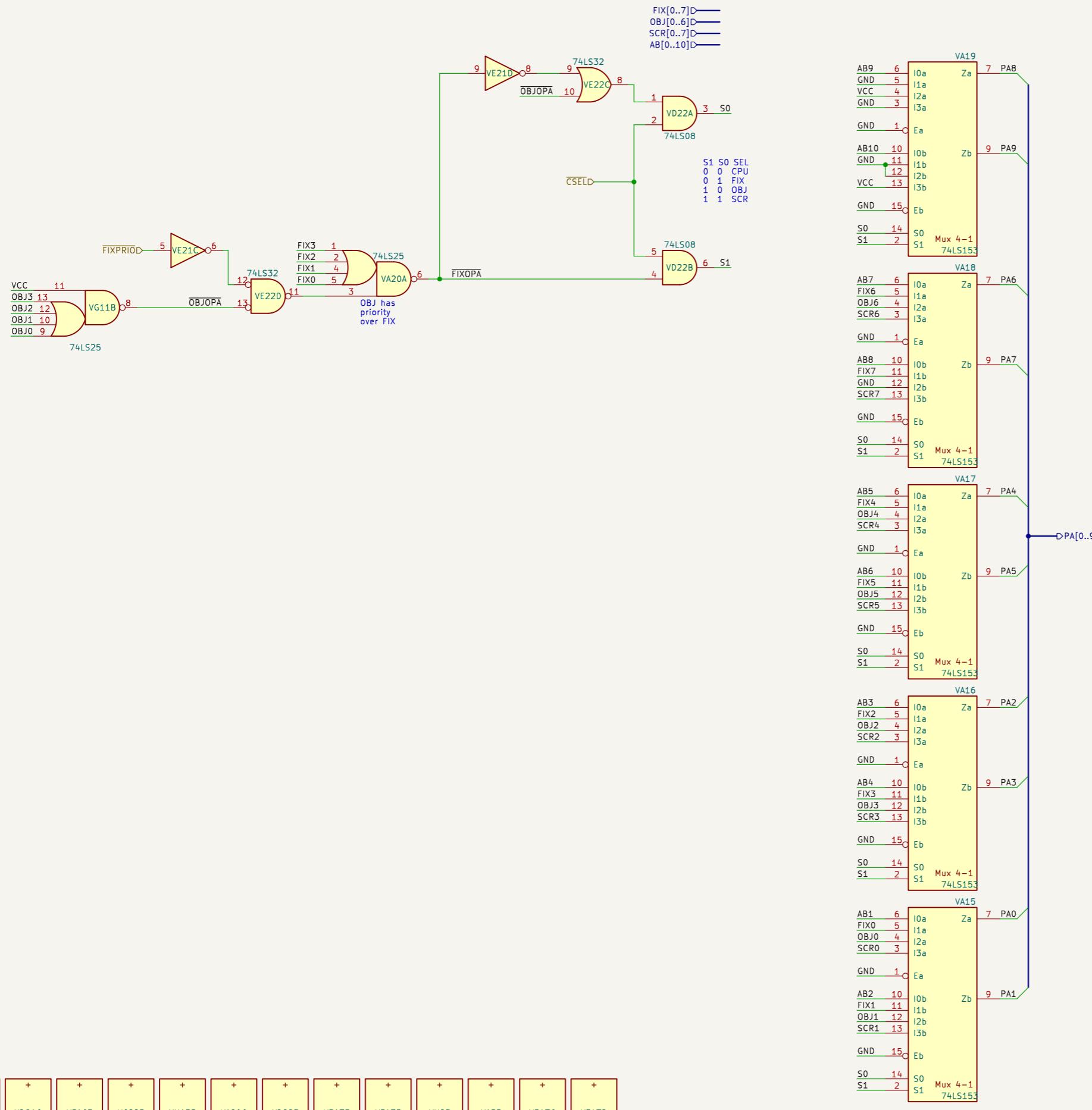
Sheet: /video board/object/
File: object.kicad_sch

Title: Object Subsystem

Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

Rev:
Id: 14/26



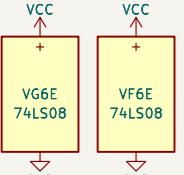
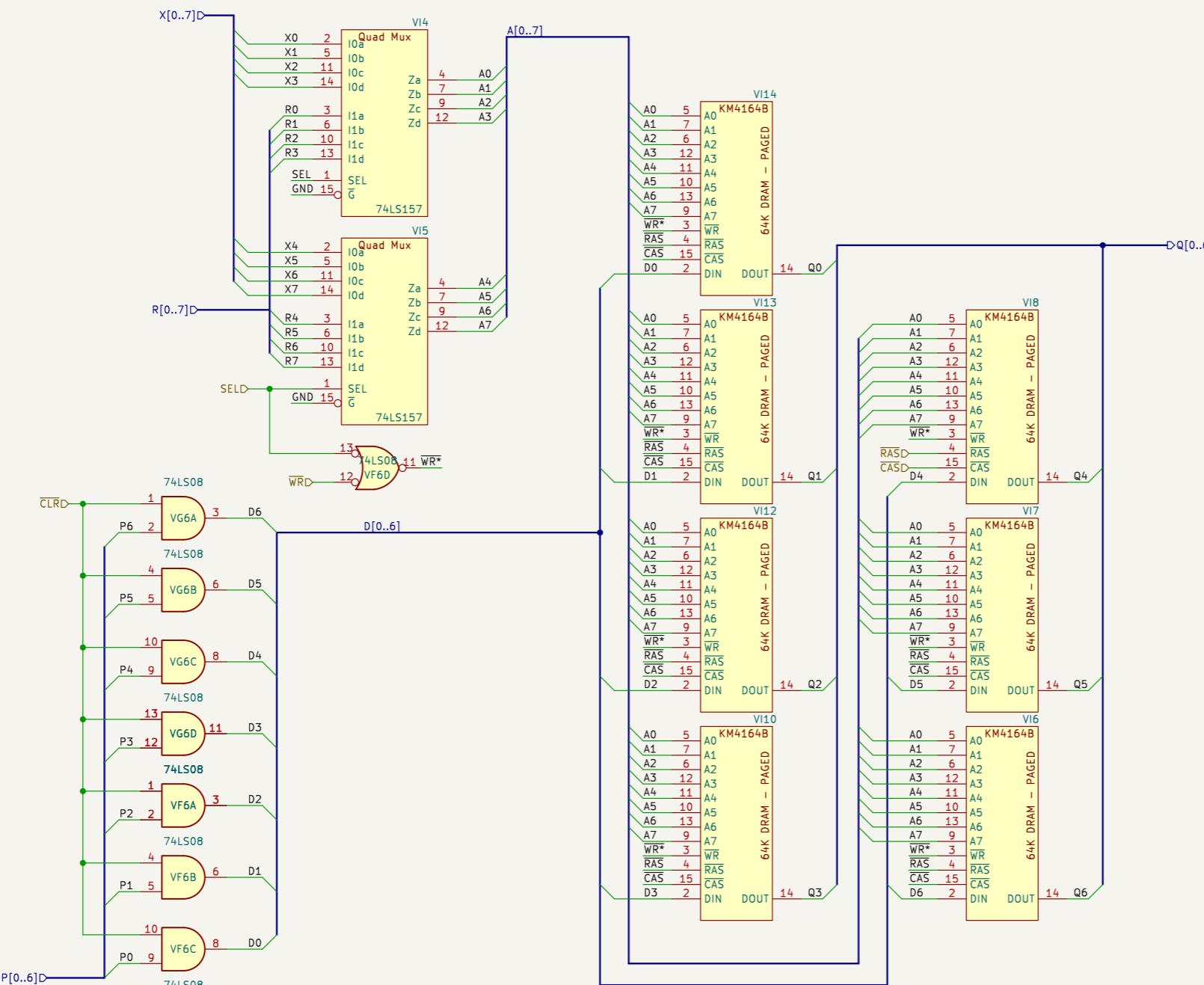


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File: colmix.kicad_sch

Size: A3 Date: 2024-11-1
KiCad EDA 8.0.9



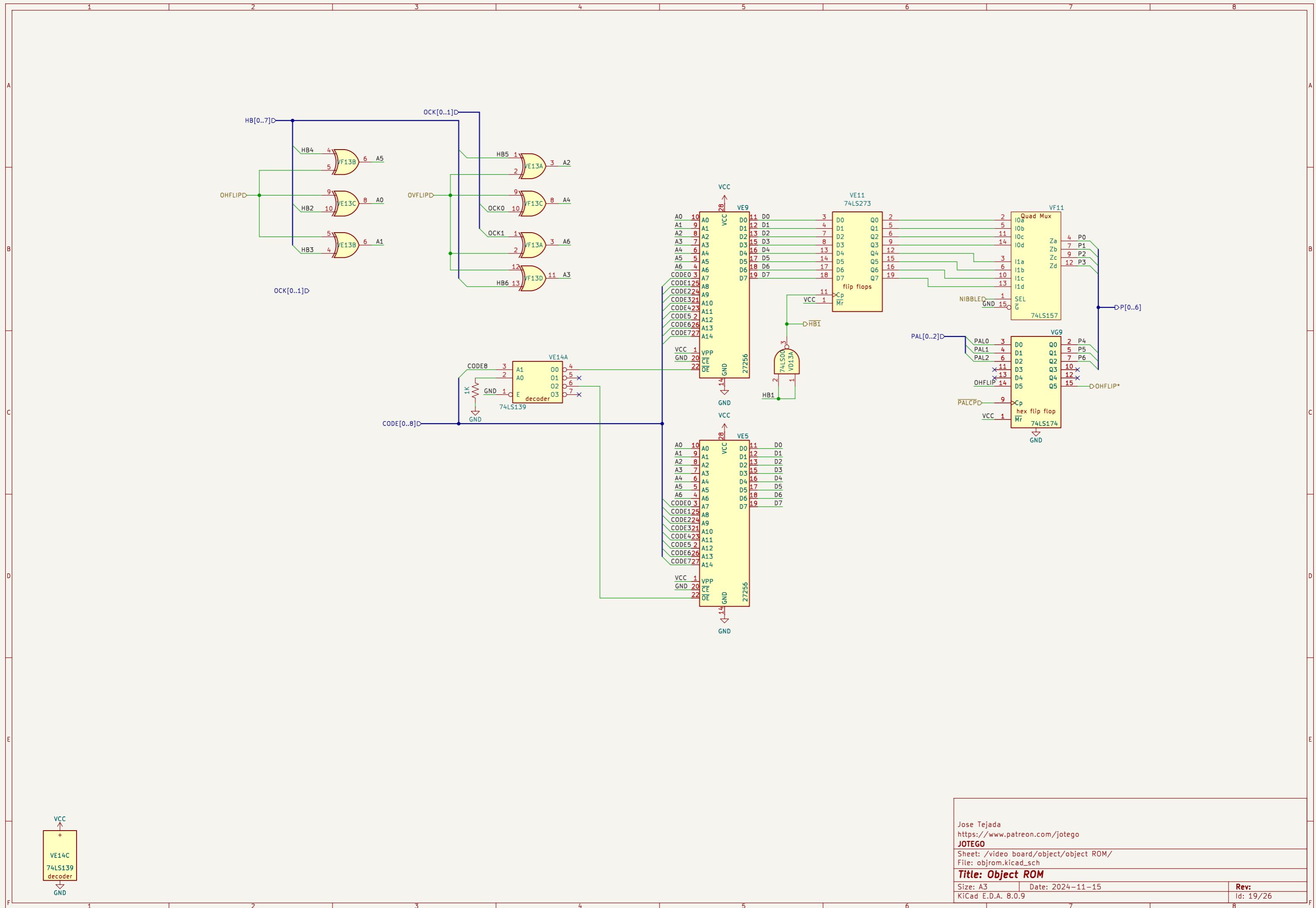
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Sheet: /video board/object/frame buffer A/
File: buffer.kicad_sch

Title: Line Buffer

Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

Rev:
Id: 17/26



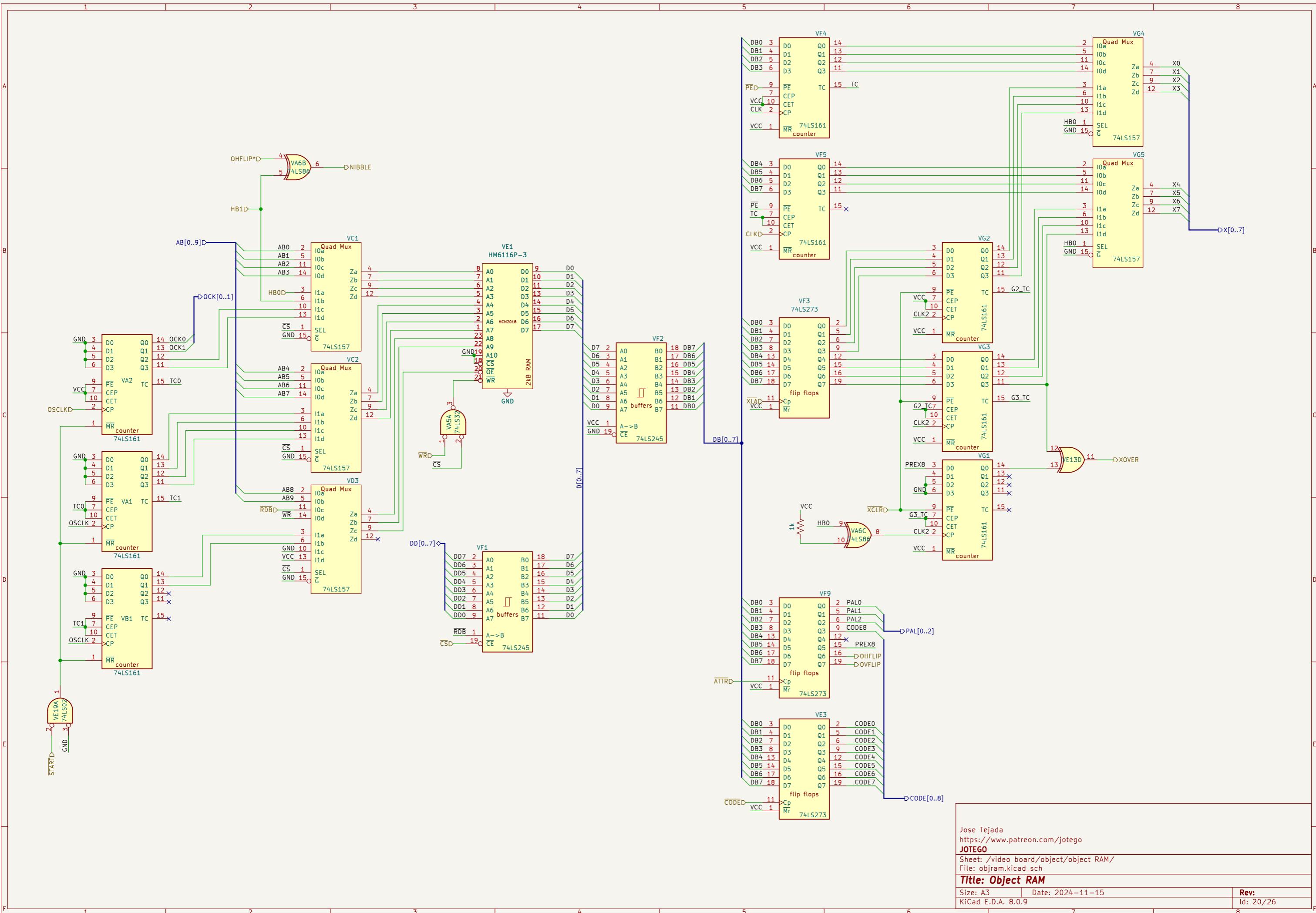
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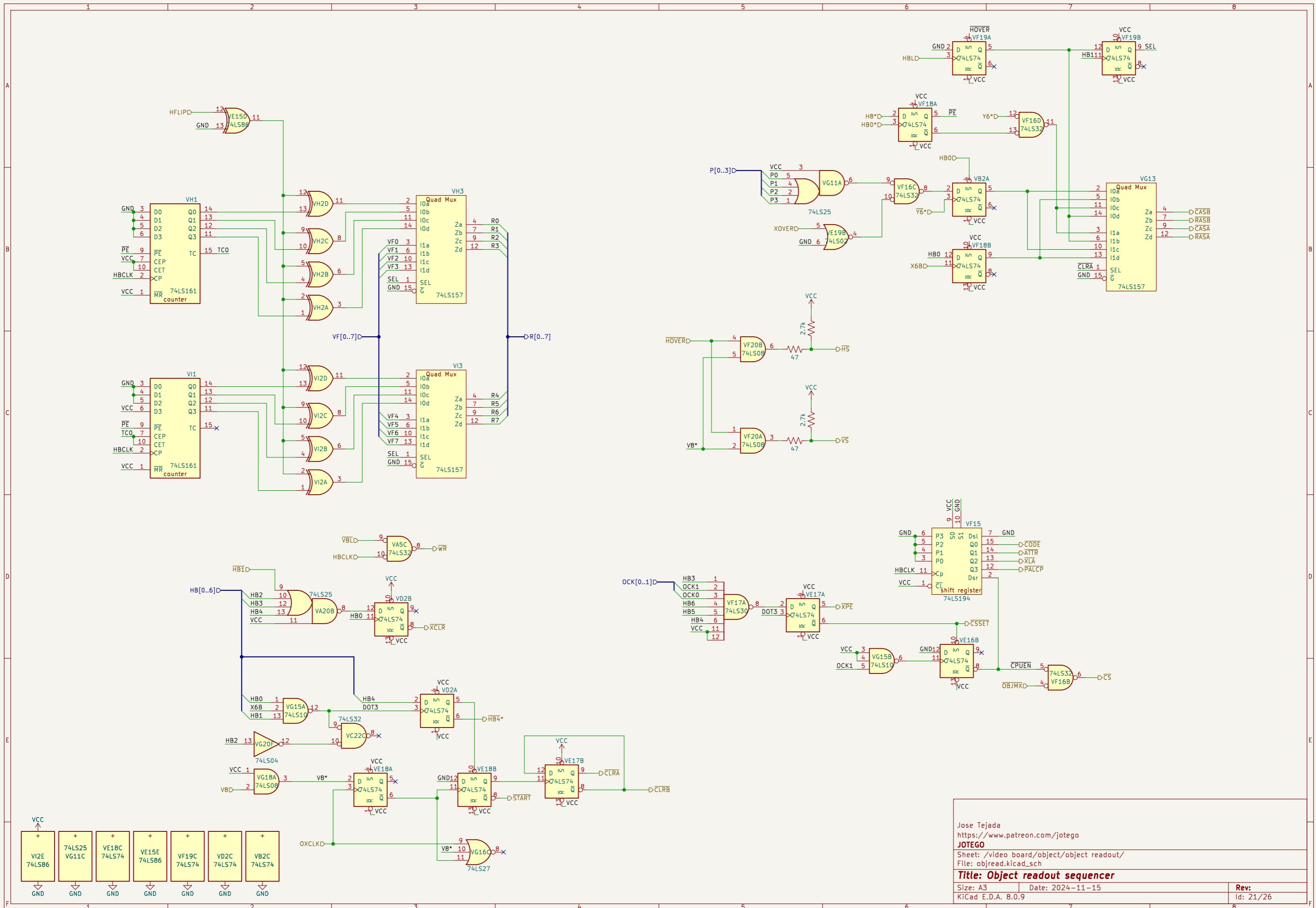
Sheet: /video board/object/object ROM/
File: objrom.kicad_sch

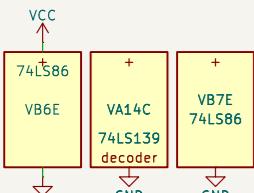
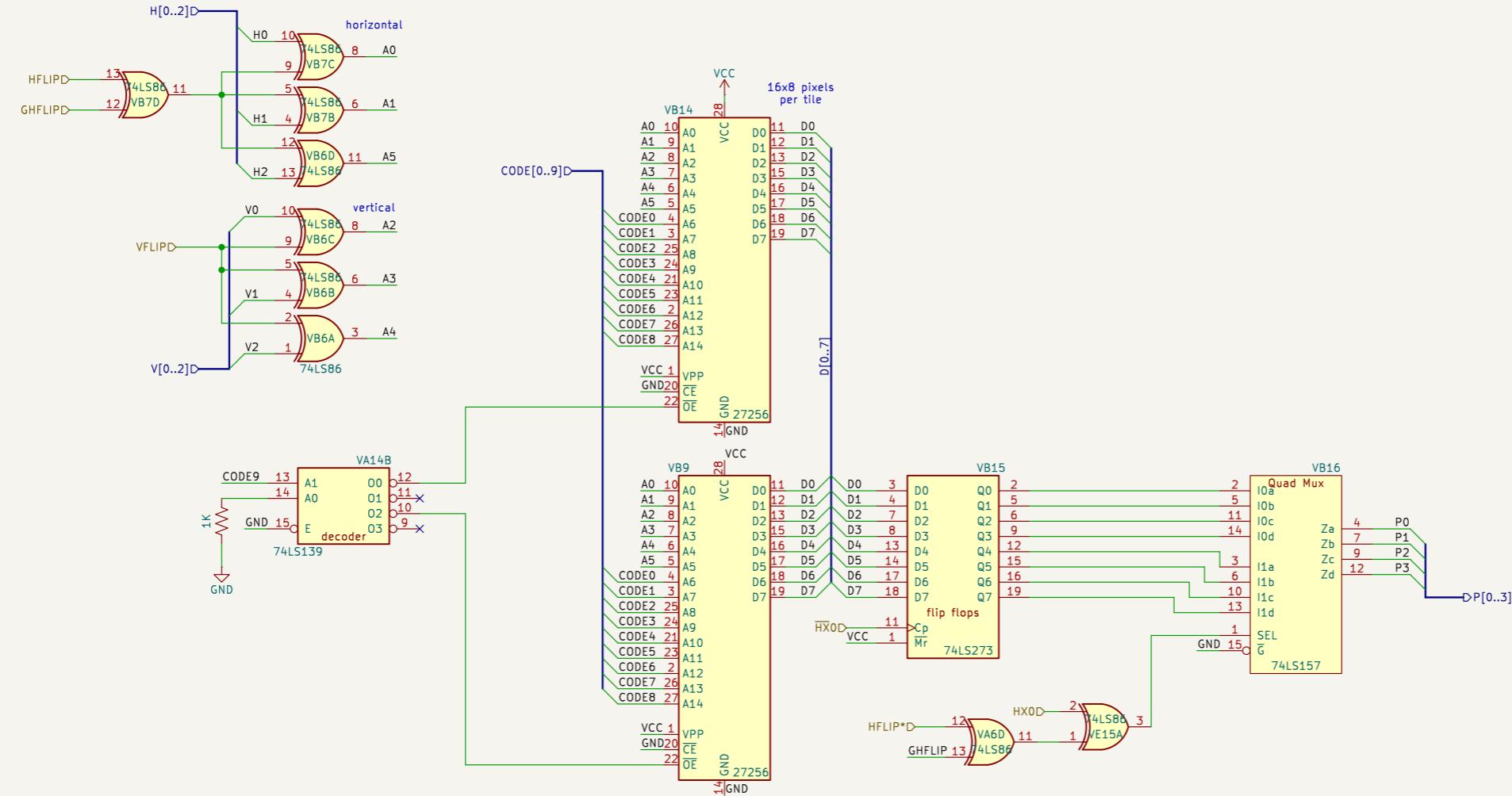
Title: Object ROM

Size: A3 | Date: 2024-11-15
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Rev: 19/26







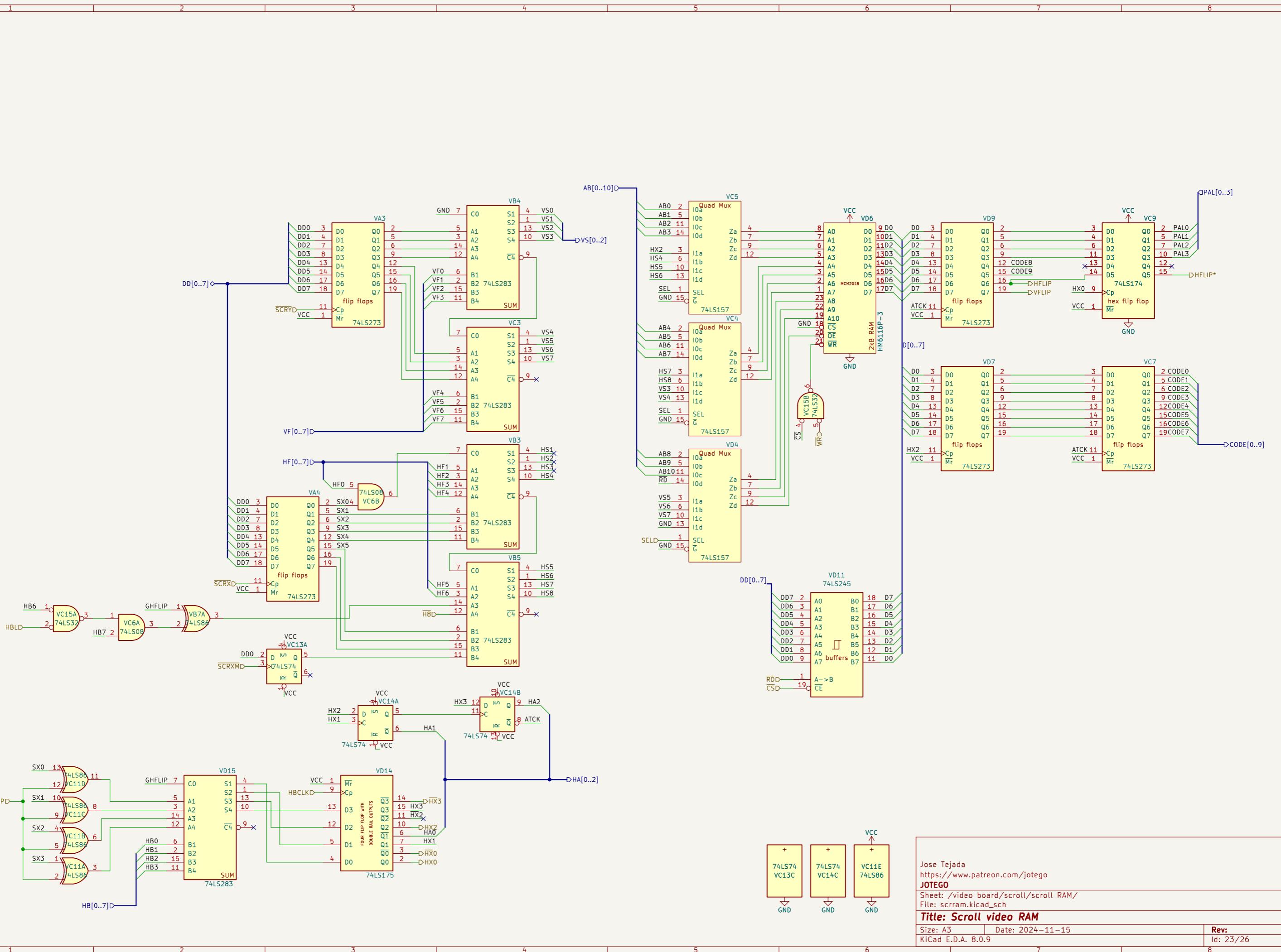
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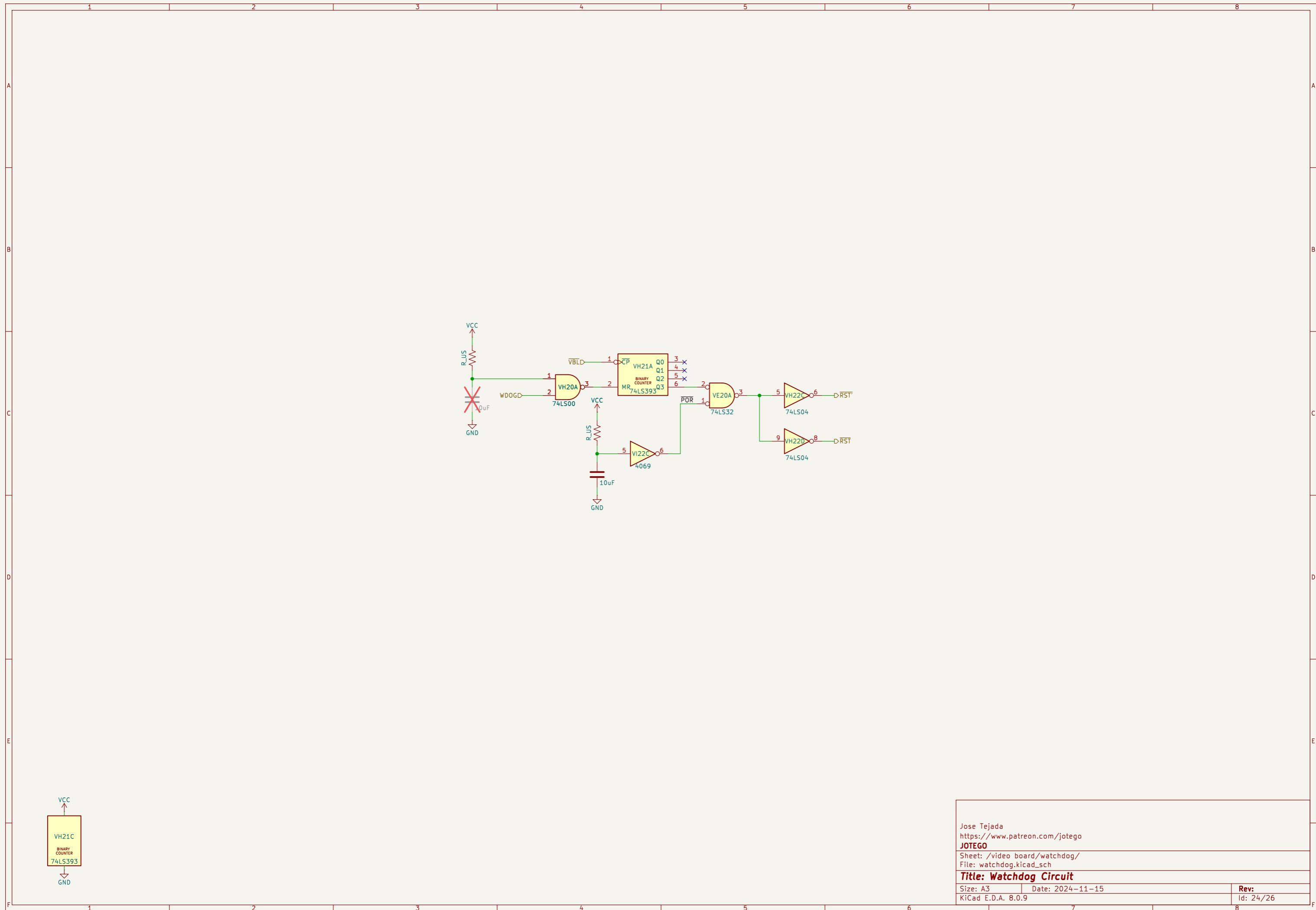
Sheet: /video board/scroll/scroll ROM/
File: scrom.kicad_sch

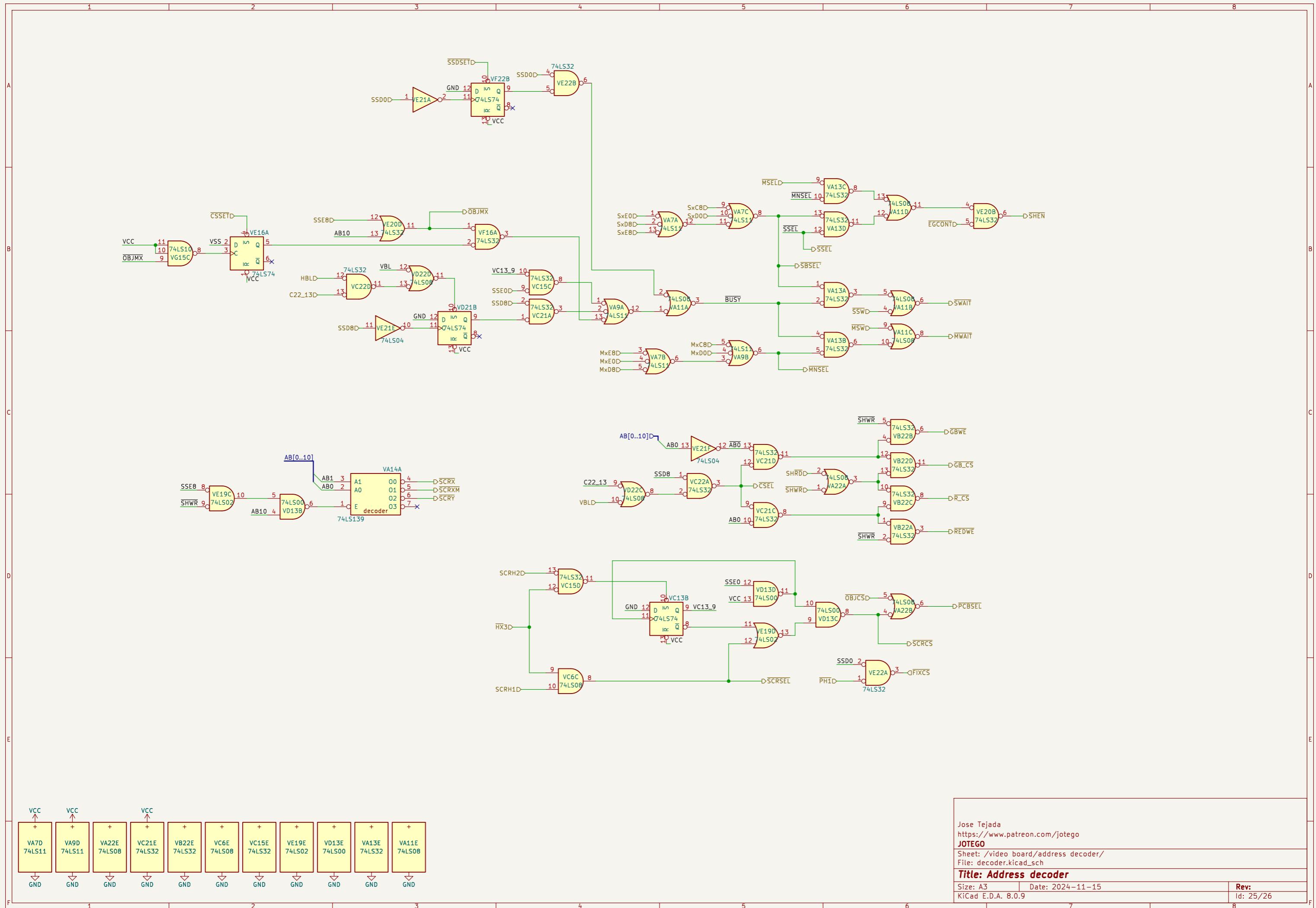
Title: scroll ROM

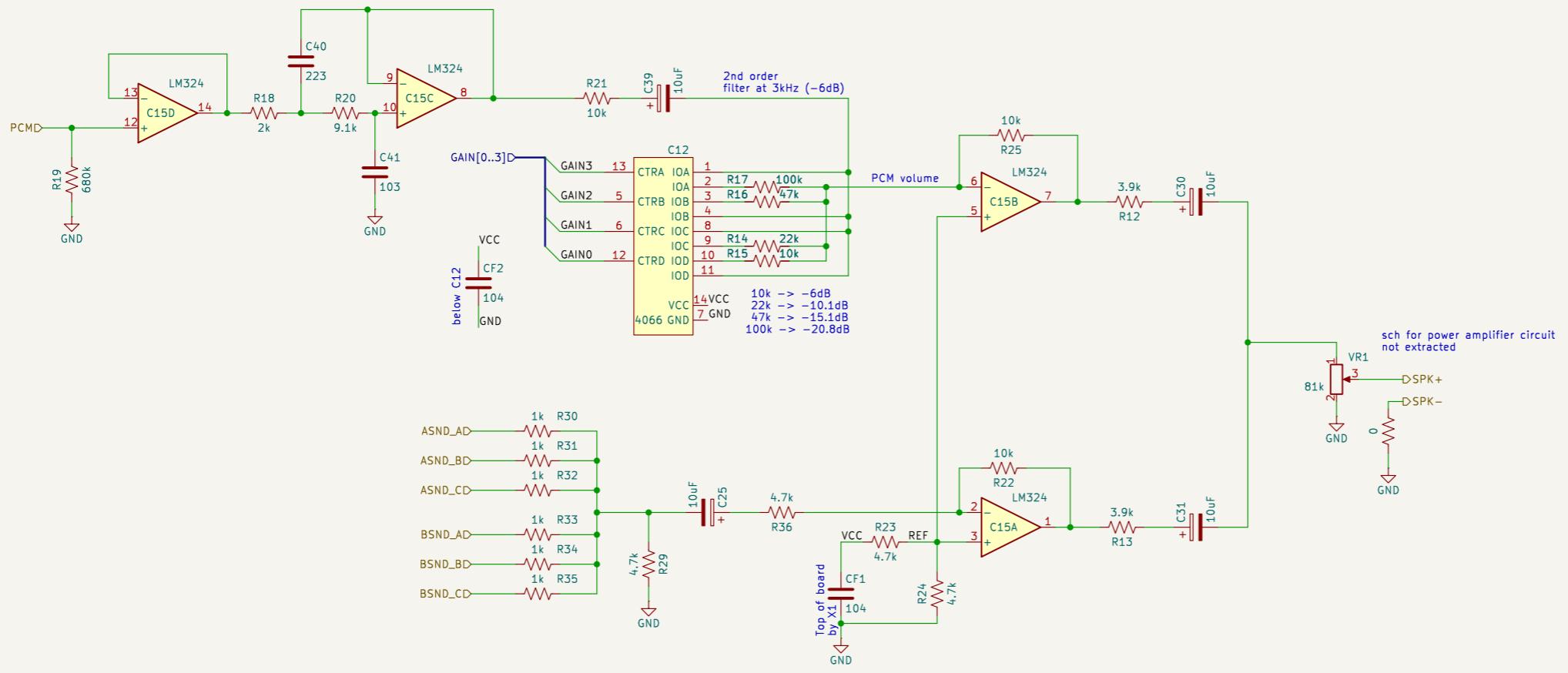
Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

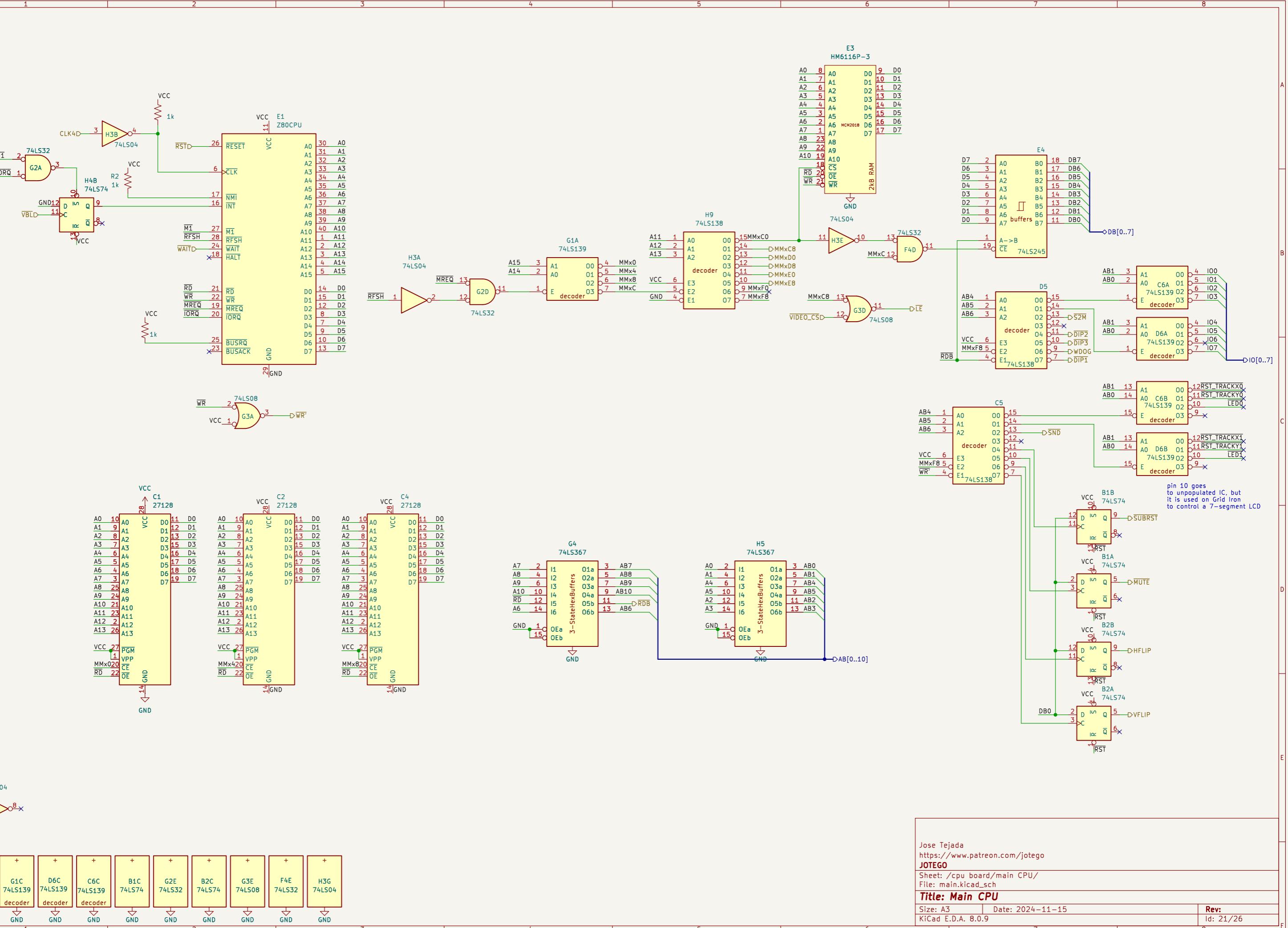
Rev:
Id: 22/26

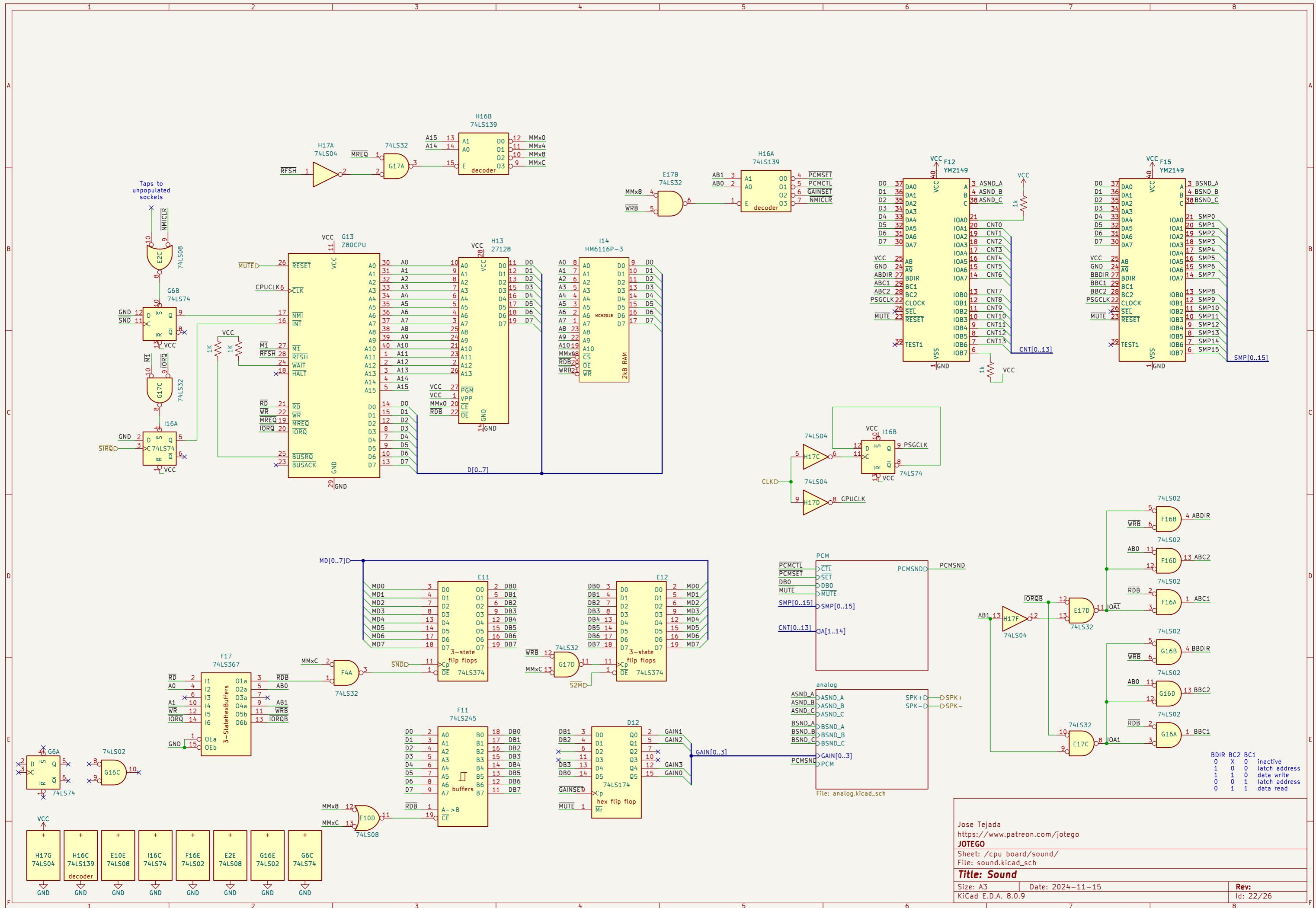


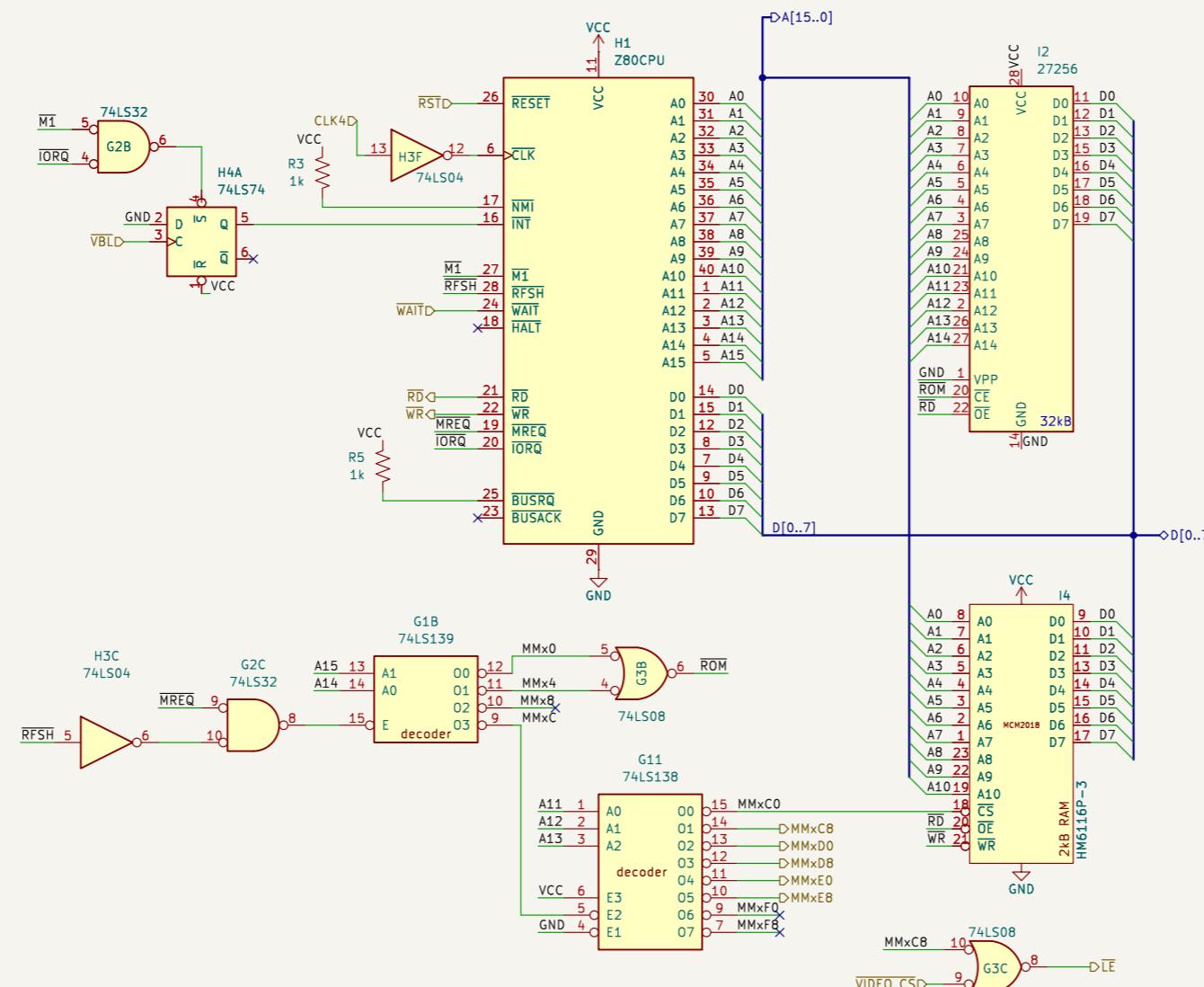












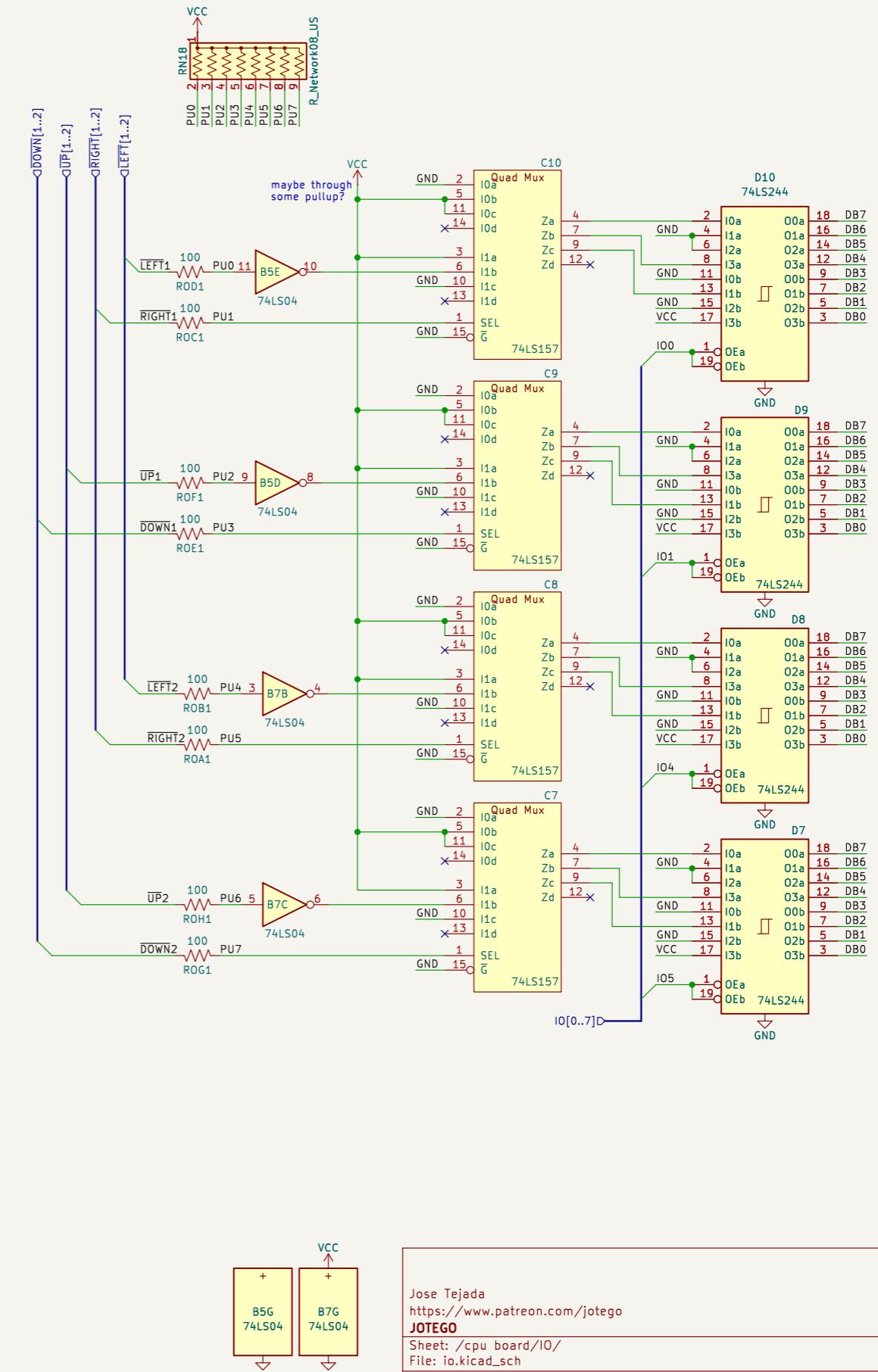
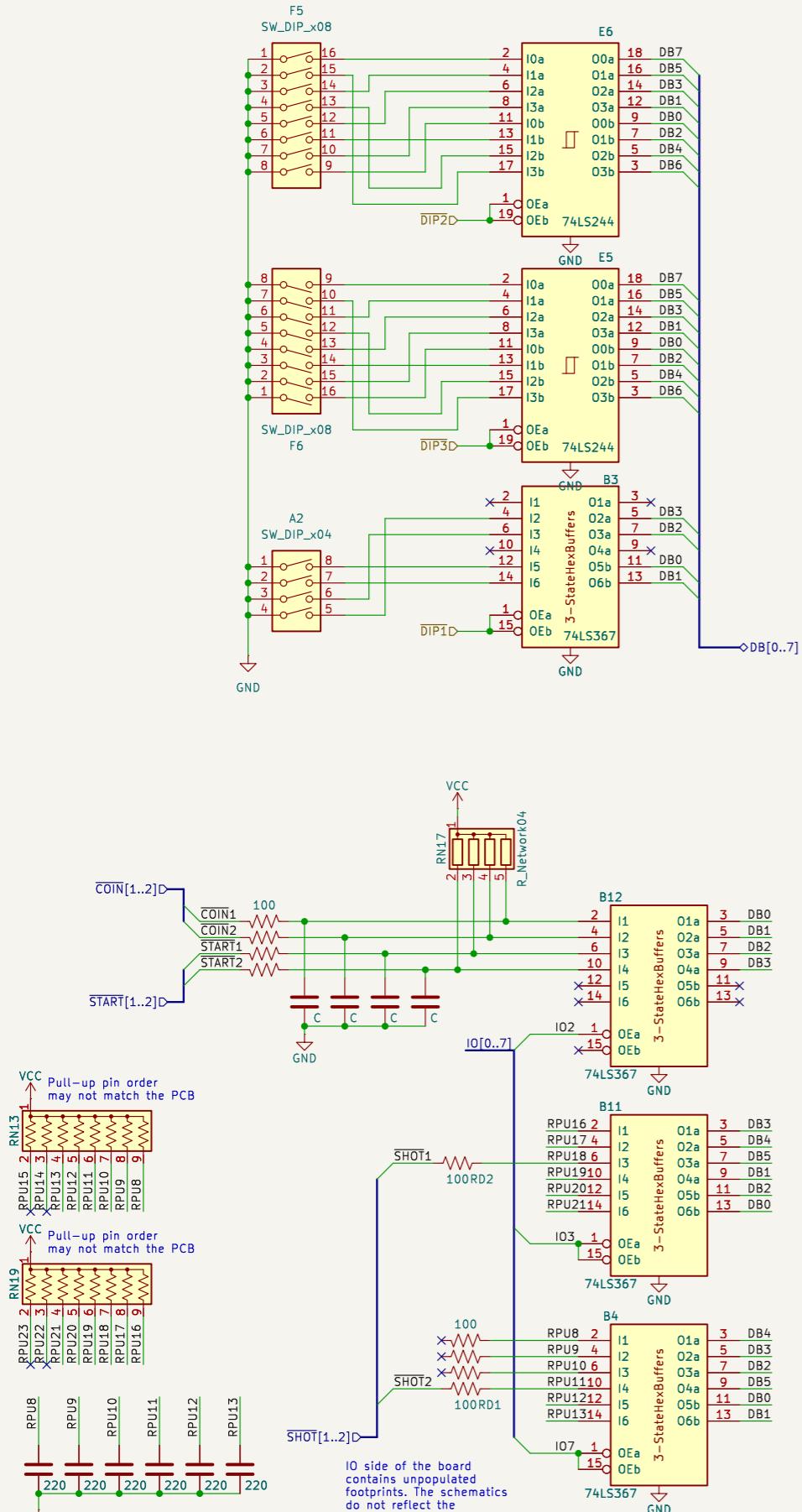
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Sheet: /cpu board/sub CPU/
File: subcpu.kicad_sch

Title: Subordinate CPU

Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

Rev:
Id: 23/26



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Sheet: /cpu_board/IO/
File: io.kicad_sch

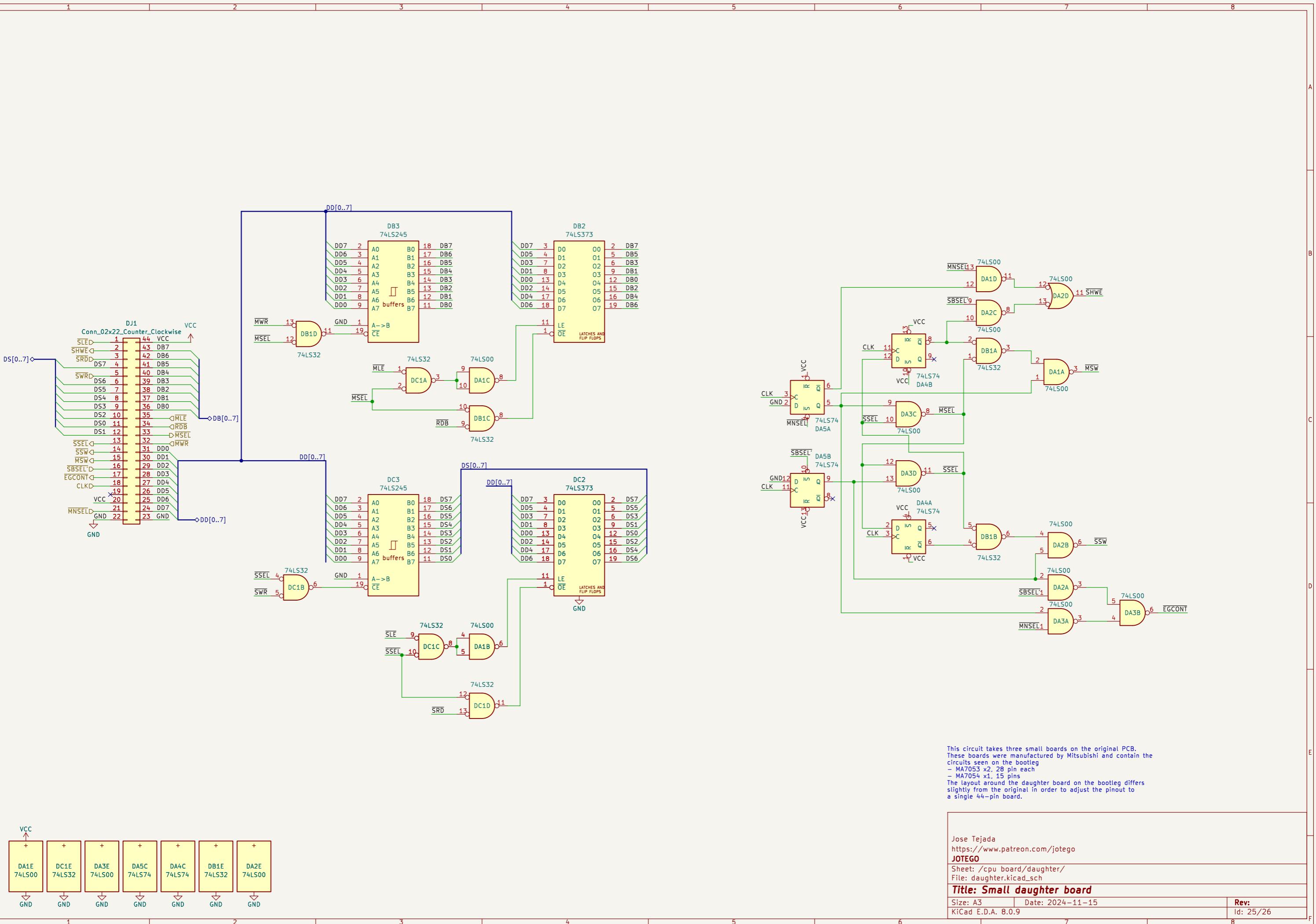
Title: Cabinet Input/Output

Size: A3 | Date: 2024-11-15

KiCad E.D.A. 8.0.9

Rev:

Id: 24/26



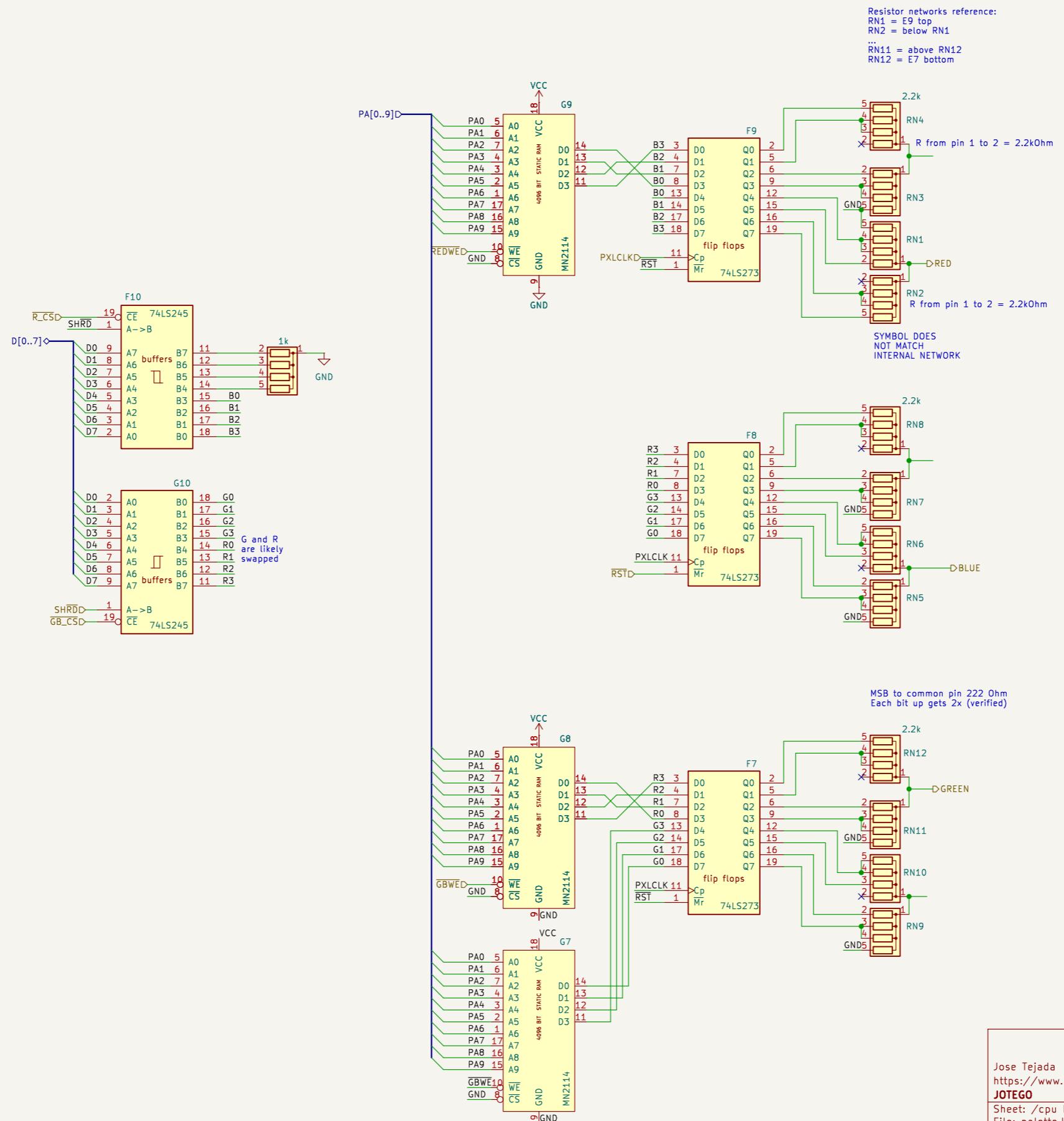
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Sheet: /cpu_board/daughter/
File: daughter.kicad_sch

Title: Small daughter board

Size: A3 | Date: 2024-11-15
KiCad E.D.A. 8.0.9

Rev: 25/26



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Title: Color Palette

Size: A3 Date: 2024-11-15
KiCad E.D.A. 8.0.9