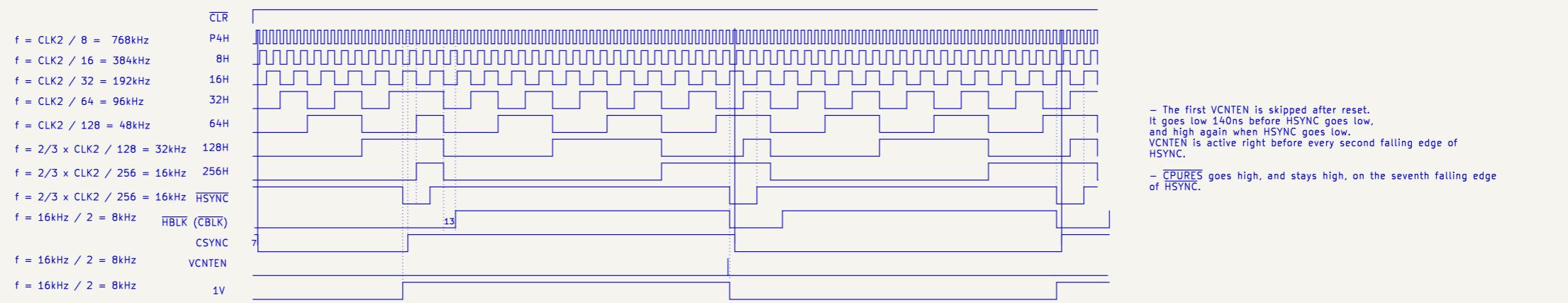


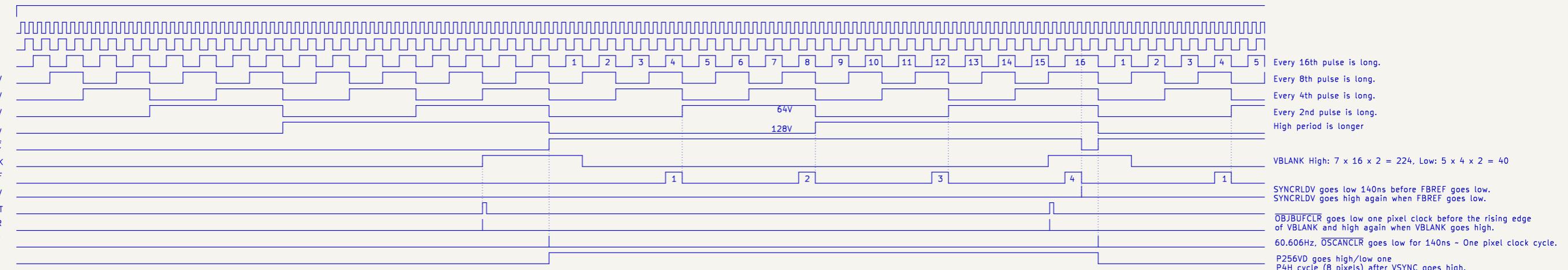
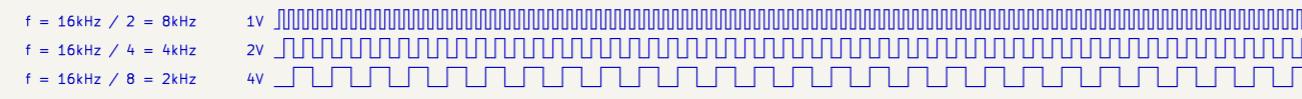
Horizontal signals



- The first VCOUNTEN is skipped after reset.
It goes low 140ns before HSYNC goes low,
and high again when HSYNC goes low.
VCOUNTEN is active right before every second falling edge of
HSYNC.

- CPURES goes high, and stays high, on the seventh falling edge
of HSYNC.

Vertical signals



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Sheet: /Timing diagrams/
File: timing_diagrams.kicad_sch

Title: The Final Round

Size: A3 Date: 2023-05-11
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Rev: Id: 11/12

A

B

C

D

E

F

A

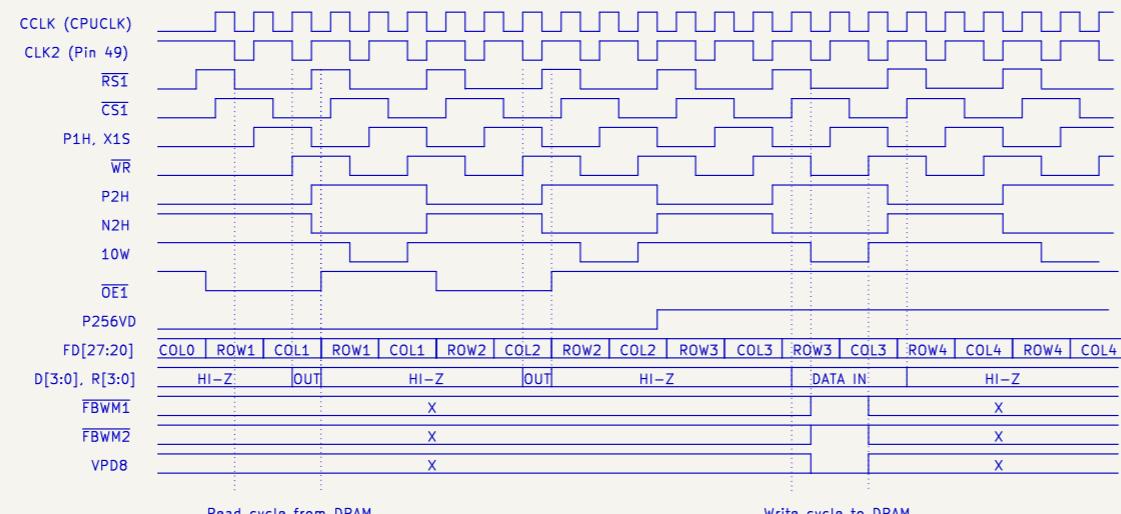
B

C

D

E

F



The same row and column addresses are repeated twice.
One for read cycle and the other for write cycles.

Konami GX870

Sheet: /Sprite timing diagrams/
File: sprite_timing_diagrams.kicad_sch

Title: The Final Round

Size: A3	Date: 2023-05-11
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