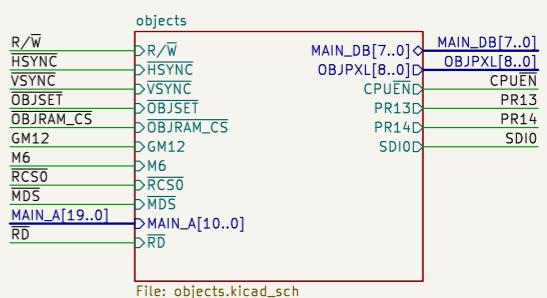
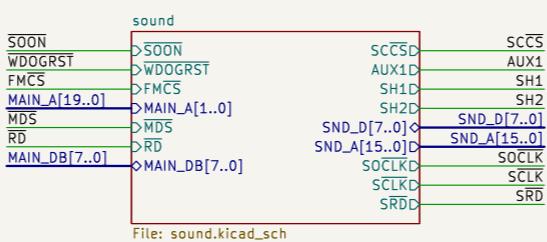
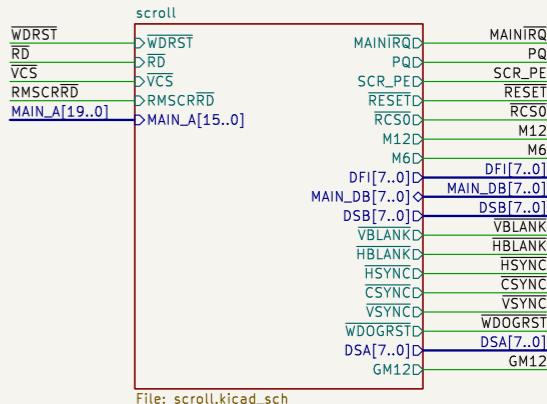
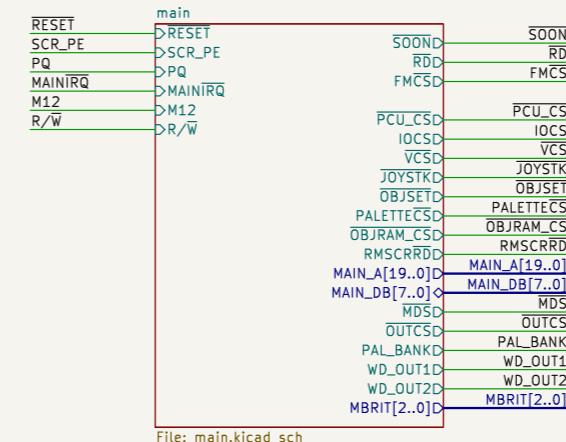
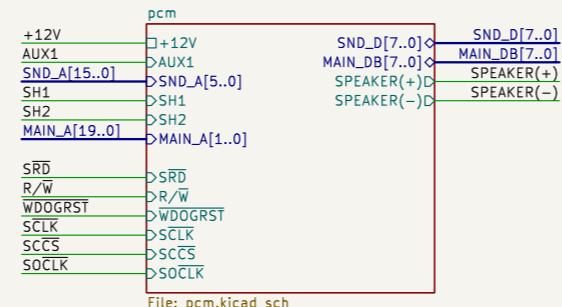
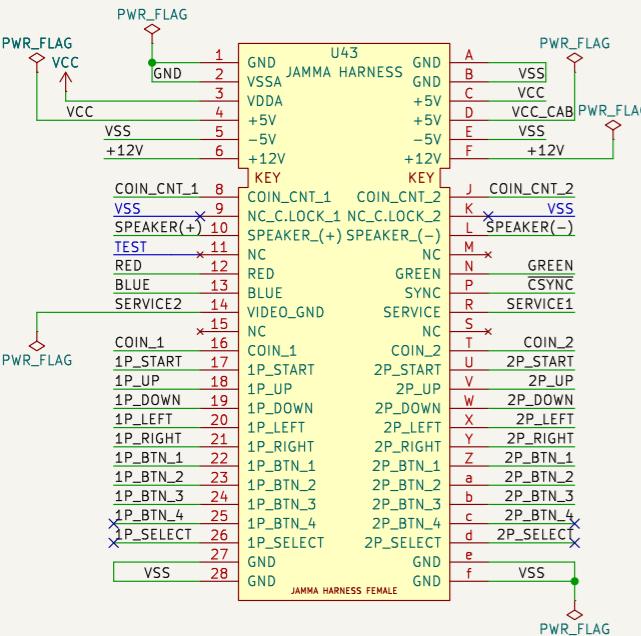


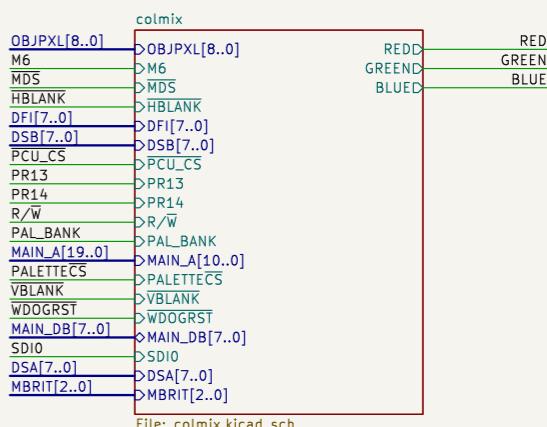
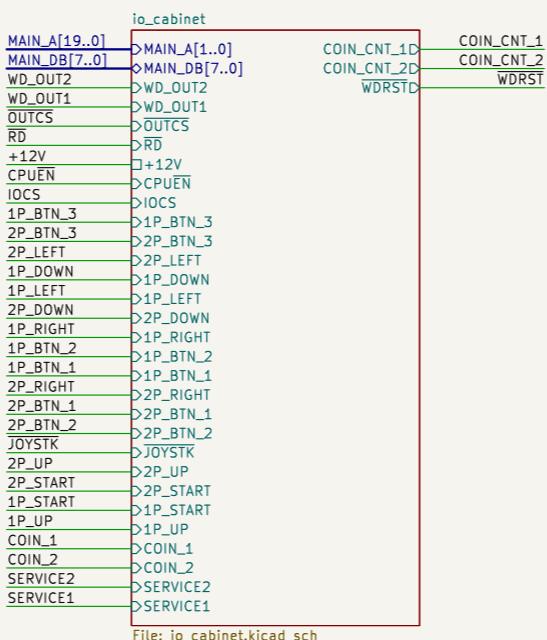
A



B



C



PWB352020B

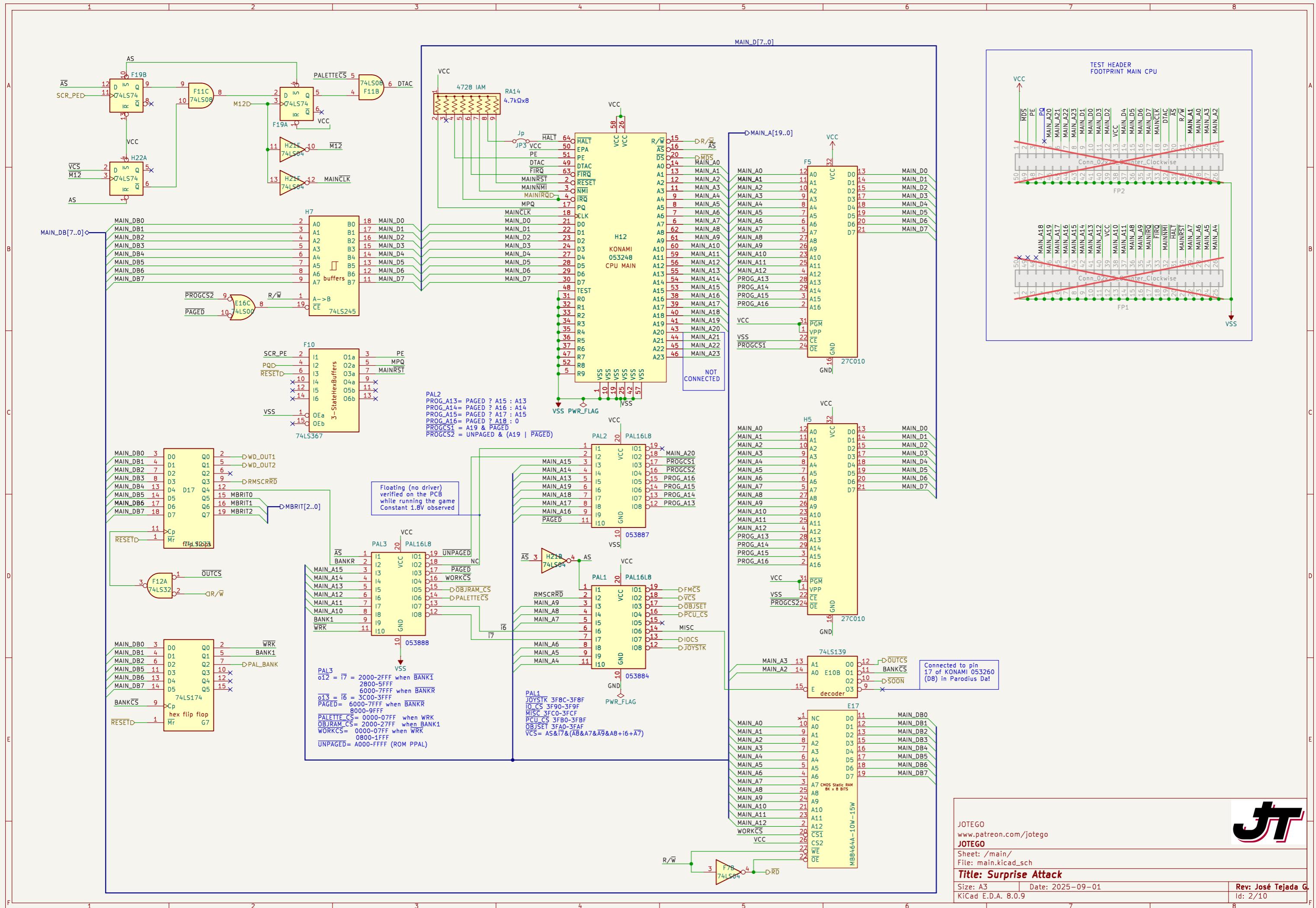
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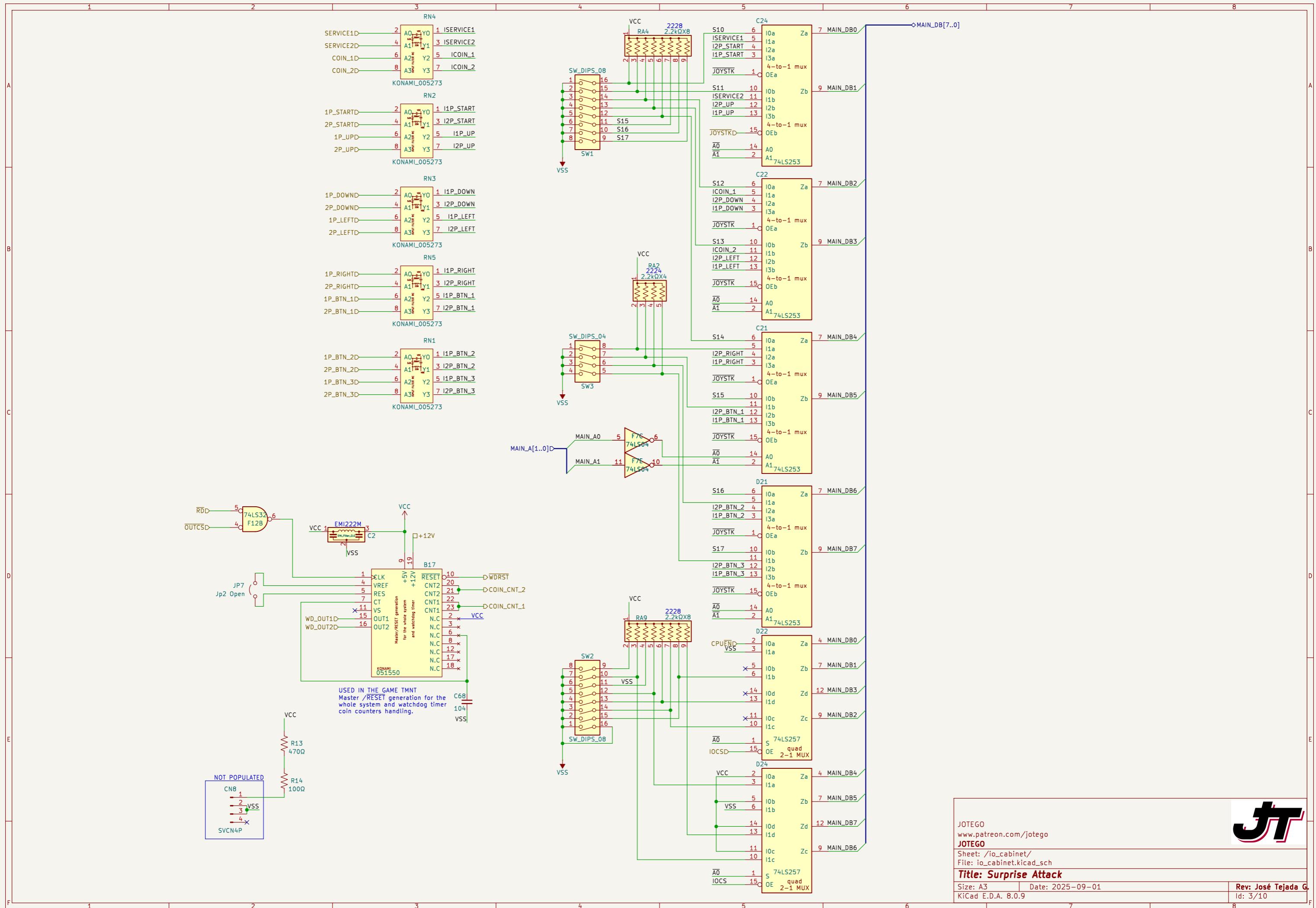
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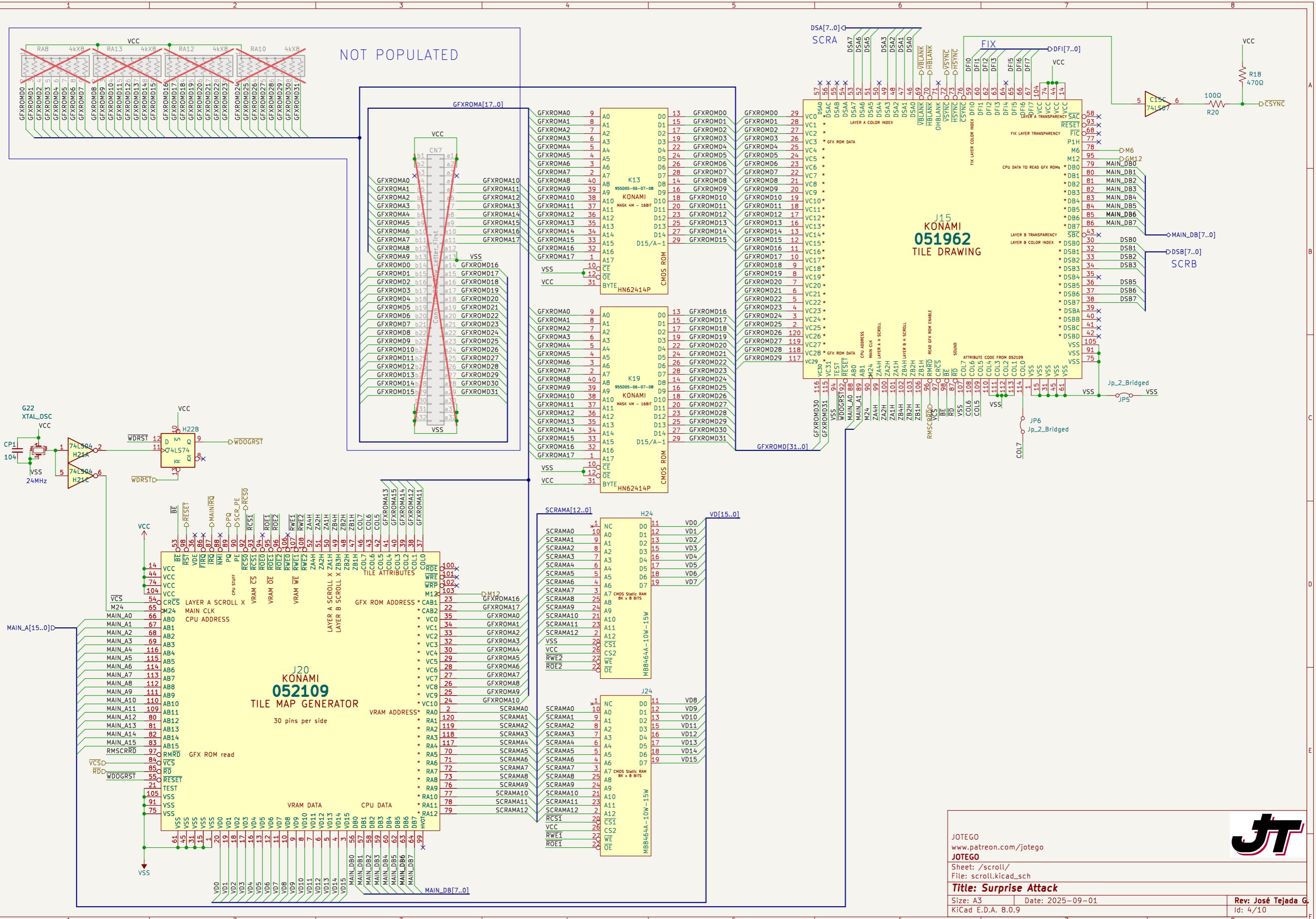
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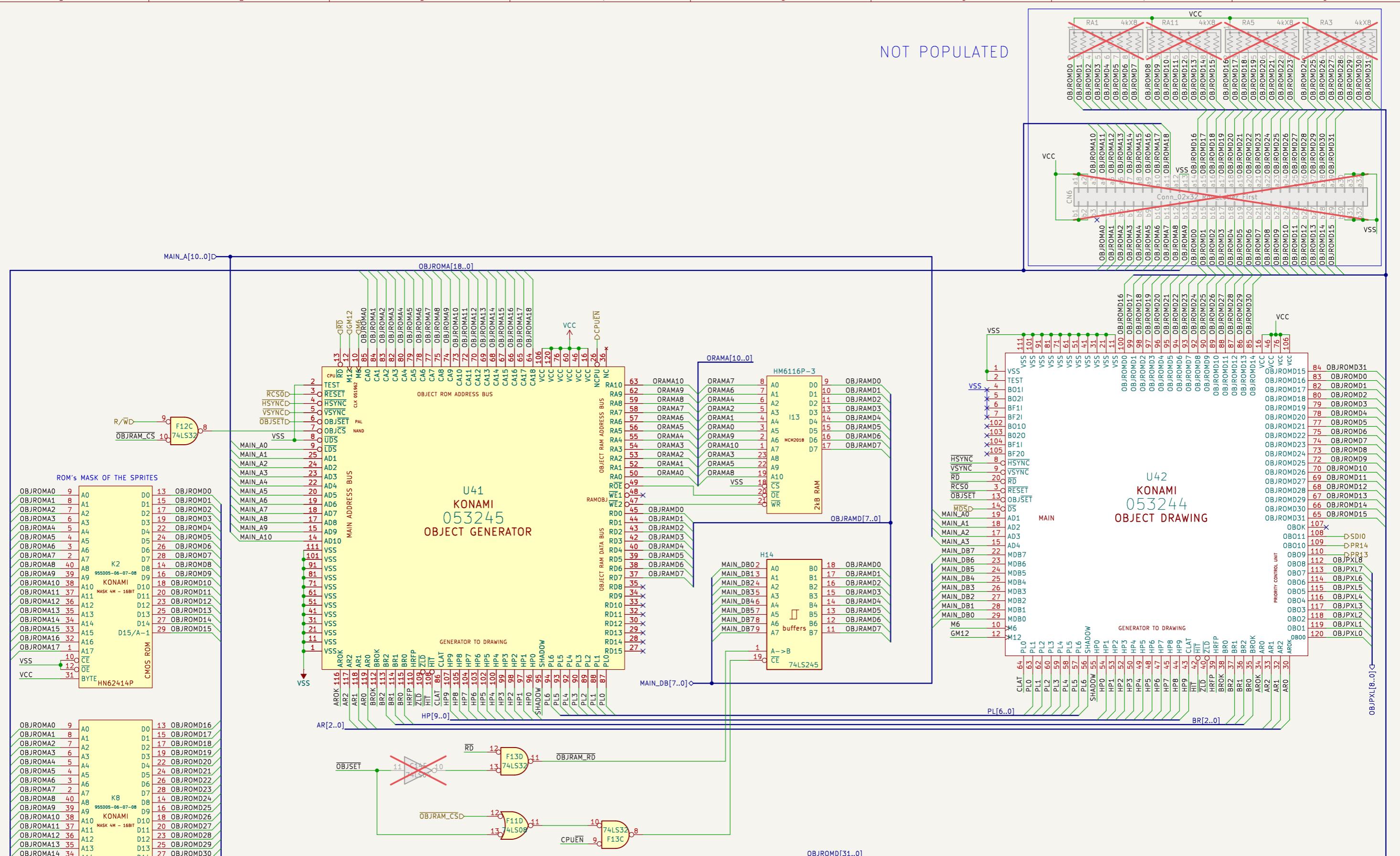


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Title: Surprise Attack

Title: Surprise Attack

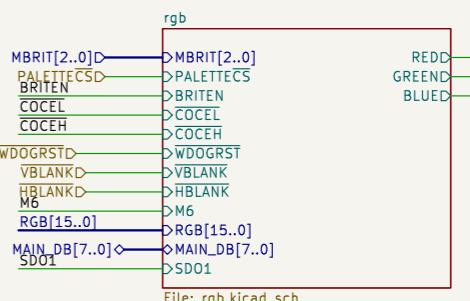
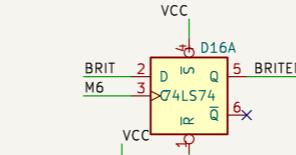
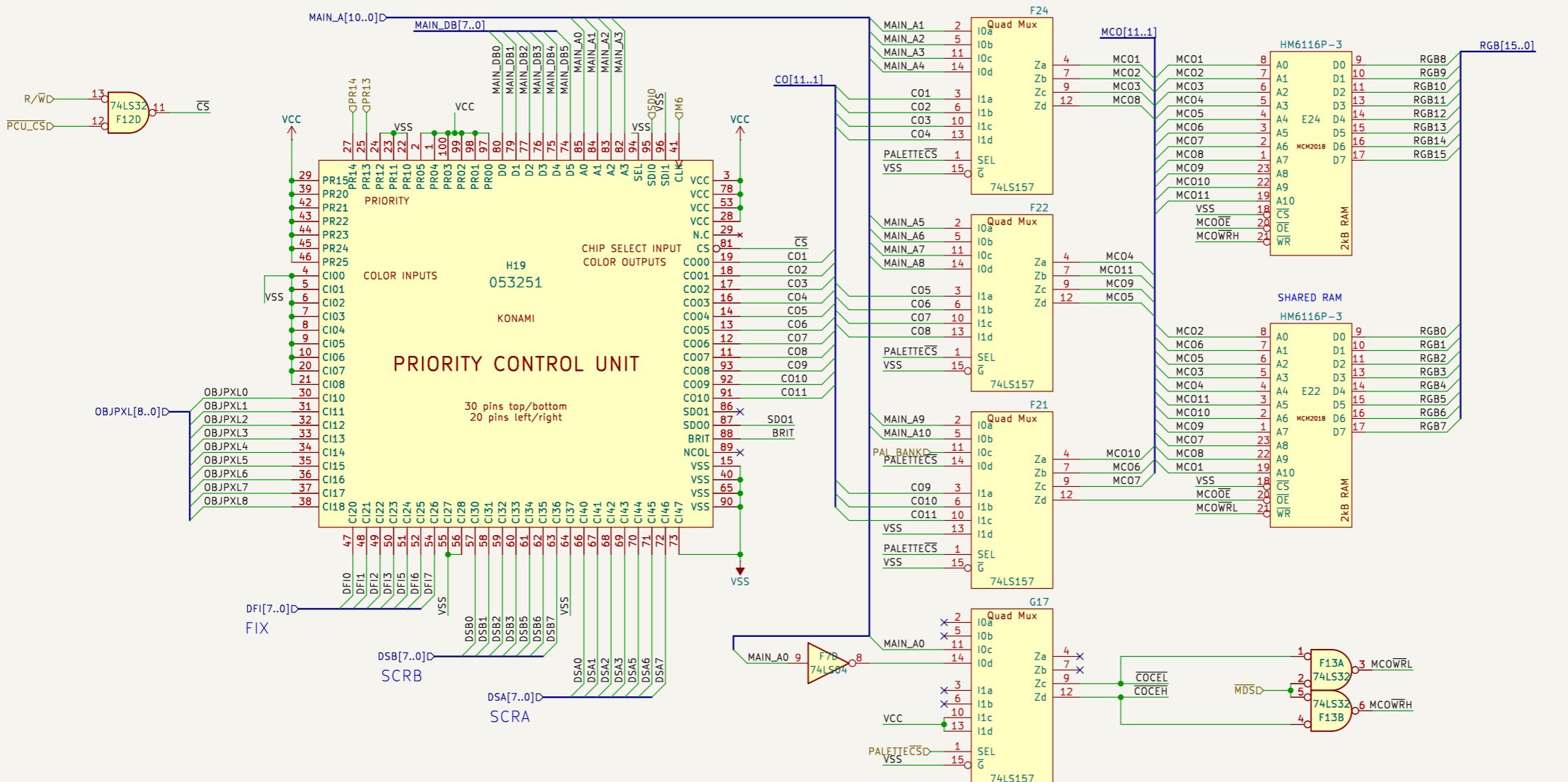
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7

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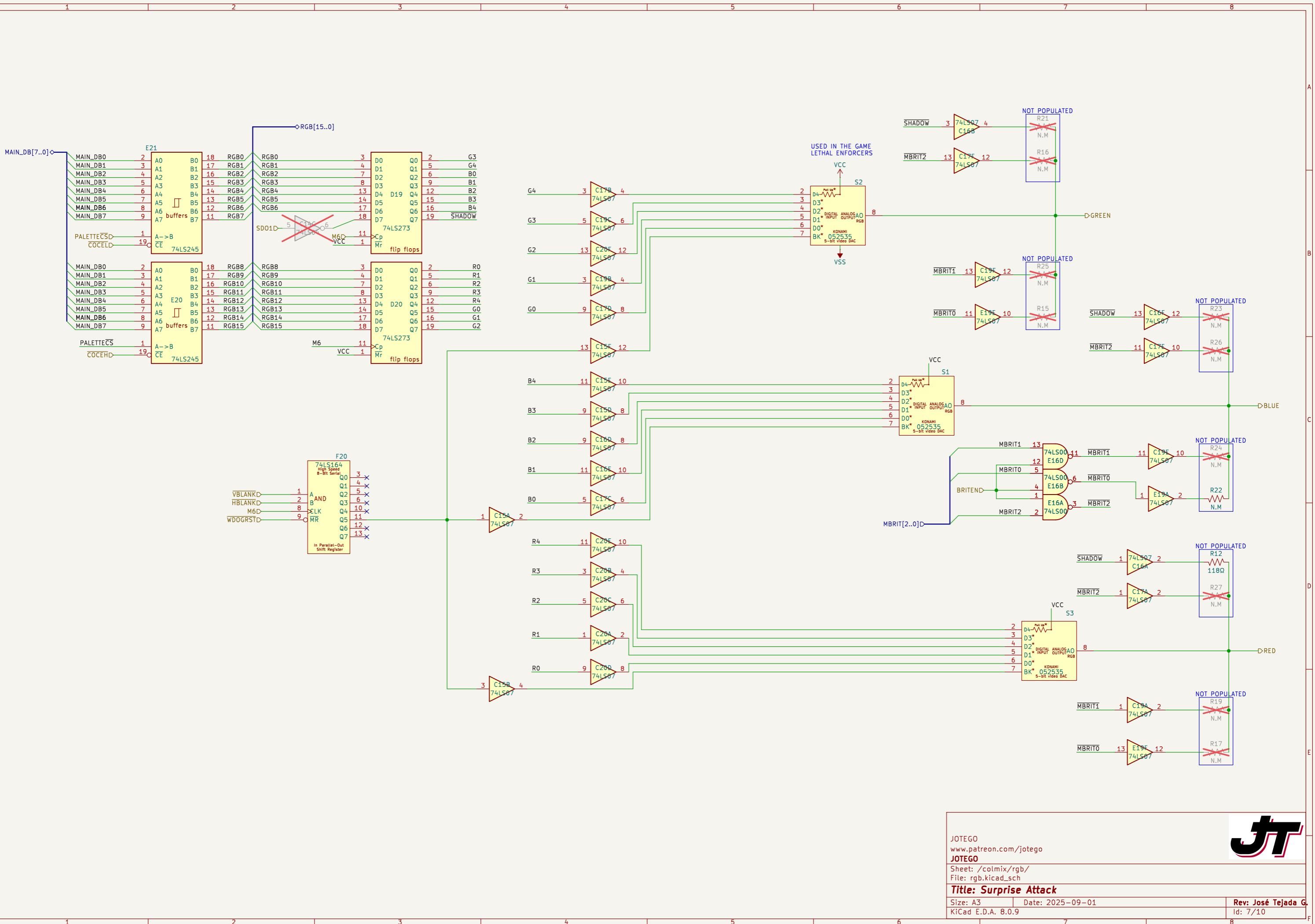
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1 / 1

JT



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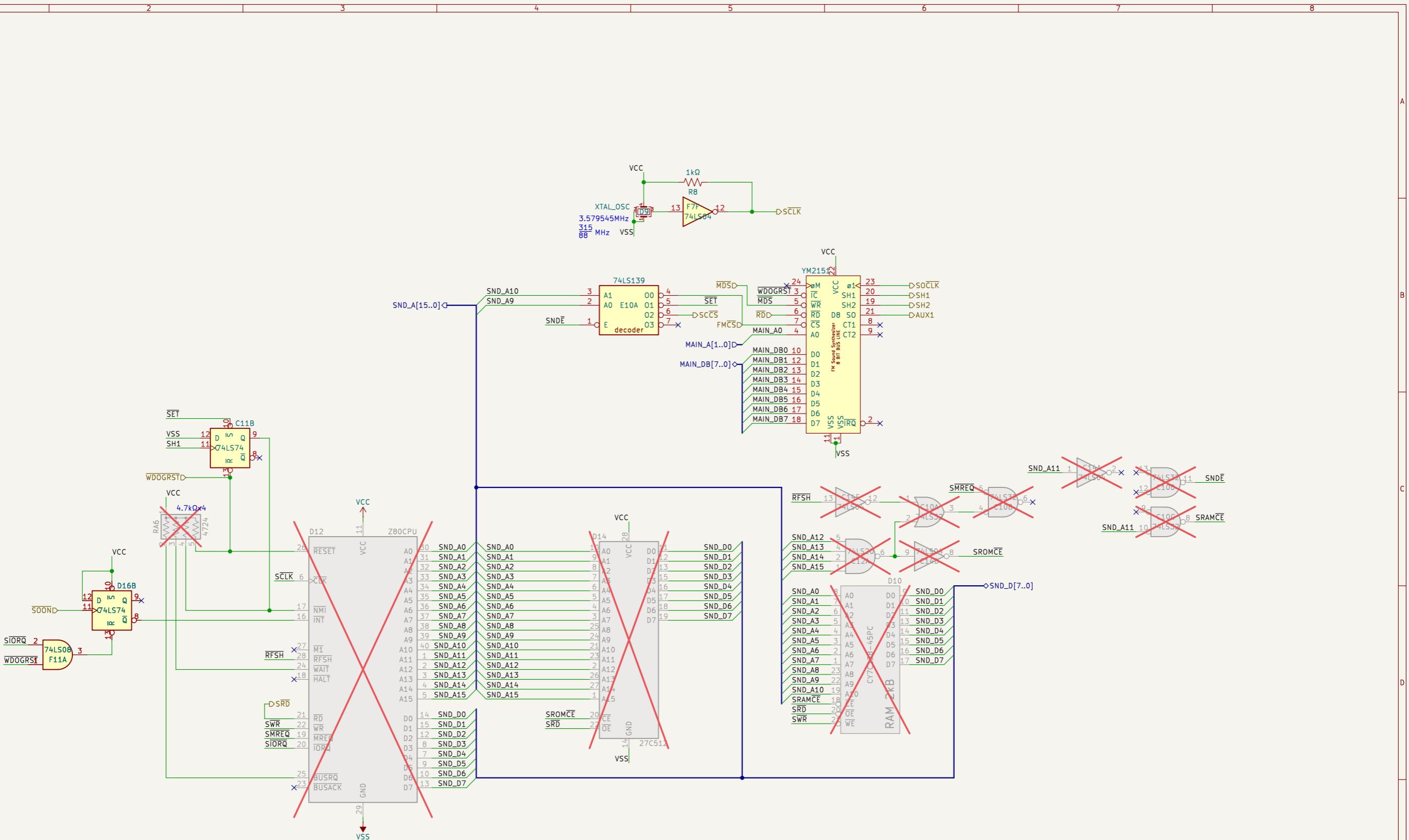


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**Title: Surprise Attack**

Size: A3 | Date: 2025-09-01  
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Id: 8/10

1 2 3 4 5 6 7 8

NOT POPULATED

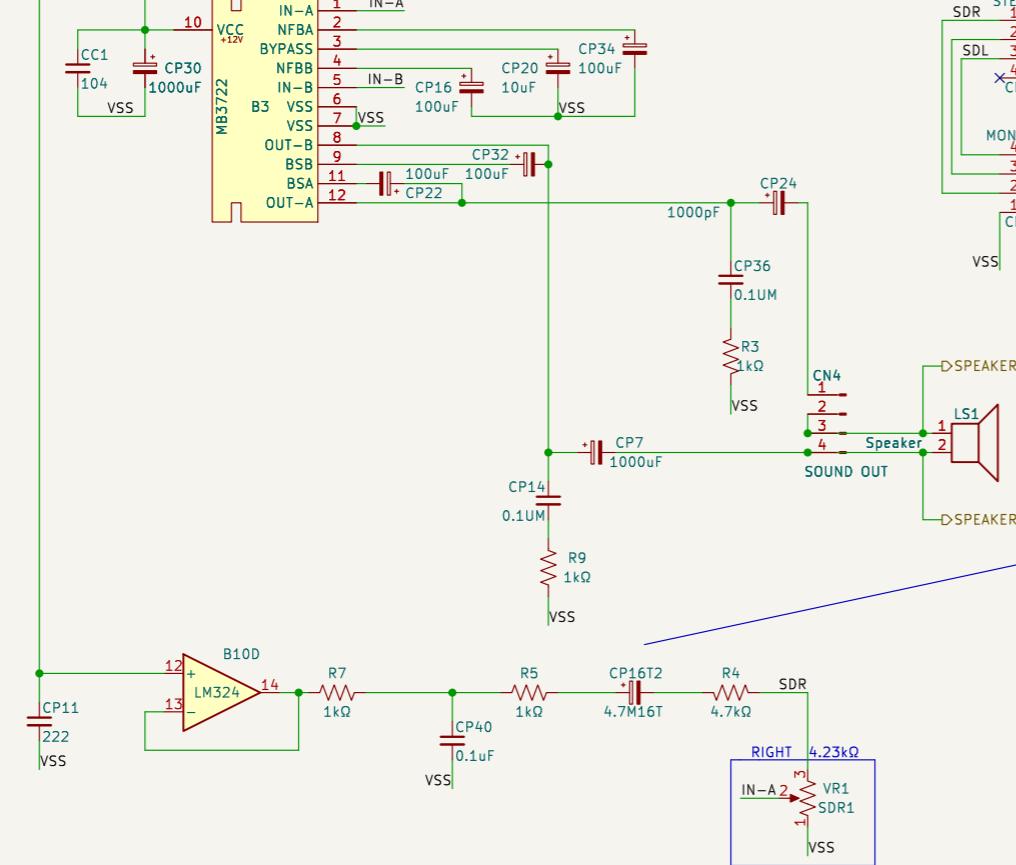
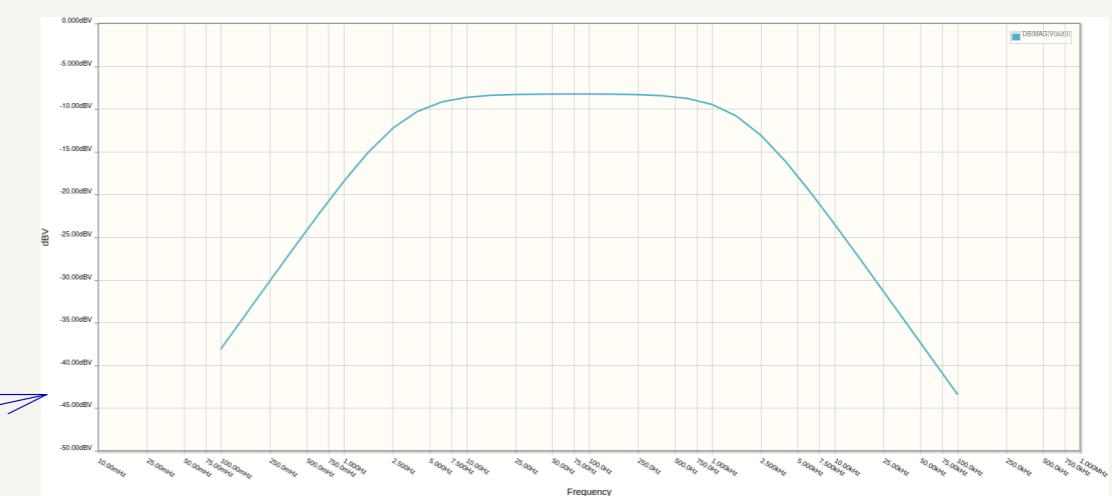
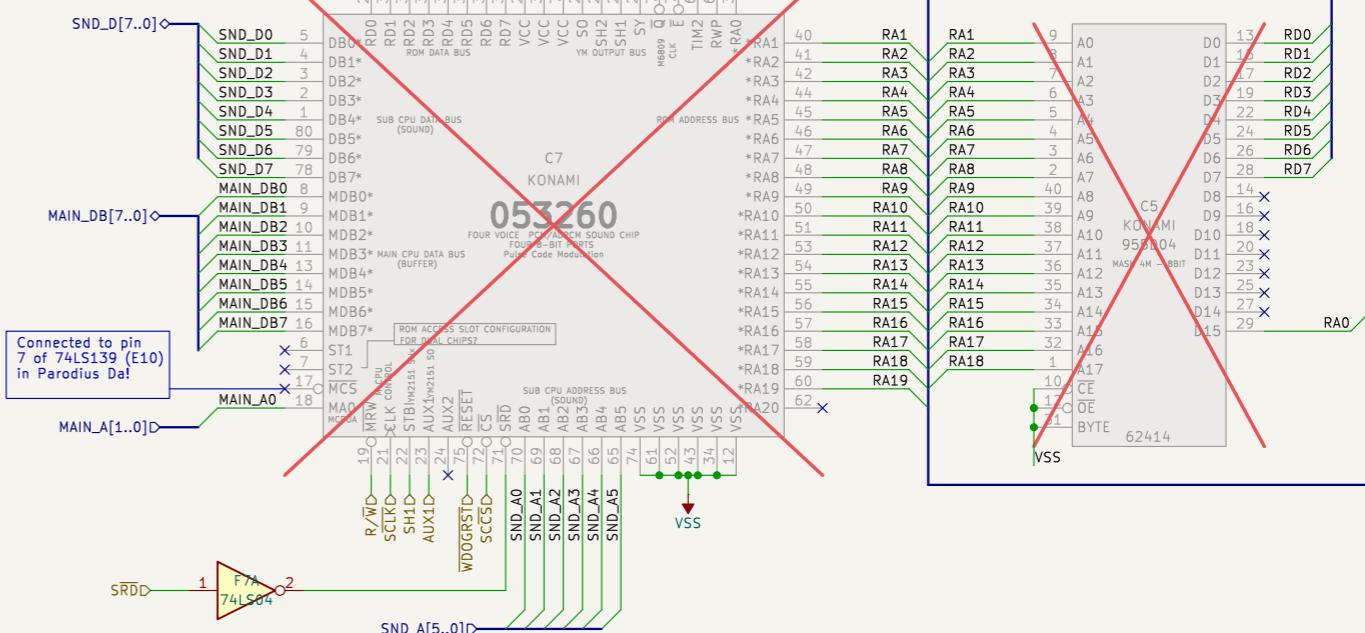
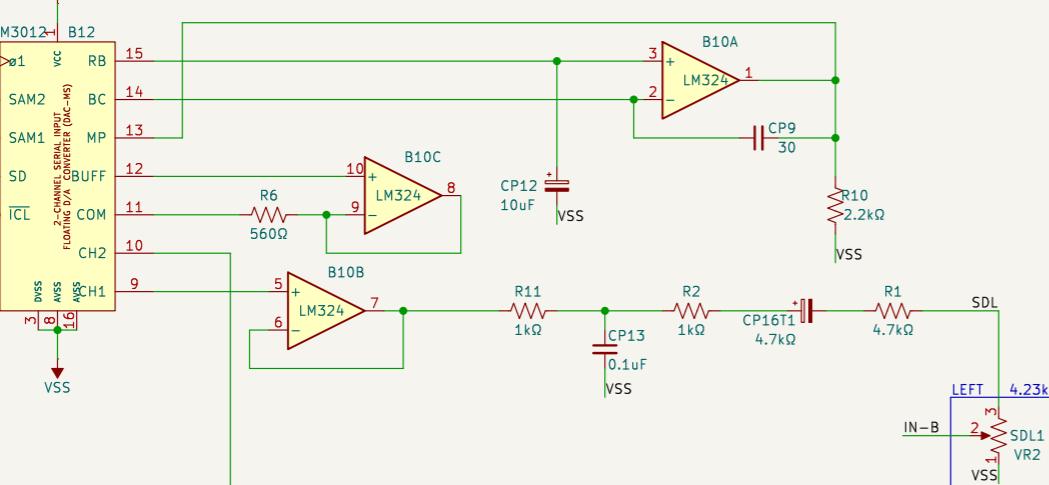
RA[19..0]

Communication between the main CPU  
and the audio CPU.

RD[7..0]

Connected to pin 21 of alternate YM2151 footprint.

JP\_2\_Bridged



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Sheet: /pcm/  
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Title: Surprise Attack

Size: A3 Date: 2025-09-01  
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JT

Rev: José Tejada G.  
Id: 9/10

1 2 3 4 5 6 7 8

A A

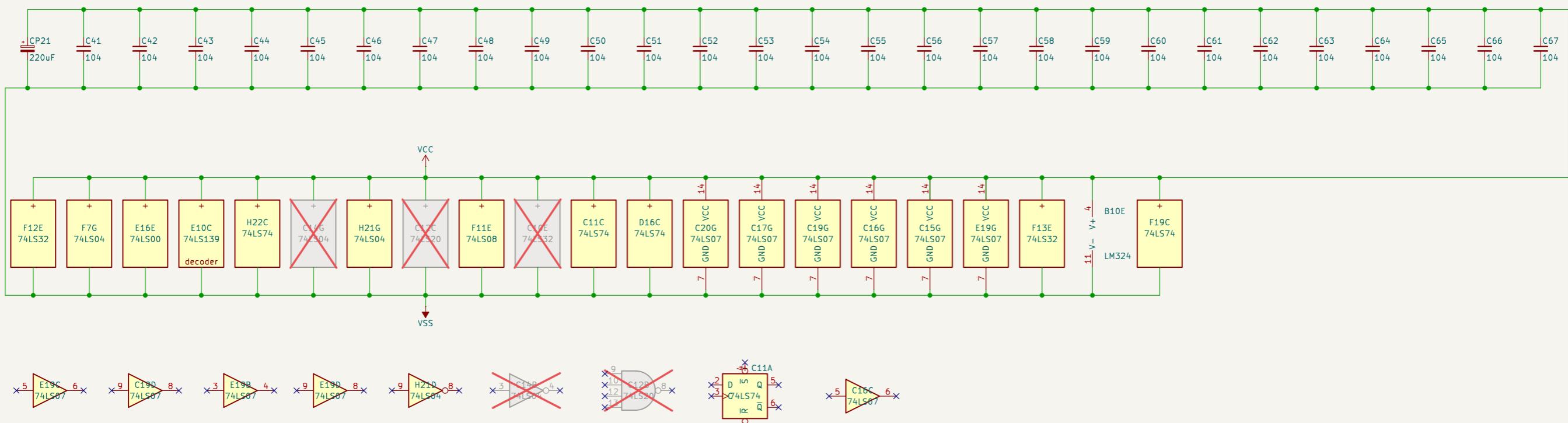
B B

C C

D D

E E

F F



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Id: 10/10

