

A

A

B

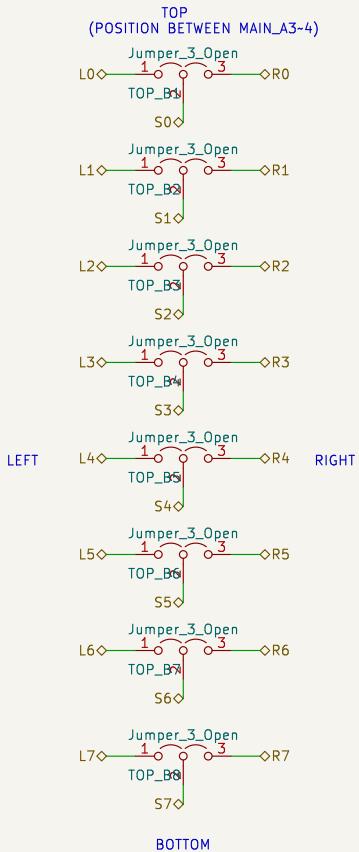
B

C

C

D

D



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JOTEGO

Sheet: /ROM board/scrjumpers/
File: romjp.kicad_sch

Title: SEGA System 18 MODEL 837-7178

Size: A4

Date: 2022-03-18

KiCad E.D.A. 8.0.8

Rev: José Tejada

Id: 3/26

A

A

B

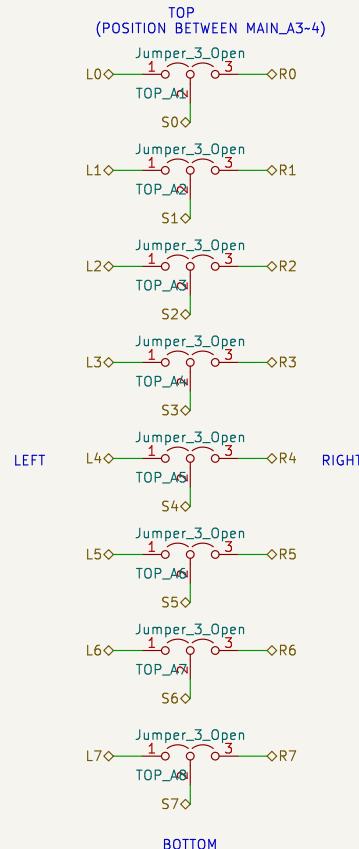
B

C

C

D

D


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JOTEGO
 Sheet: /ROM board/sndjumpers/
 File: romjp.kicad_sch
**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.8

Rev: José Tejada

Id: 4/26

A

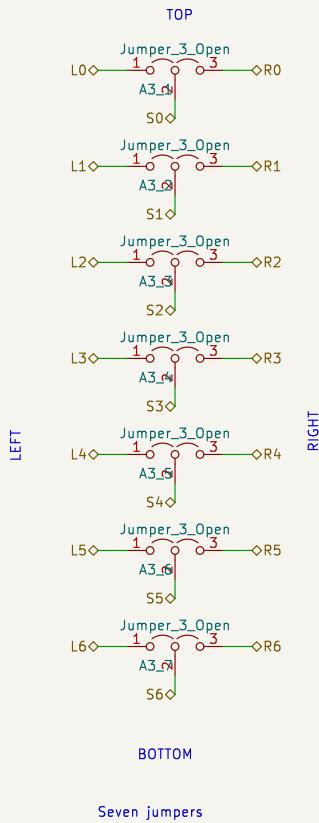
A

B

B

C

C



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Sheet: /ROM board/objjumpers/
File: romjp7.kicad_sch

Title: SEGA System 18 MODEL 837-7178

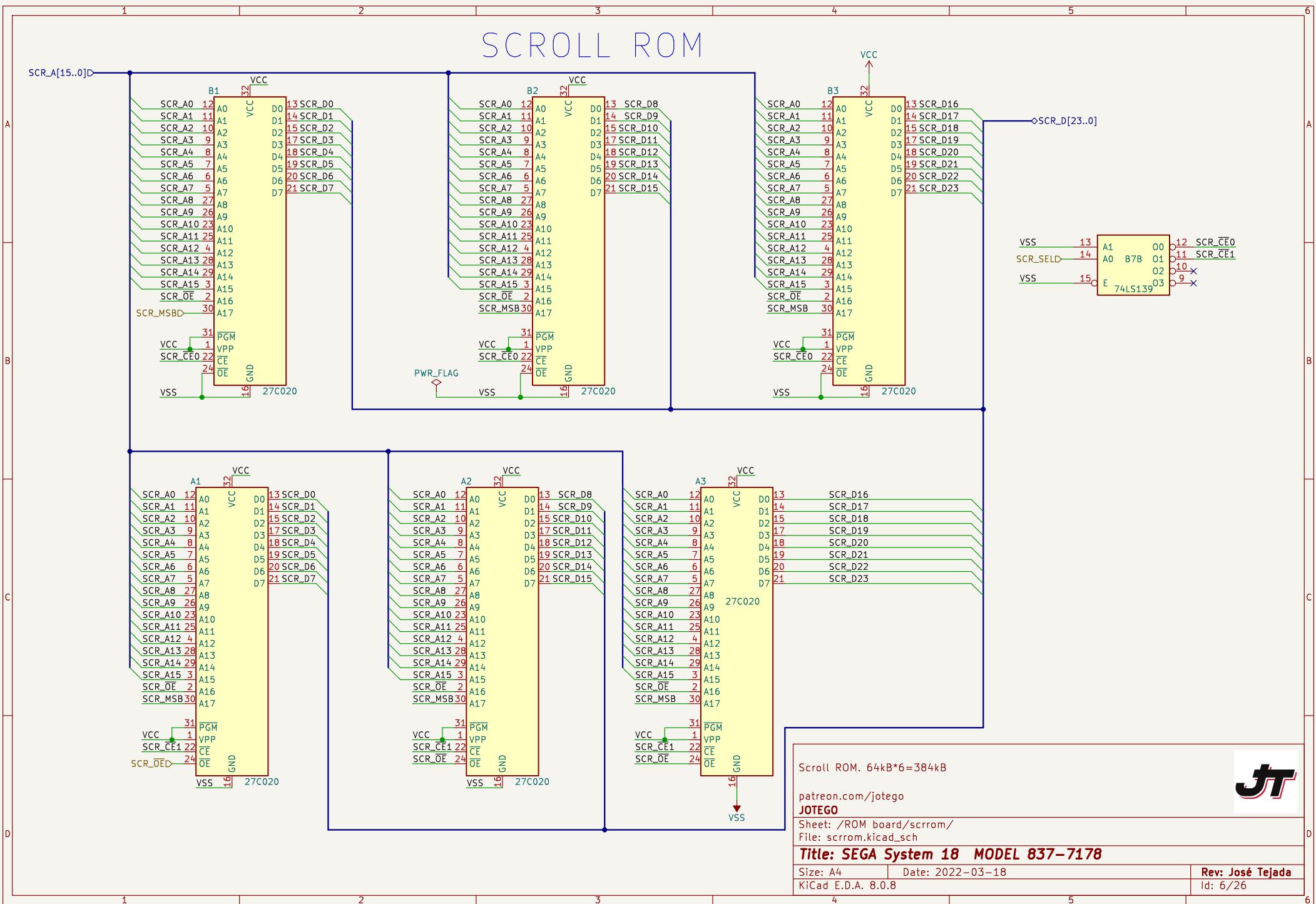
Size: A4 Date: 2022-03-18

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Rev: José Tejada

Id: 5/26

SCROLL ROM



Scroll ROM. 64kB*6=384kB

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Sheet: /ROM board/scrrom/

File: scrrom.kicad_sch

Title: SEGA System 18 MODEL 837-7178

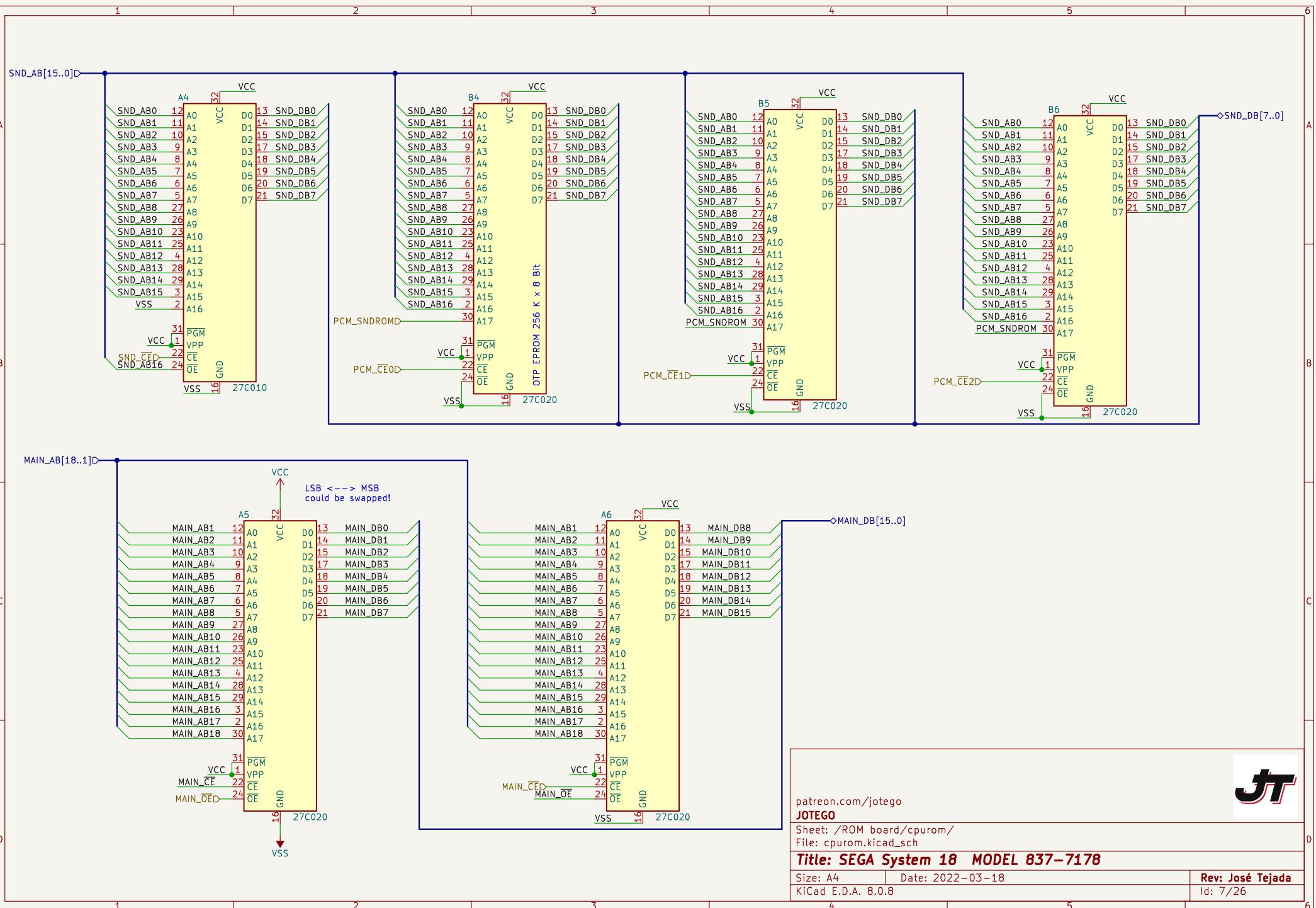
Size: A4 Date: 2022-03-18

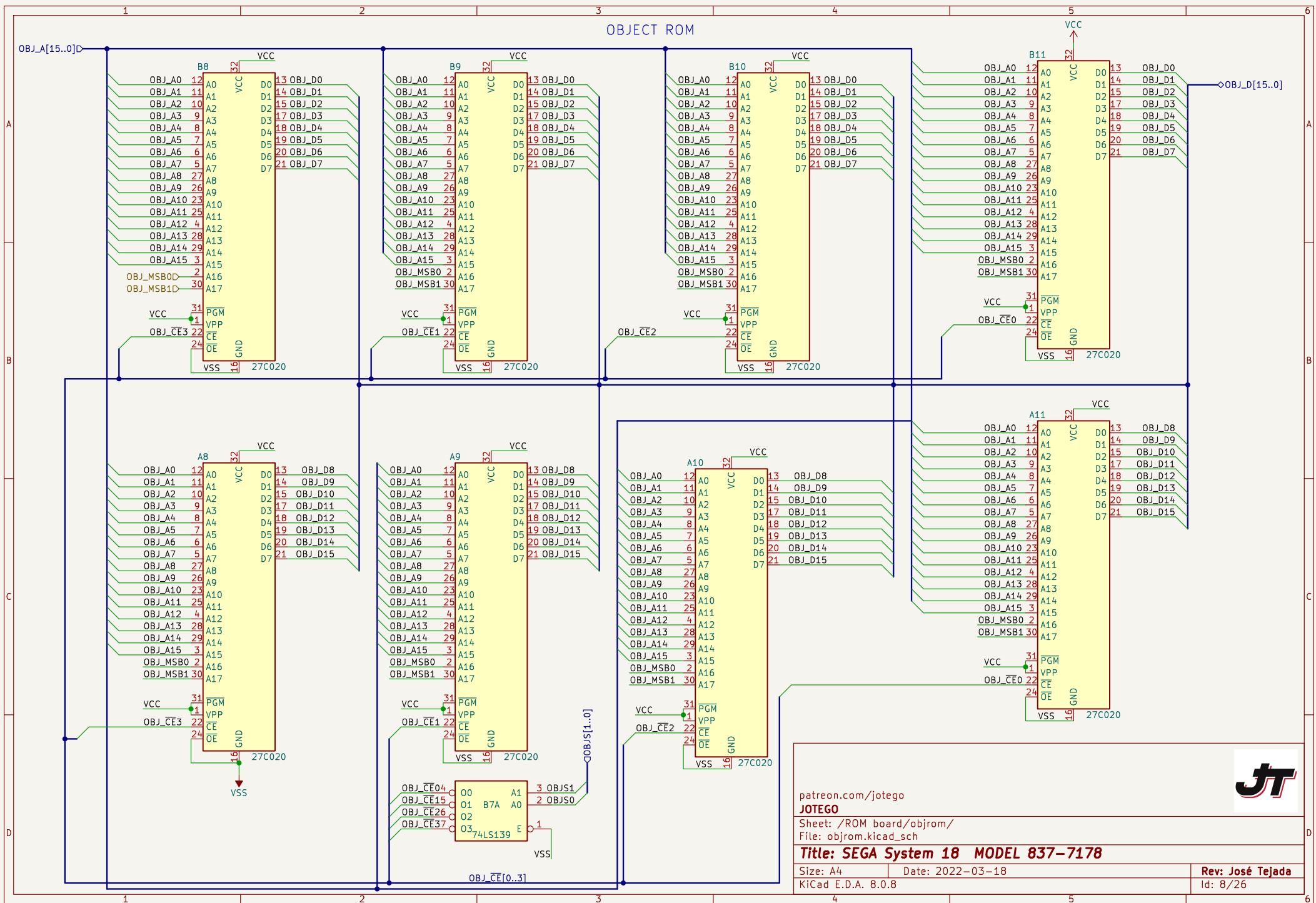
KiCad E.D.A. 8.0.8

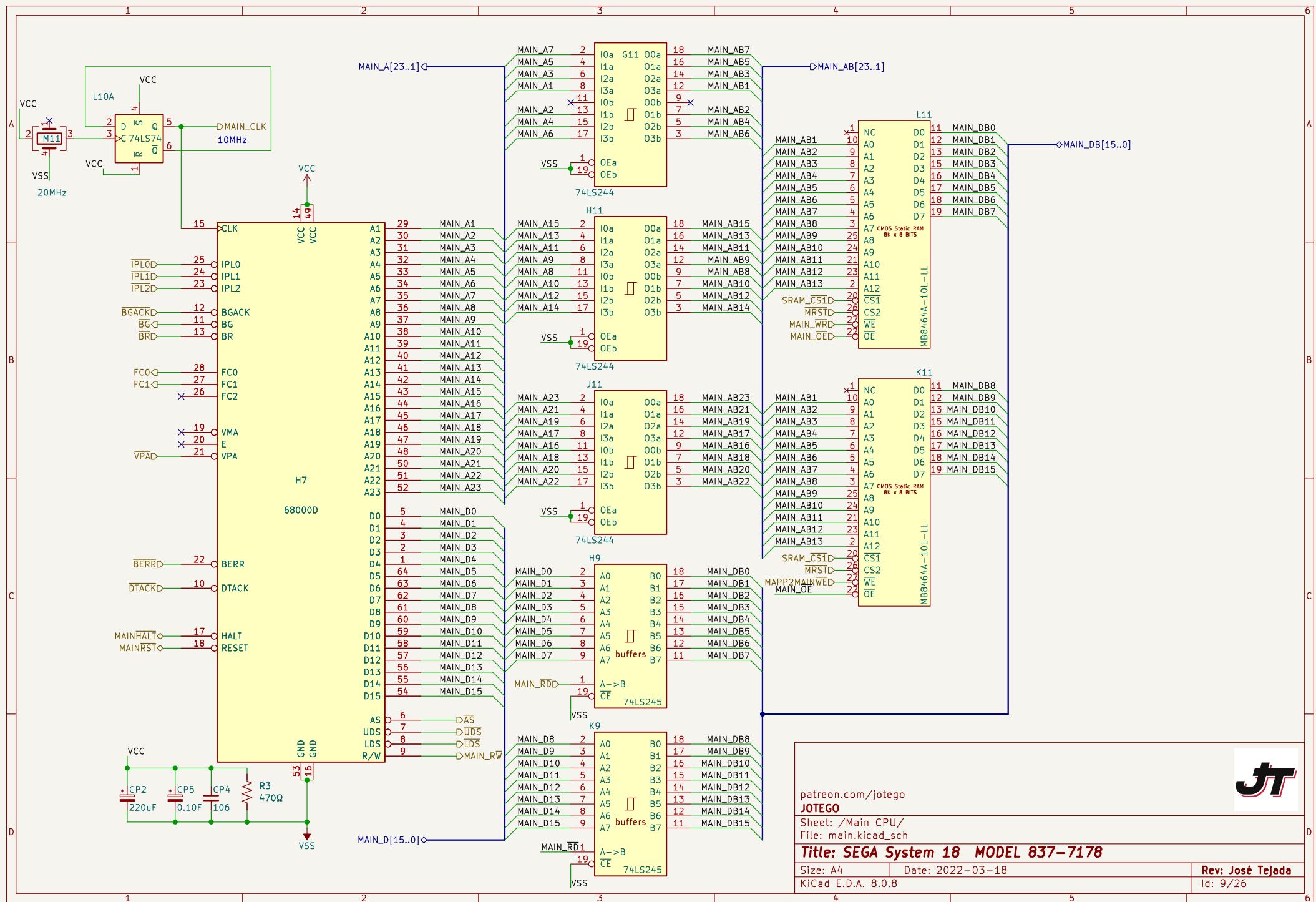


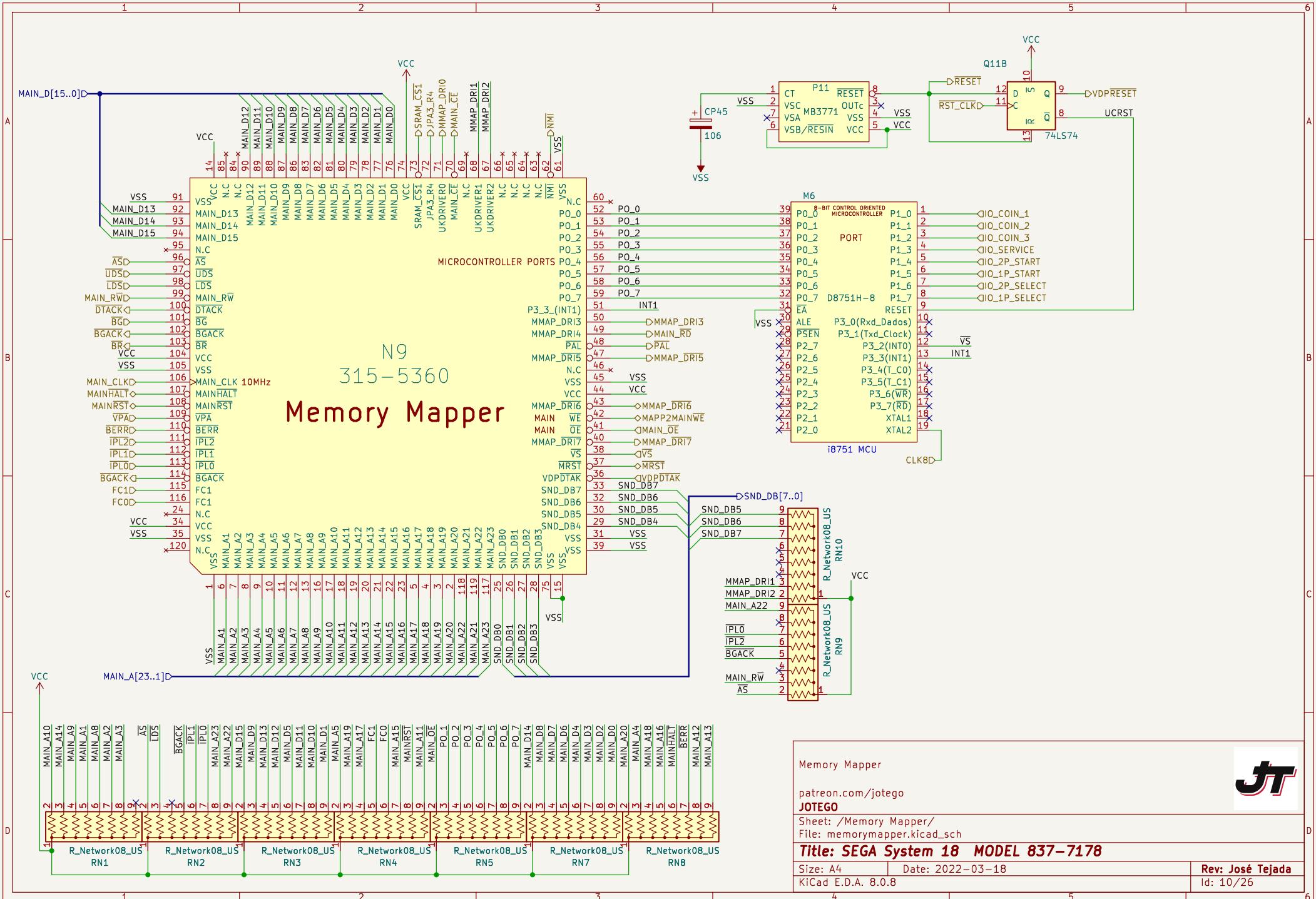
Rev: José Tejada

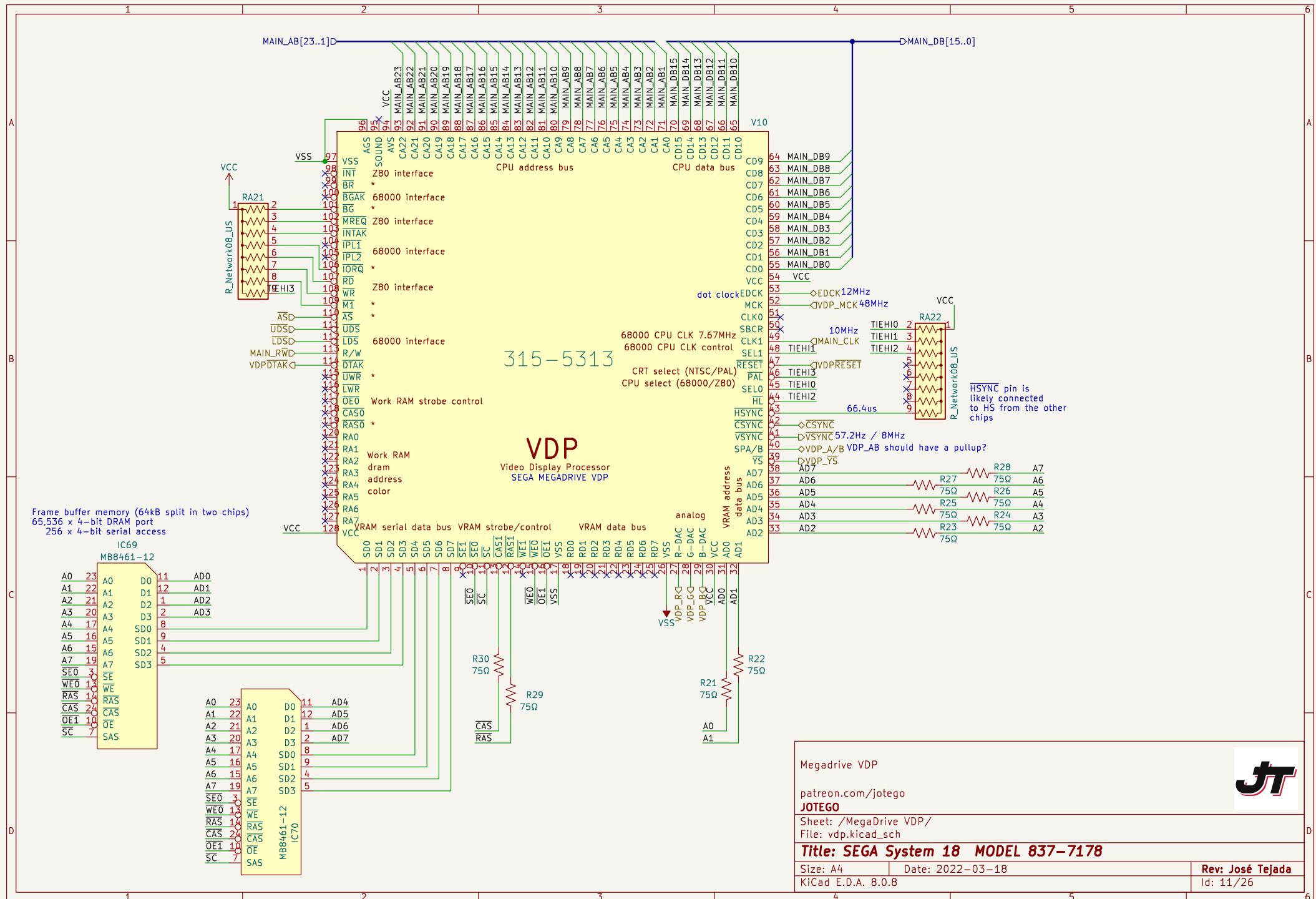
Id: 6/26

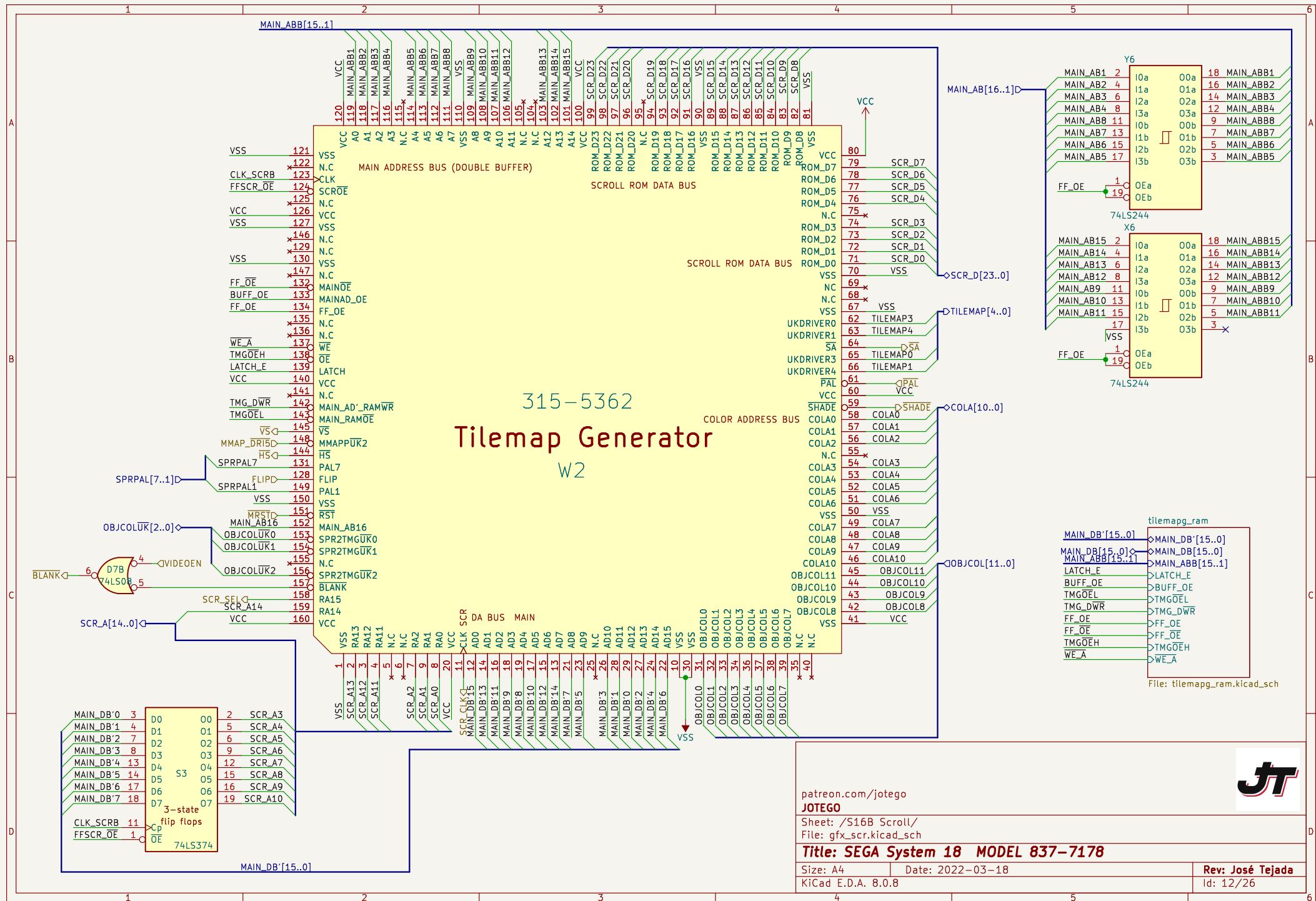


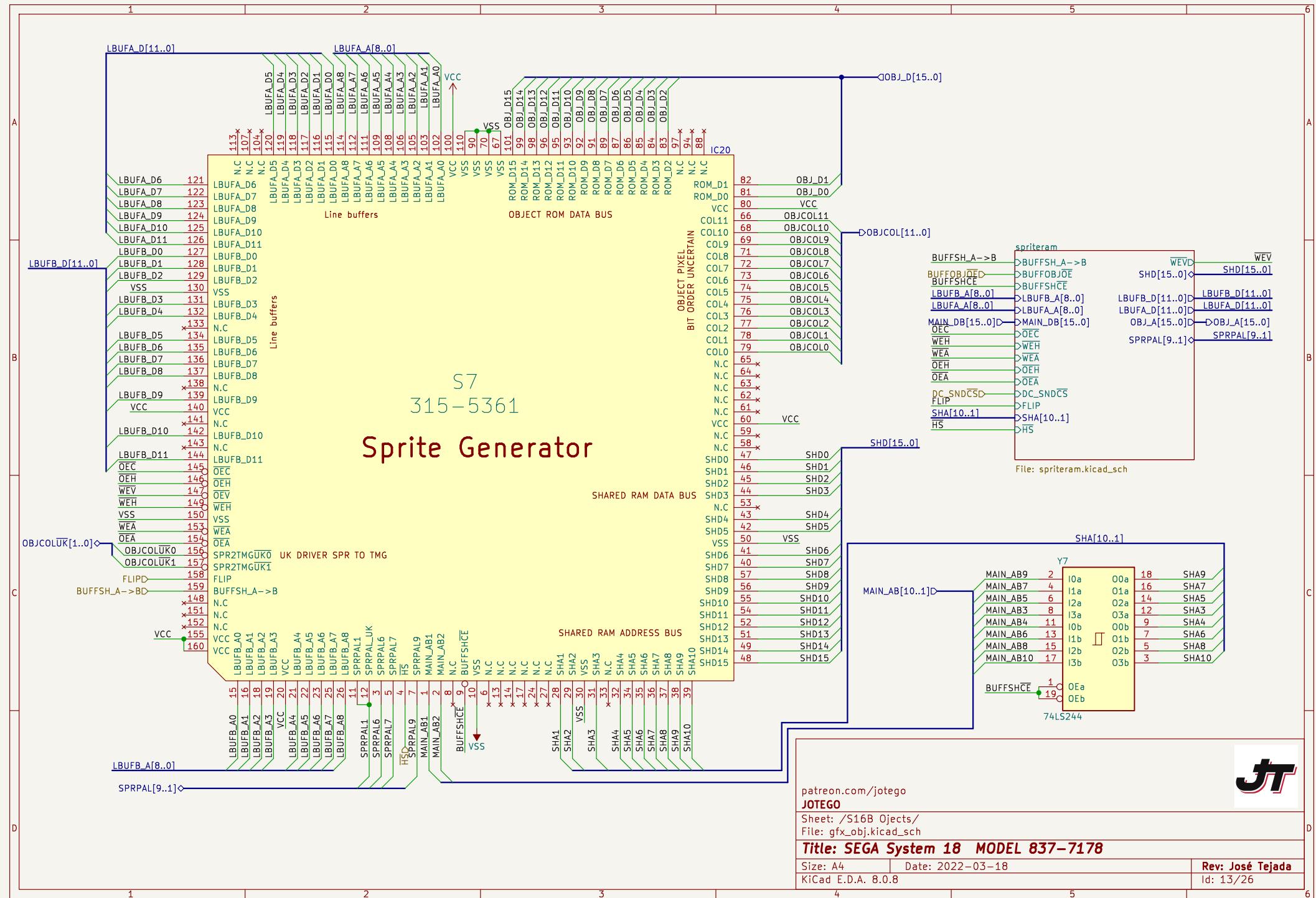


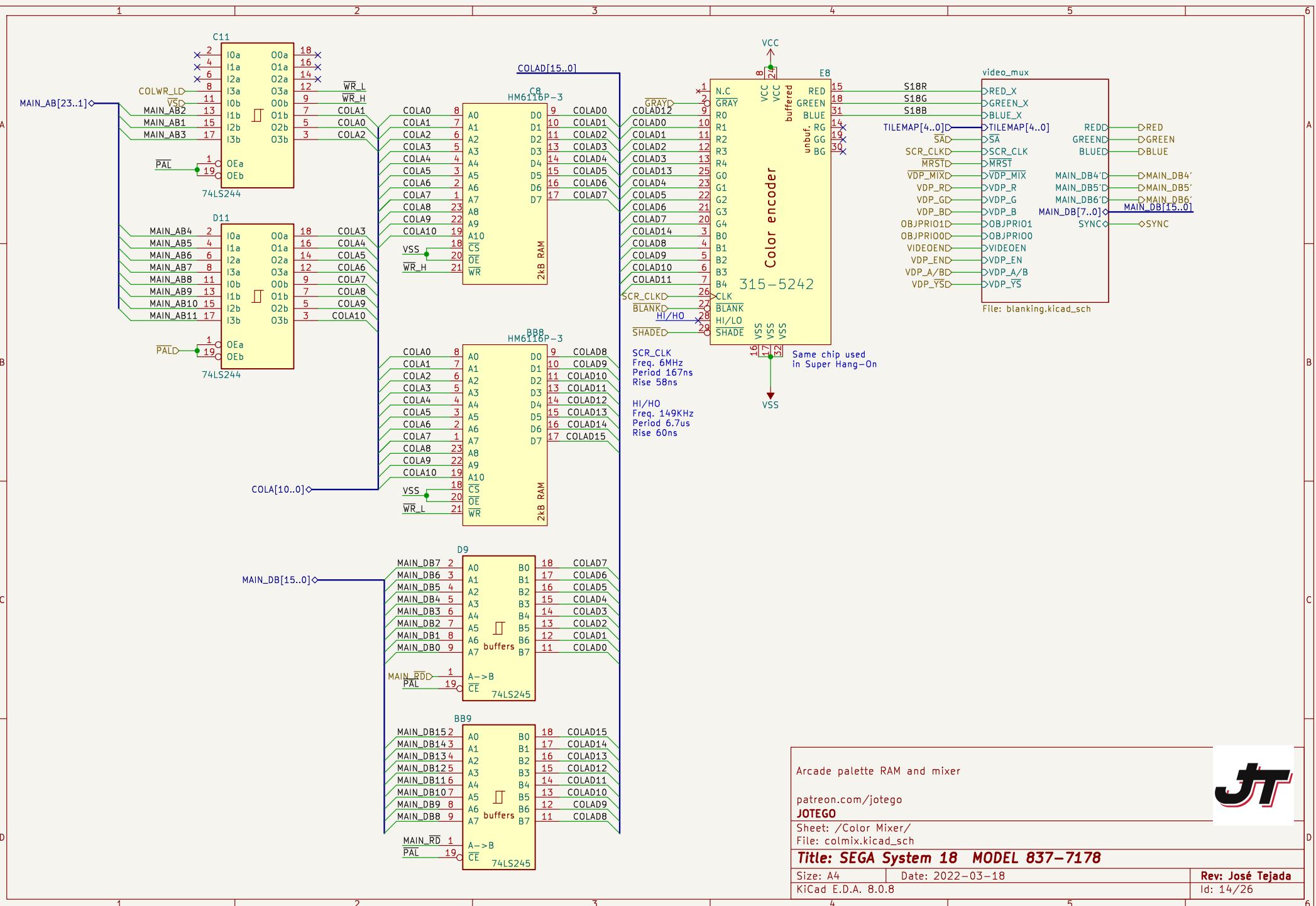


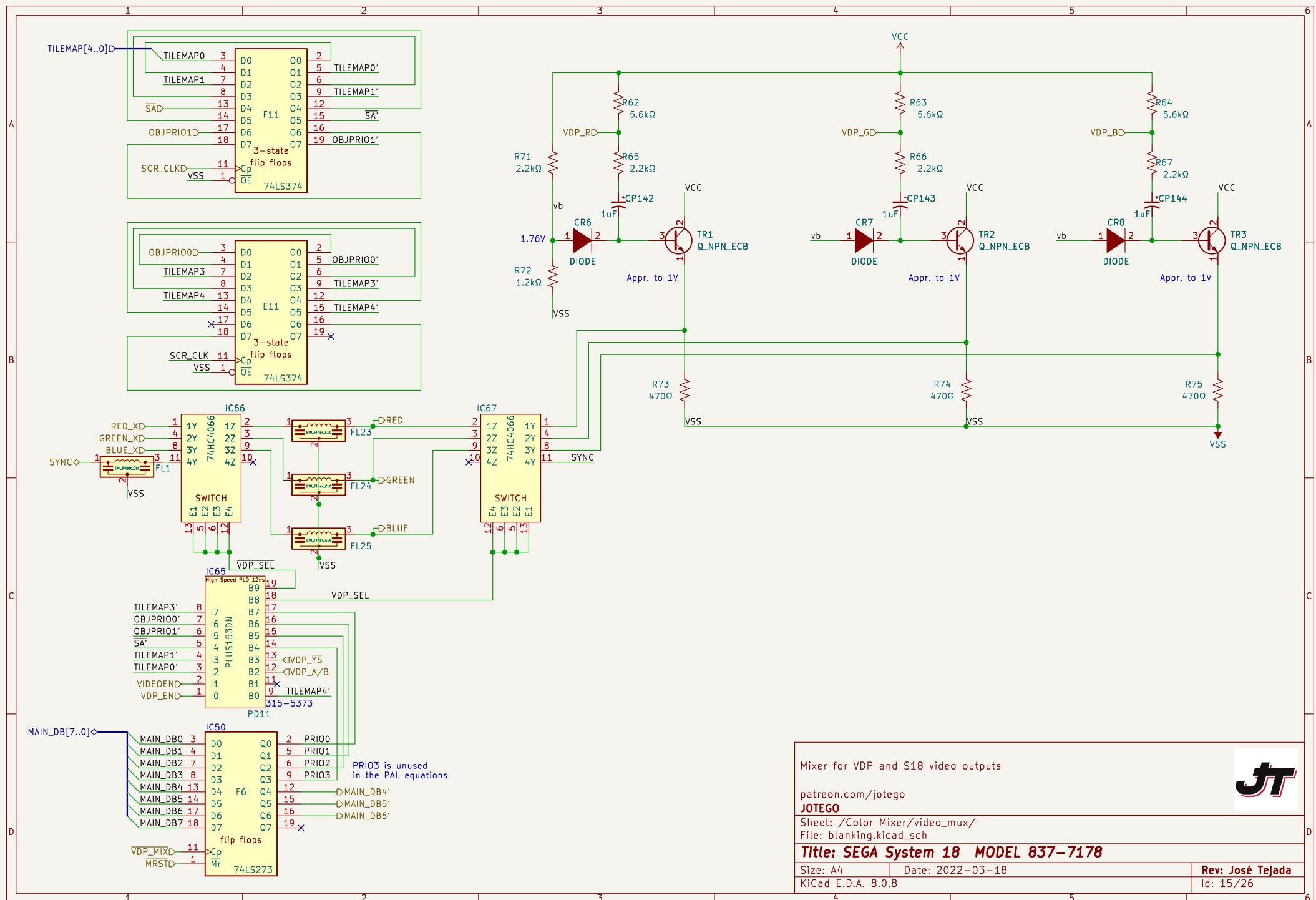




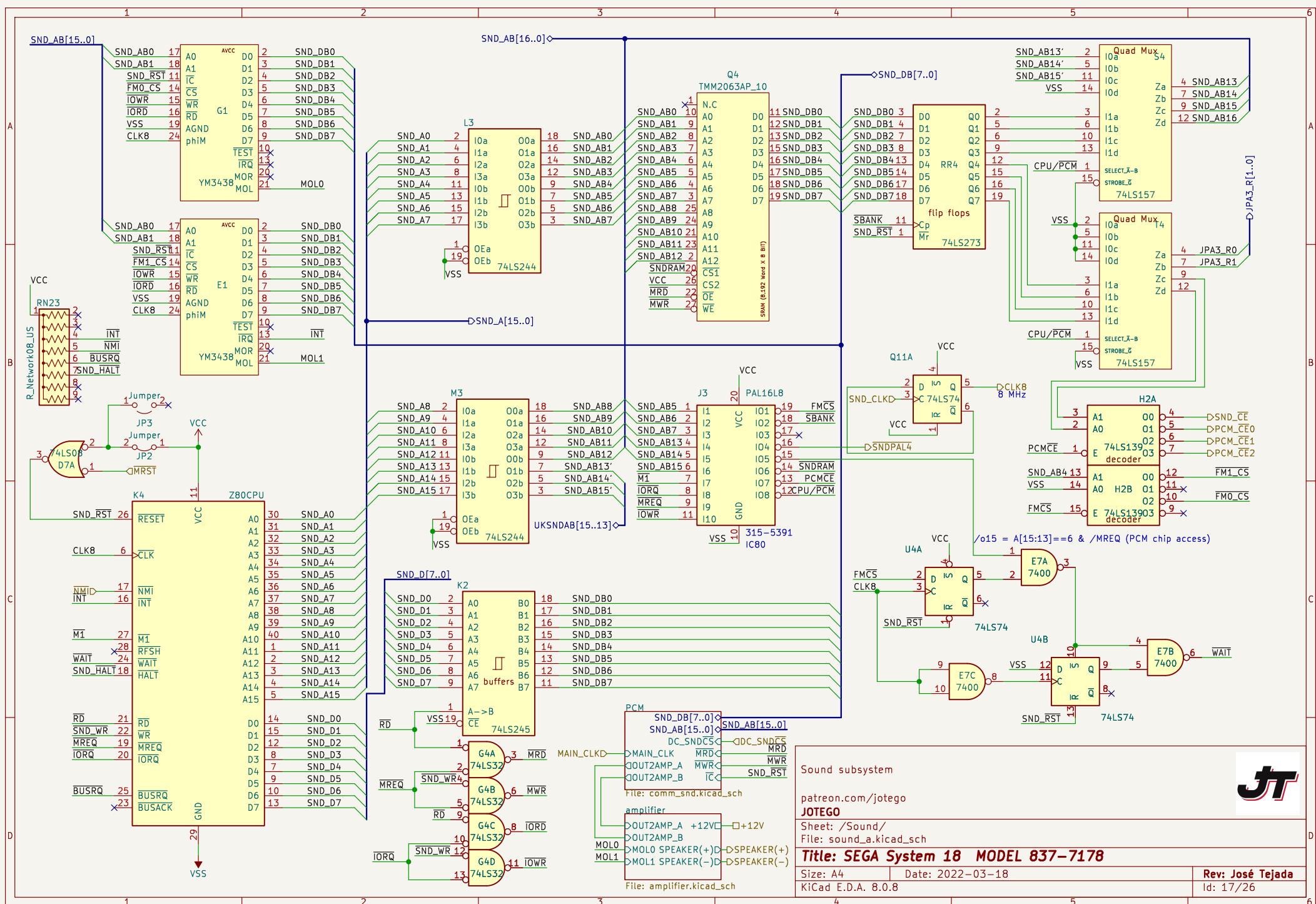


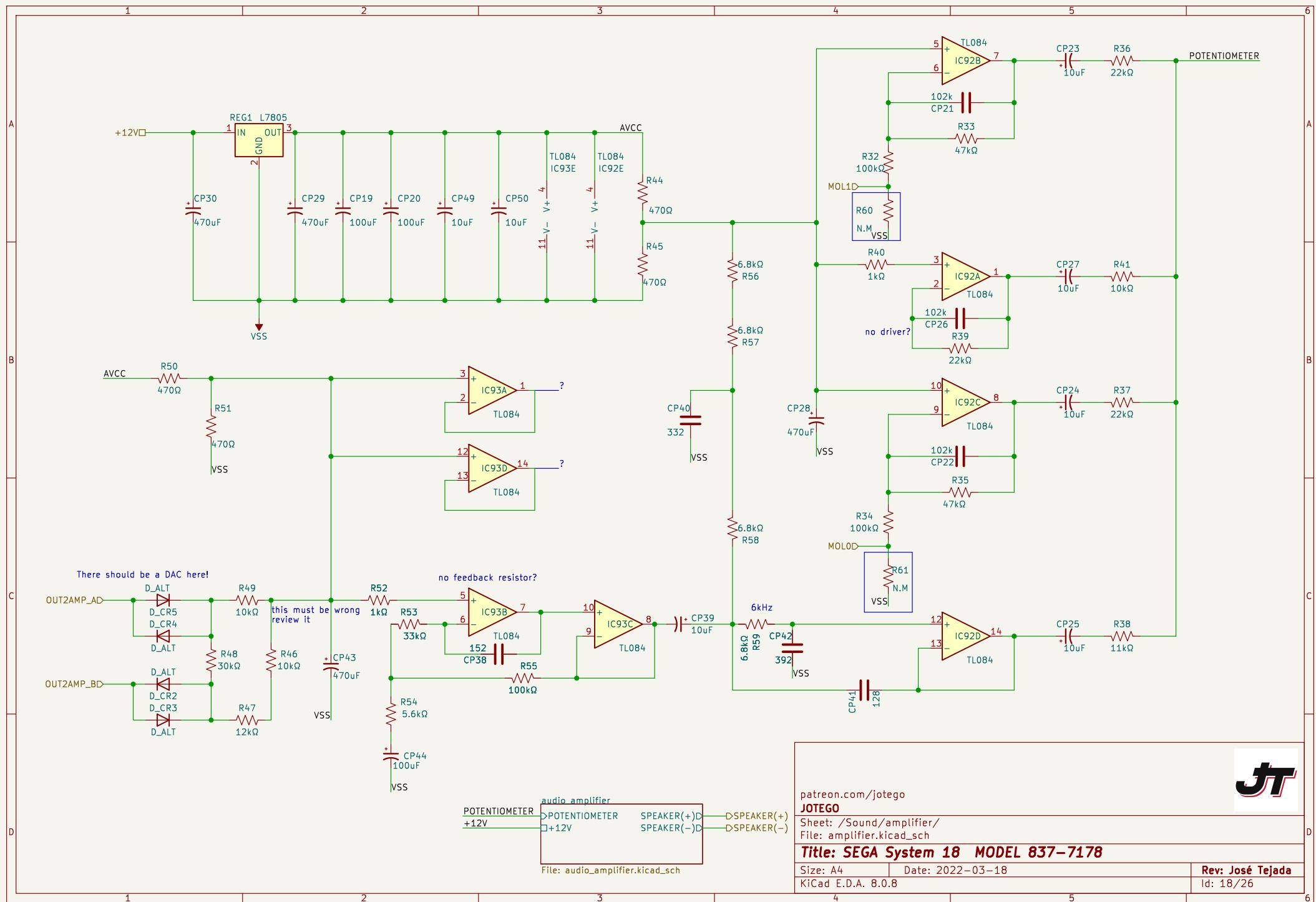




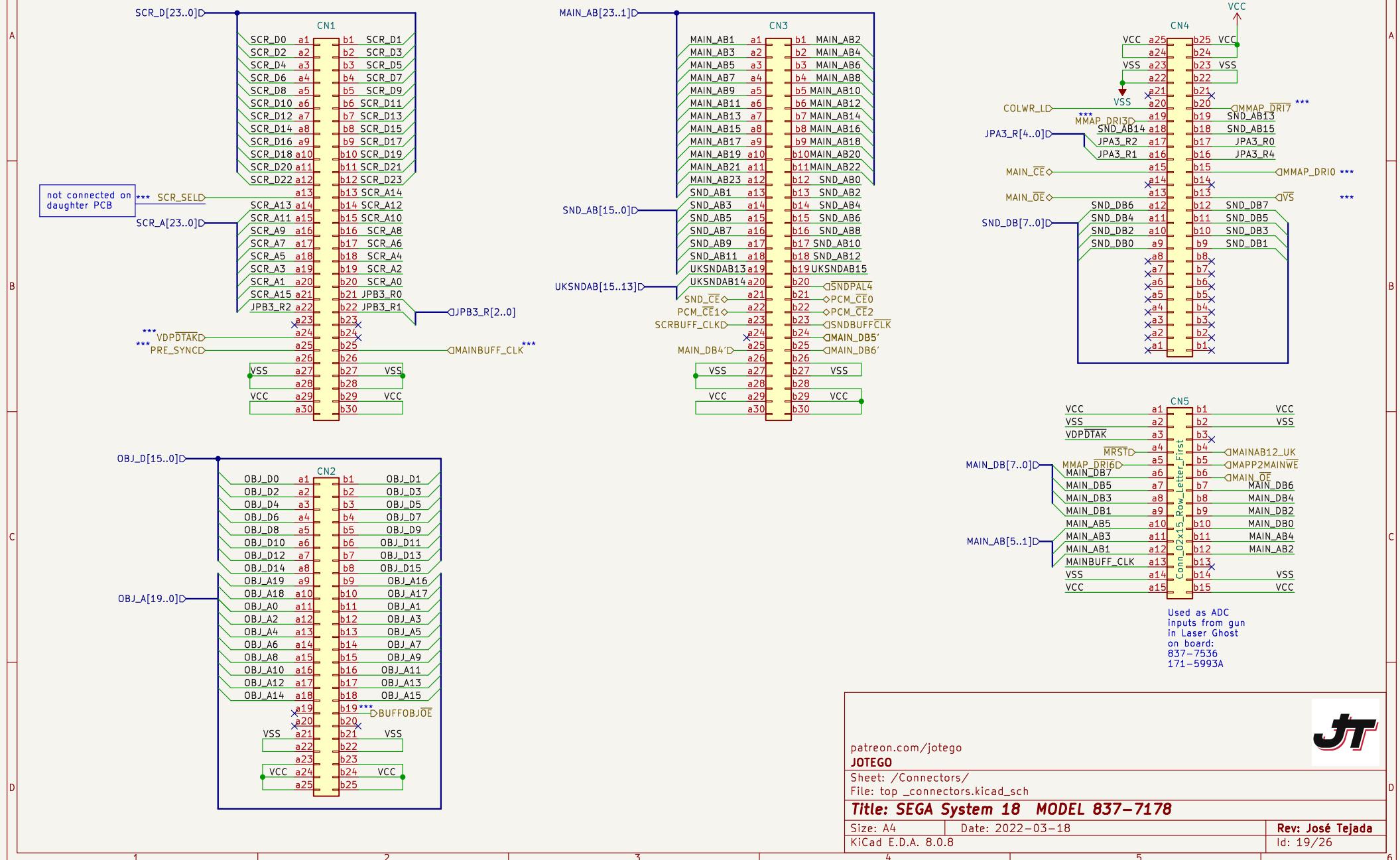


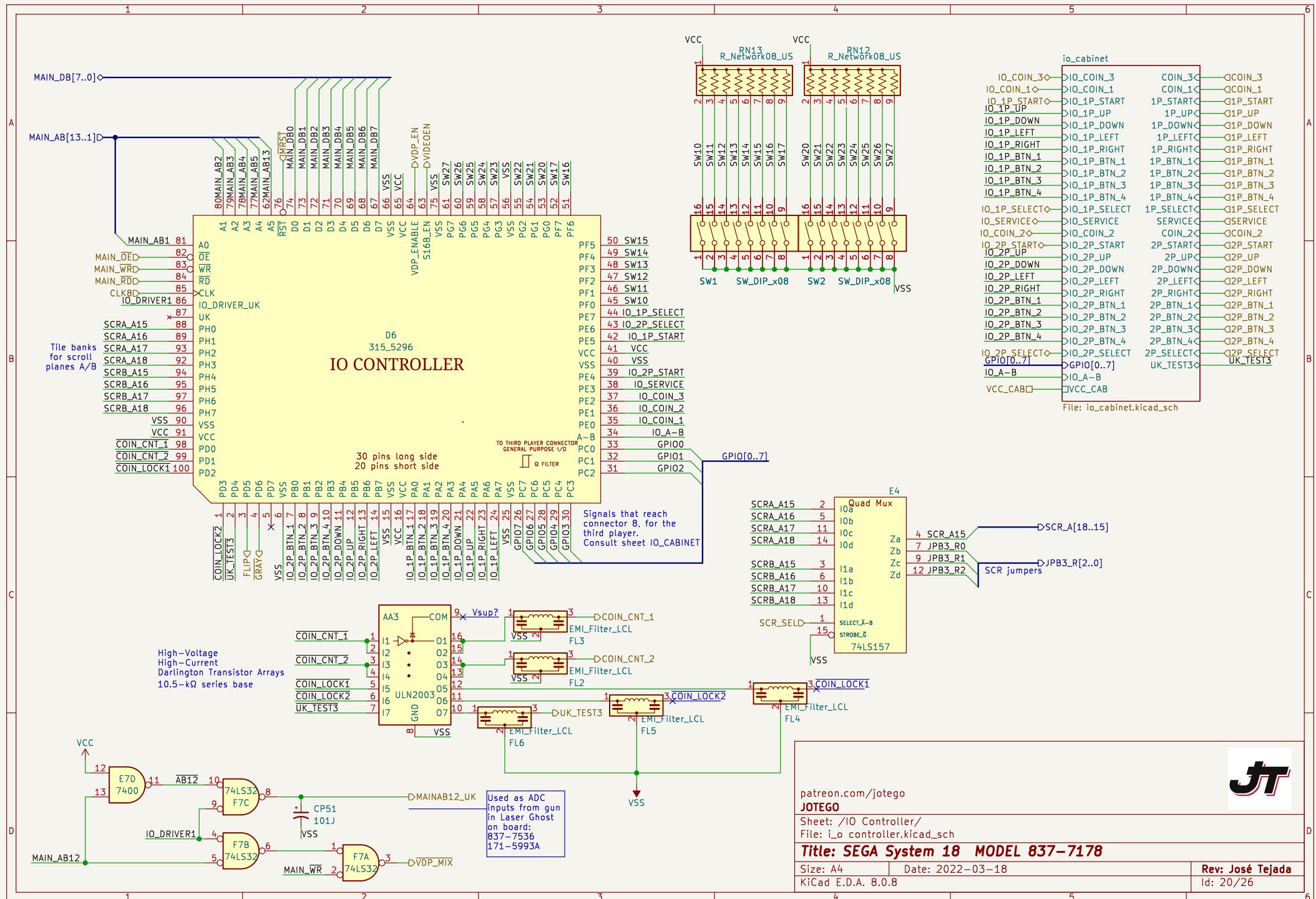


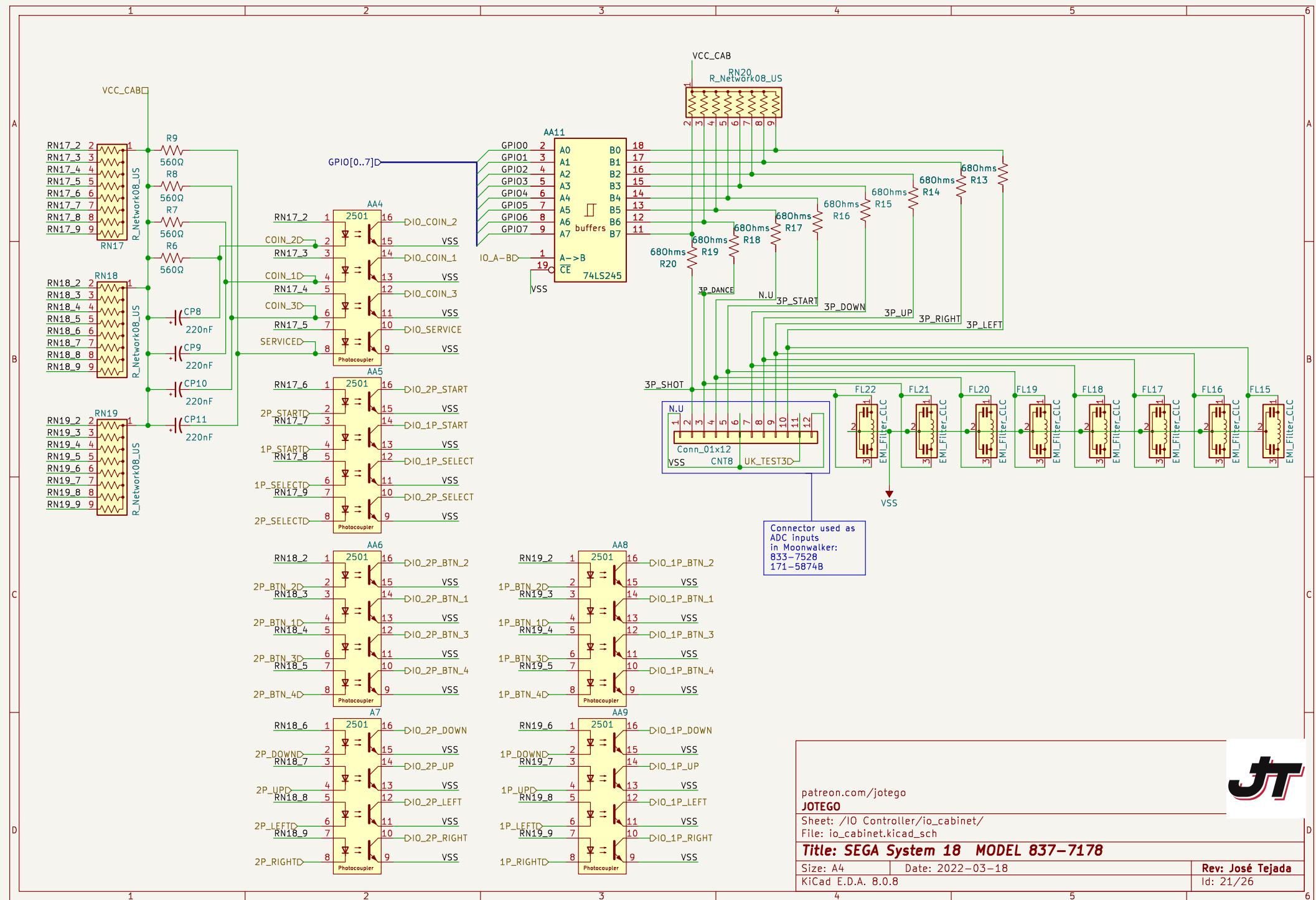


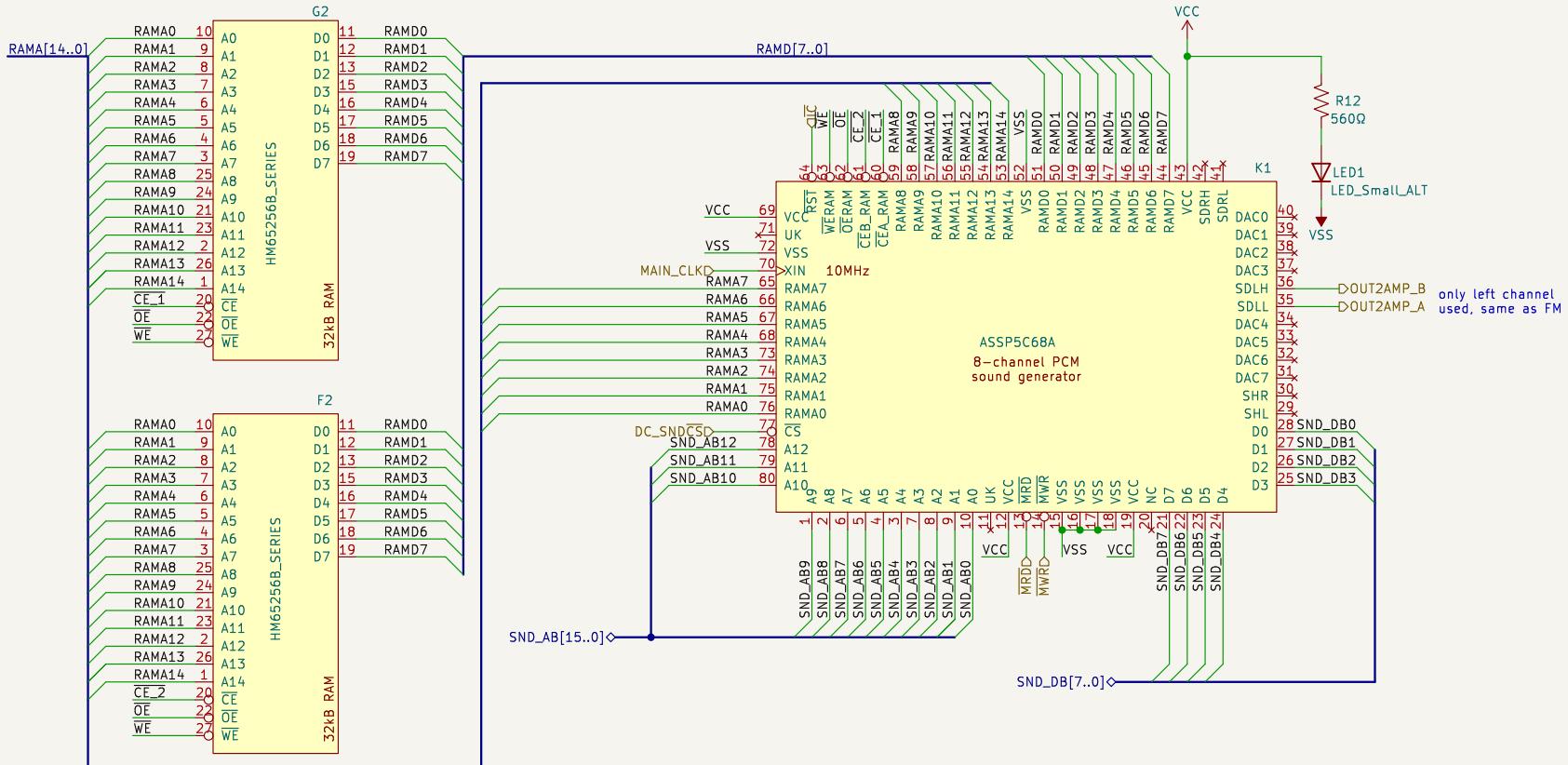


MOTHER AND DAUGHTER PCB CONNECTORS







**PCM sound generator**patreon.com/jotego**JOTEGO**

Sheet: /Sound/PCM/

File: comm_snd.kicad_sch

Title: SEGA System 18 MODEL 837-7178

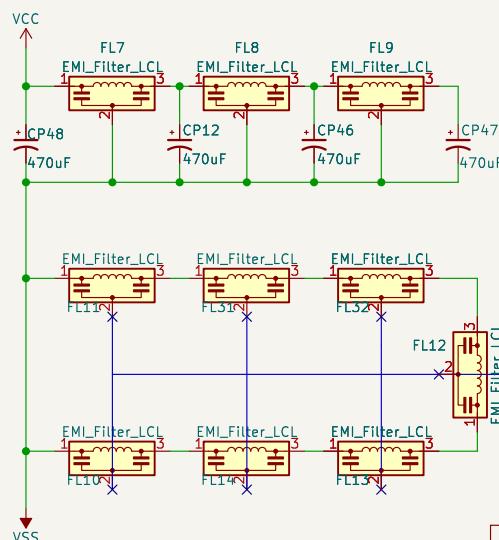
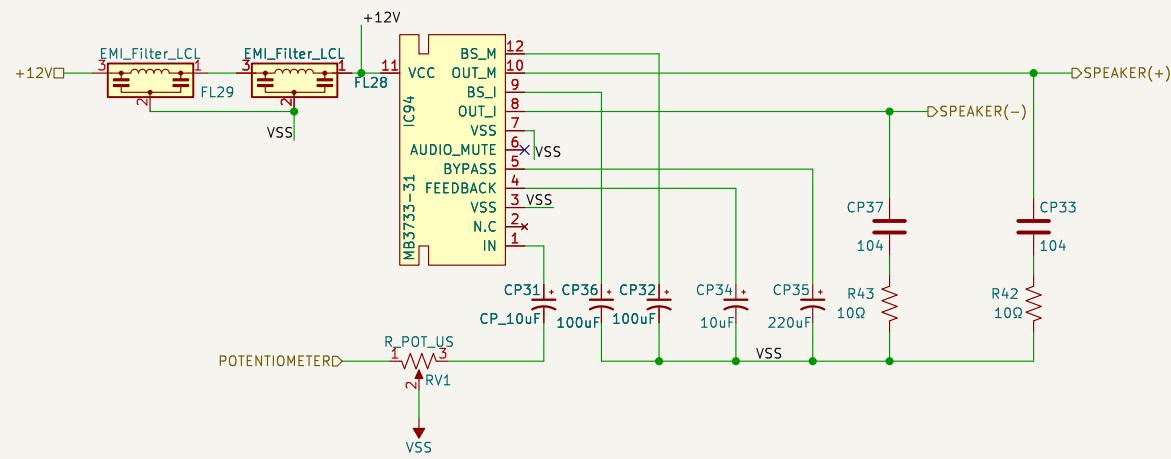
Size: A4 Date: 2022-03-18

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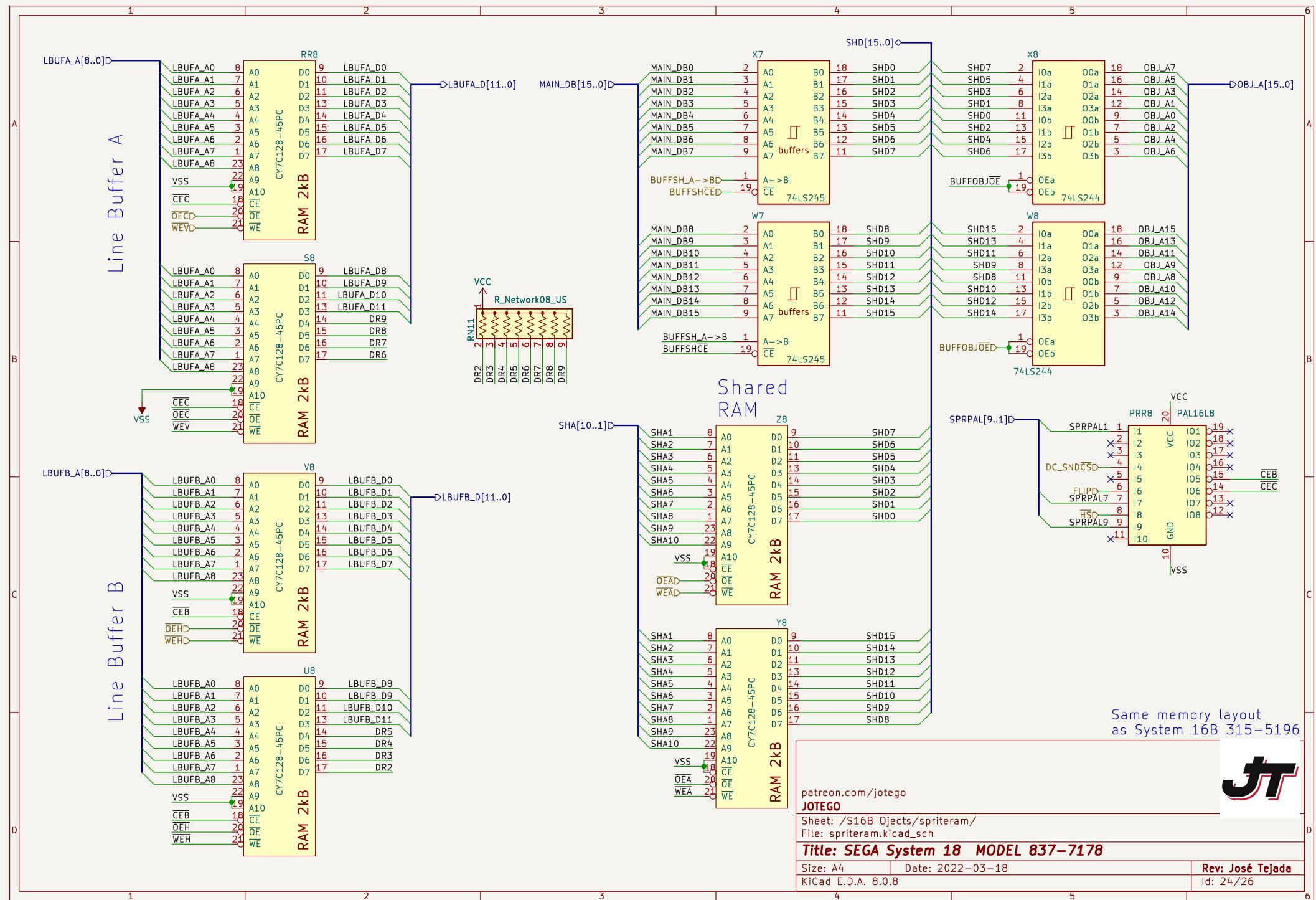
Rev: José Tejada

Id: 22/26

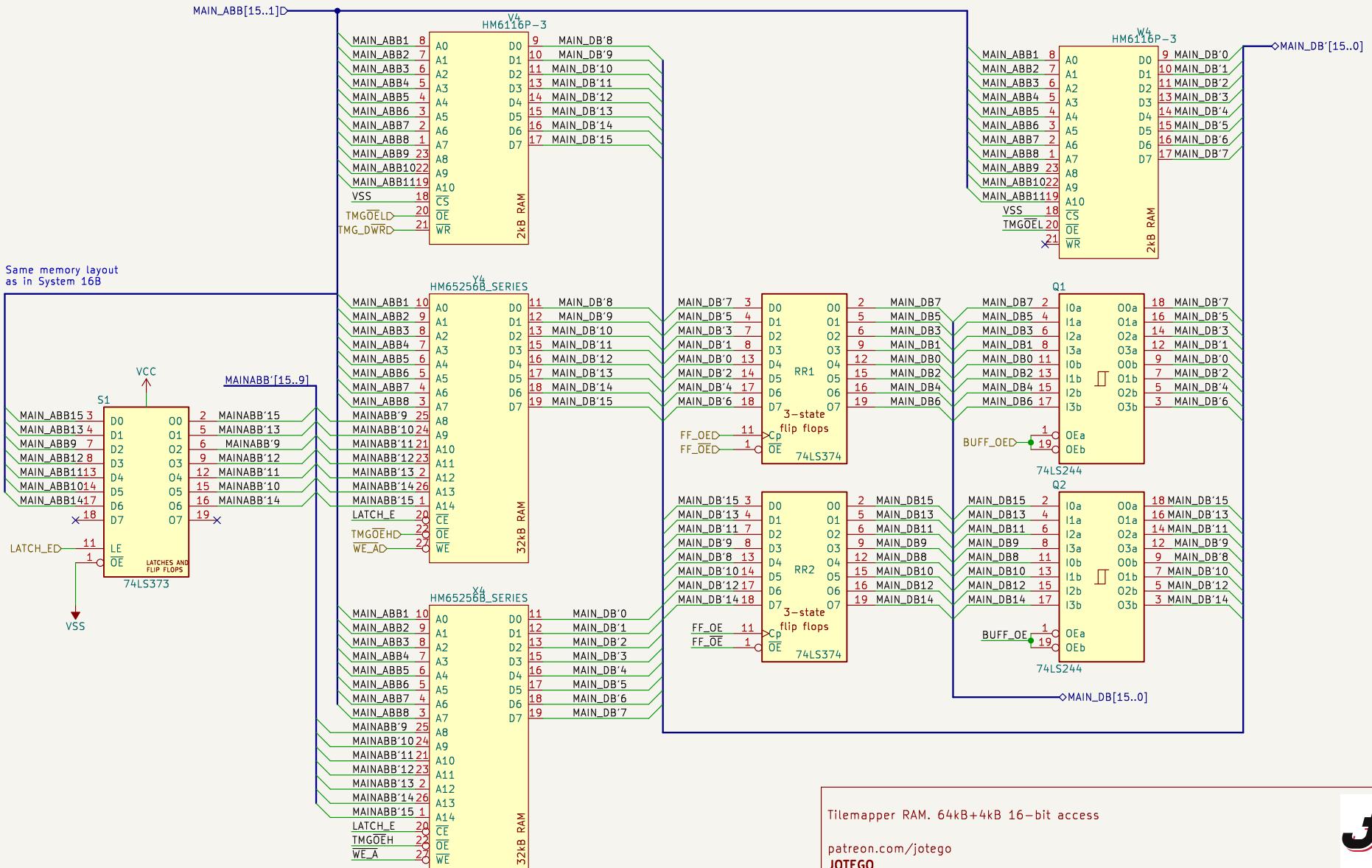


Leaded EMIFI
Three-terminal Capacitor type
DSS6NE52A222p
2200pF 80/-20%
100Vdc 6A -25 to +85°C

A kind of a track is observed between the two PCB layers, as if it were another layer, but there is no certainty about that. The track seems to be used to delimit or isolate the EMI (ElectroMagnetic Interference) filters. They may have wanted to isolate the noise between digital and analog devices.



1 2 3 4 5 6



Tilemapper RAM. 64kB+4kB 16-bit access

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Sheet: /S16B_Scroll/tilemapg_ram/

File: tilemapg_ram.kicad_sch

Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18

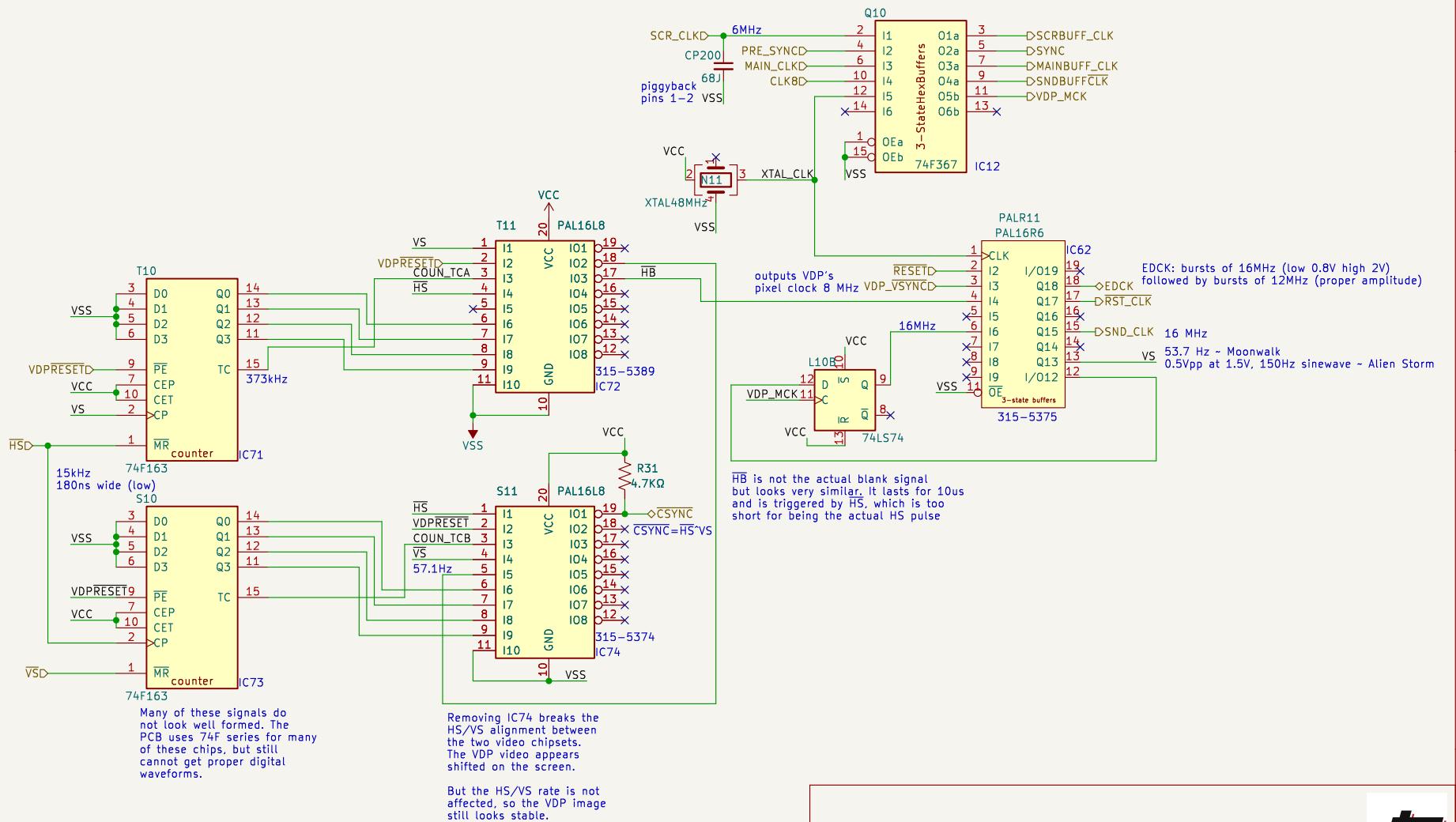
KiCad E.D.A. 8.0.8



Rev: José Tejada

Id: 25/26

1 2 3 4 5 6



VDP pixel clock and csync generation
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Sheet: /Video Sync/
 File: buffers.kicad_sch

Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18
 KiCad E.D.A. 8.0.8



Rev: José Tejada
 Id: 26/26