

A

A

B

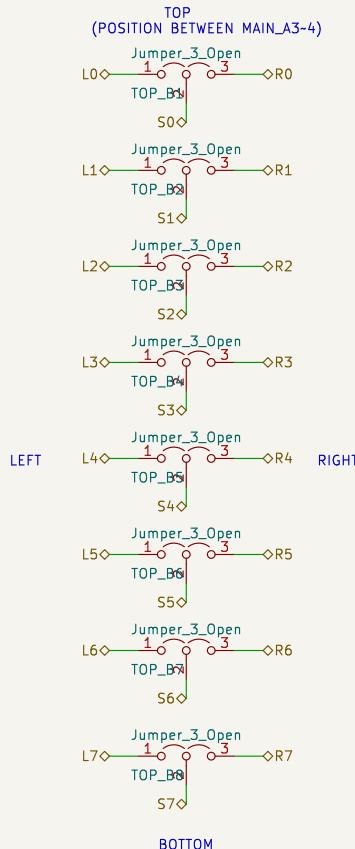
B

C

C

D

D



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JOTEGO

Sheet: /ROM board/scrjumpers/  
File: romjp.kicad\_sch

# Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.9

Rev: José Tejada

Id: 3/26

A

A

B

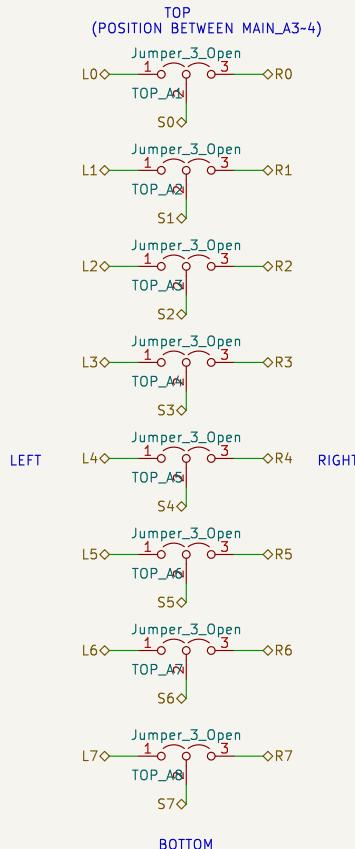
B

C

C

D

D


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**JOTEGO**

Sheet: /ROM board/sndjumpers/  
File: romjp.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.9



Rev: José Tejada

Id: 4/26

A

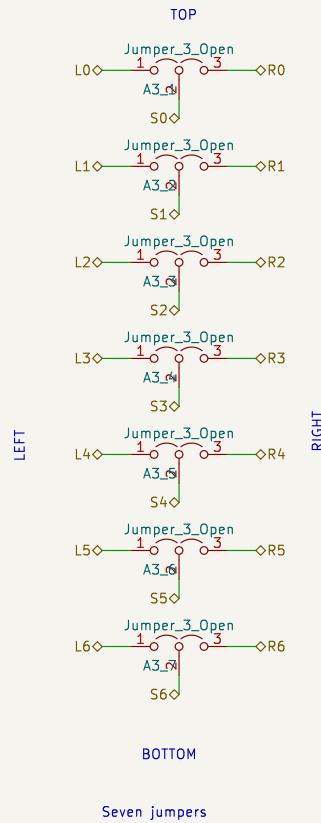
A

B

B

C

C



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JOTEGO

Sheet: /ROM board/objjumpers/  
File: romjp7.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

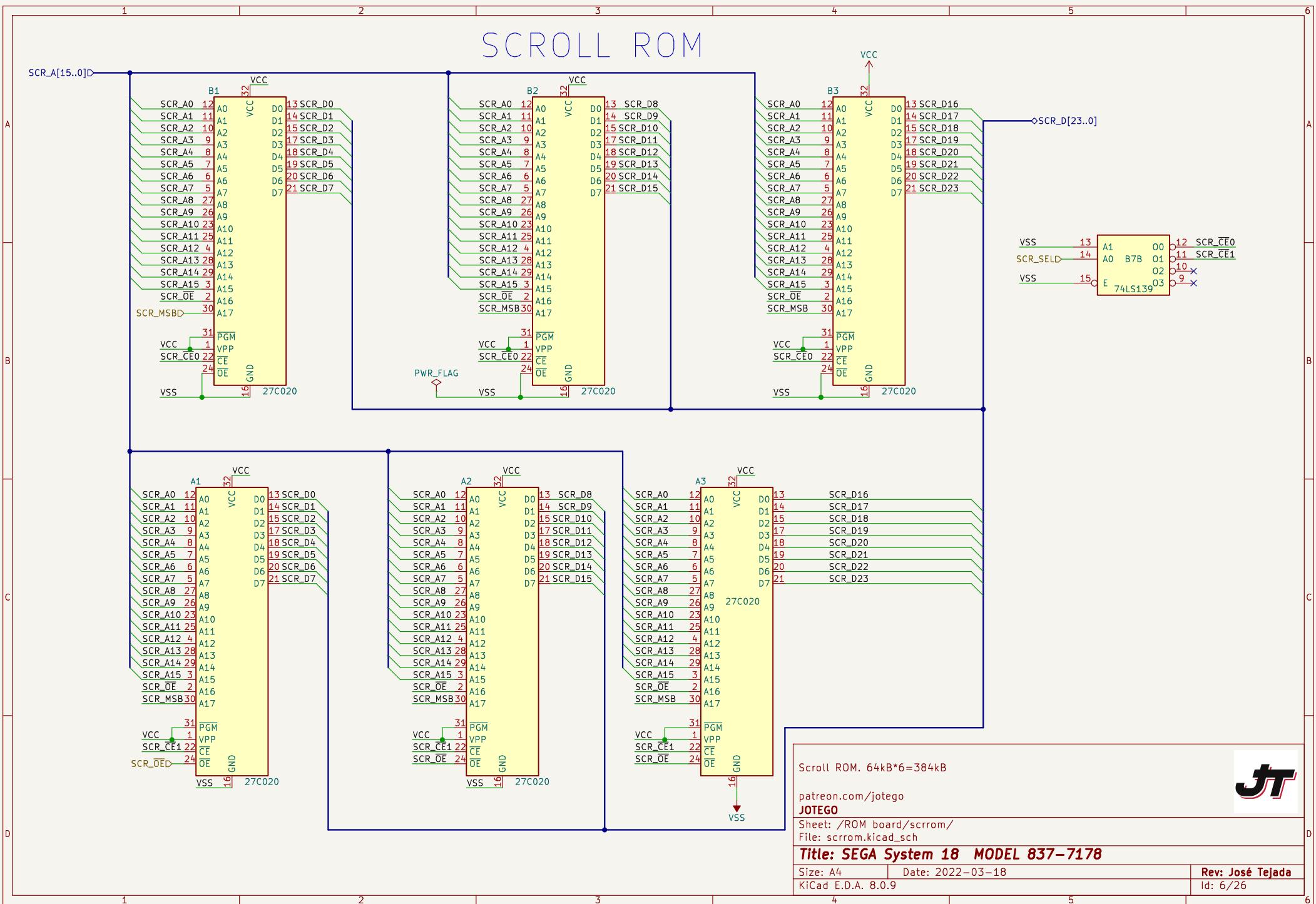
Size: A4 Date: 2022-03-18

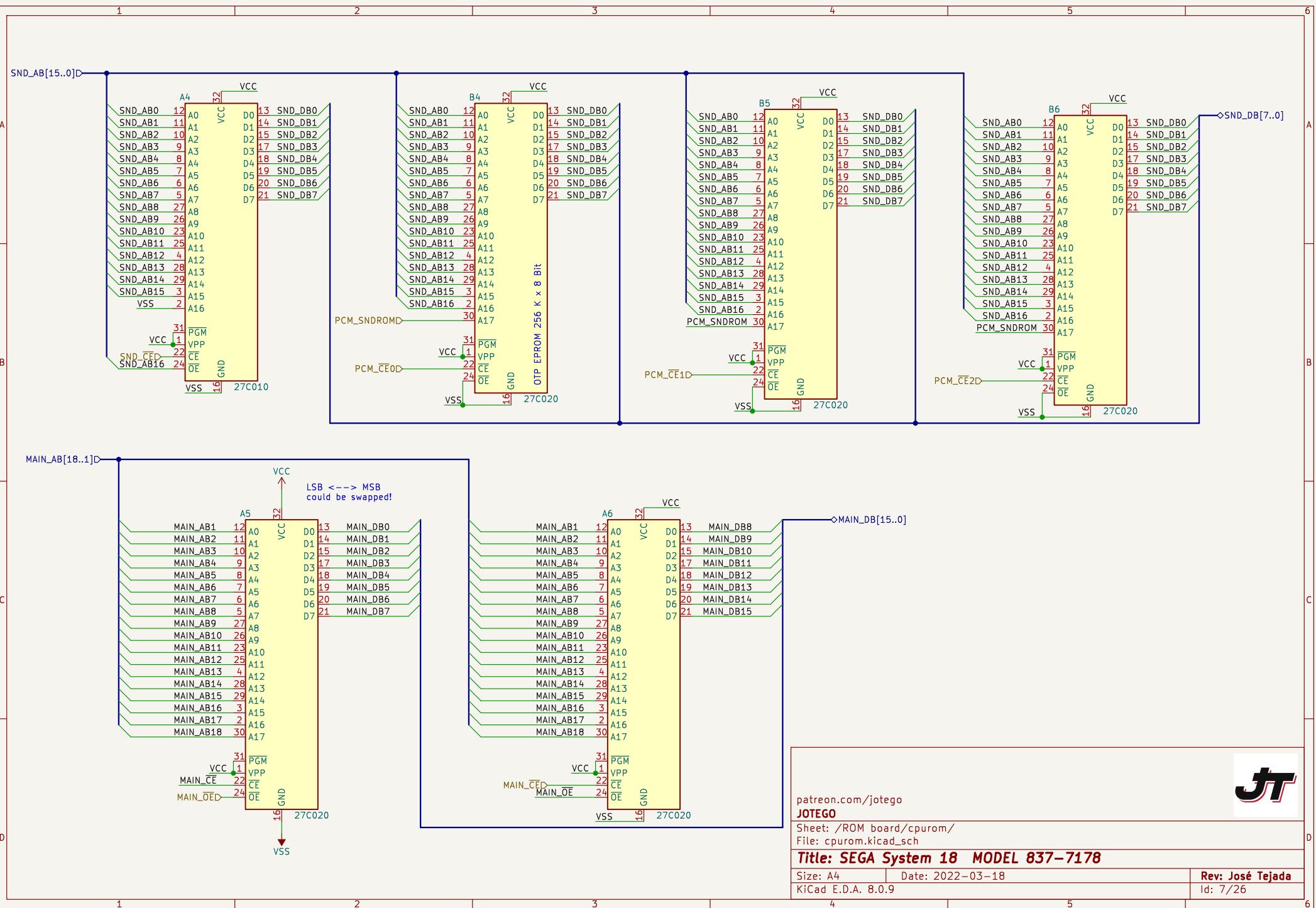
KiCad E.D.A. 8.0.9

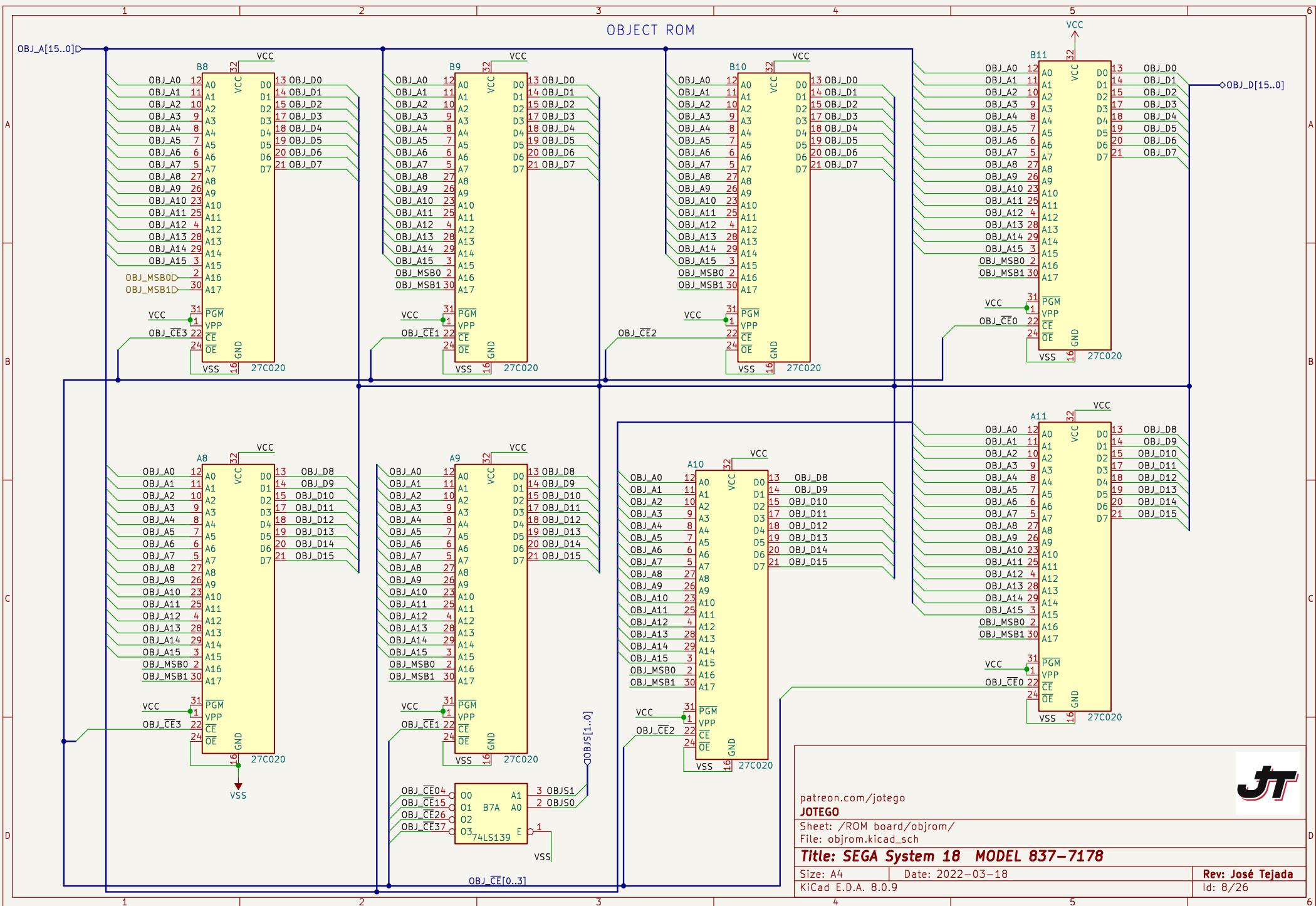
Rev: José Tejada

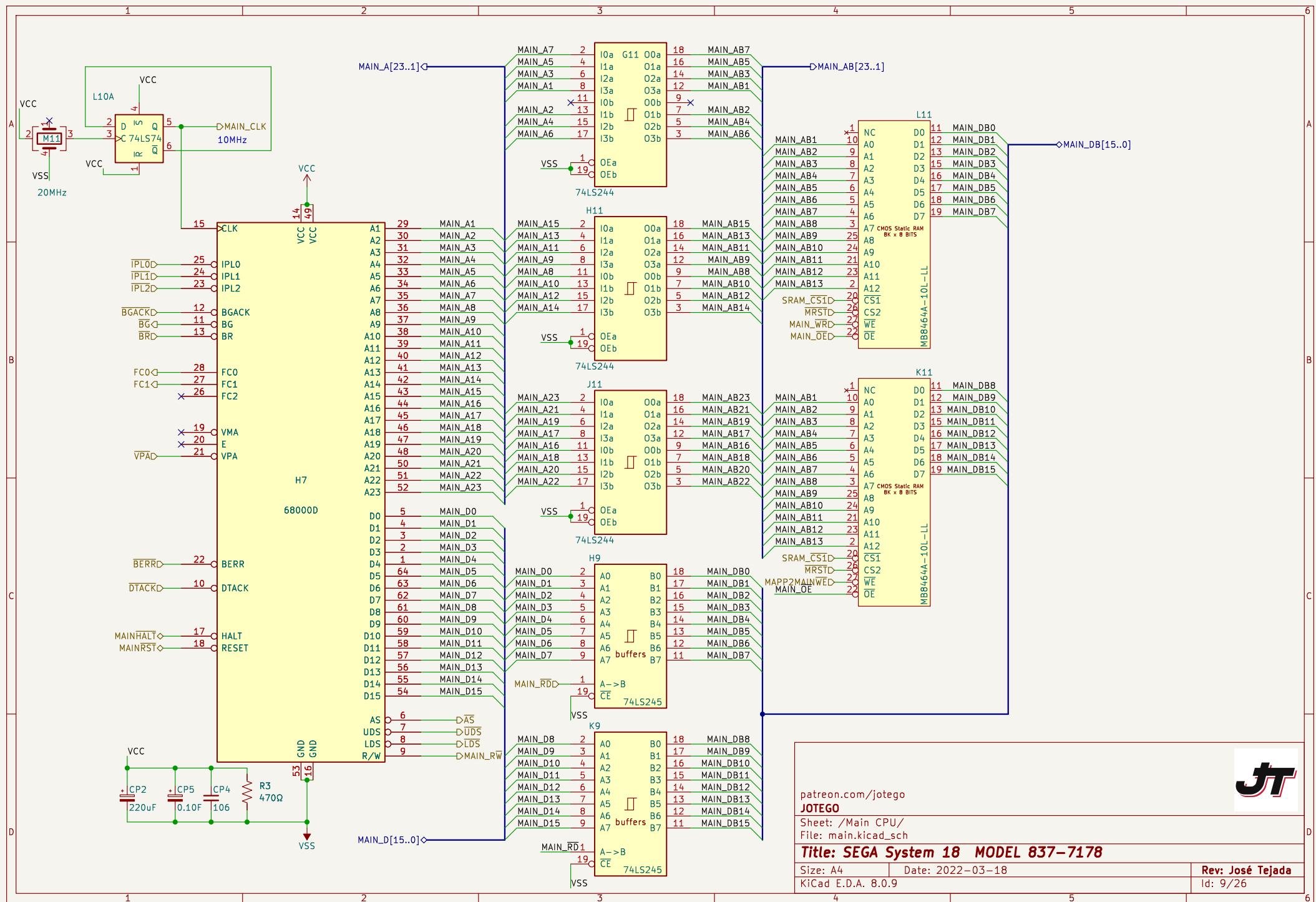
Id: 5/26

# SCROLL ROM









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JOTEGO

Sheet: /Main CPU/  
File: main.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

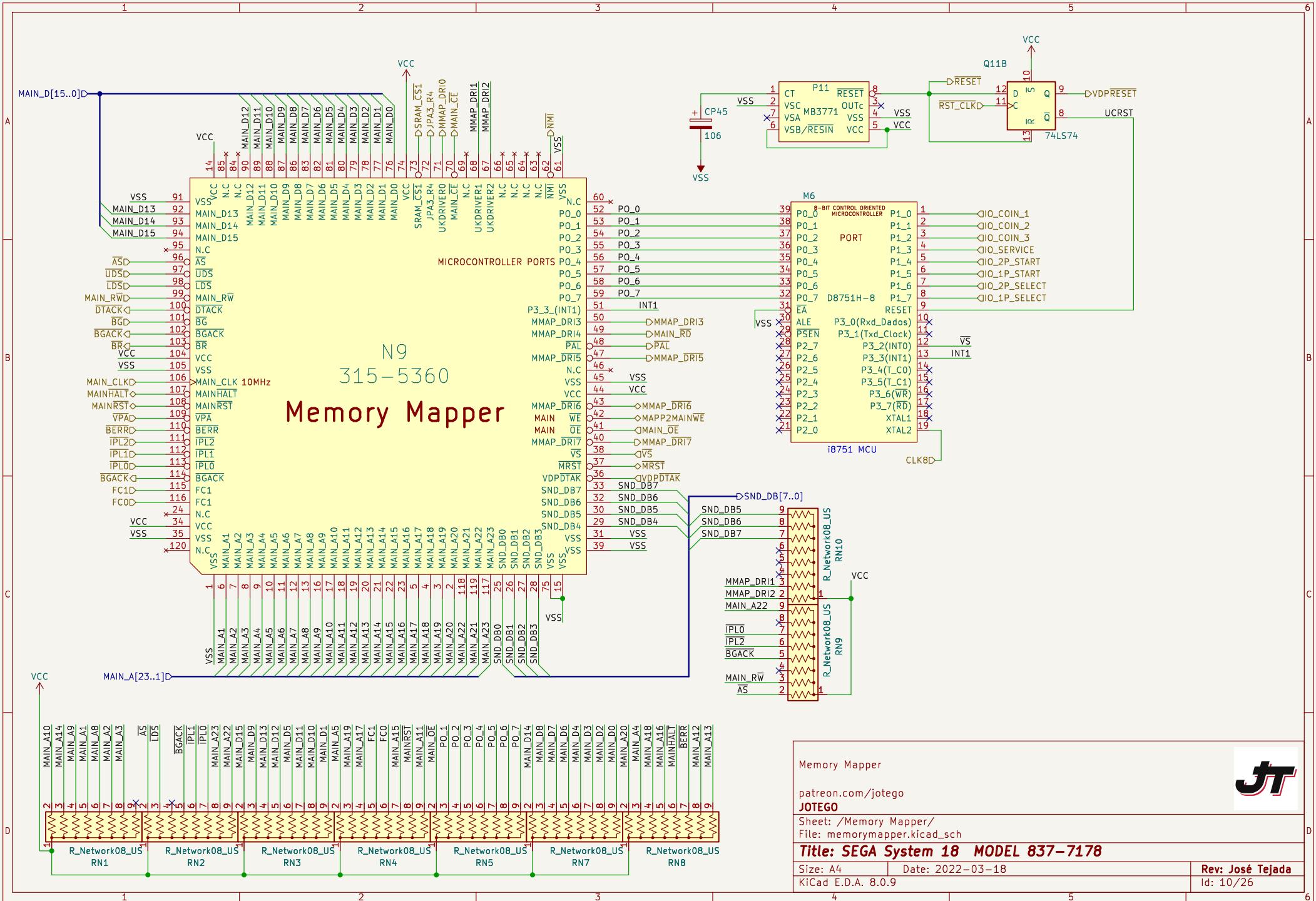
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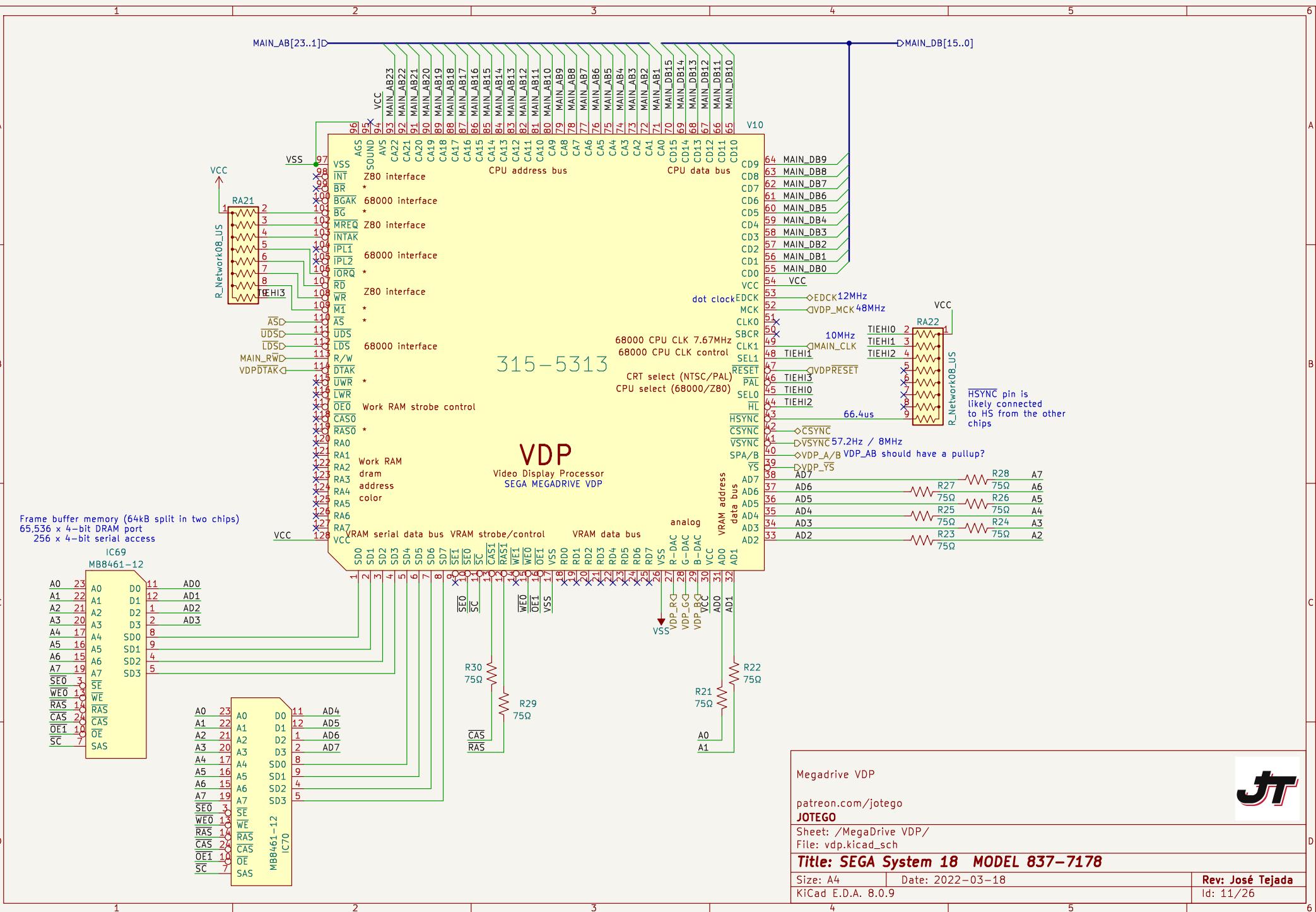
KiCad E.D.A. 8.0.9

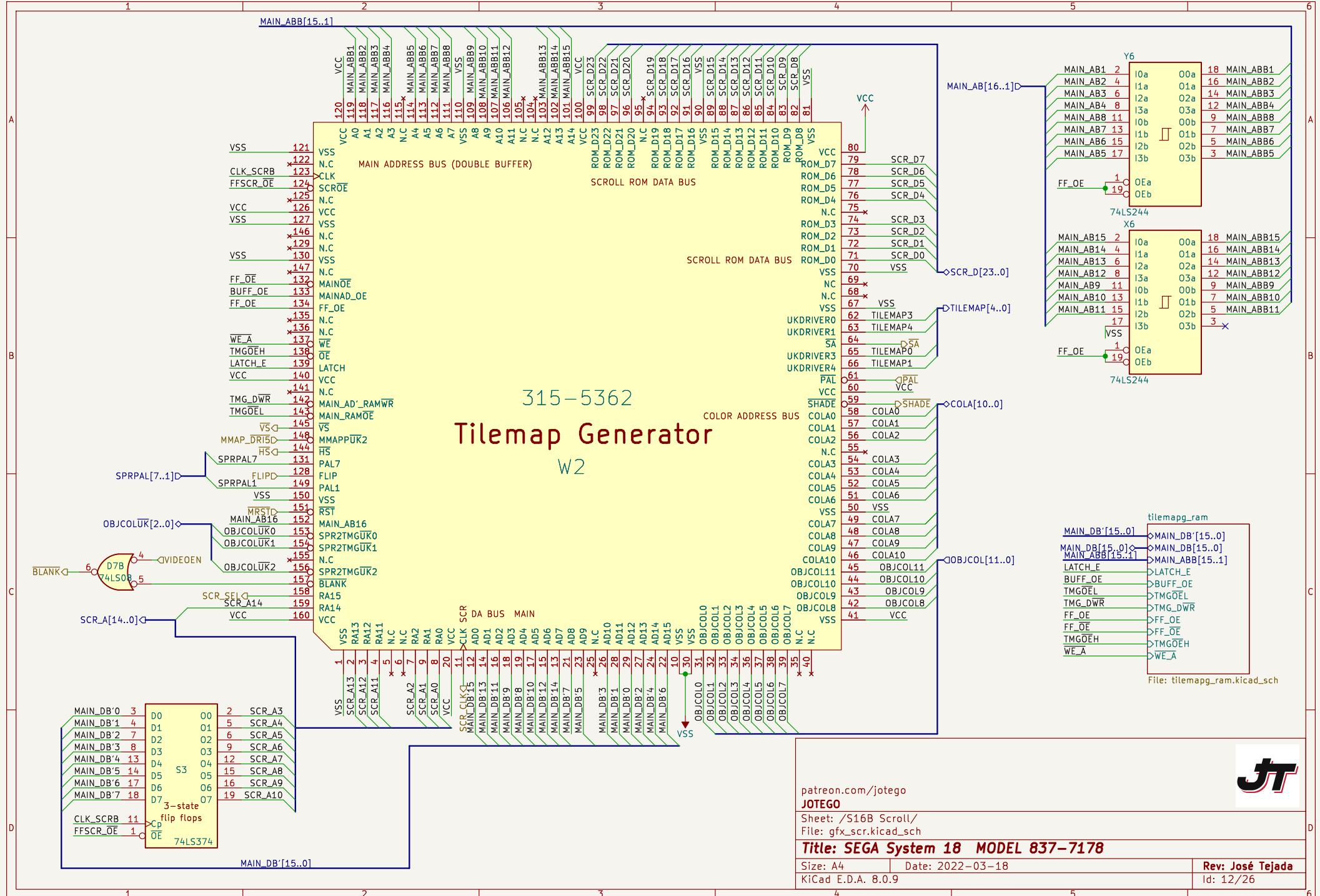


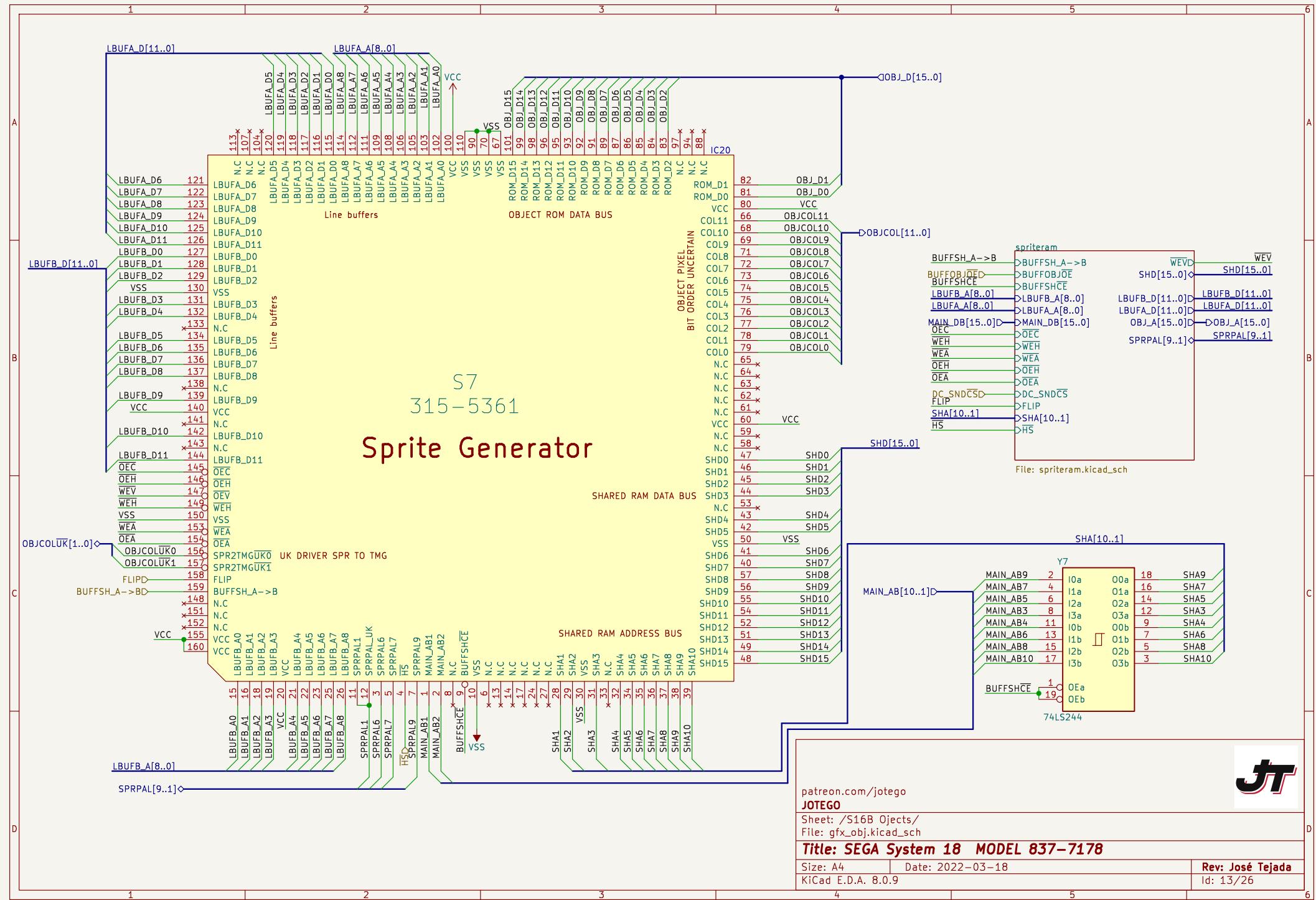
Rev: José Tejada

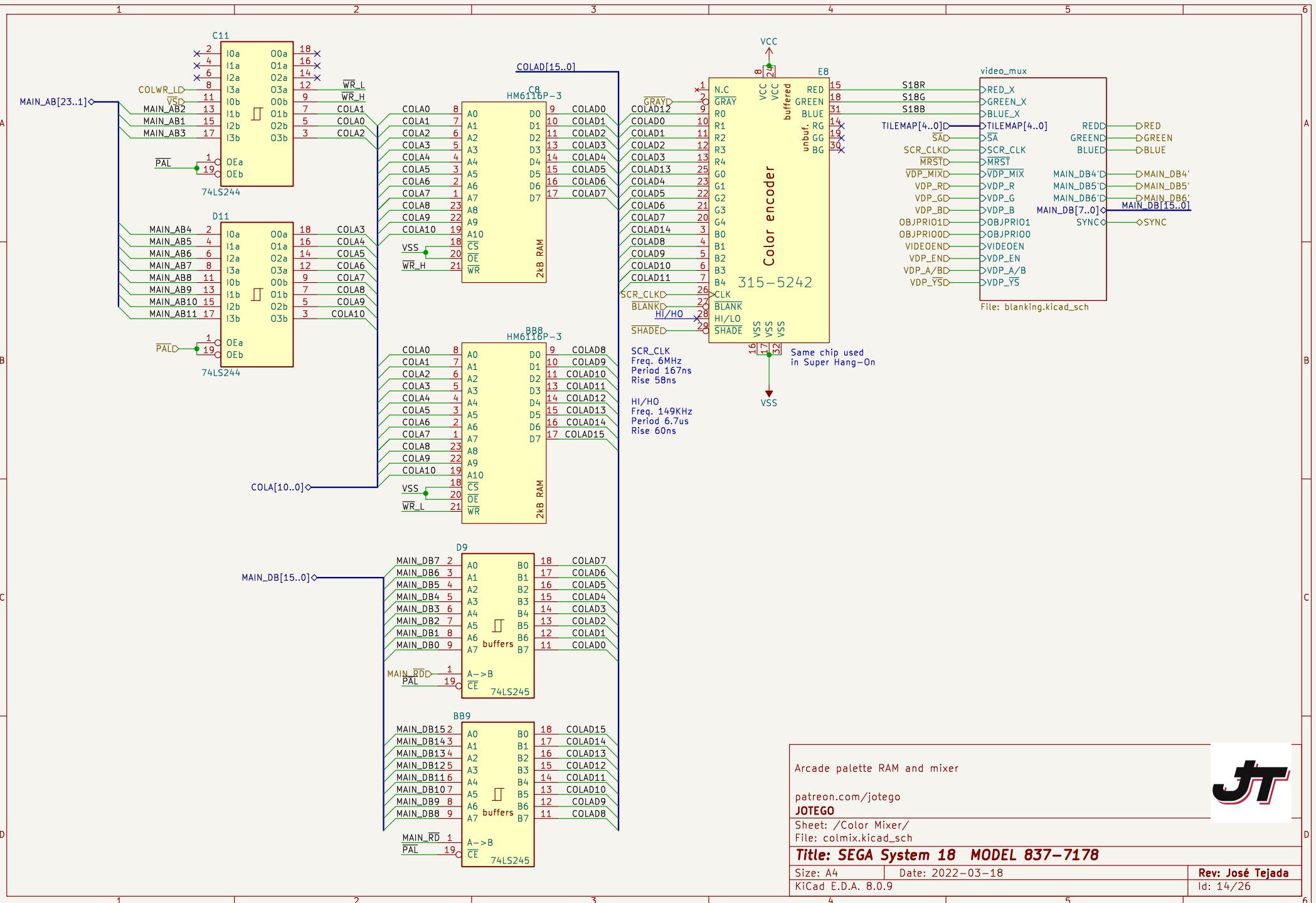
Id: 9/26

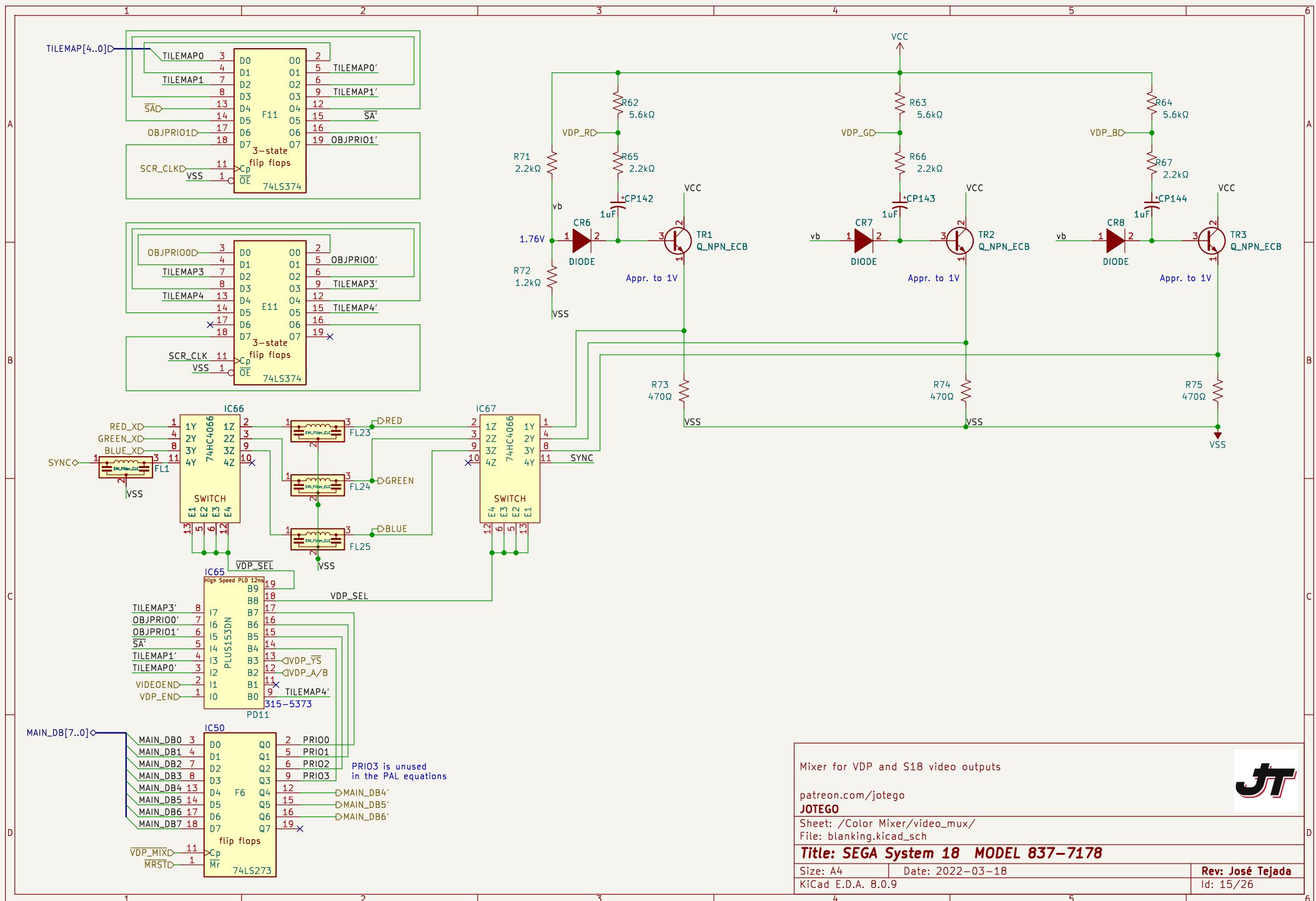




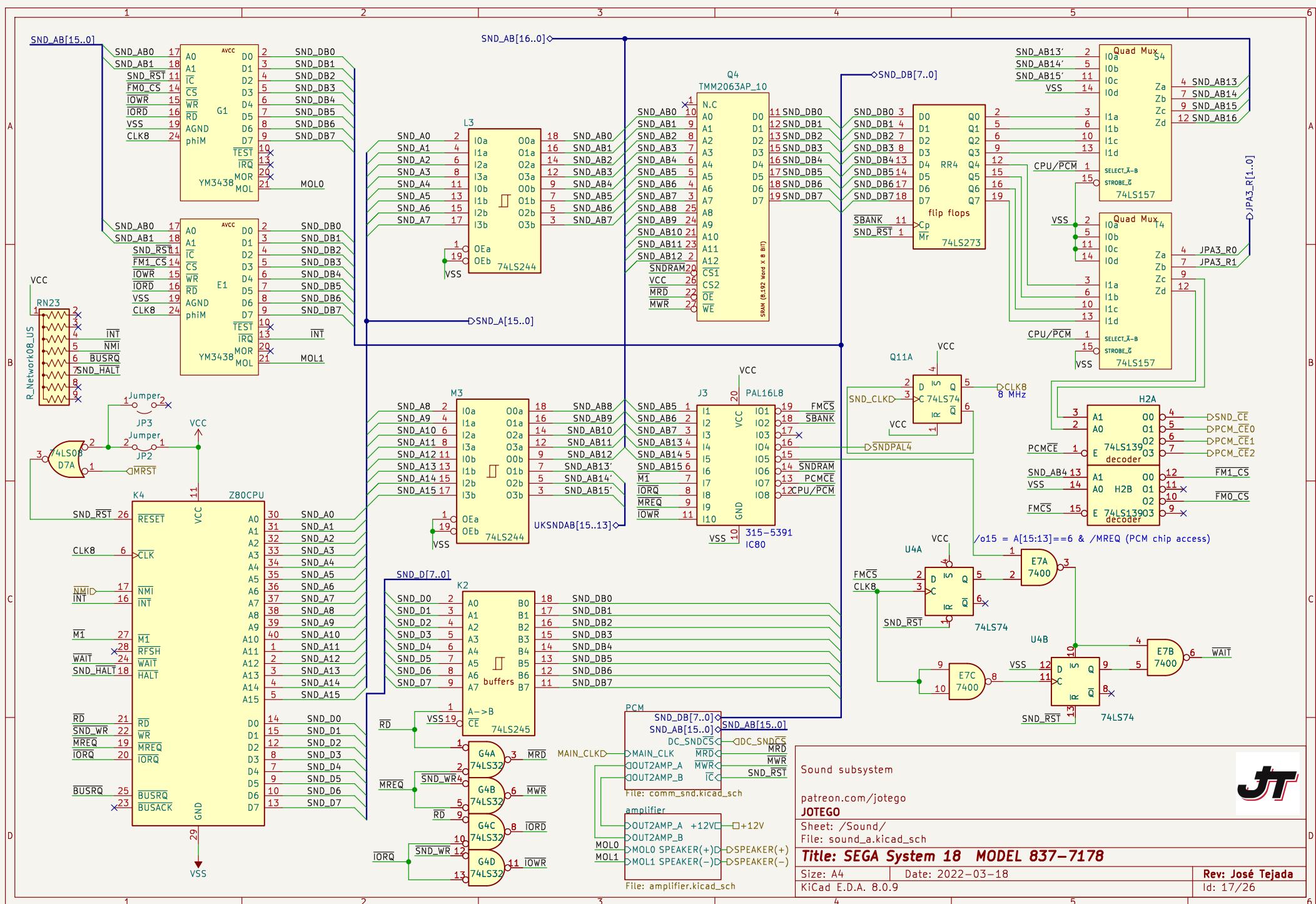


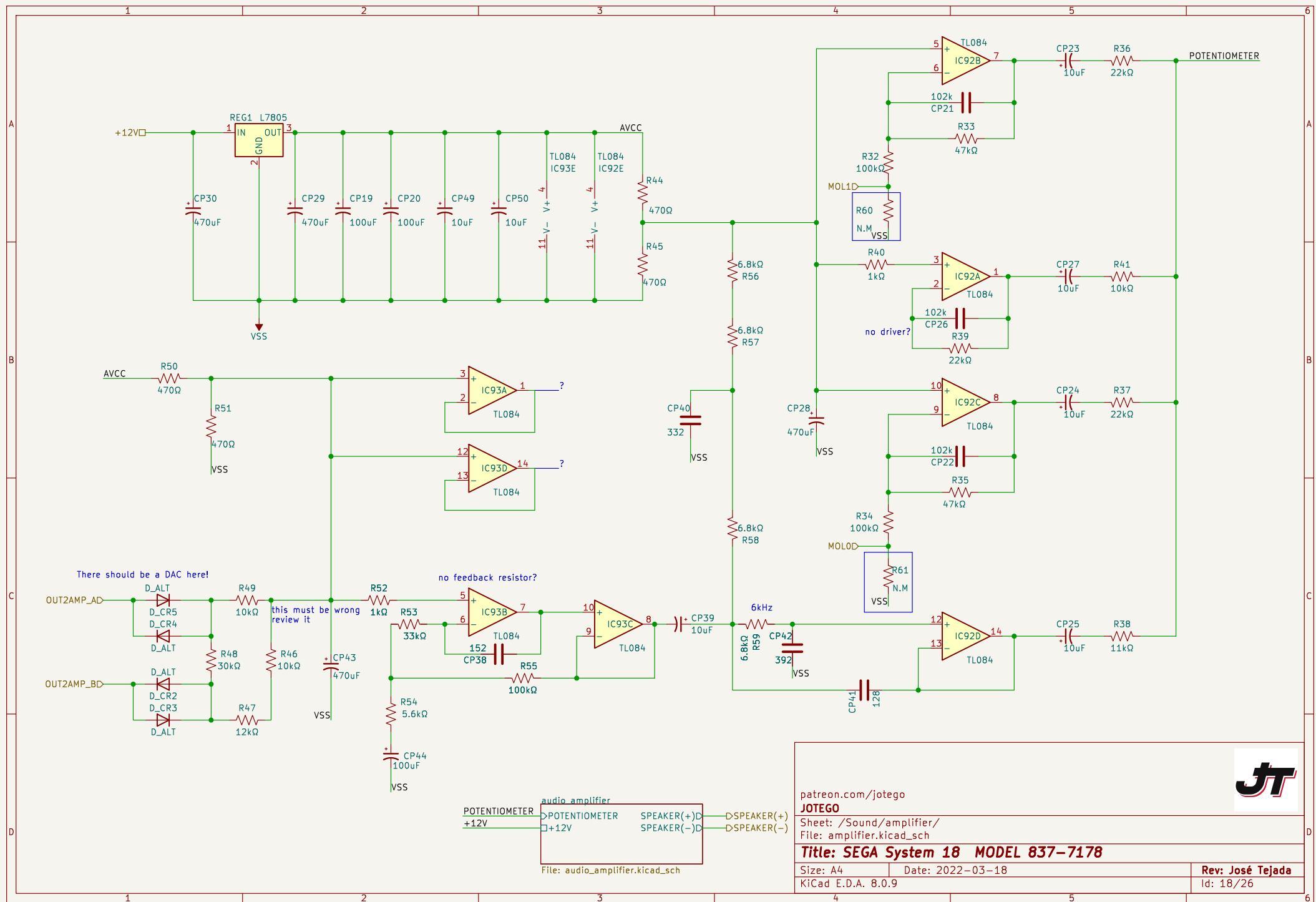




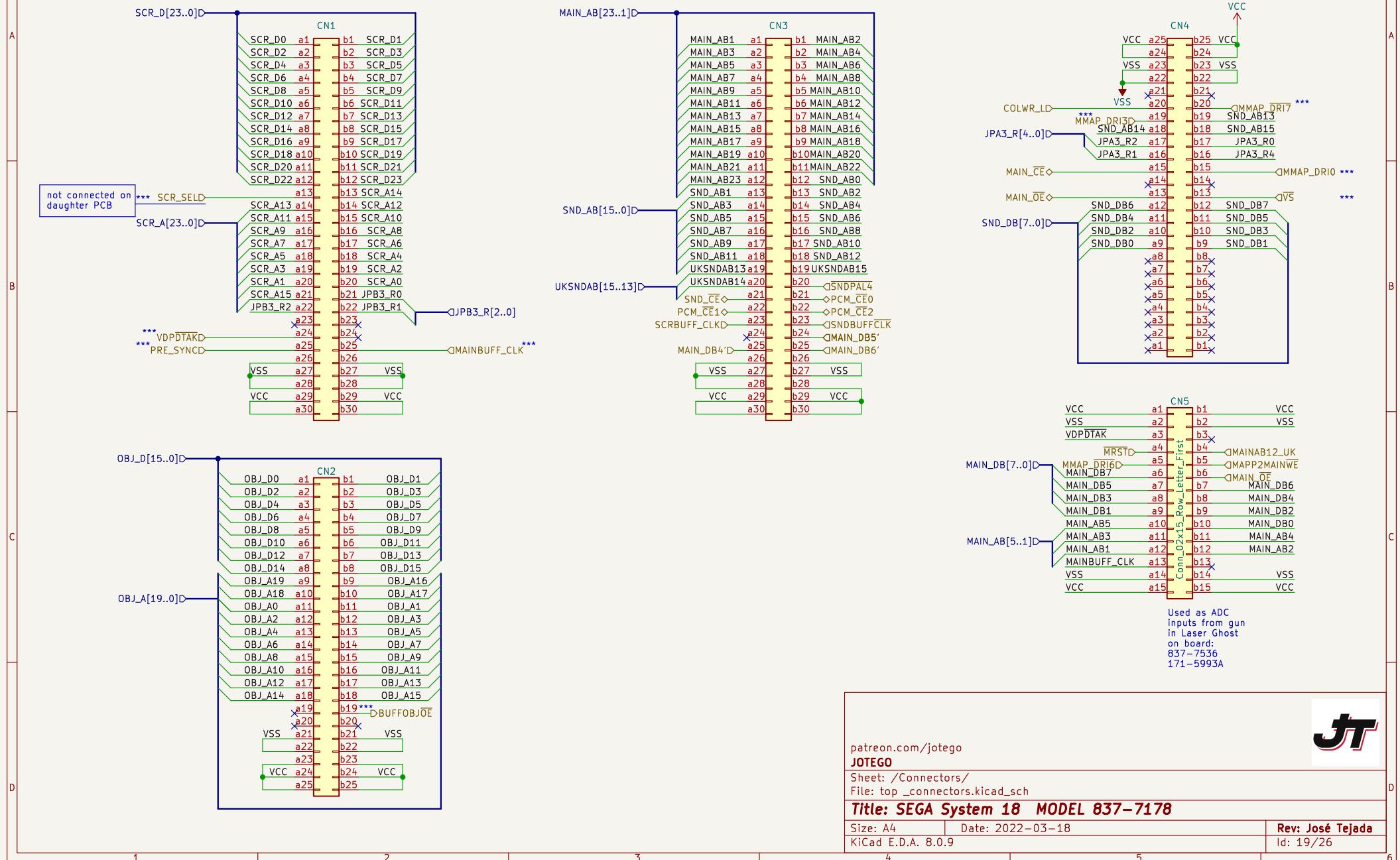


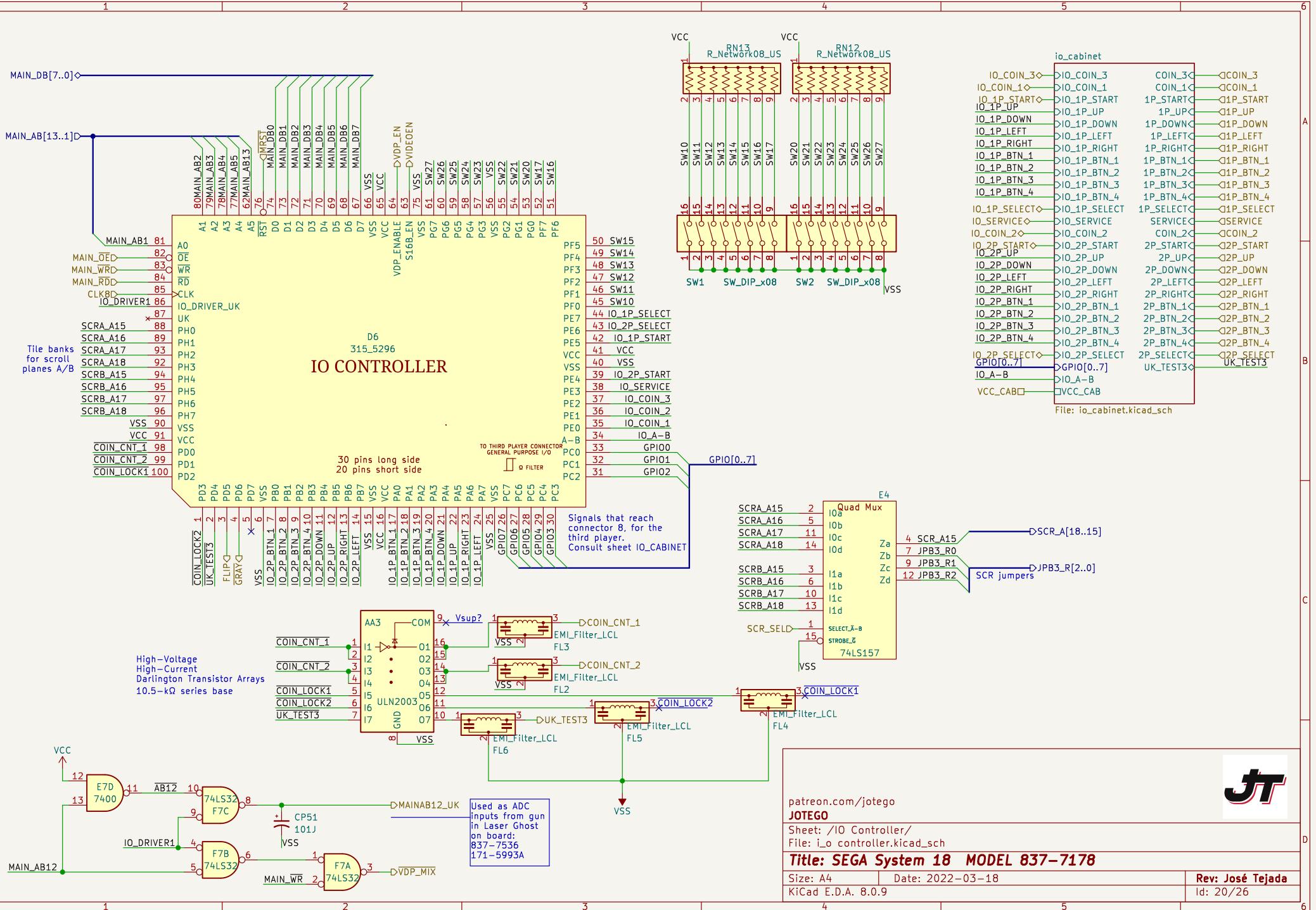


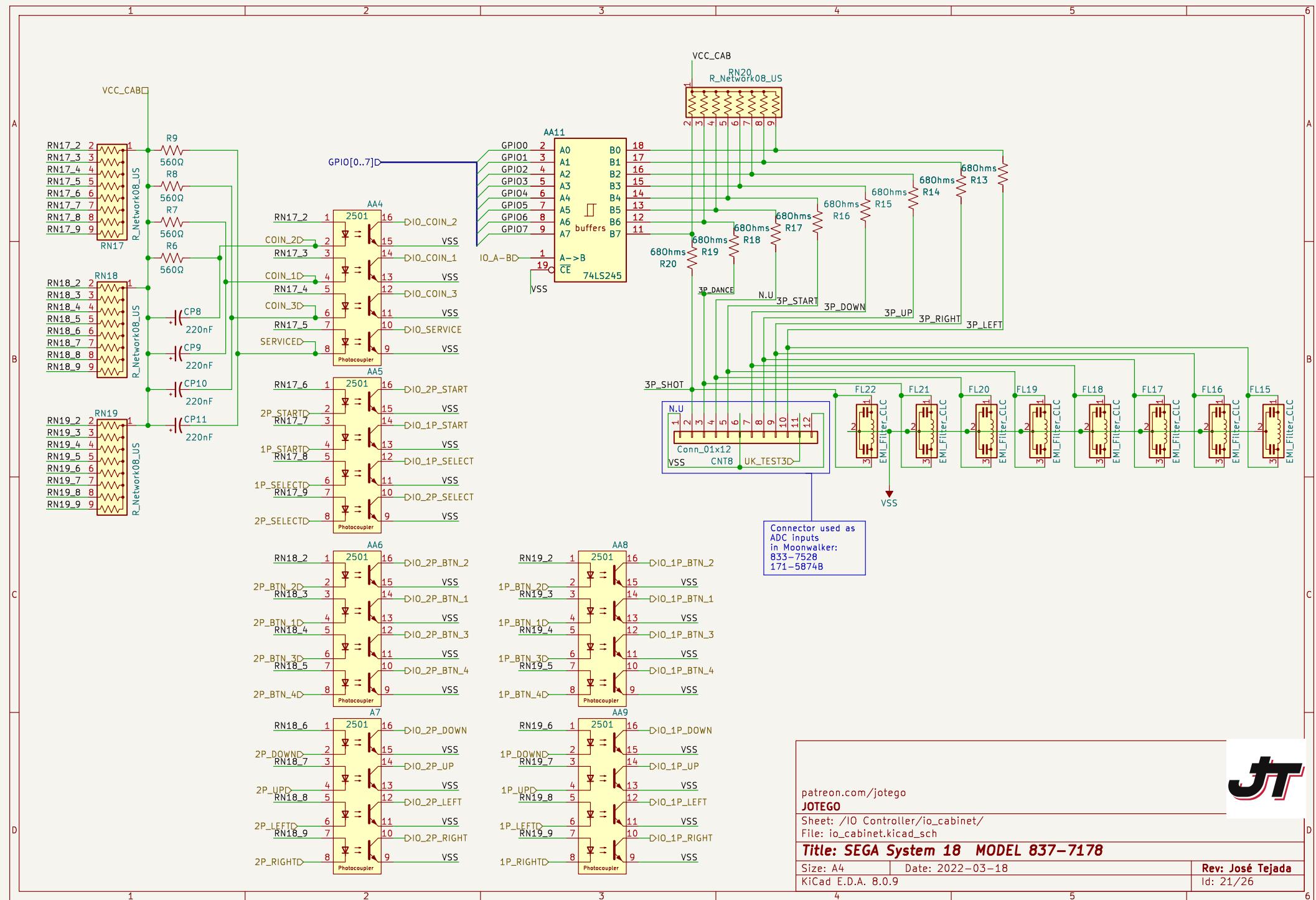


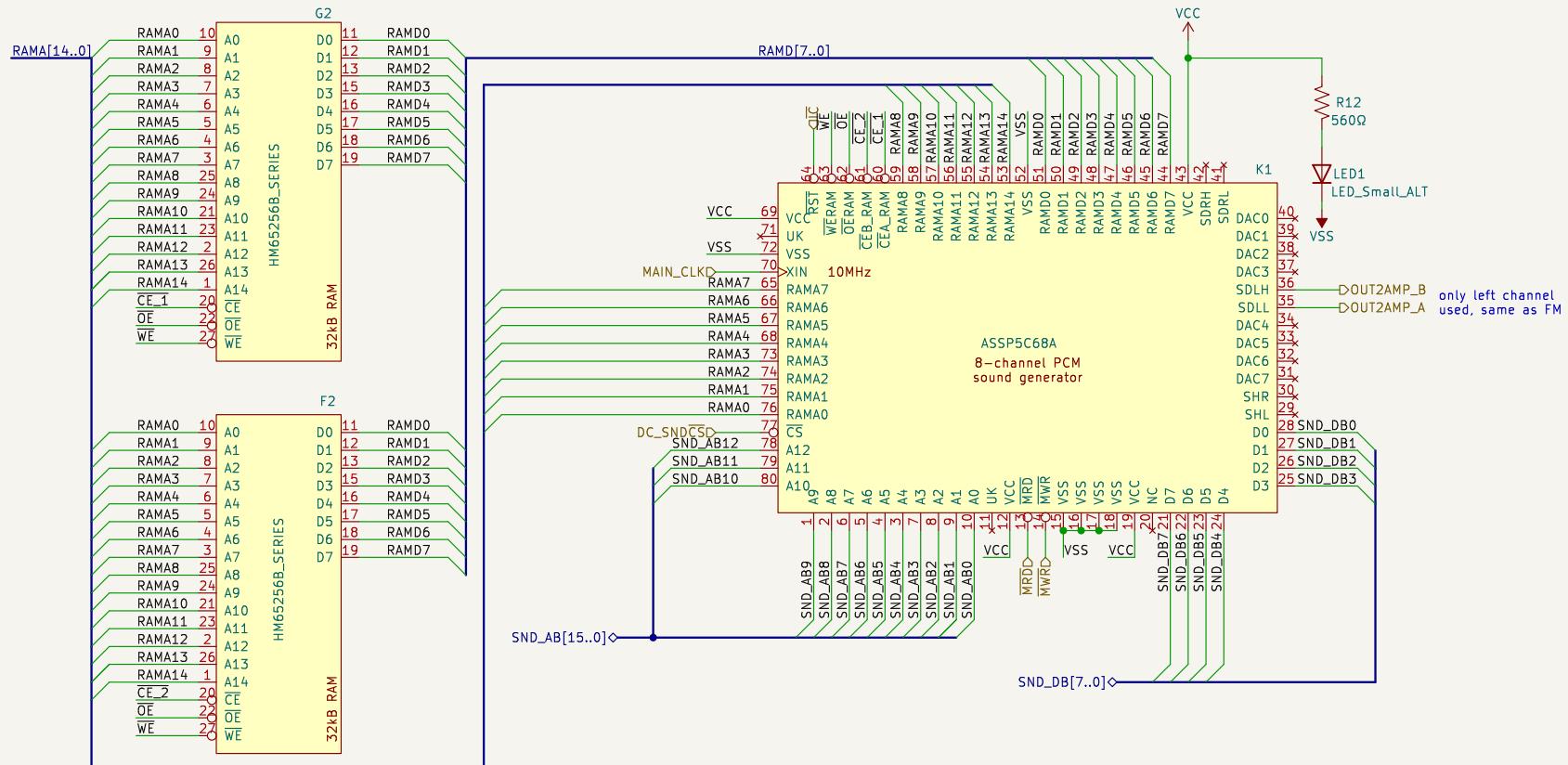


# MOTHER AND DAUGHTER PCB CONNECTORS









#### PCM sound generator

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JOTEGO

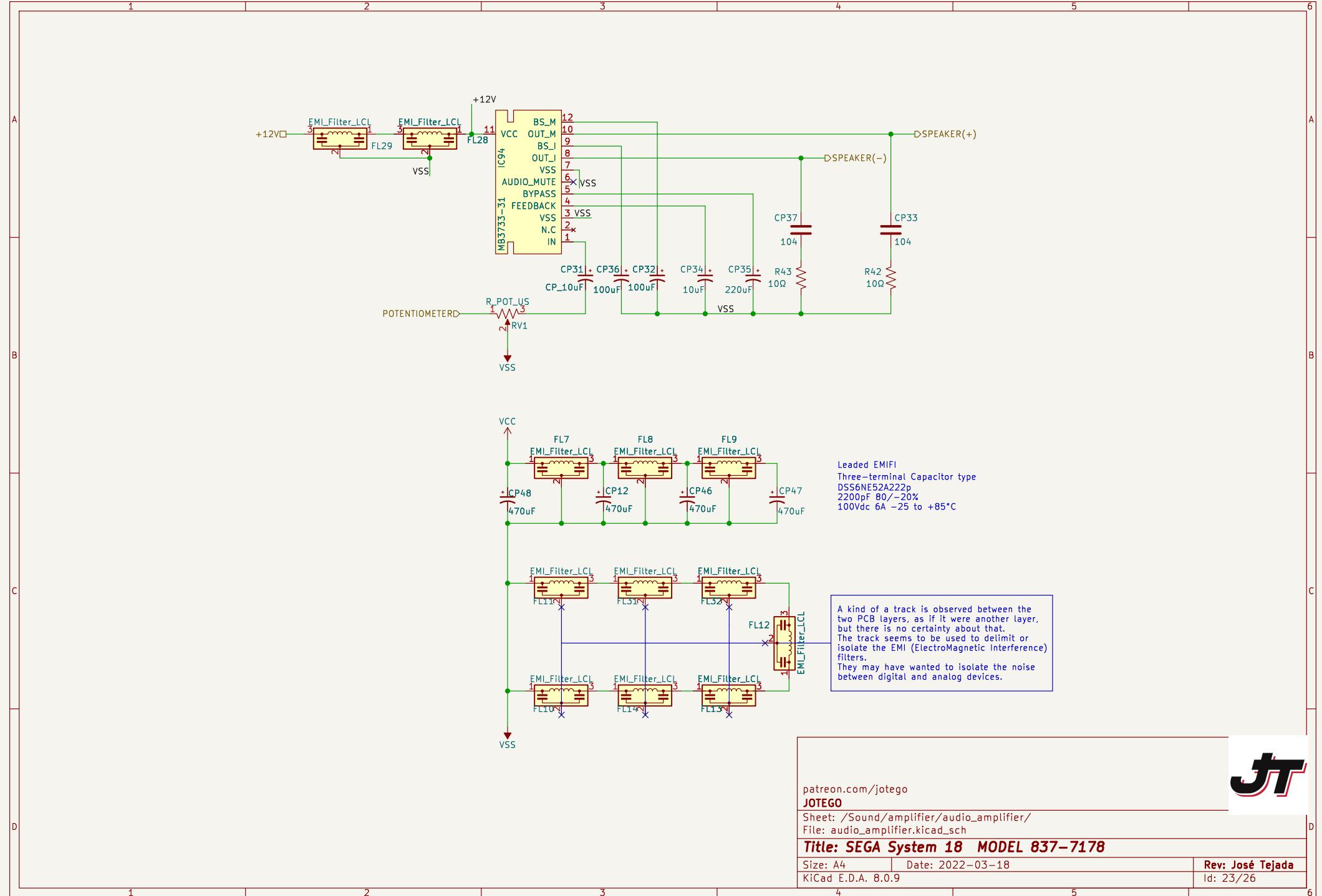
Sheet: /Sound/PCM/  
File: comm\_snd.kicad\_sch

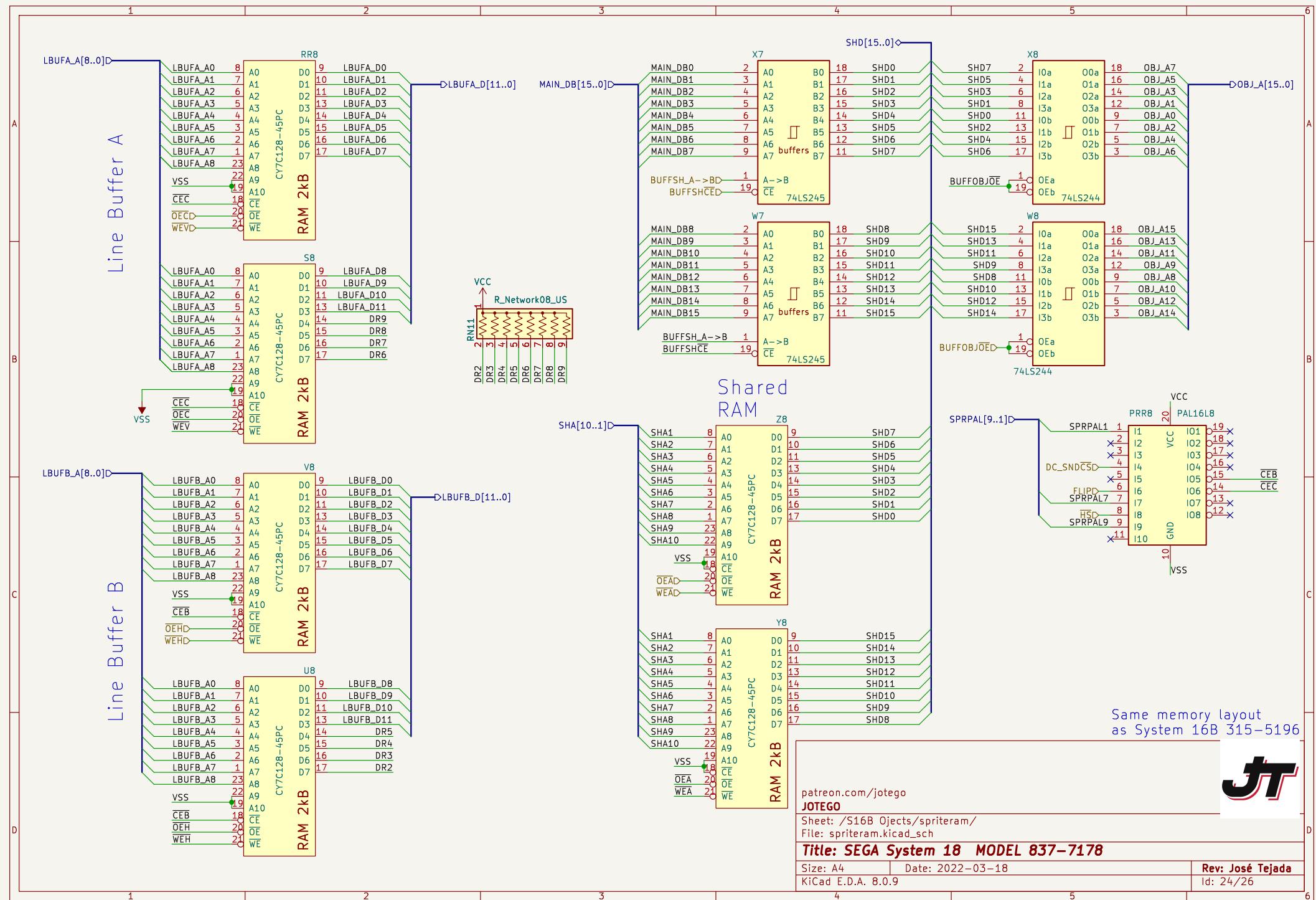
**Title: SEGA System 18 MODEL 837-7178**

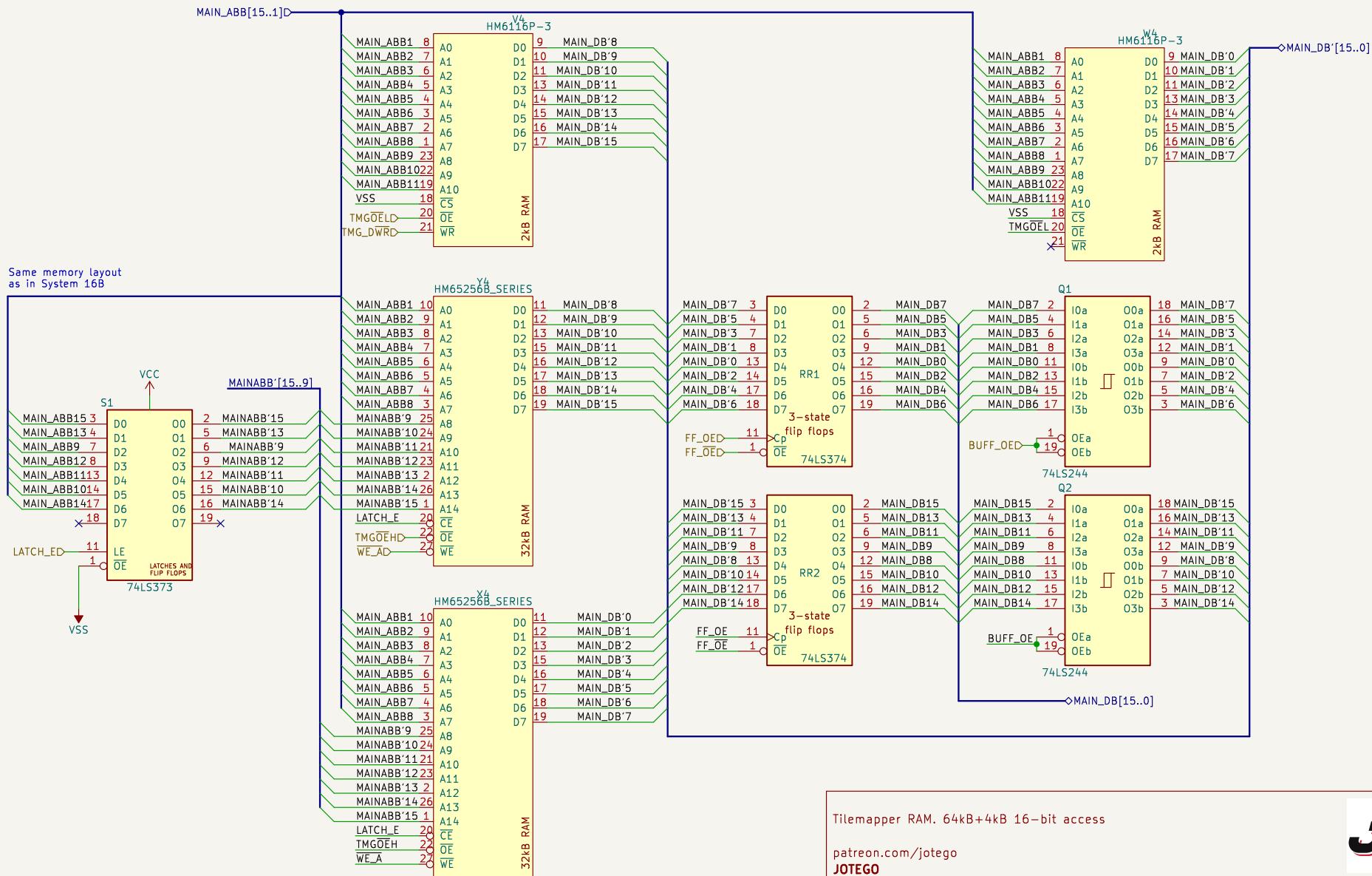
Size: A4 Date: 2022-03-18

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TiledMapper RAM. 64kB+4kB 16-bit access

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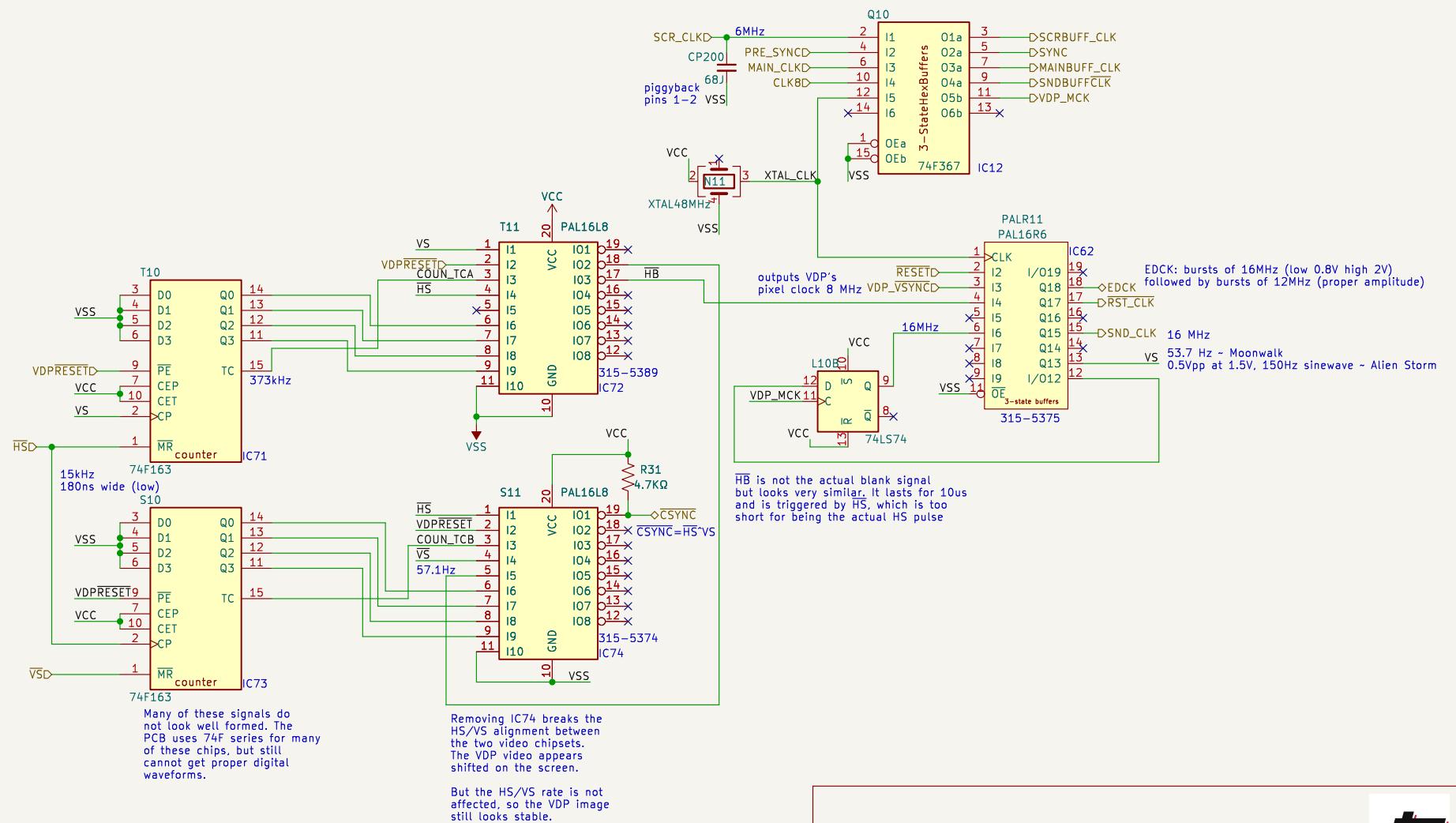
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File: tilemapg\_ram.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

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VDP pixel clock and csync generation  
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Sheet: /Video Sync/  
 File: buffers.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

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Rev: José Tejada  
 Id: 26/26