

A

A

B

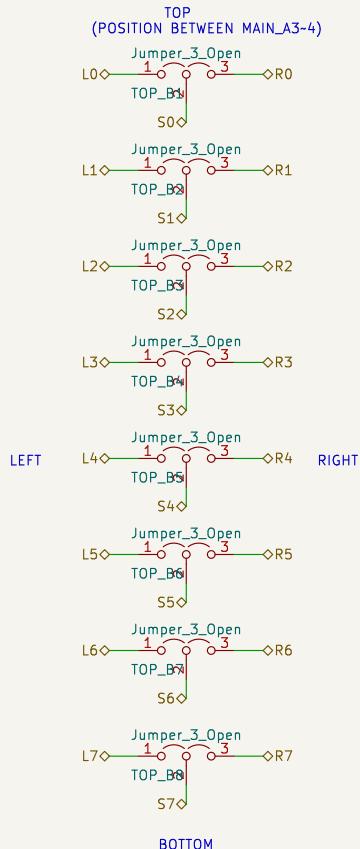
B

C

C

D

D



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JOTEGO

Sheet: /ROM board/scrjumpers/  
File: romjp.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.4

Rev: José Tejada

Id: 3/26

A

B

C

D

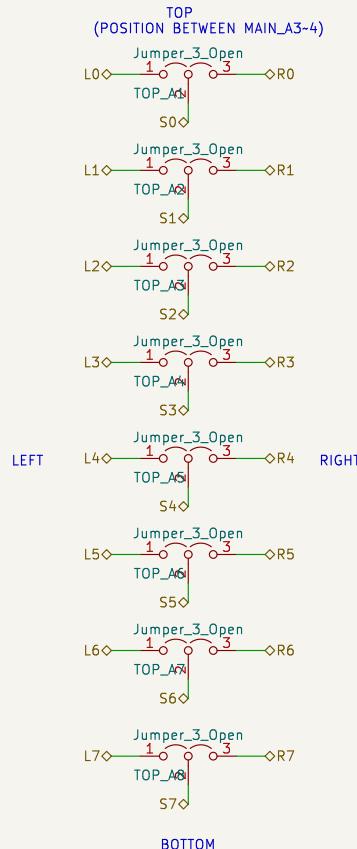
2

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3

5.0

1



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**JOTEGO**

Sheet: /ROM board/sndjumpers/  
File: romjp.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18  
KiCad EDA 8.0.4

RICOH E.D.A. 8.0.4



Rev: José Tejada  
Id: 4/26

A

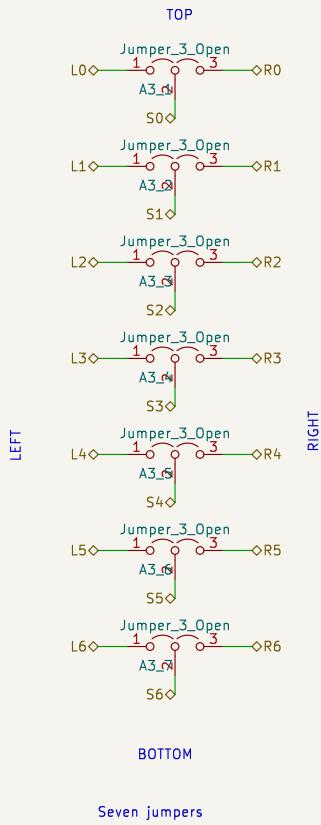
A

B

B

C

C



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JOTEGO

Sheet: /ROM board/objjumpers/  
File: romjp7.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4

Date: 2022-03-18

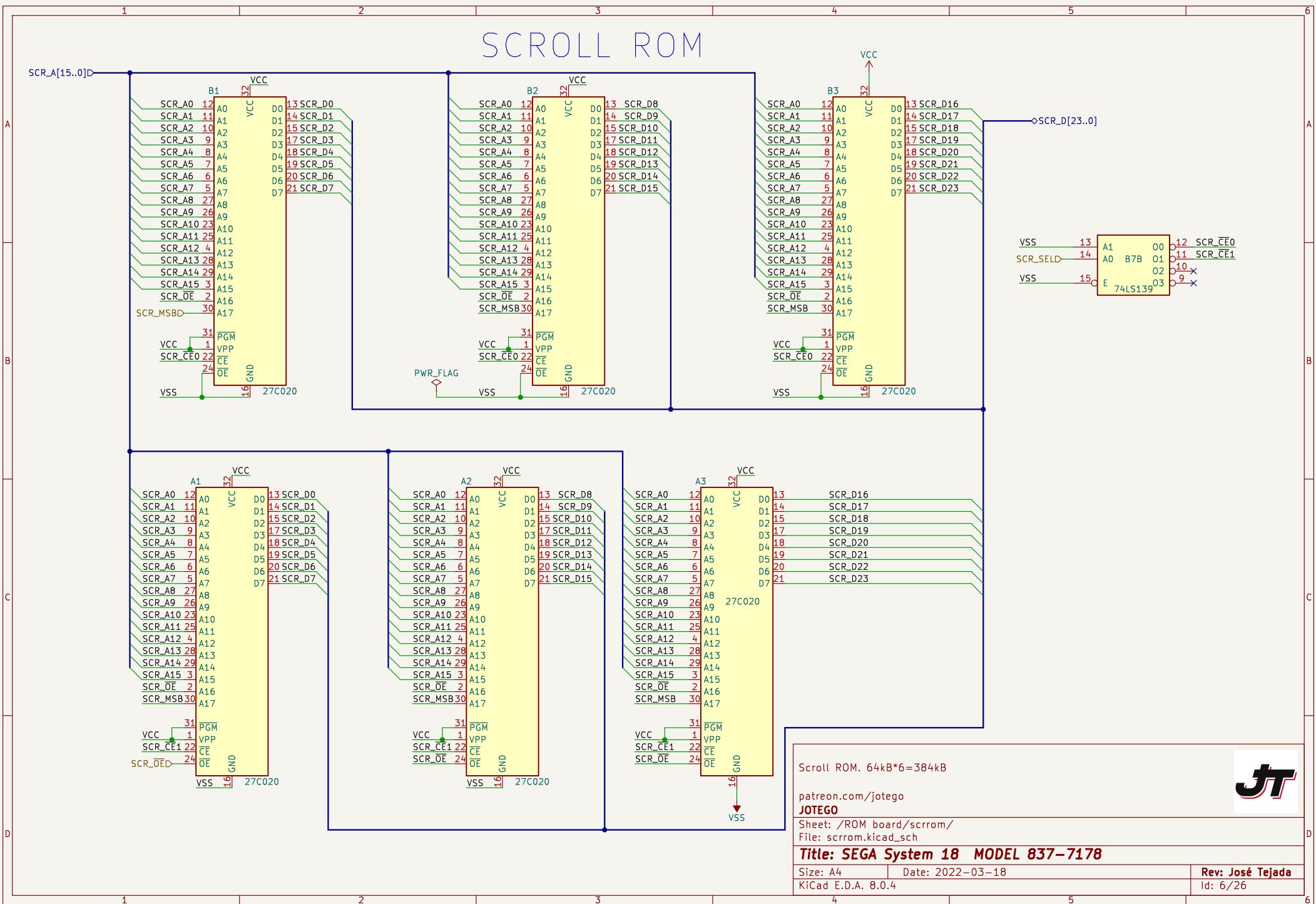
KiCad\_EDA\_8.0.4

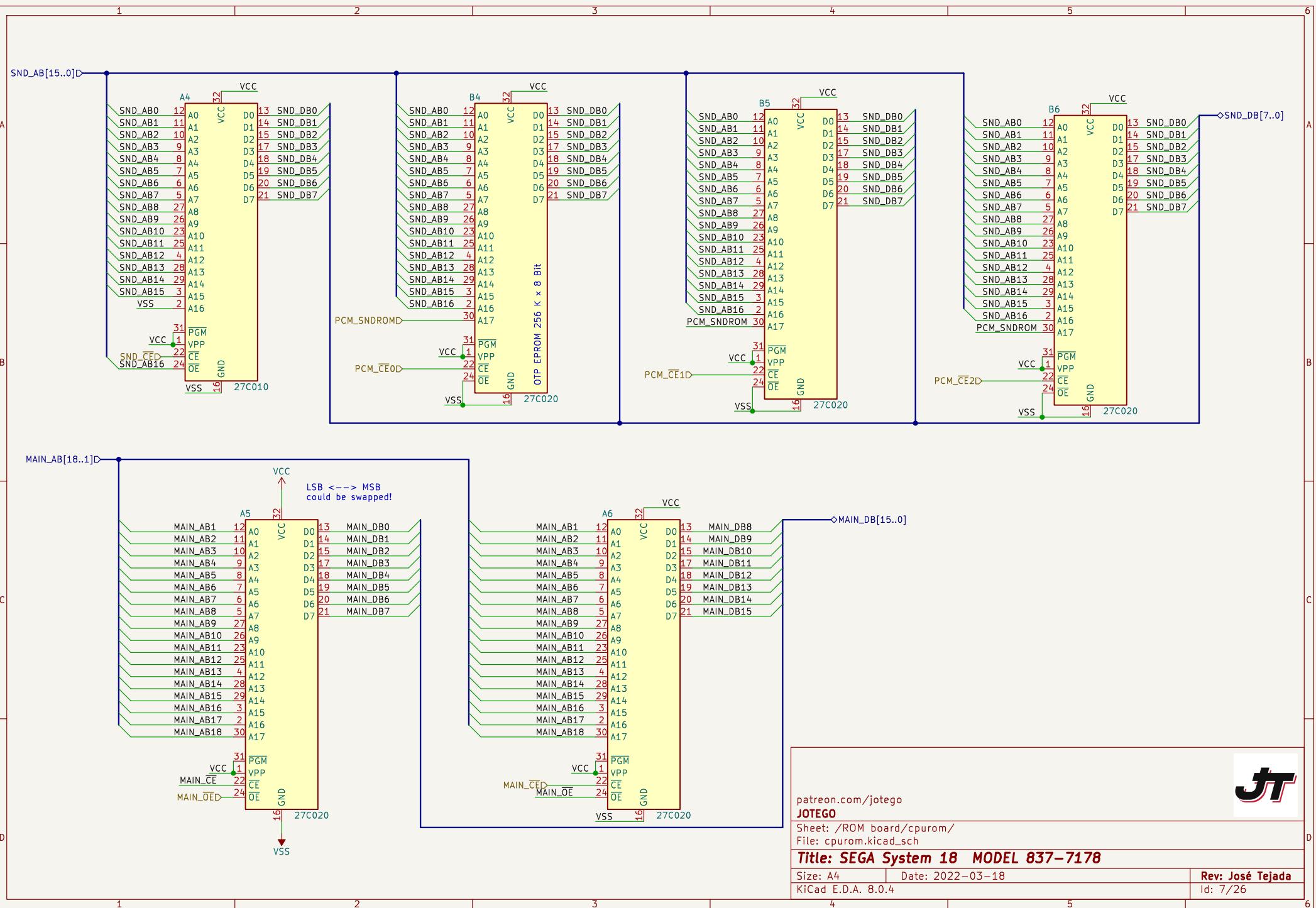
Ricad E.D.A. 8.0.4

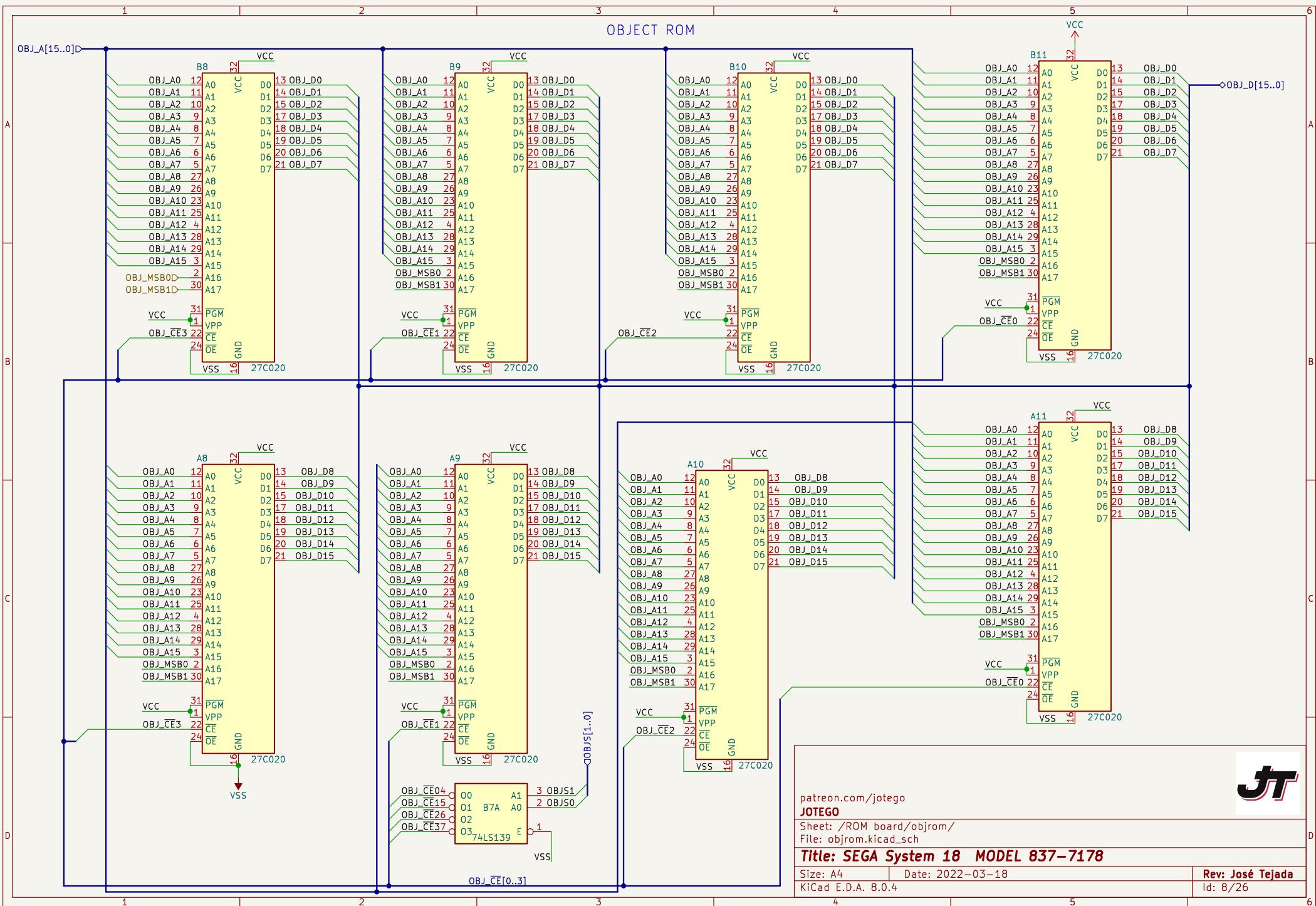
Rev: José Tejada

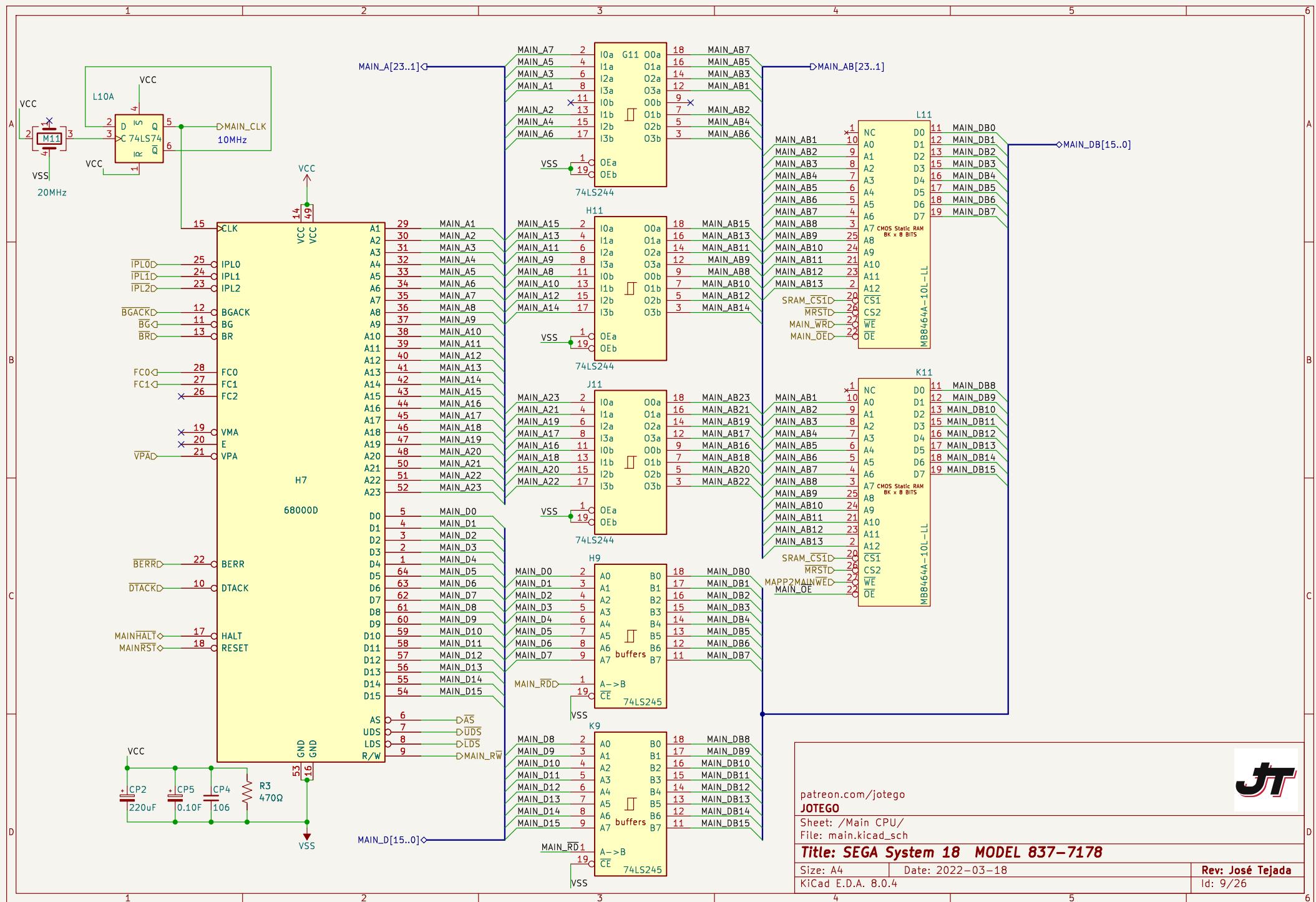
10: 3/26

# SCROLL ROM









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JOTEGO

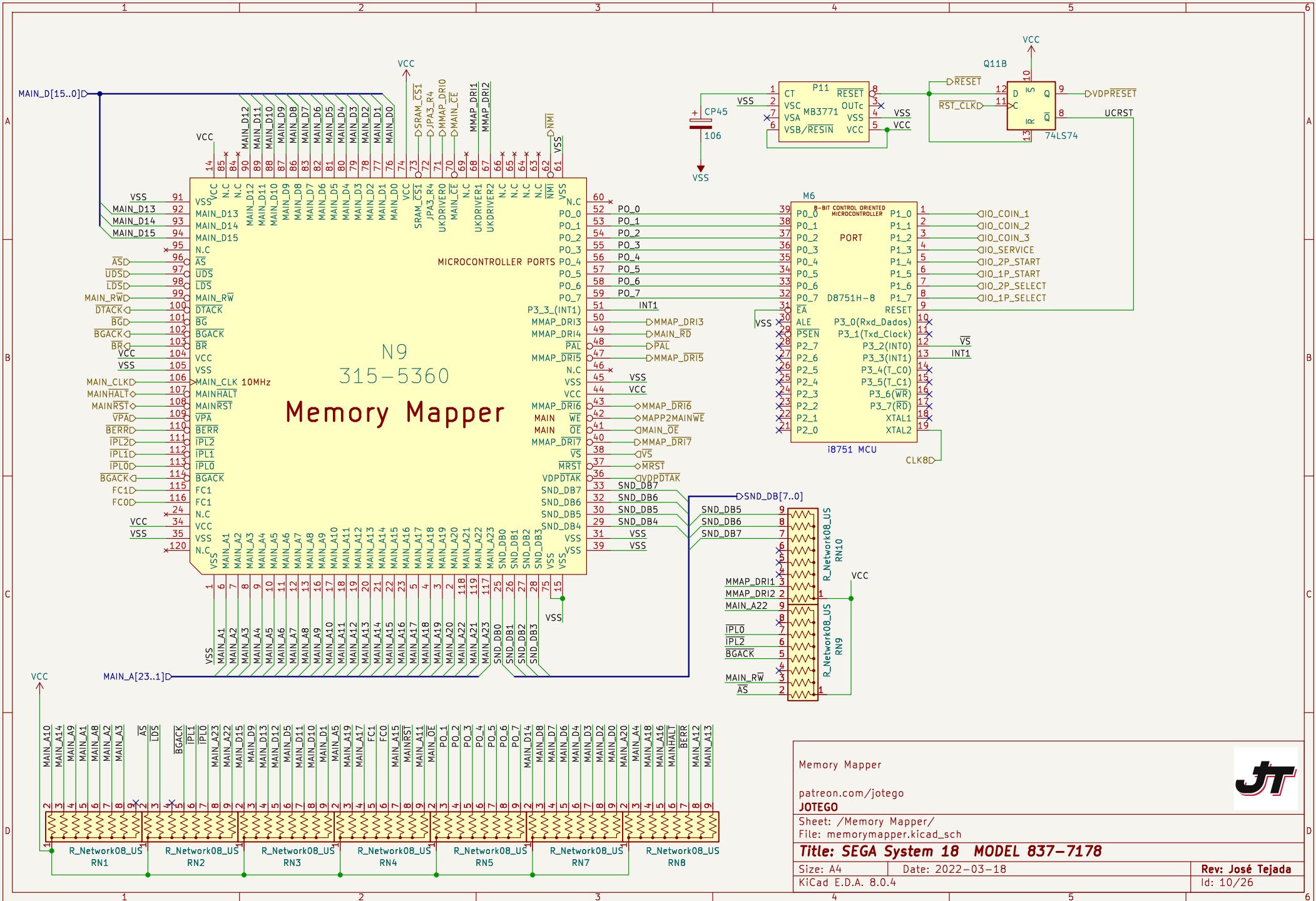
Sheet: /Main CPU/  
File: main.kicad\_sch

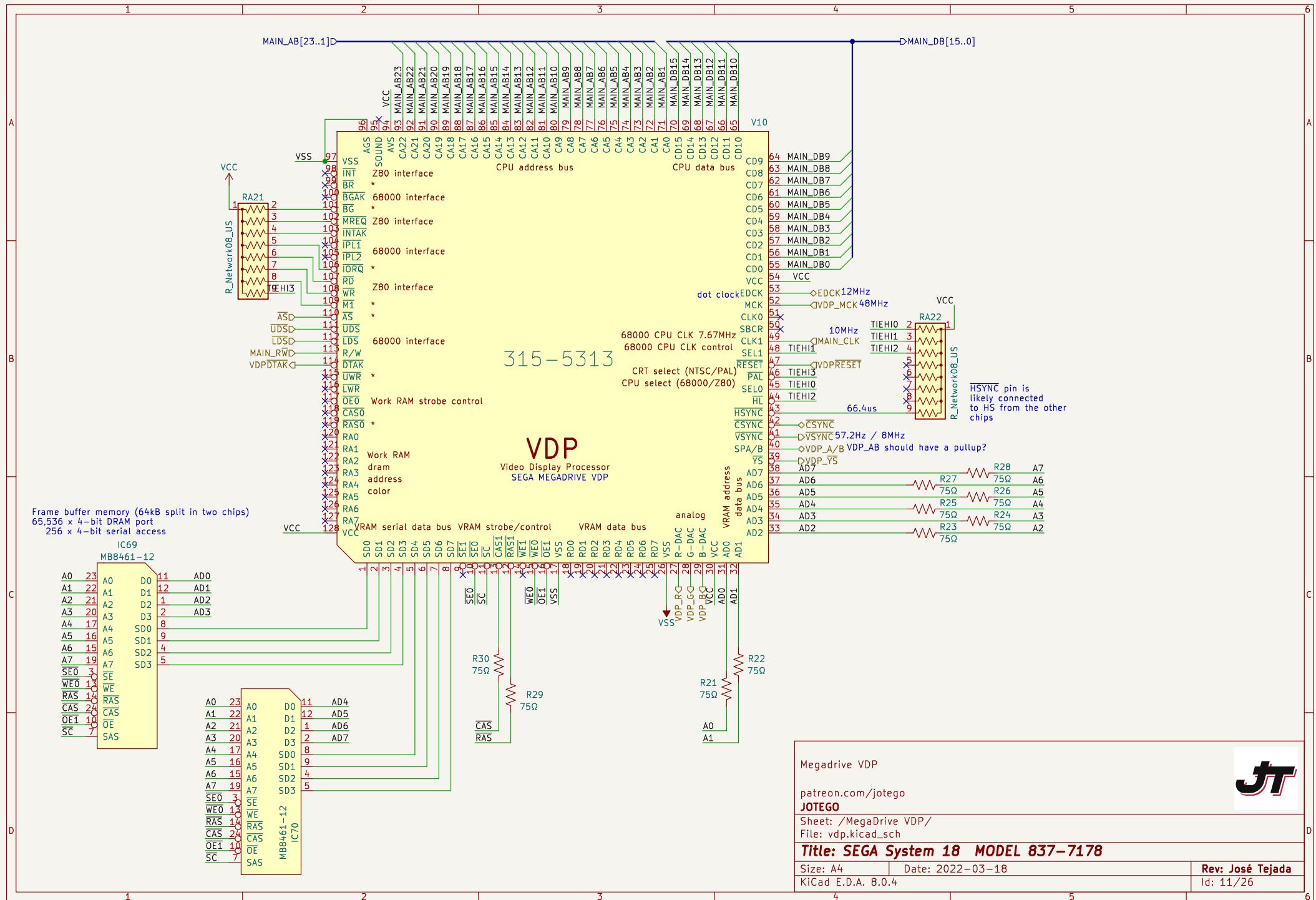
**Title: SEGA System 18 MODEL 837-7178**

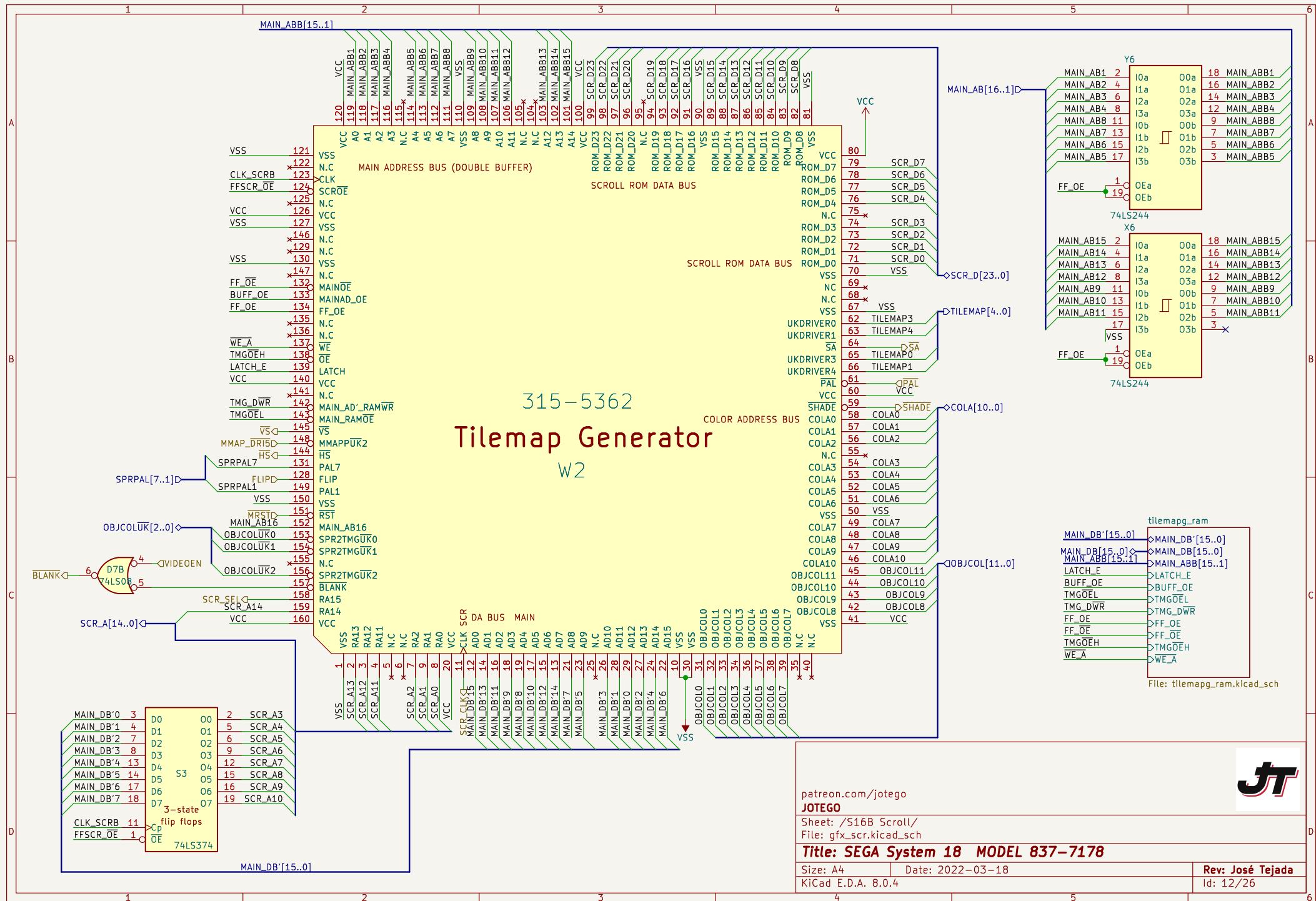
Size: A4 Date: 2022-03-18  
KiCad E.D.A. 8.0.4

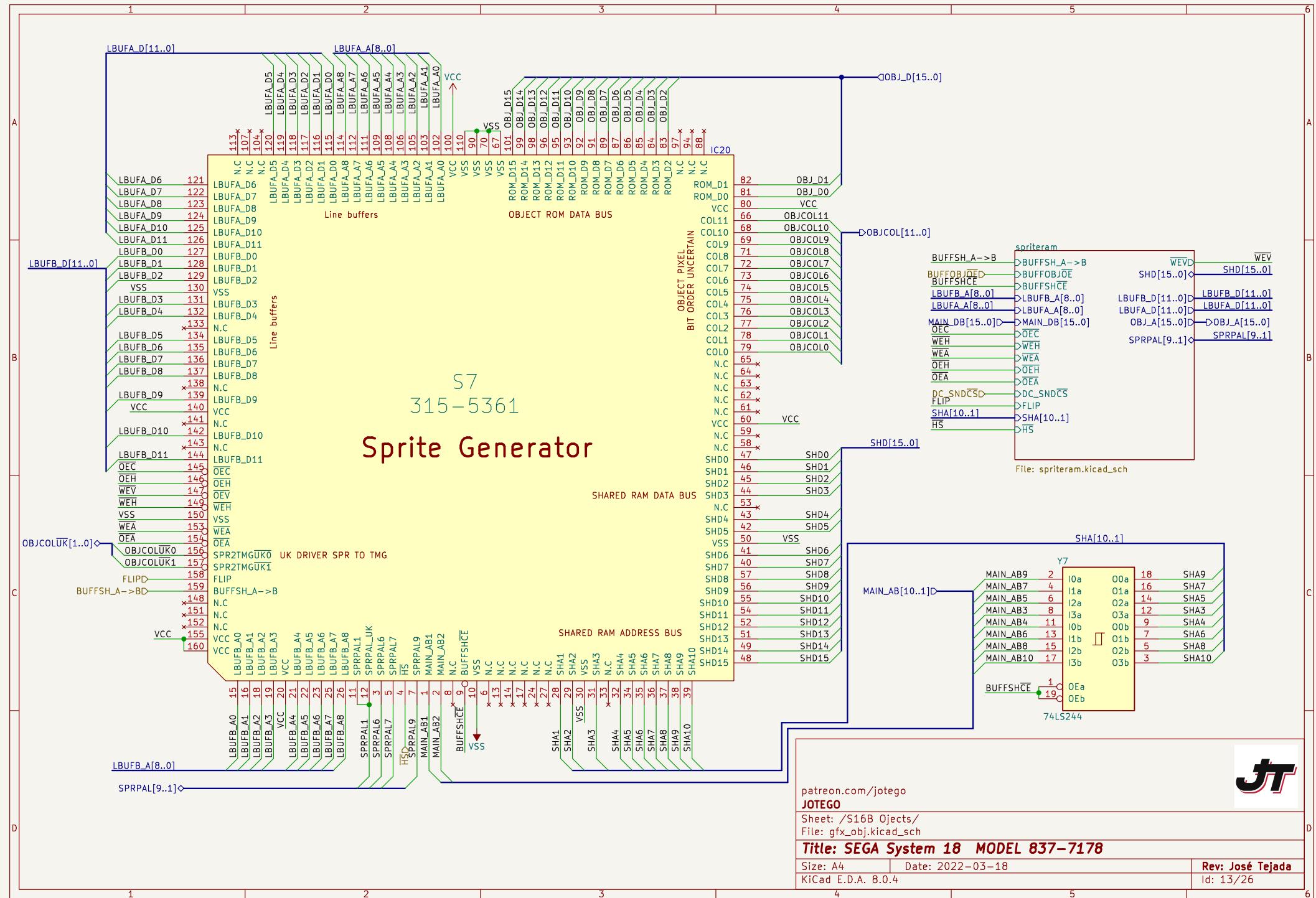


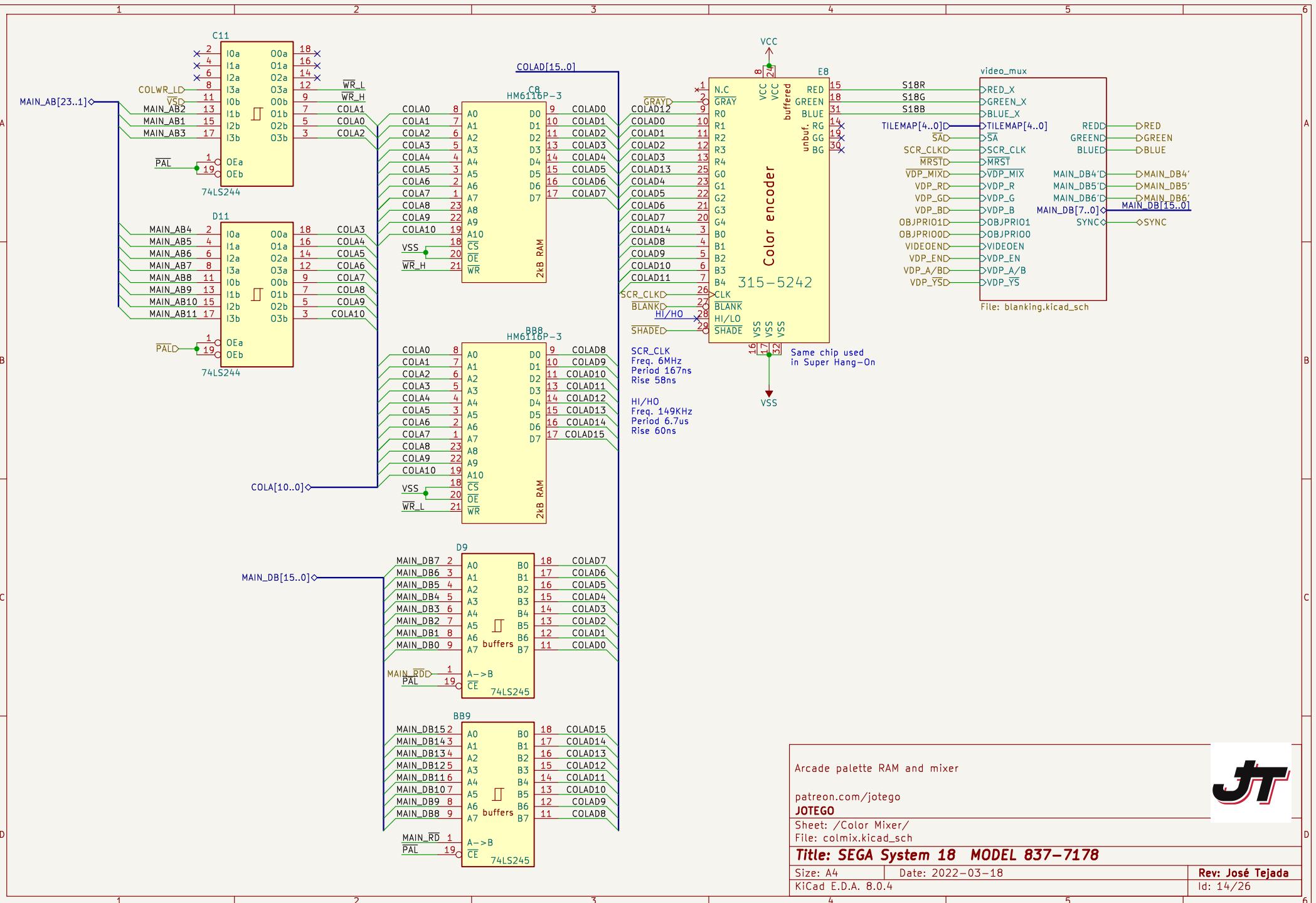
Rev: José Tejada  
Id: 9/26

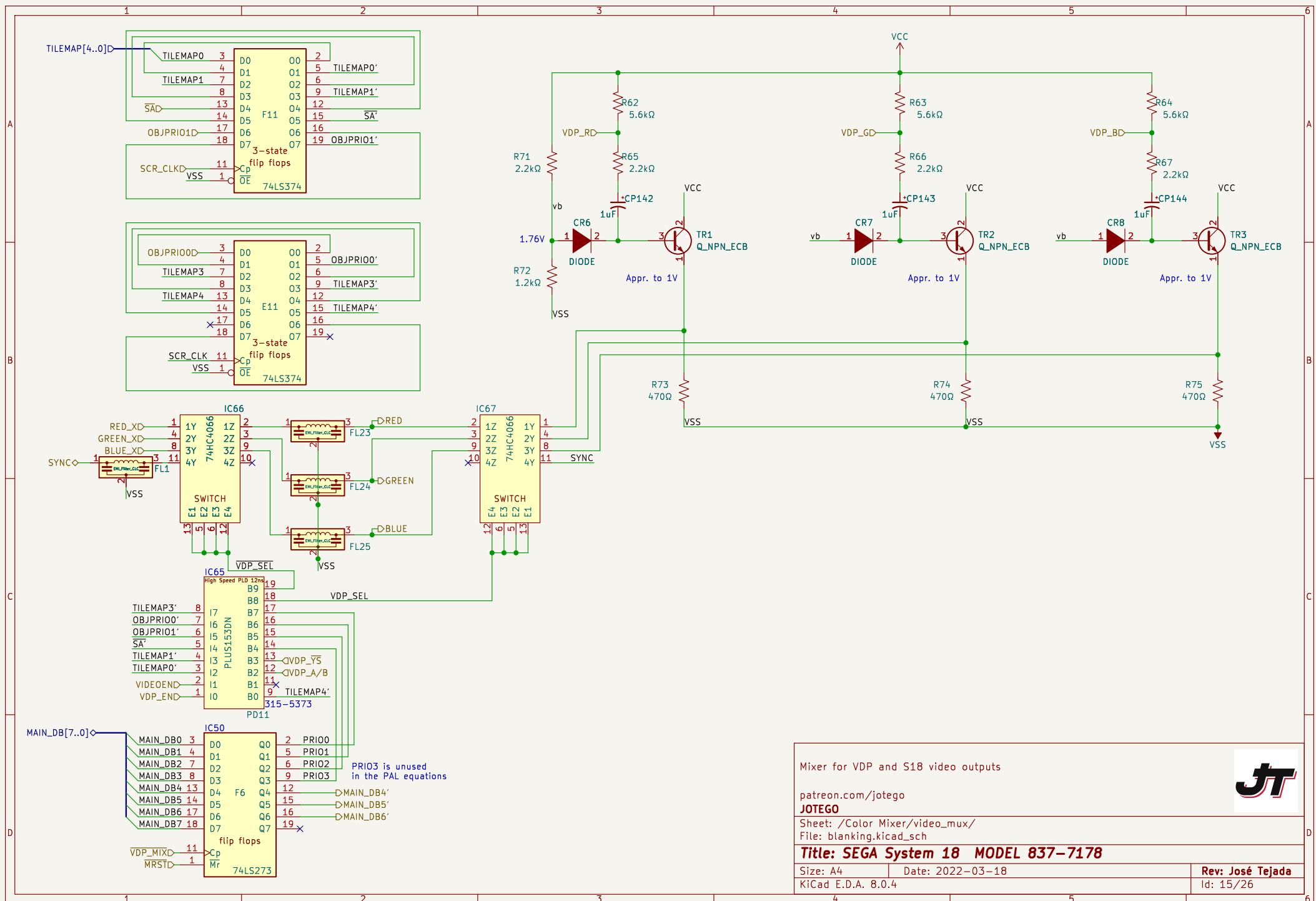




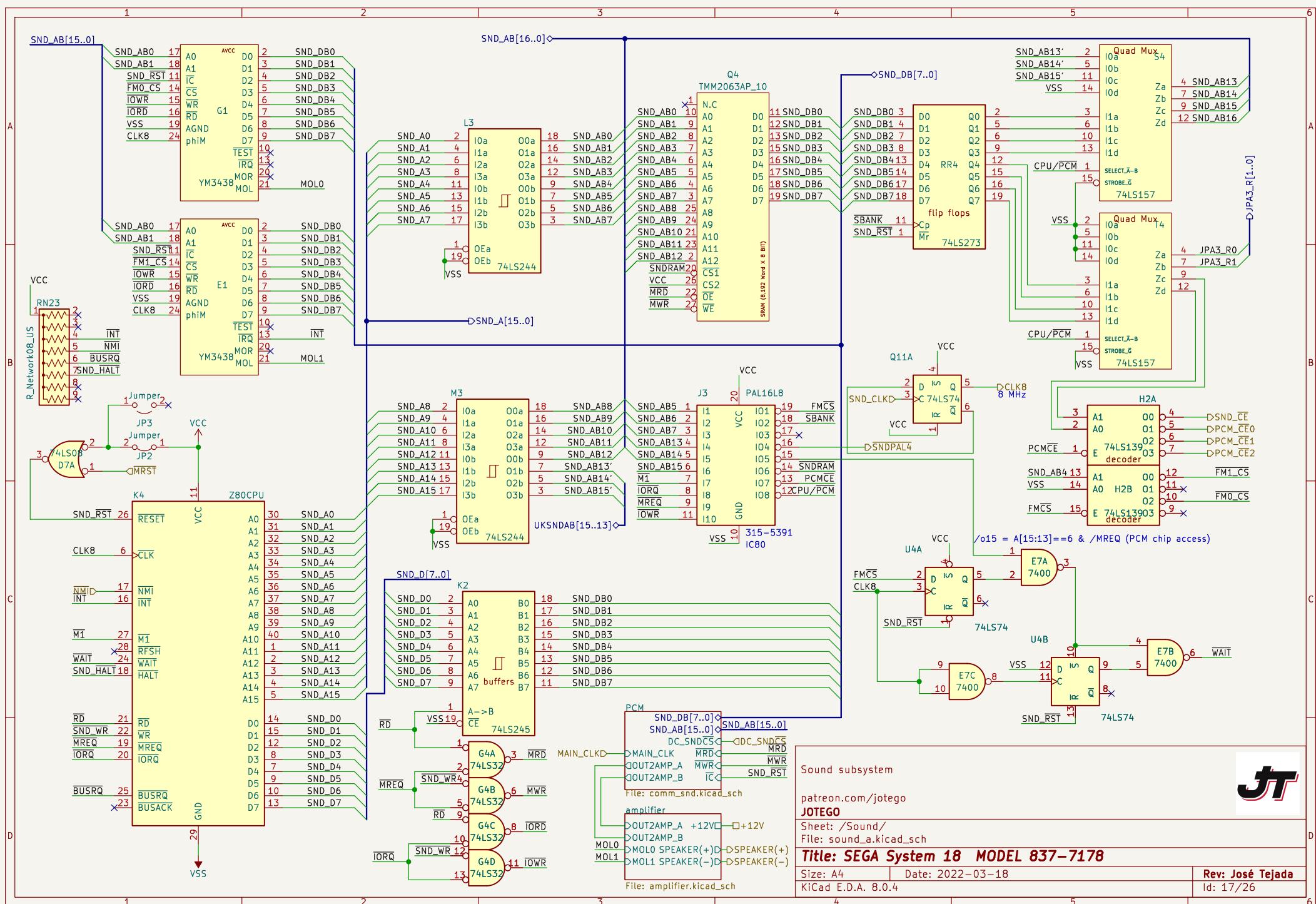


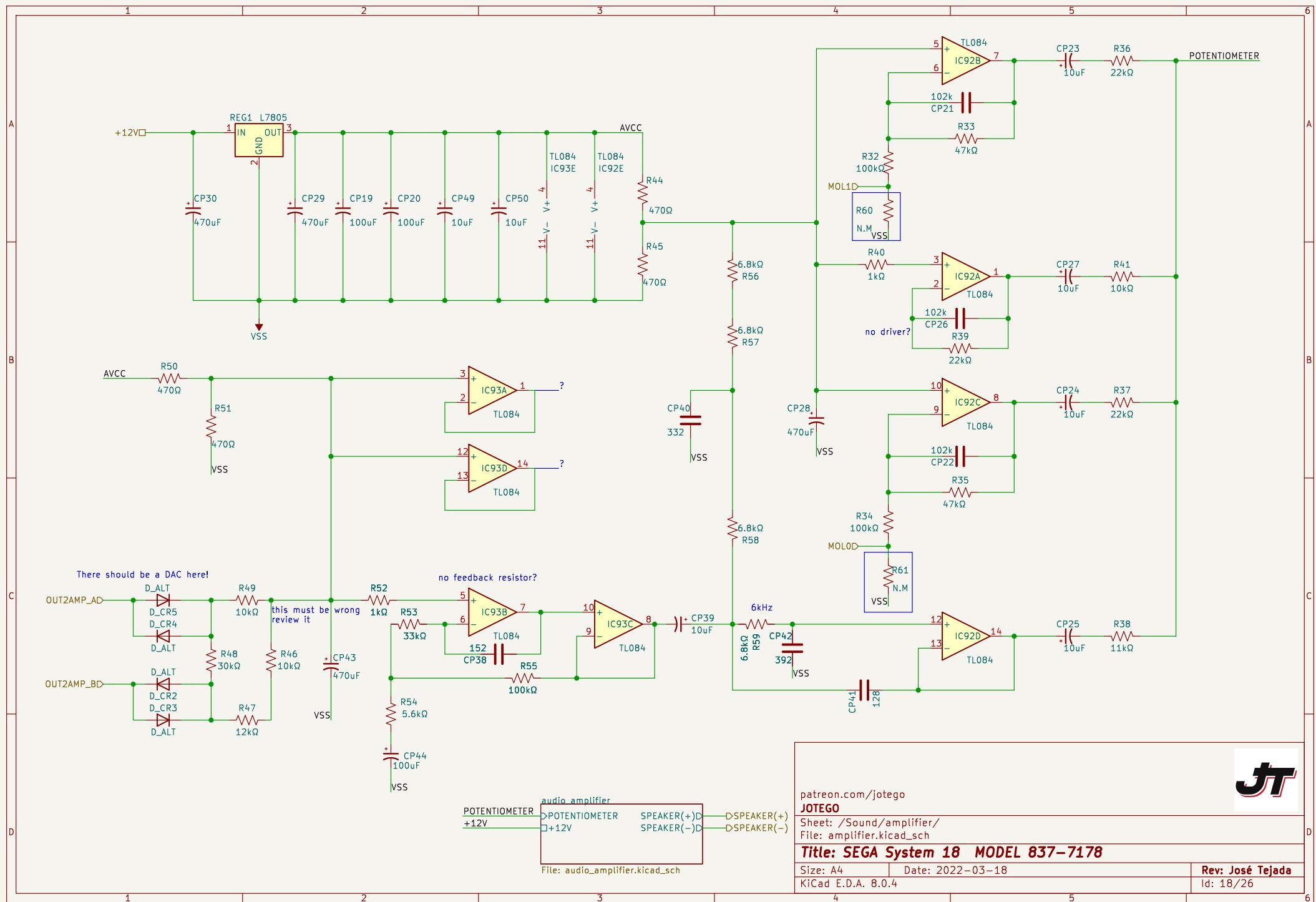




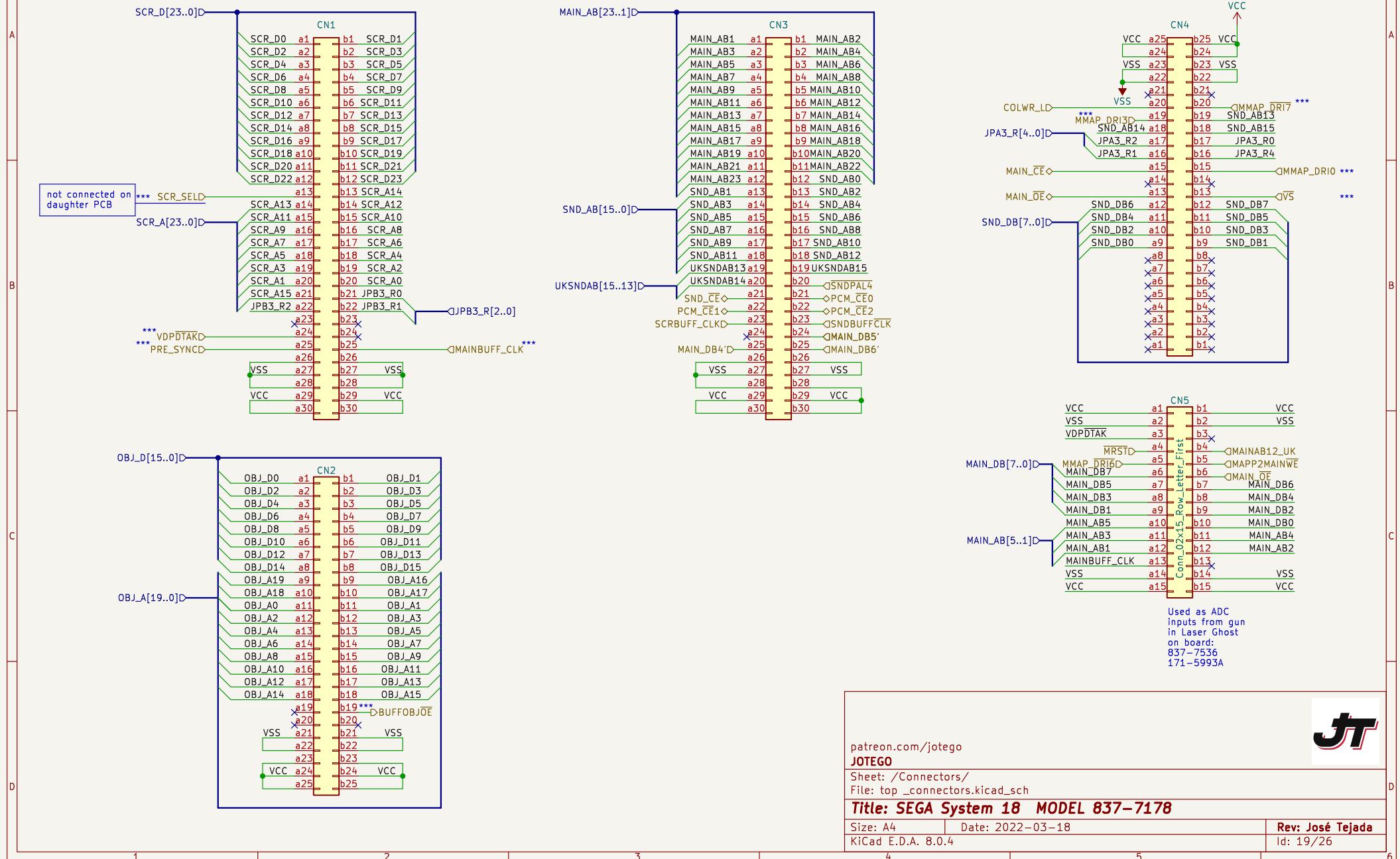


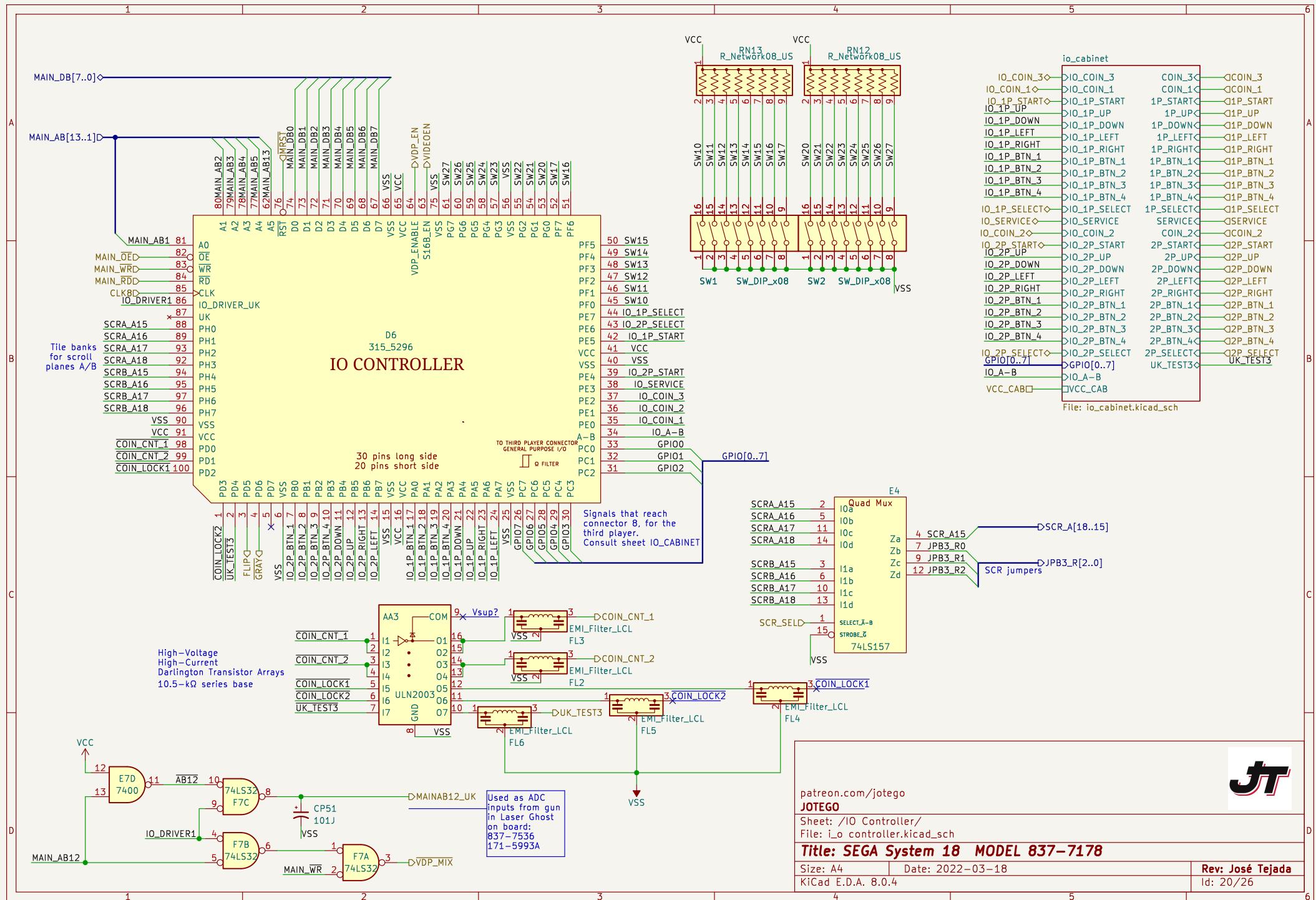


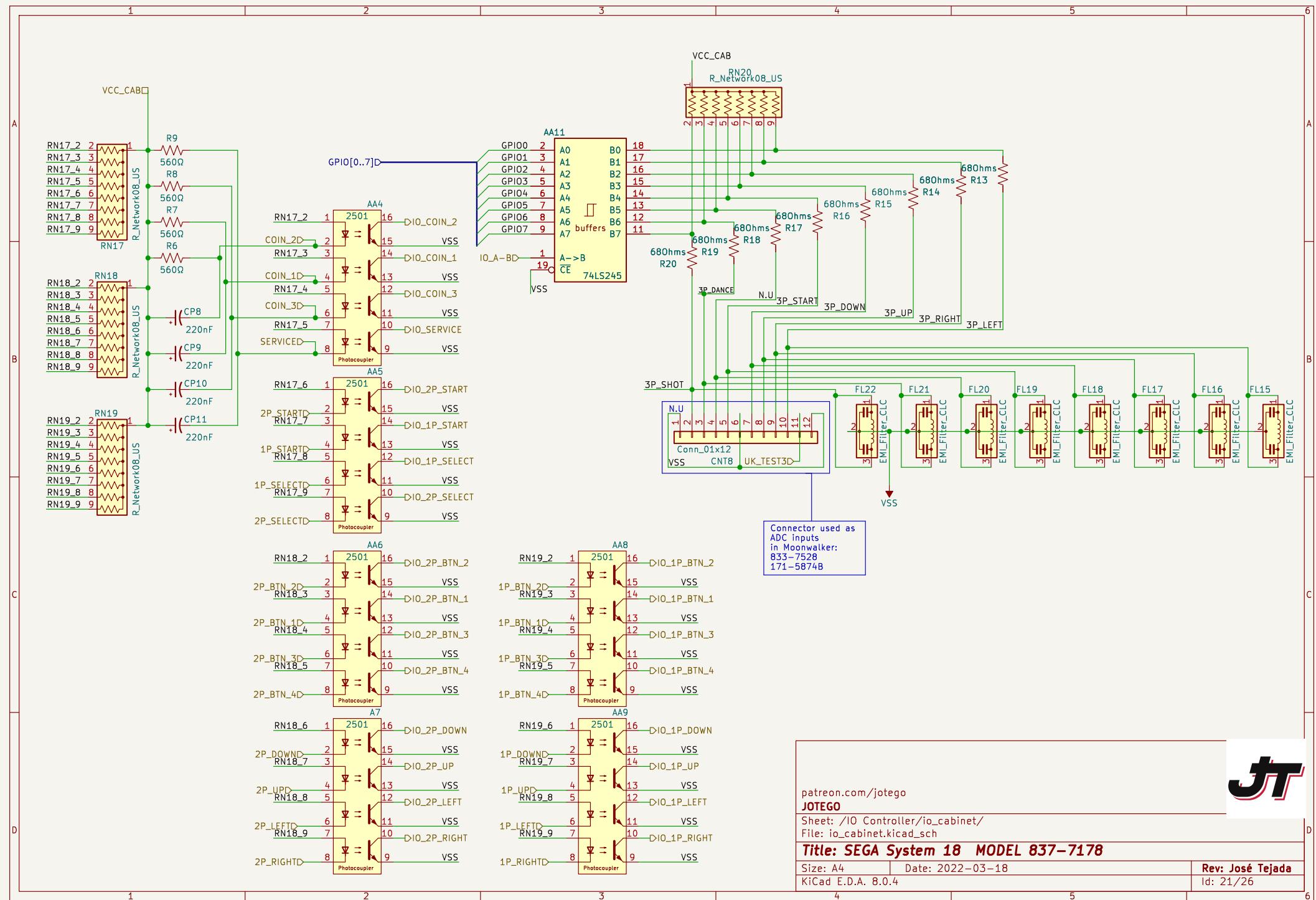


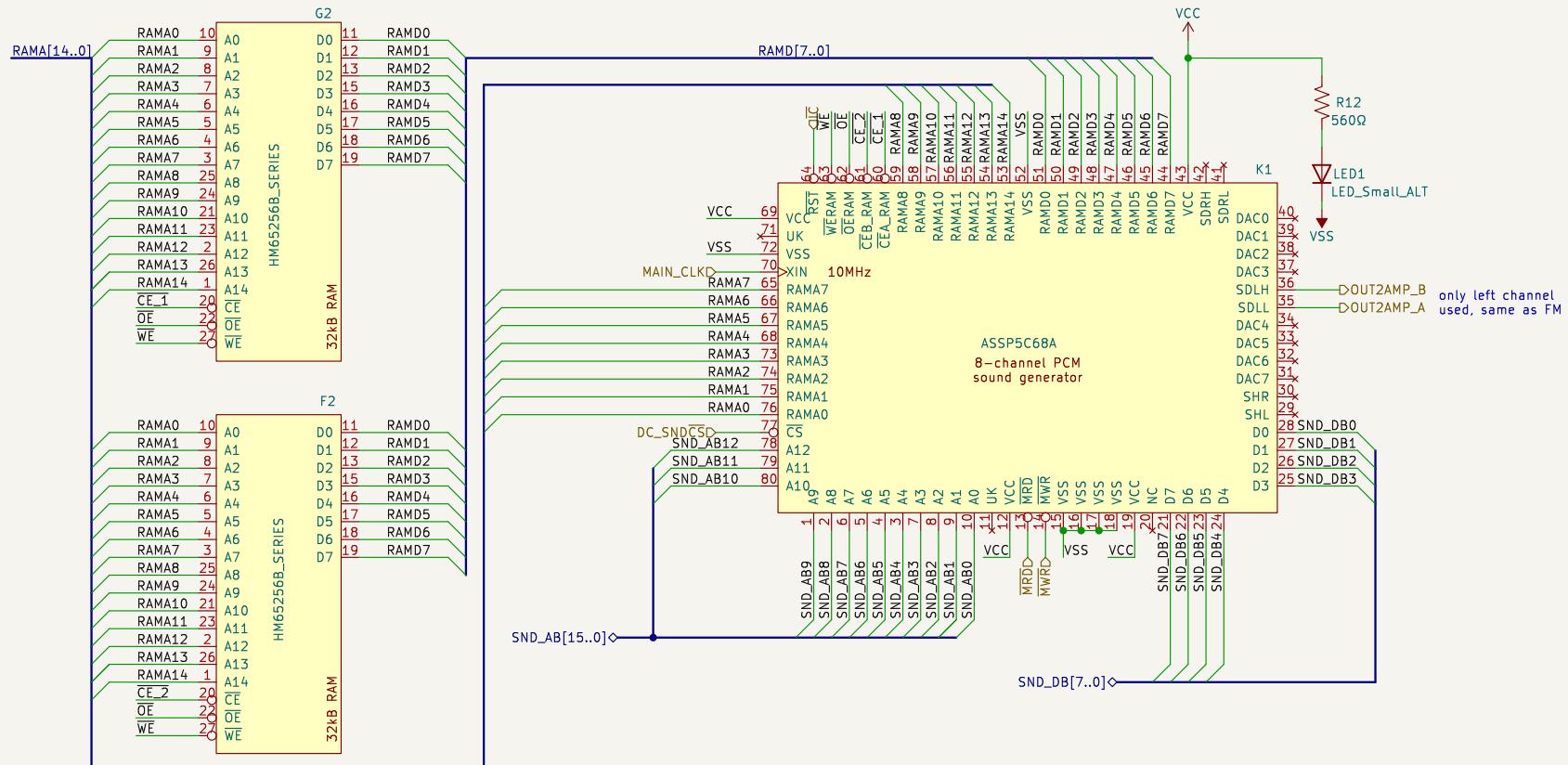


# MOTHER AND DAUGHTER PCB CONNECTORS









#### PCM sound generator

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JOTEGO

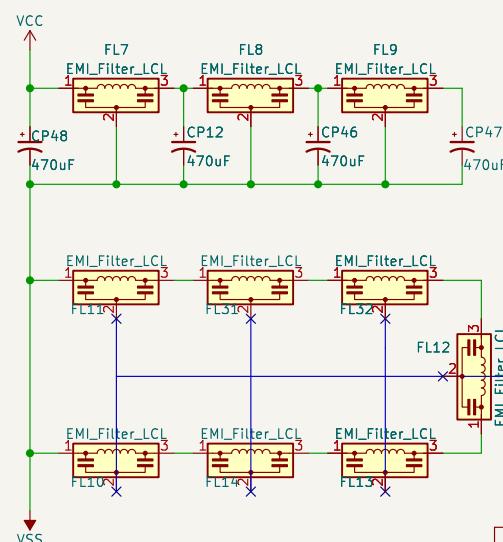
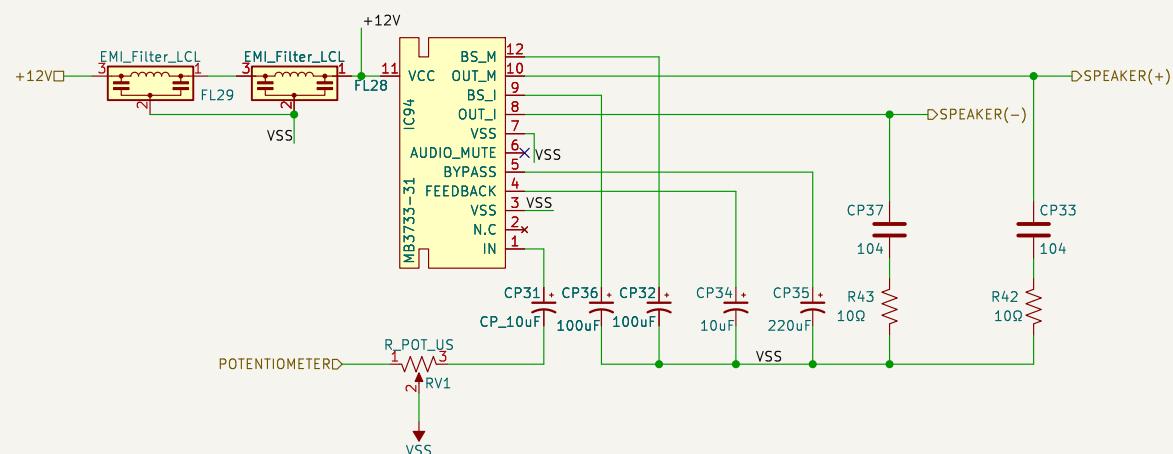
Sheet: /Sound/PCM/  
File: comm\_snd.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

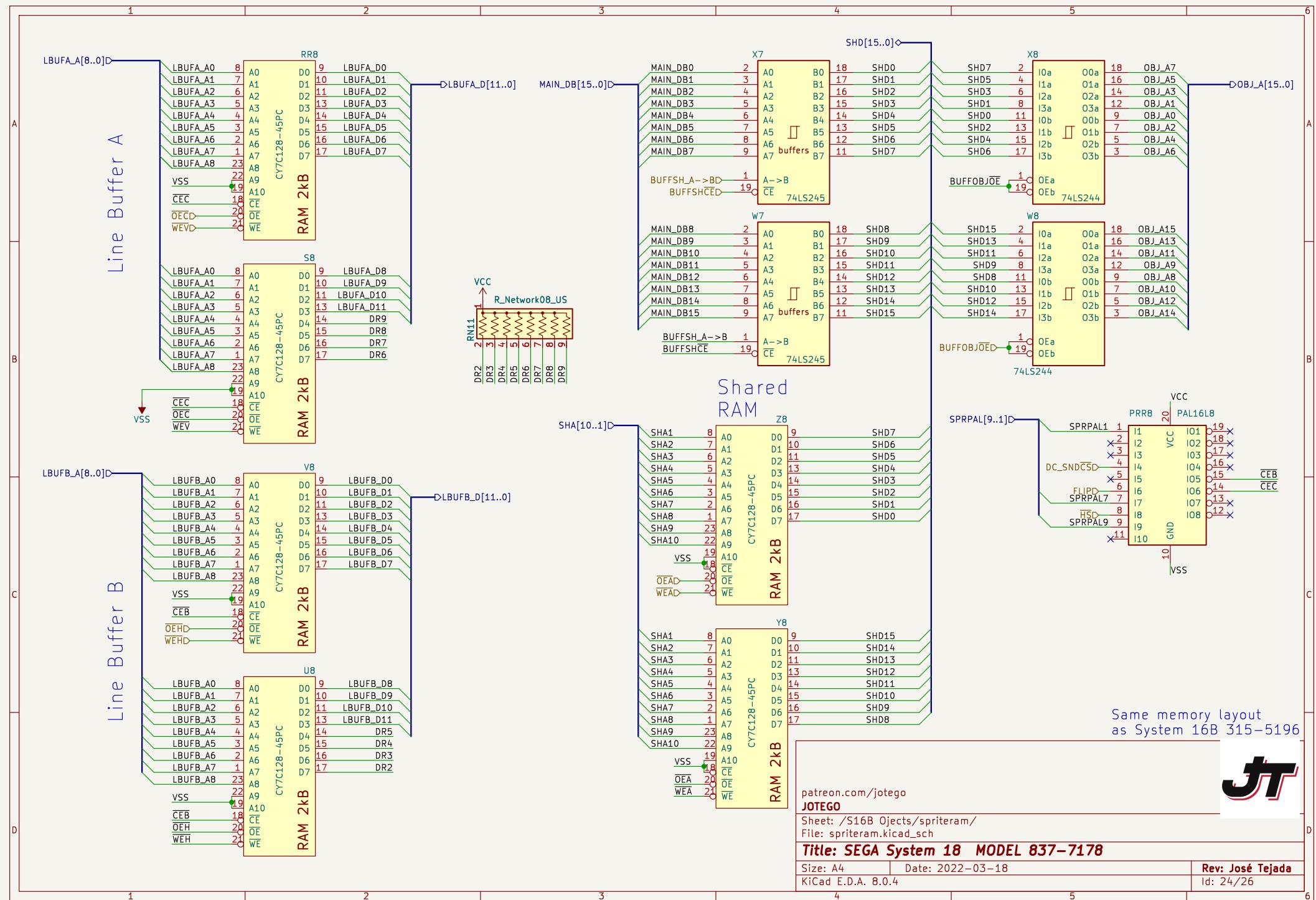
KiCad E.D.A. 8.0.4

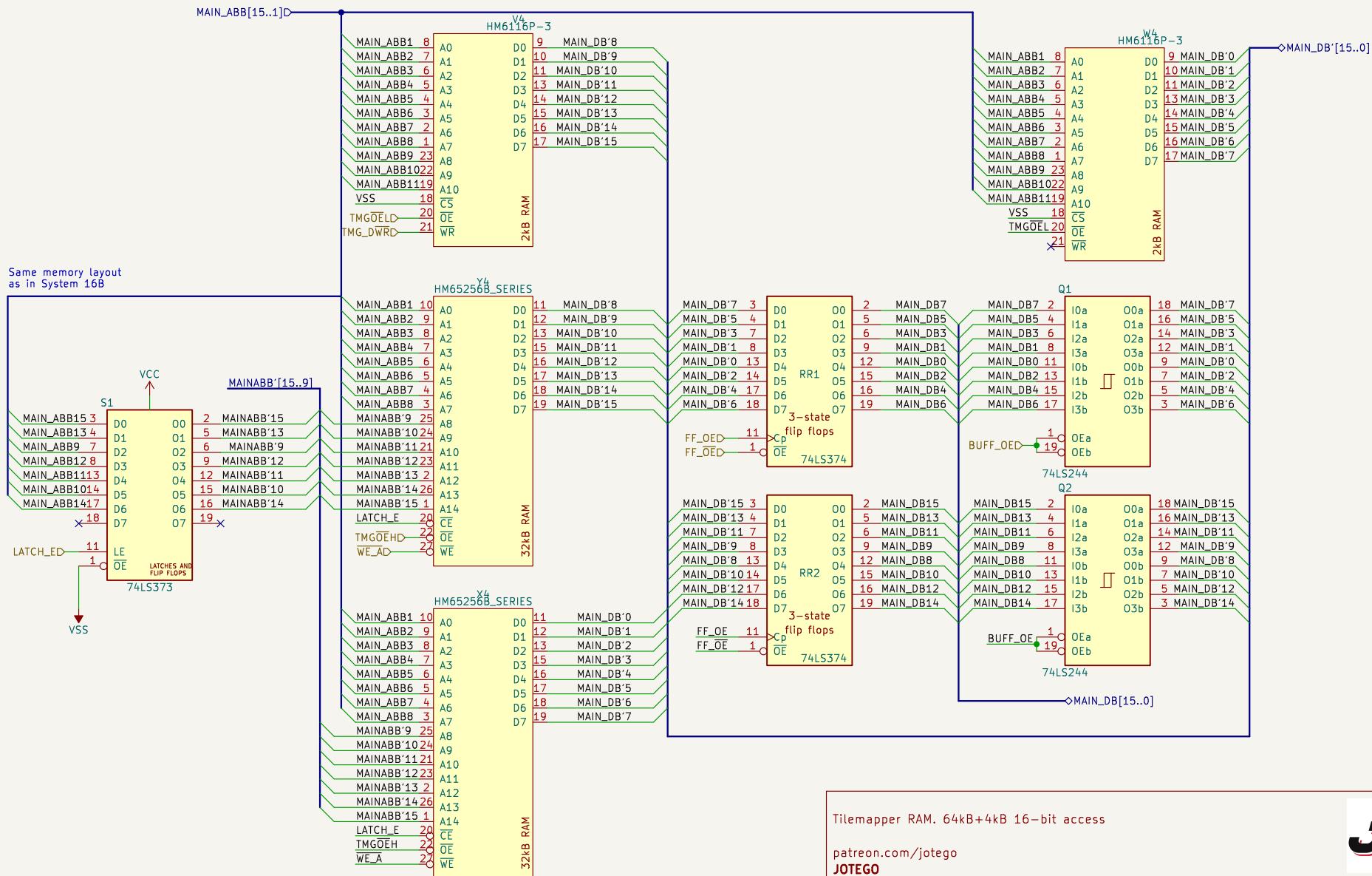




Leaded EMIFI  
Three-terminal Capacitor type  
DSS6NE52A222p  
2200pF 80/-20%  
100Vdc 6A -25 to +85°C

A kind of a track is observed between the two PCB layers, as if it were another layer, but there is no certainty about that. The track seems to be used to delimit or isolate the EMI (ElectroMagnetic Interference) filters. They may have wanted to isolate the noise between digital and analog devices.





Tilemapper RAM. 64kB+4kB 16-bit access

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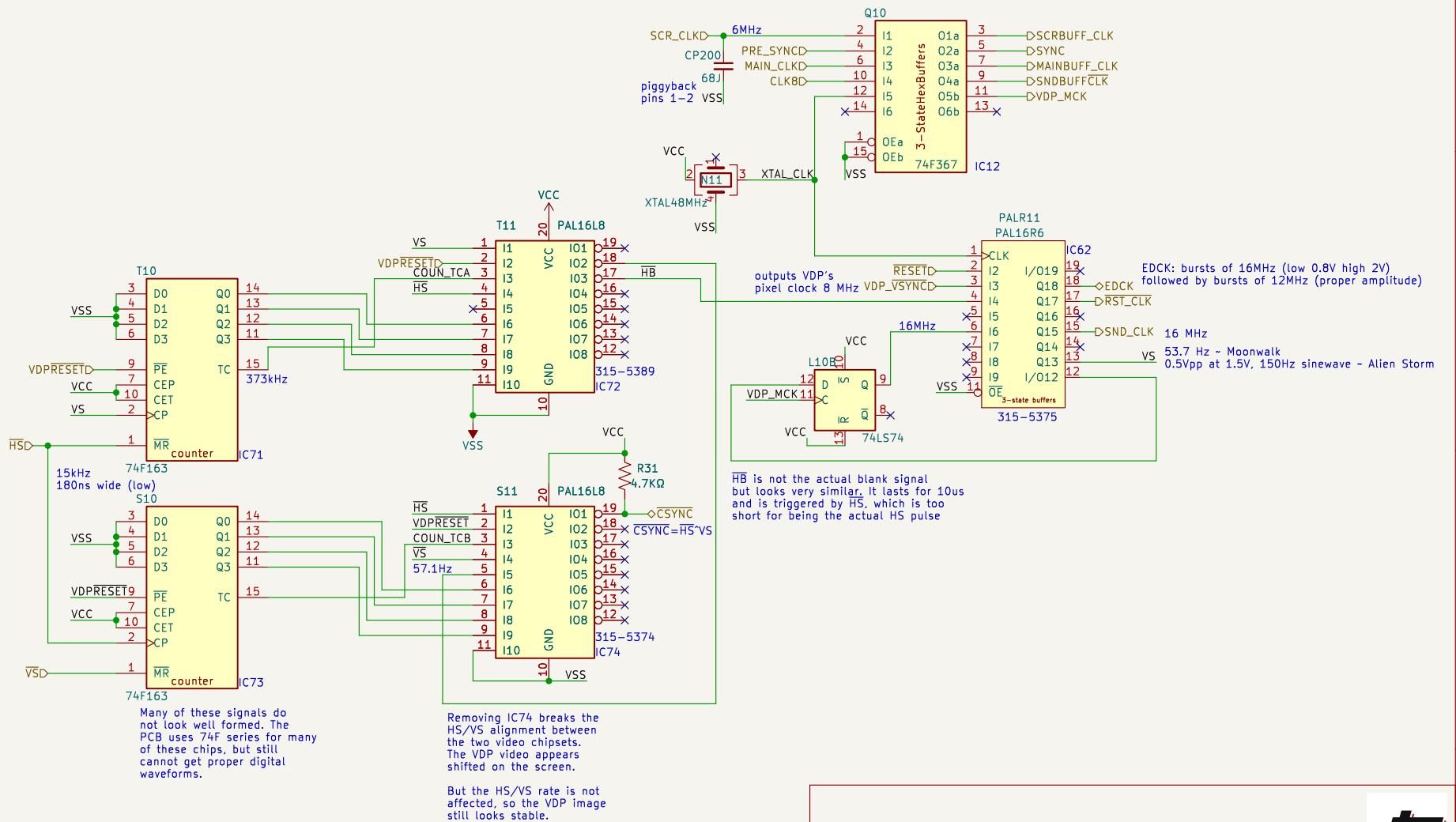
Sheet: /S16B\_Scroll/tilemapg\_ram/  
File: tilemapg\_ram.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.4

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VDP pixel clock and csync generation  
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Sheet: /Video Sync/  
File: buffers.kicad\_sch

Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18  
KiCad E.D.A. 8.0.4



Rev: José Tejada  
Id: 26/26