

1 2 3 4 5 6 7 8

A

B

C

D

E

F



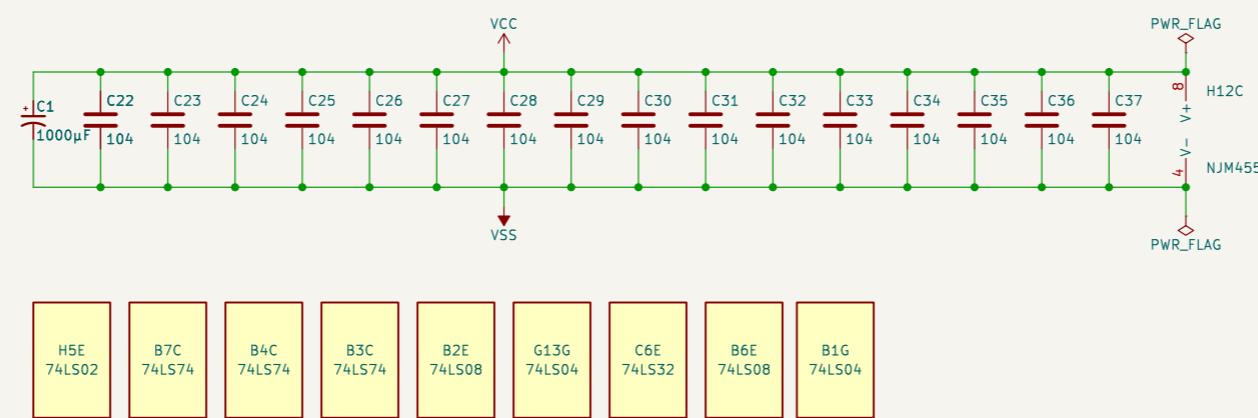
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File: capacitors.kicad_sch

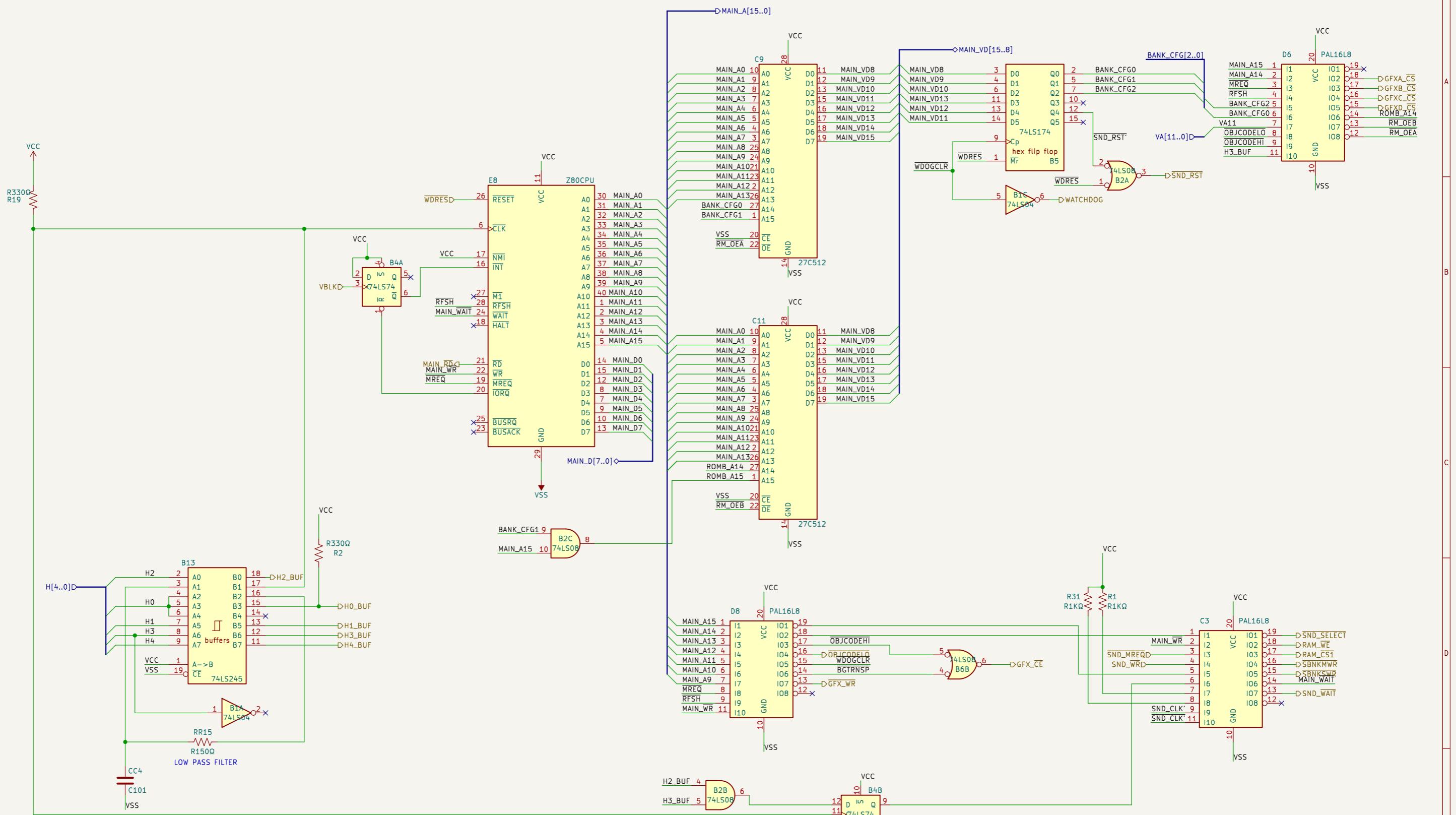
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Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1

Rev: José Tejada G. Id: 2/10



1 2 3 4 5 6 7 8



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Title: EXTERMINATION

Size: A3	Date: 29/08/2022	Rev: José Tejada G.
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1		Id: 3/10



A

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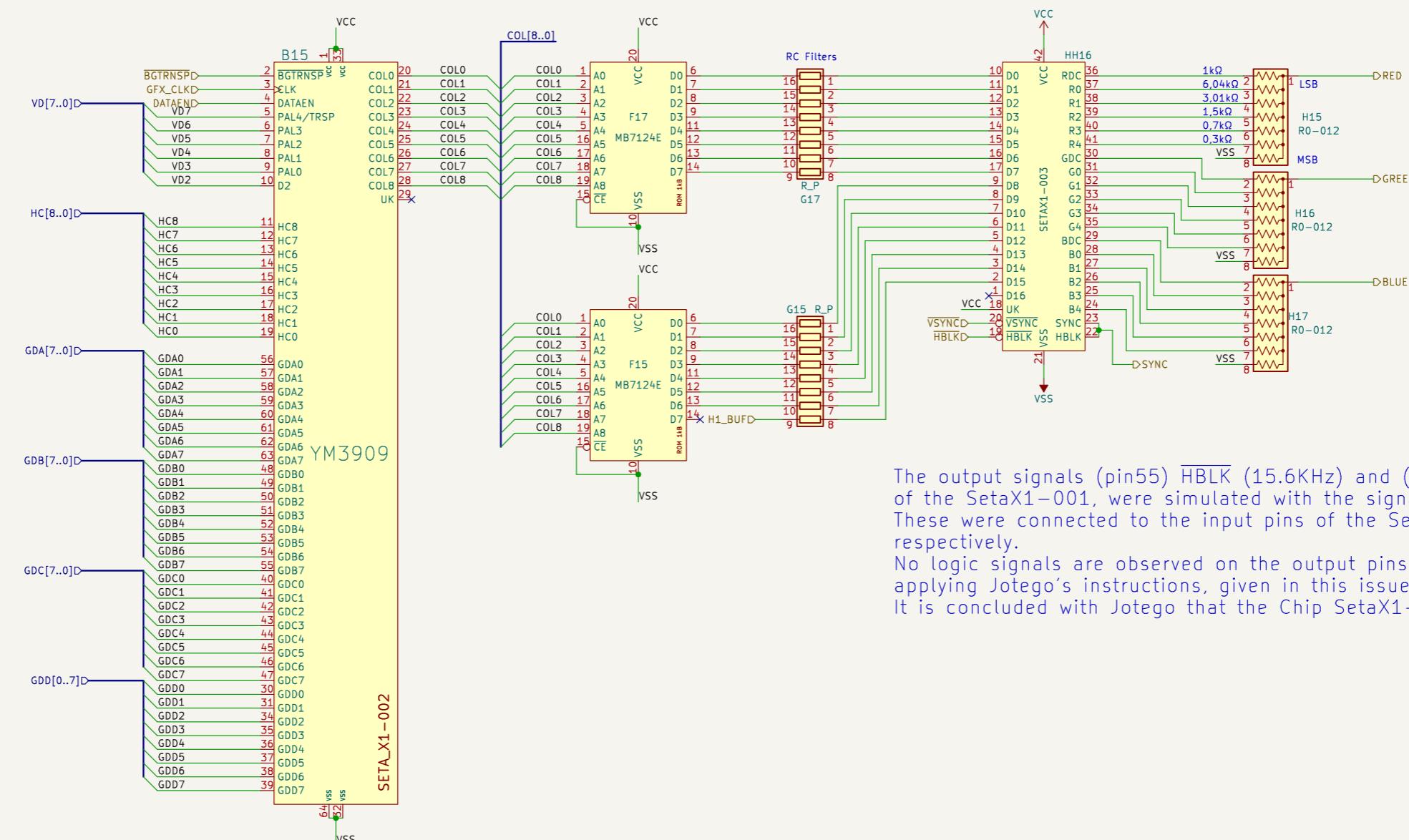
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The output signals (pin55) HBLK (15.6KHz) and (pin56) VSYNC (59.2 Hz) of the SetaX1-001, were simulated with the signal generator and an inverter. These were connected to the input pins of the SetaX01-003 (pin 19 – 20), respectively.
 No logic signals are observed on the output pins (22–23) HBLK – SYNC, applying Jotego's instructions, given in this issue.
 It is concluded with Jotego that the Chip SetaX1-001 is broken.

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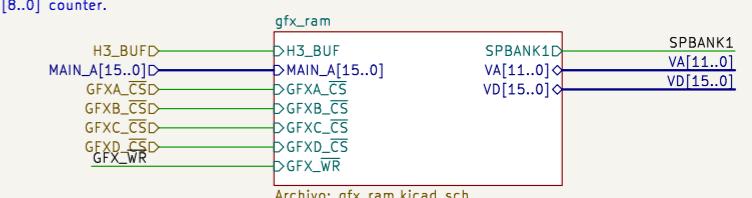
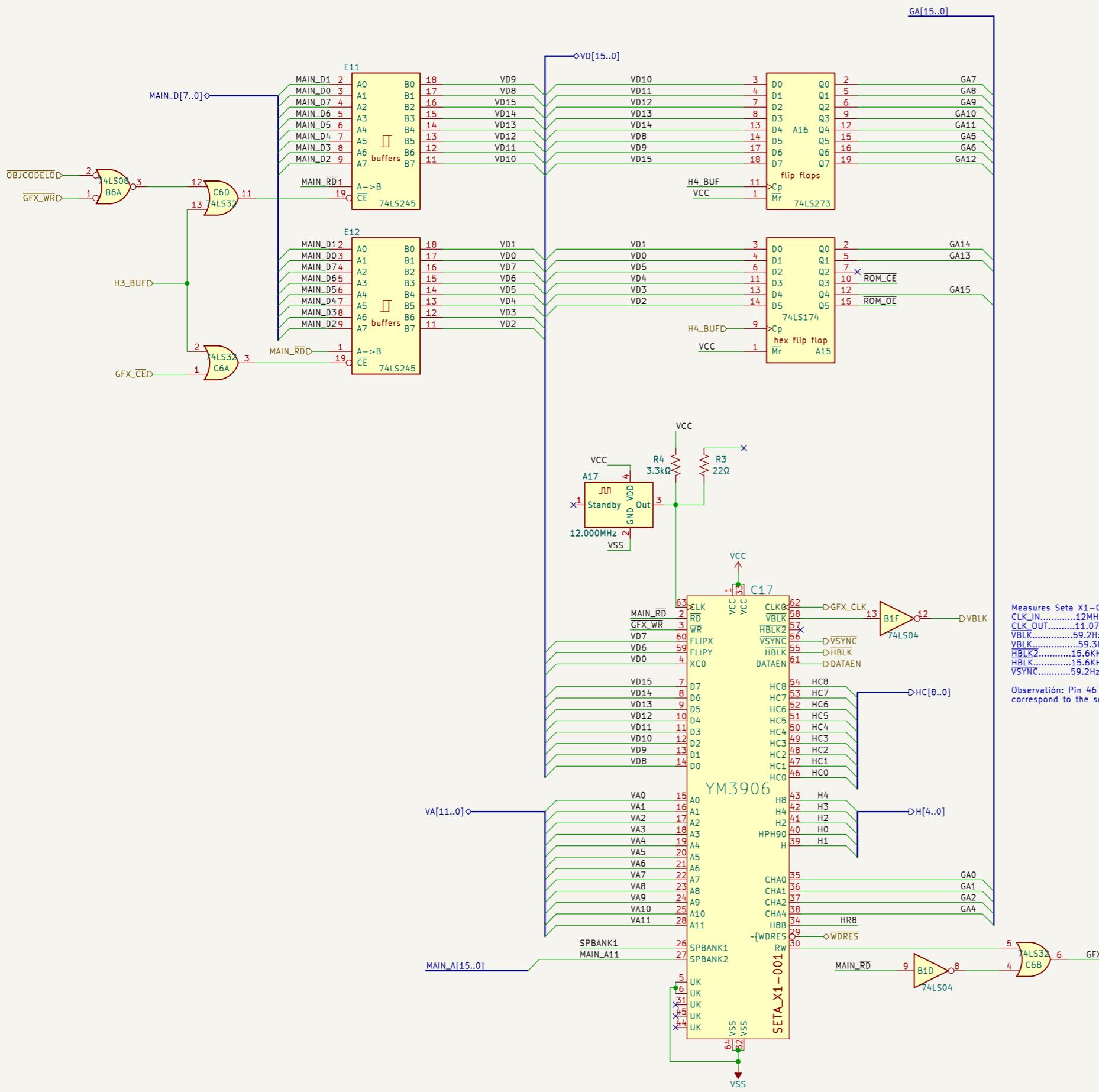
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Size: A3 | Date: 29/08/2022
KiCad E.D.A. kicad-cl 7.0.10-7.0.10-ubuntu20.04.1



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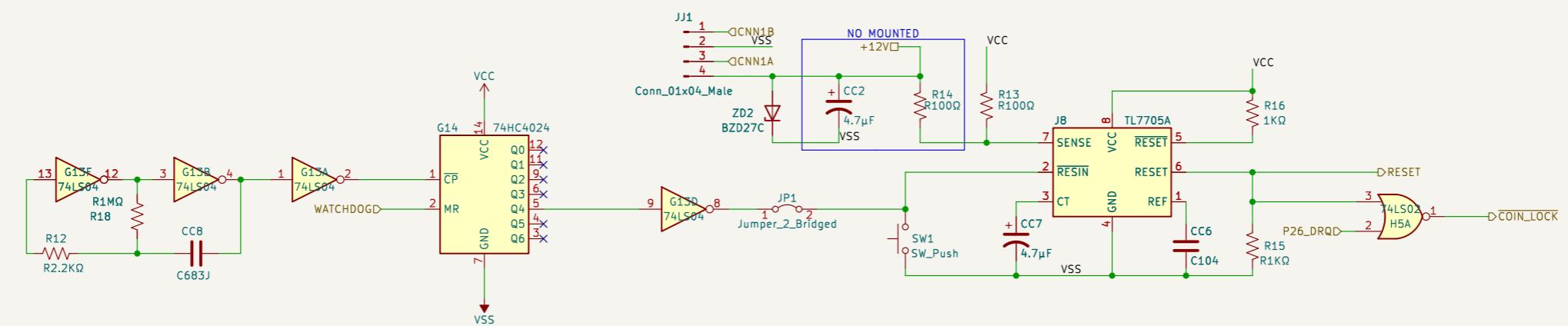
JOTEGO
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Title: EXTERMINATION

Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1





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Sheet: /watchdog/
File: watchdog.kicad_sch

Title: EXTERMINATION

Size: A3 Date: 29/08/2022
KiCad E.D.A. kicad-cli 7.0.10-7.0.10~ubuntu20.04.1

The JT logo consists of the letters 'JT' in a bold, black, sans-serif font. A red 'J' is positioned to the left of a black 'T'. The 'T' has a red stroke along its right edge and a red outline along its bottom edge.

