

A

A

B

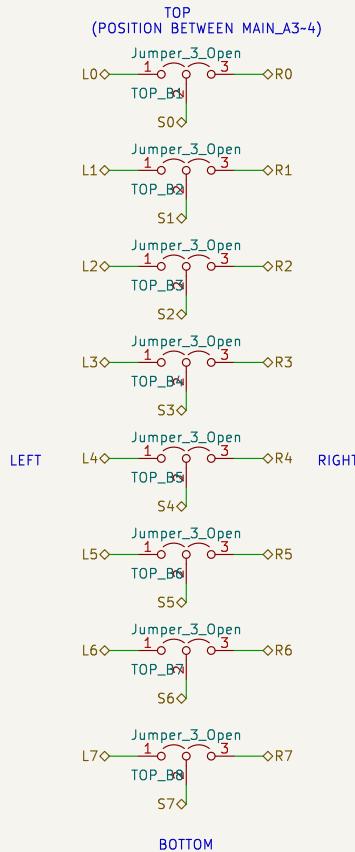
B

C

C

D

D



[patreon.com/jotego](https://patreon.com/jotego)

JOTEGO

Sheet: /ROM board/scrjumpers/  
File: romjp.kicad\_sch

# Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.4

Rev: José Tejada

Id: 3/26

A

B

C

D

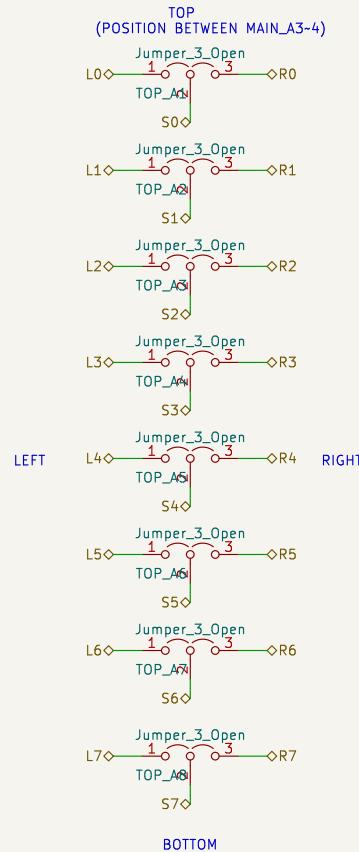
2

---

3

5.0

1



[patreon.com/jotego](https://patreon.com/jotego)  
**JOTEGO**

Sheet: /ROM board/sndjumpers/  
File: romjp.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18  
KiCad EDA 8.0.4



Rev: José Tejada  
Id: 4/26

A

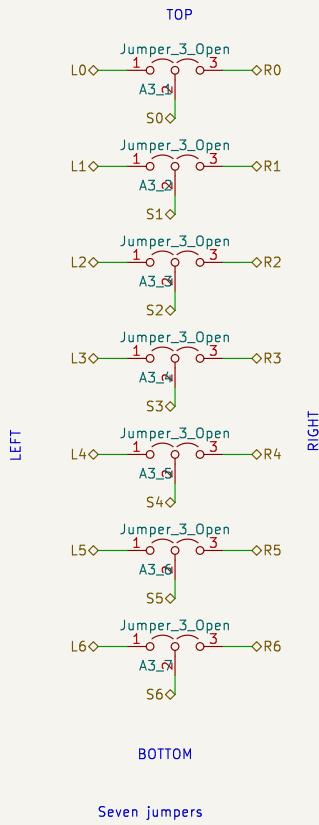
A

B

B

C

C



[patreon.com/jotego](https://patreon.com/jotego)

JOTEGO

Sheet: /ROM board/objjumpers/  
File: romjp7.kicad\_sch

# Title: SEGA System 18 MODEL 837-7178

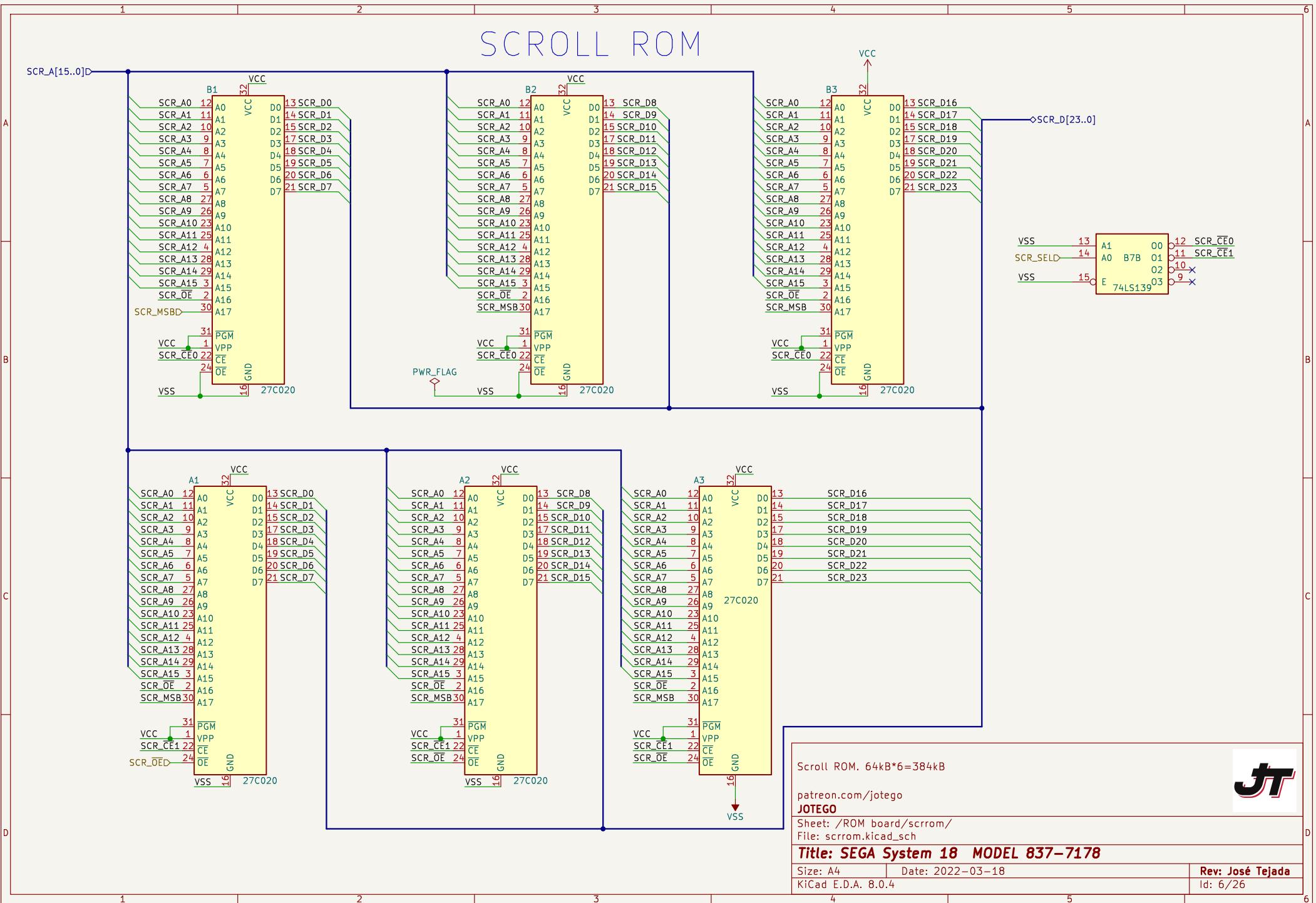
Size: A4 Date: 2022-03-18

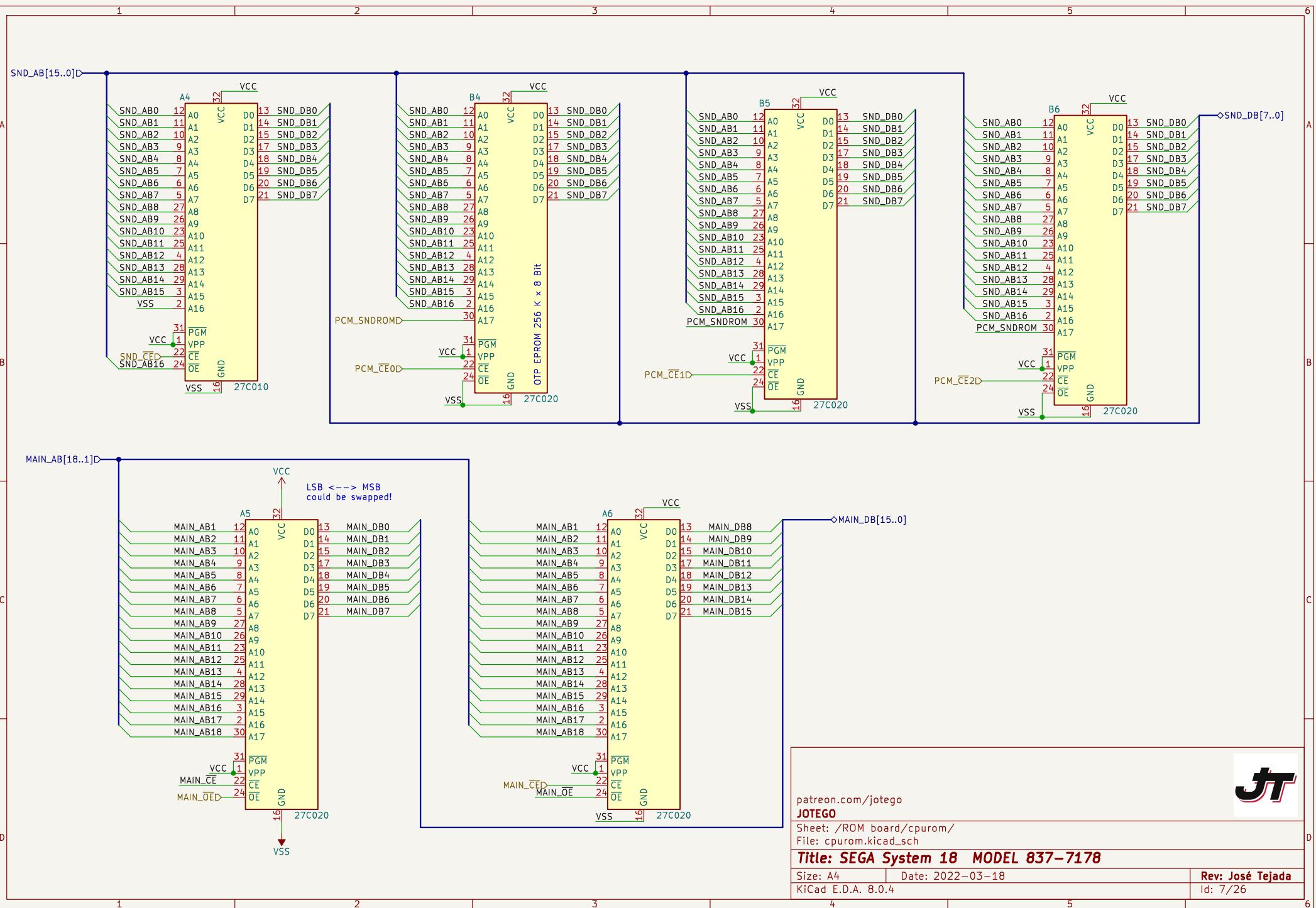
KiCad E.D.A. 8.0.4

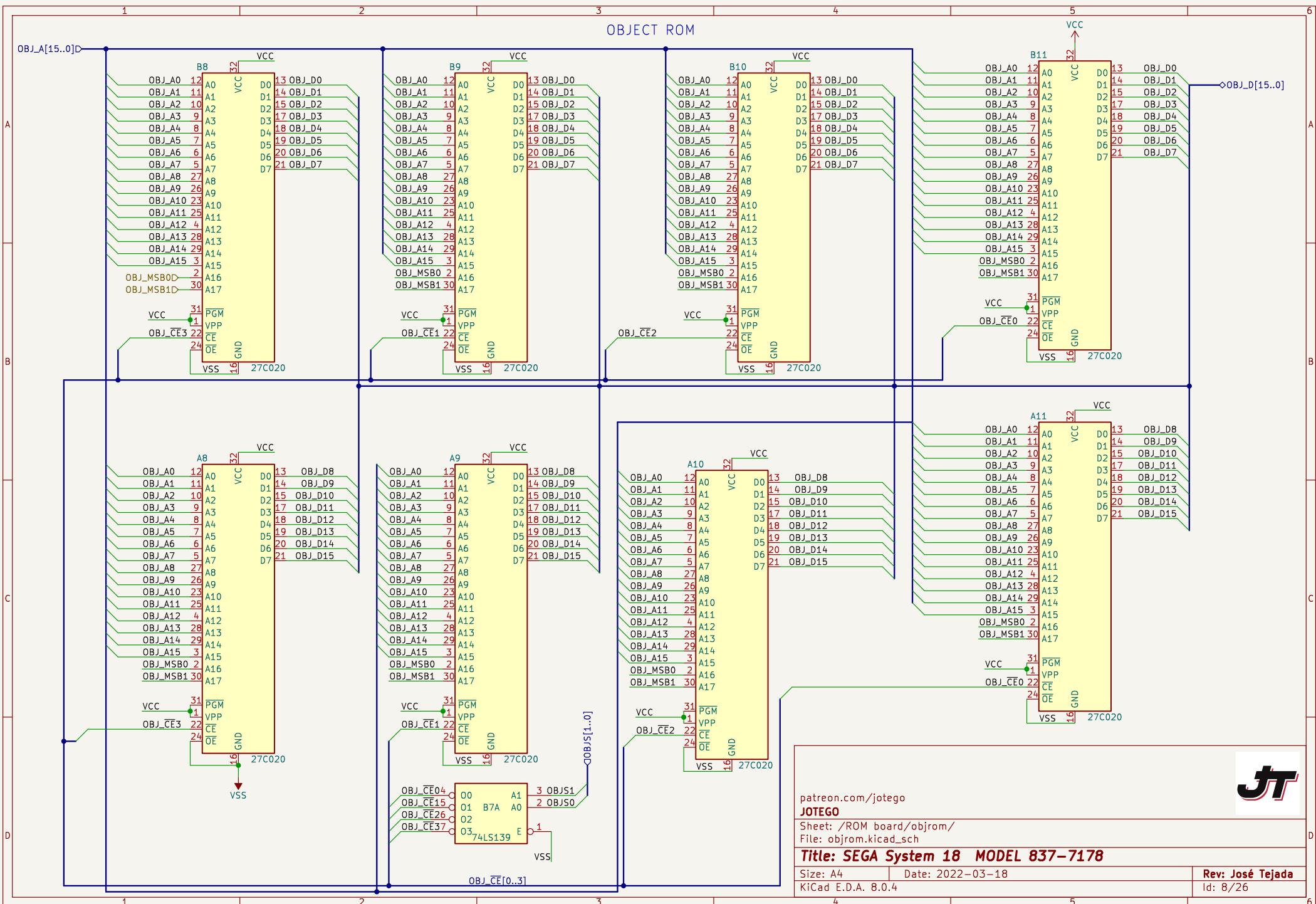
Rev: José Tejada

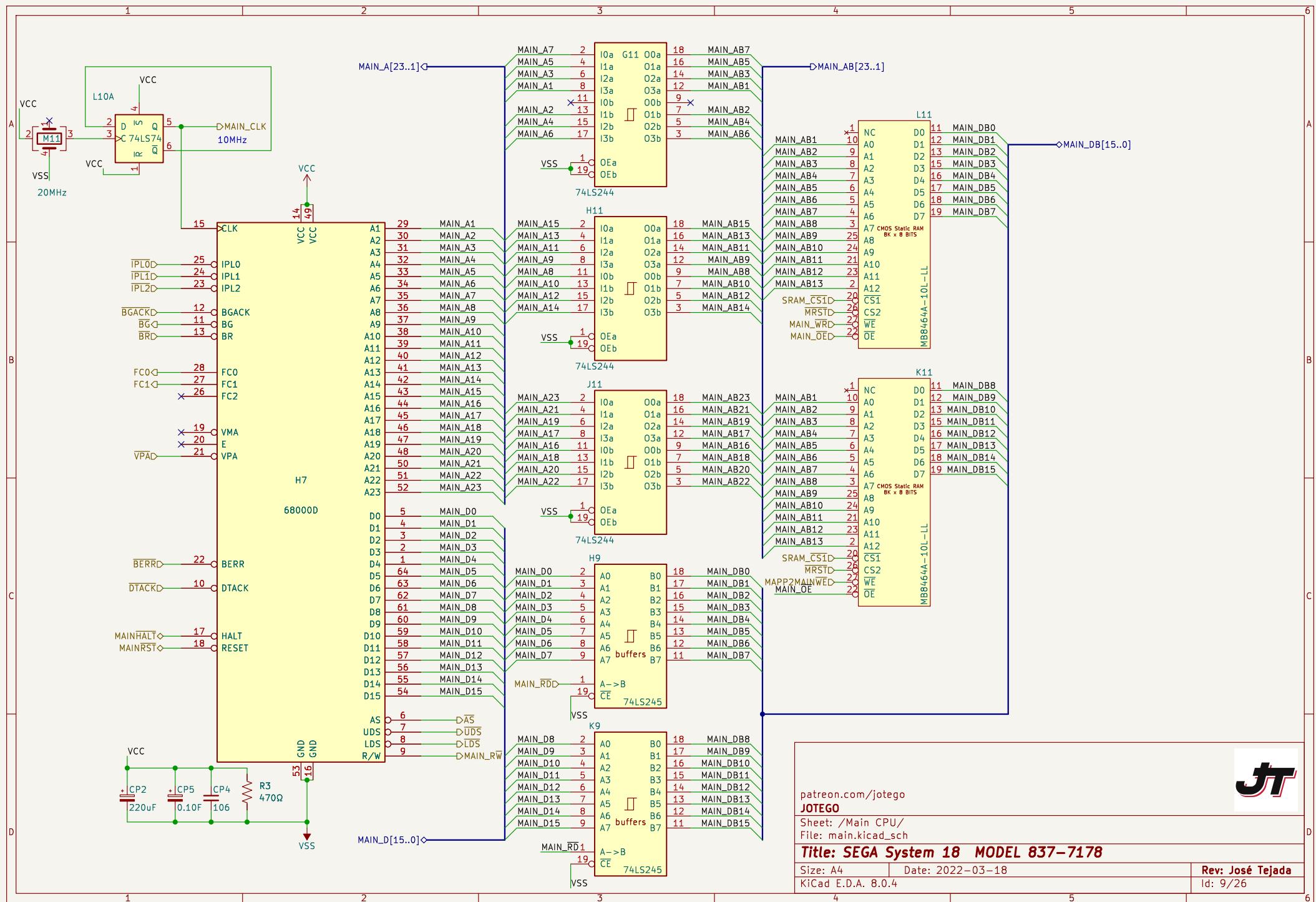
Id: 5/26

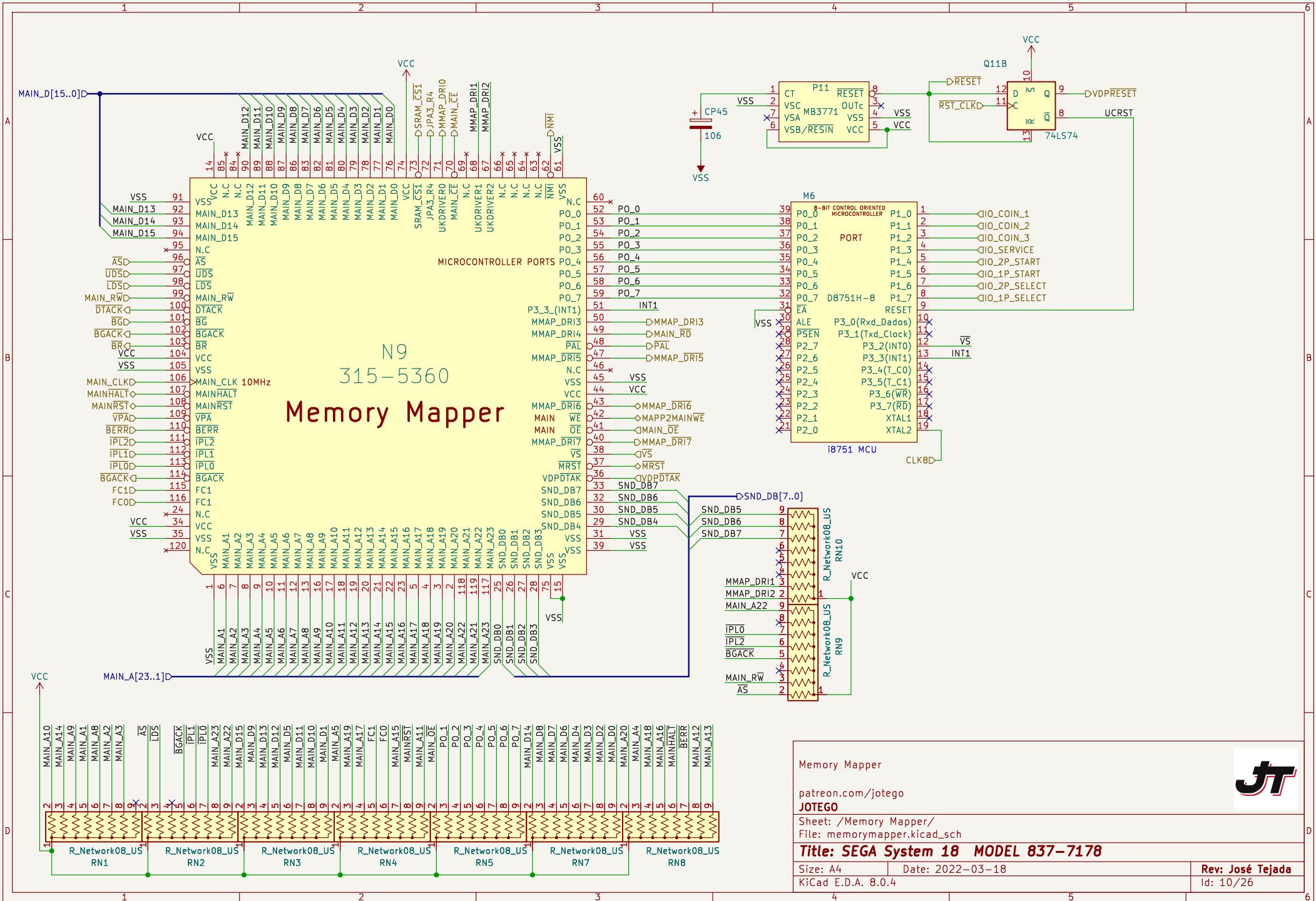
# SCROLL ROM

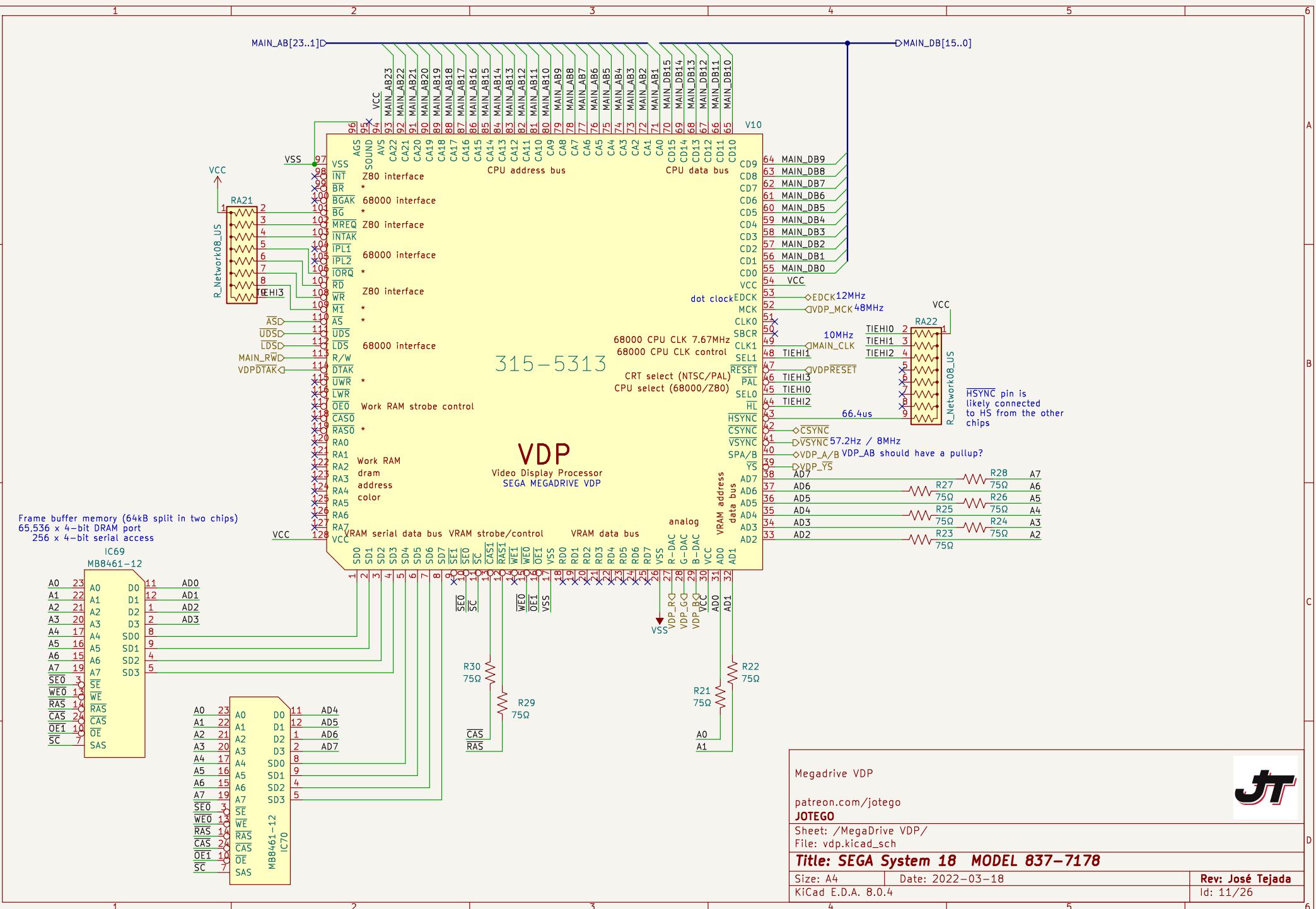


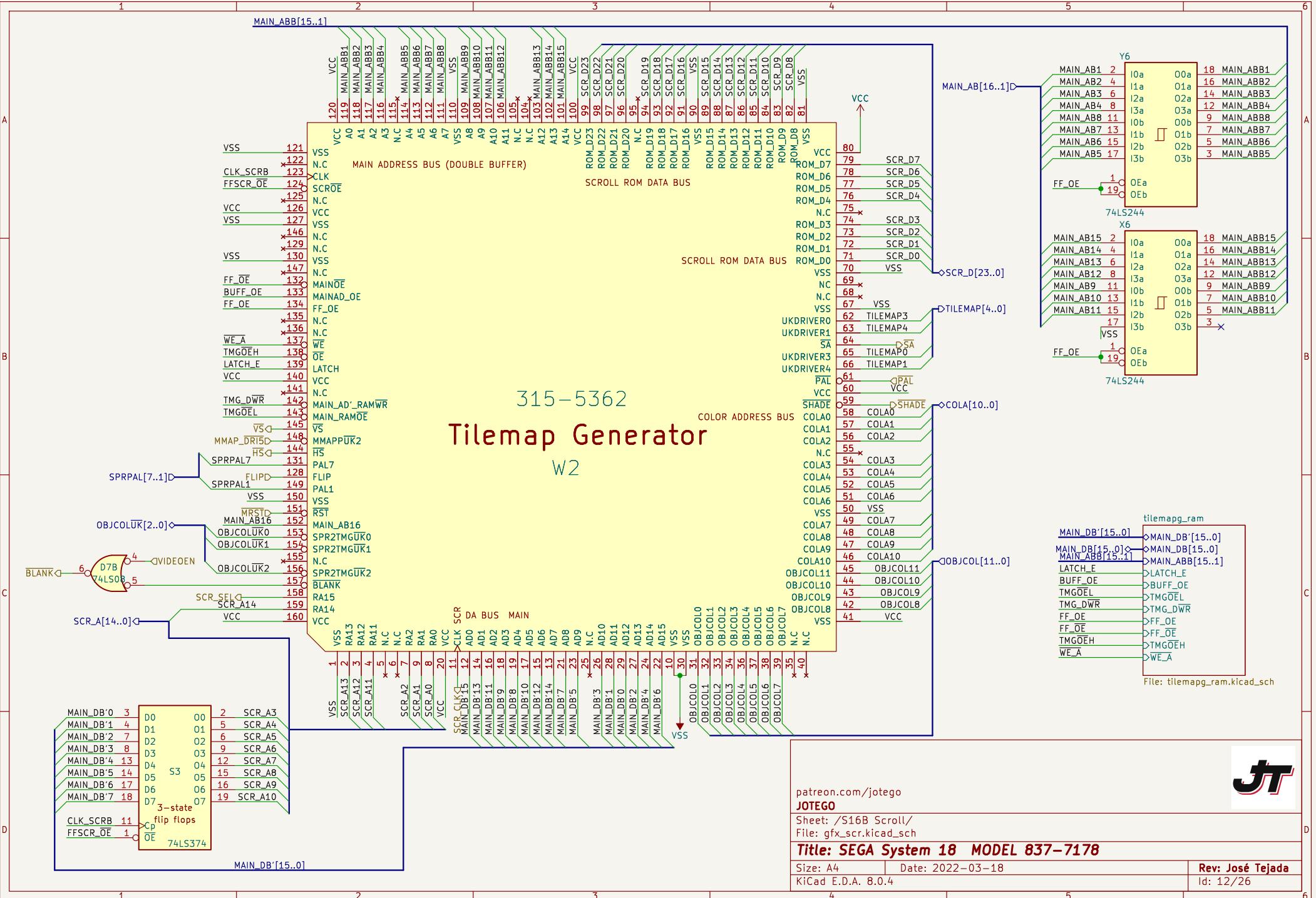


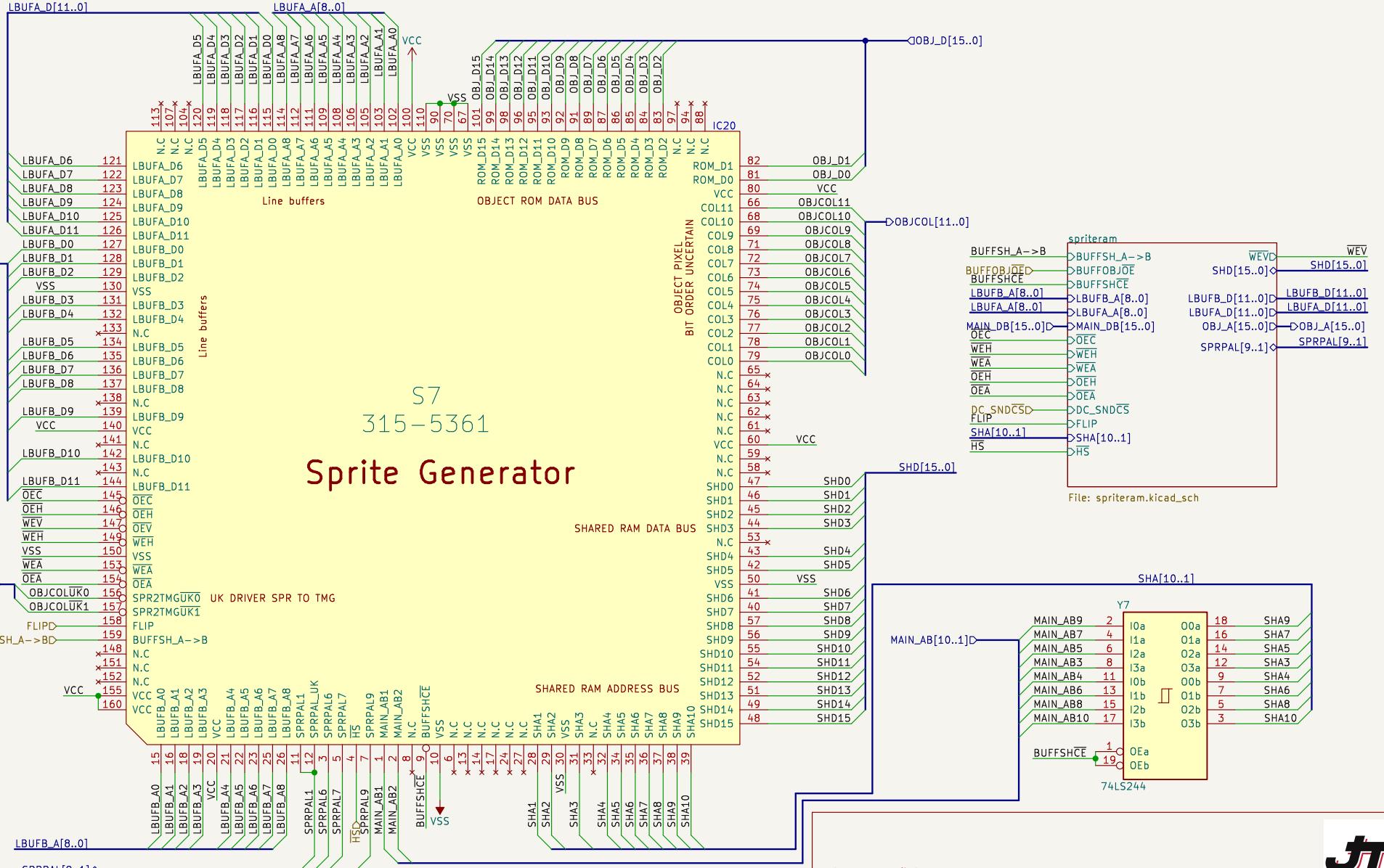


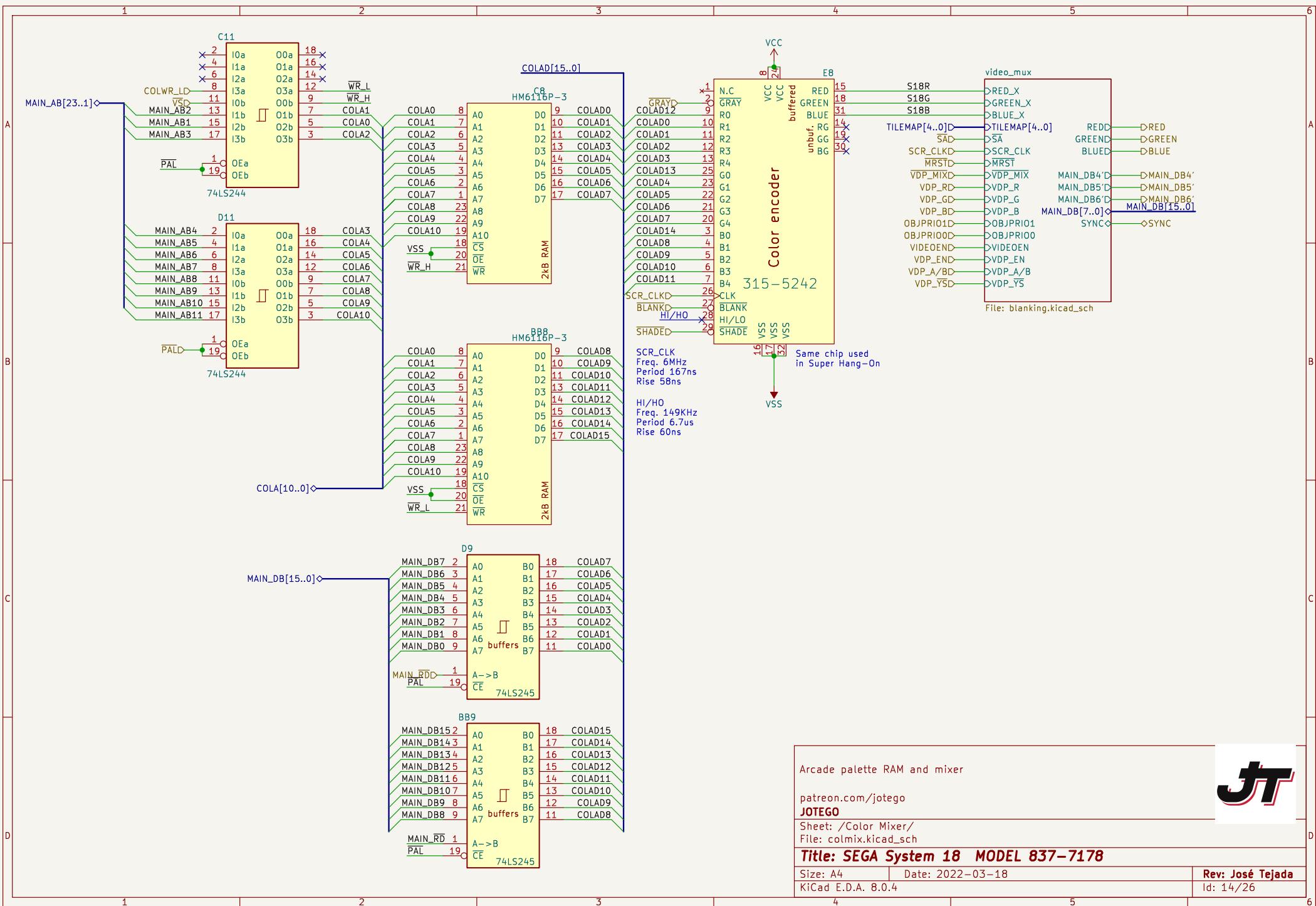


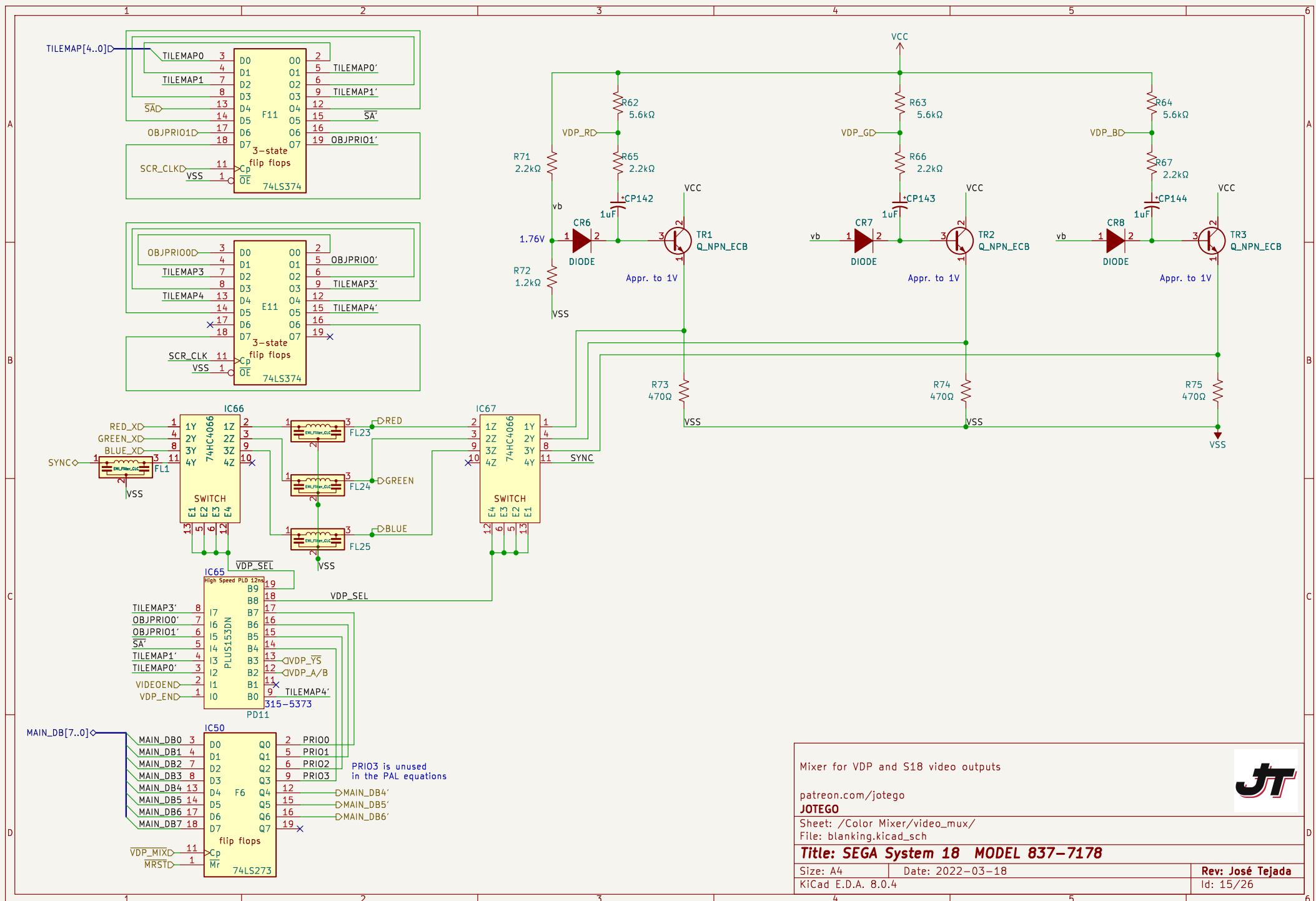












Mixer for VDP and S18 video outputs

[patreon.com/jotego](https://patreon.com/jotego)

JOTEGO

Sheet: /Color Mixer/video\_mux/  
File: blanking.kicad\_sch

Title: SEGA System 18 MODEL 837-7178

Size: A4 Date: 2022-03-18

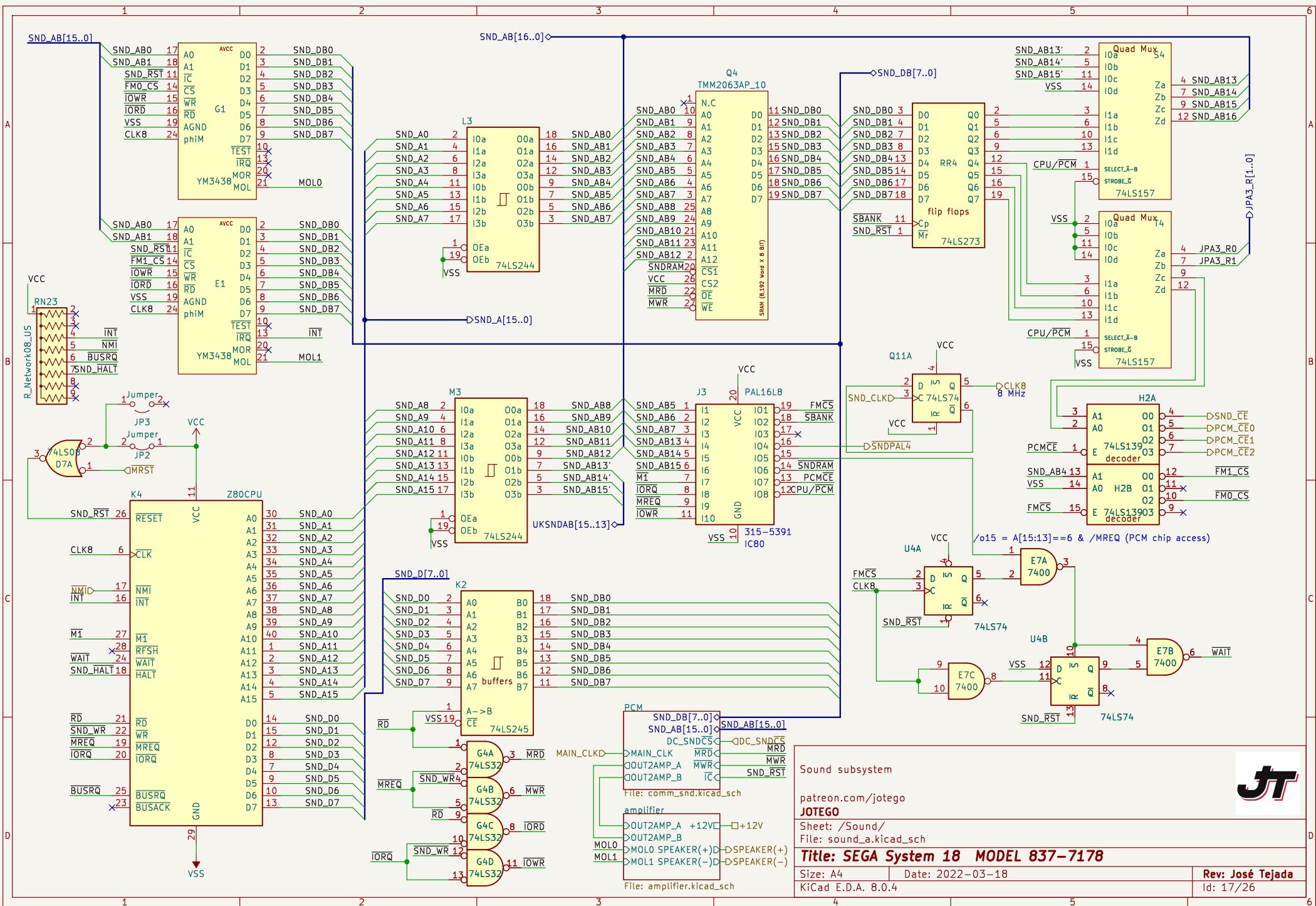
KiCad E.D.A. 8.0.4

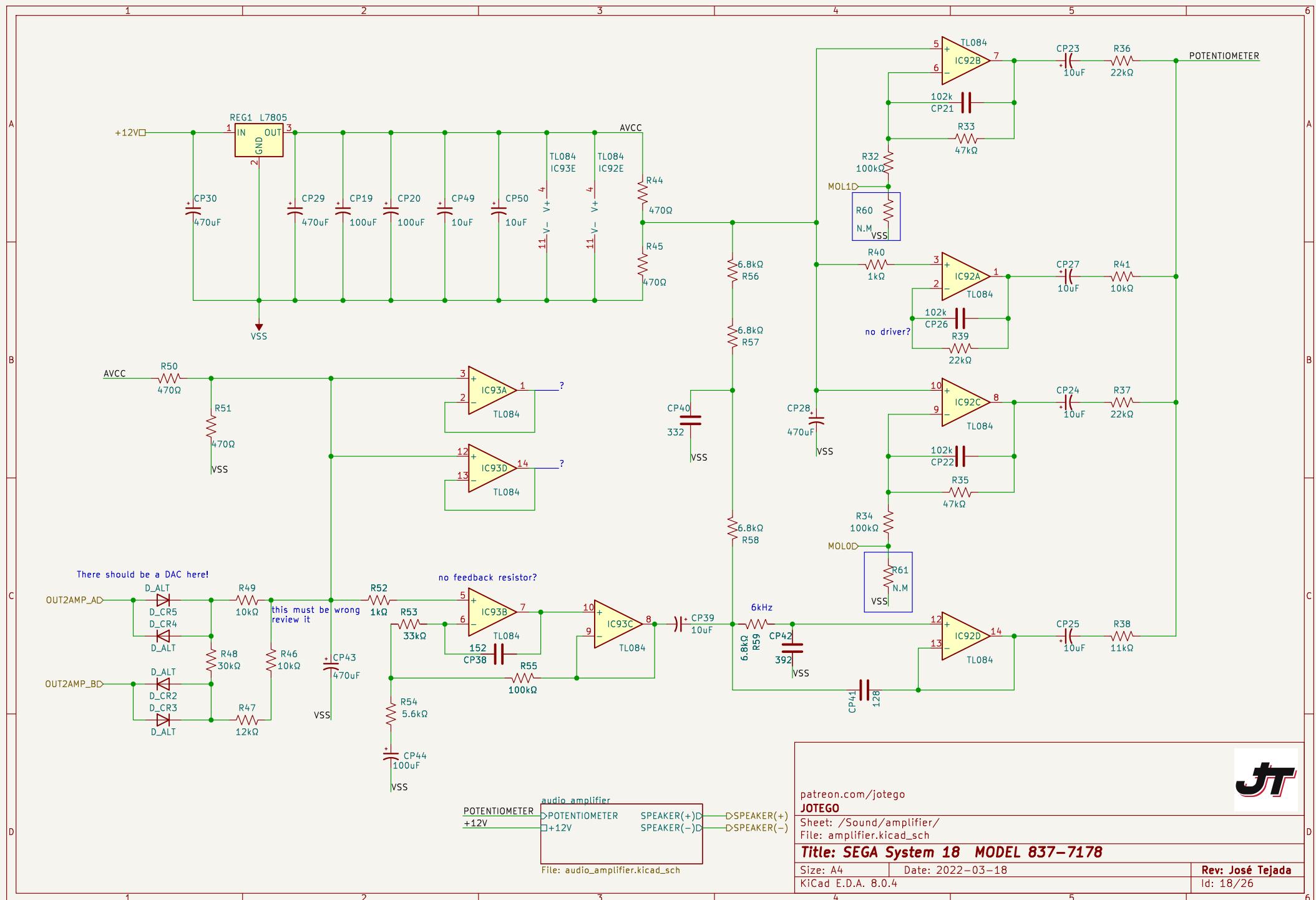


Rev: José Tejada

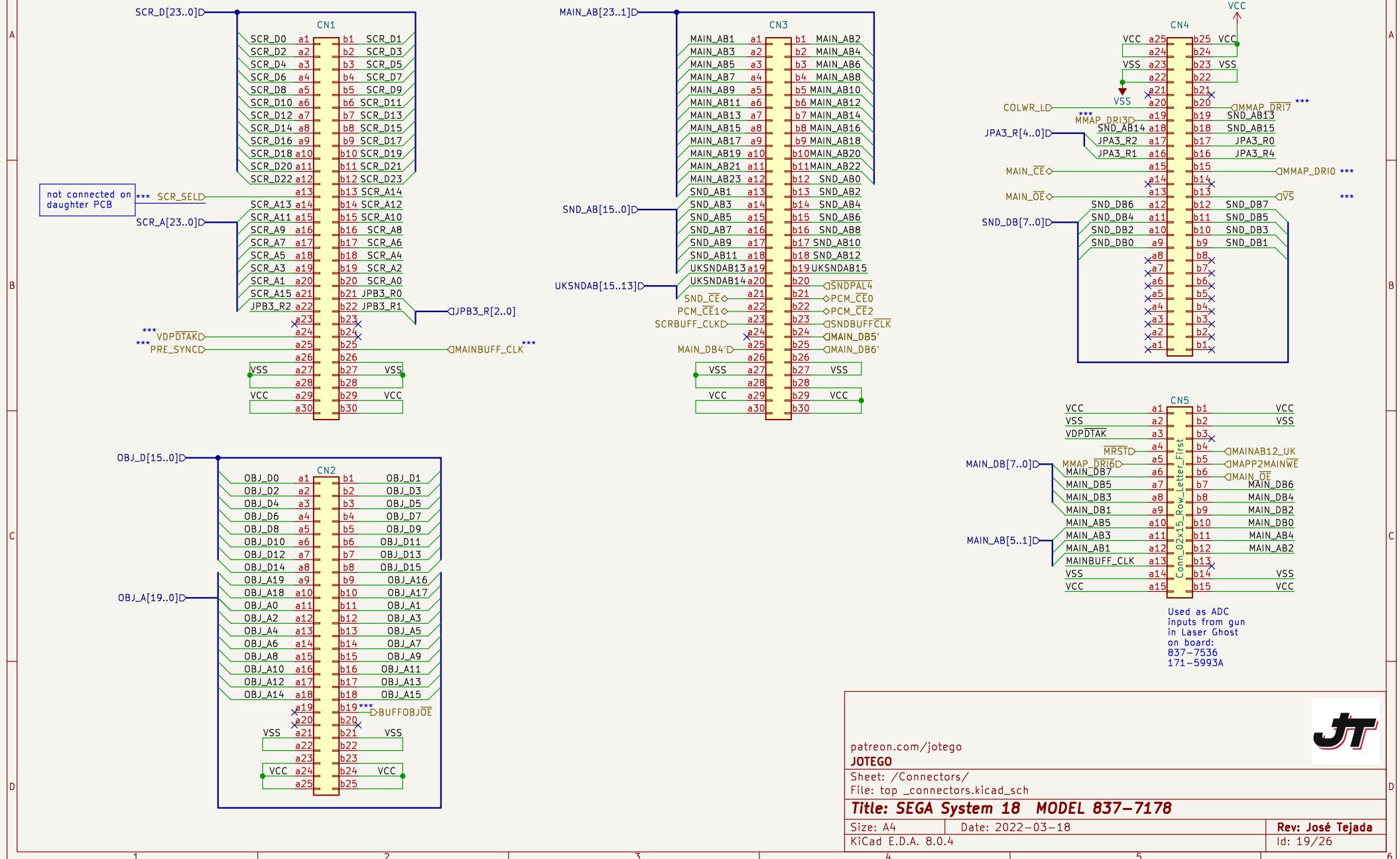
Id: 15/26

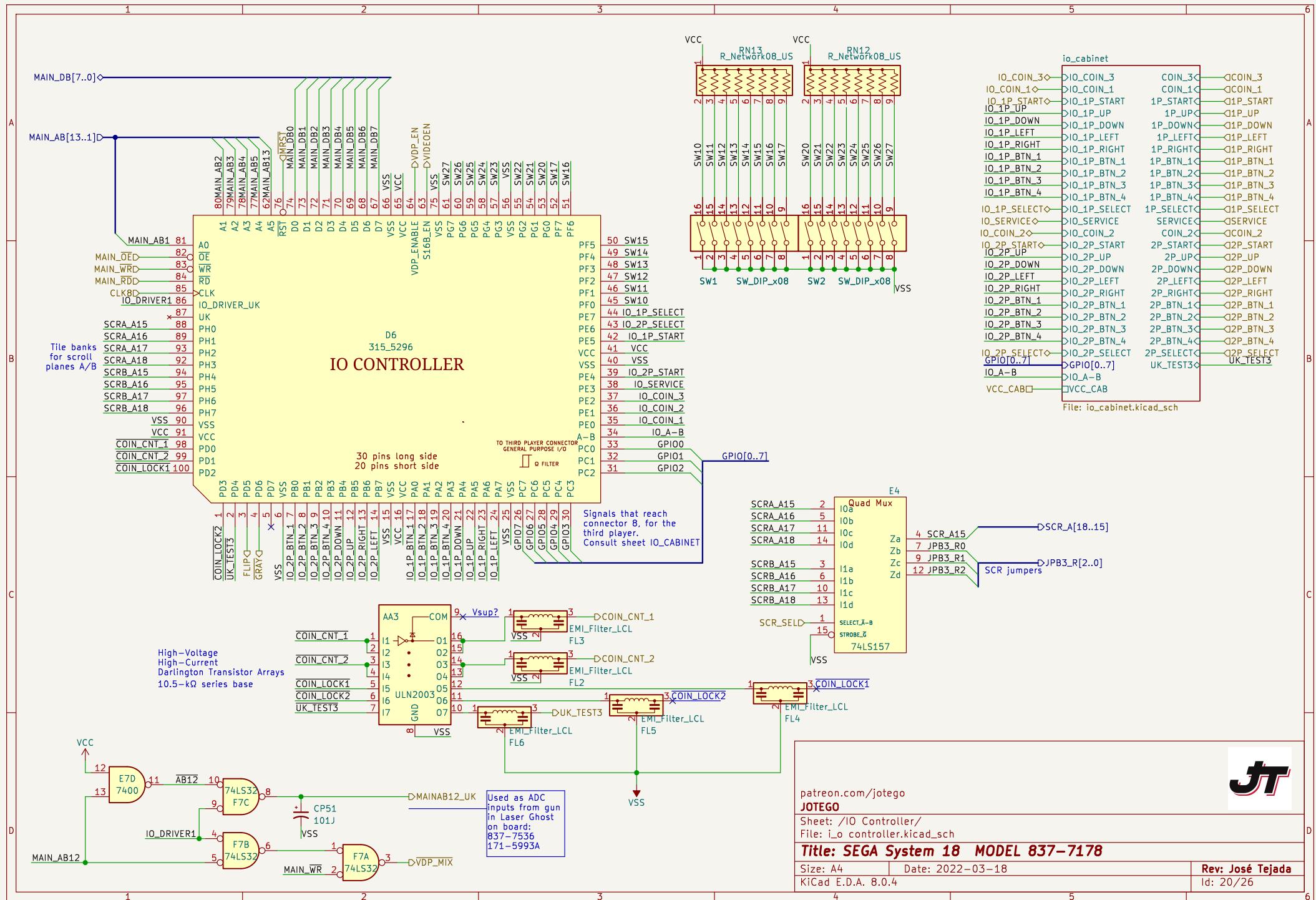


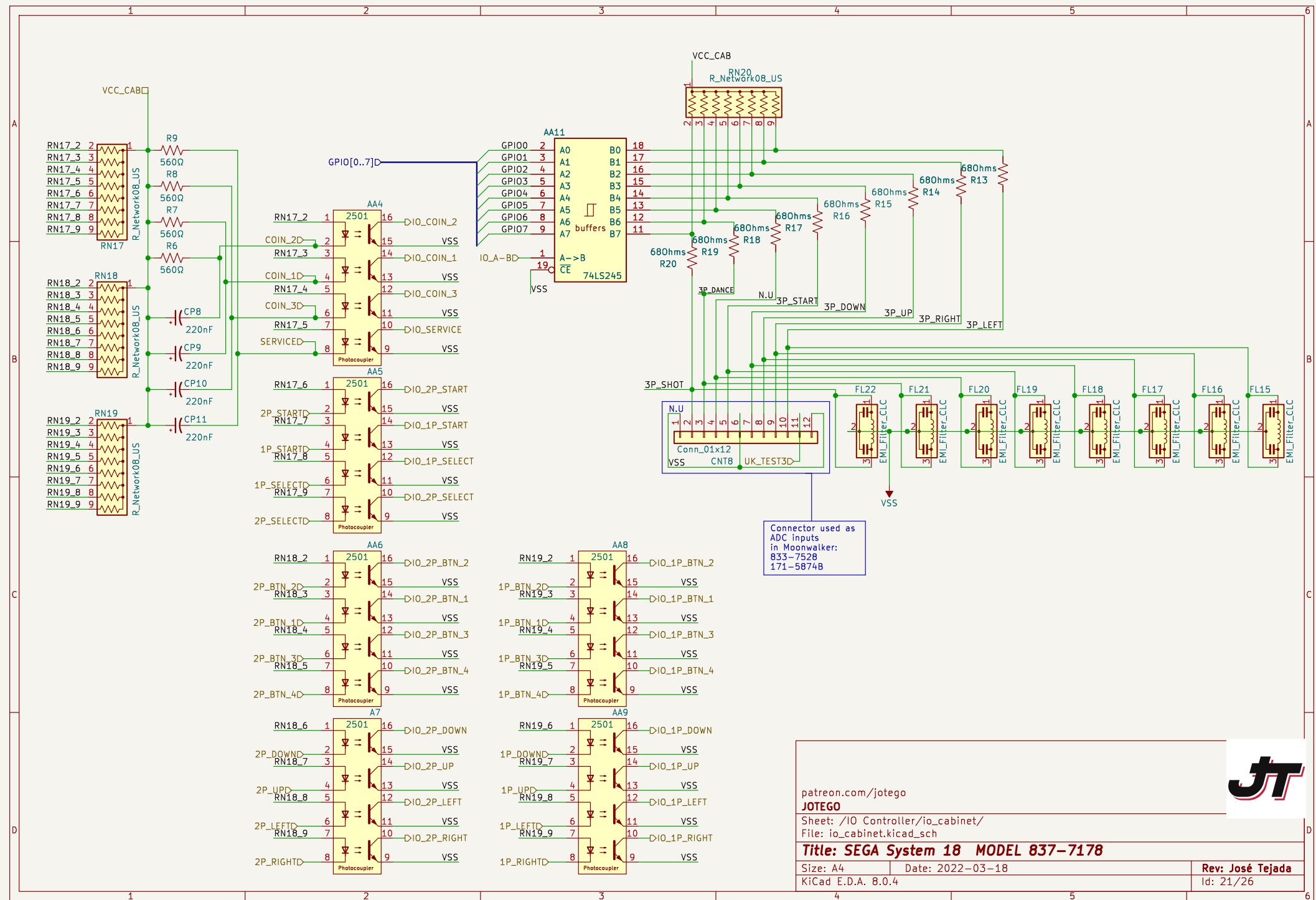


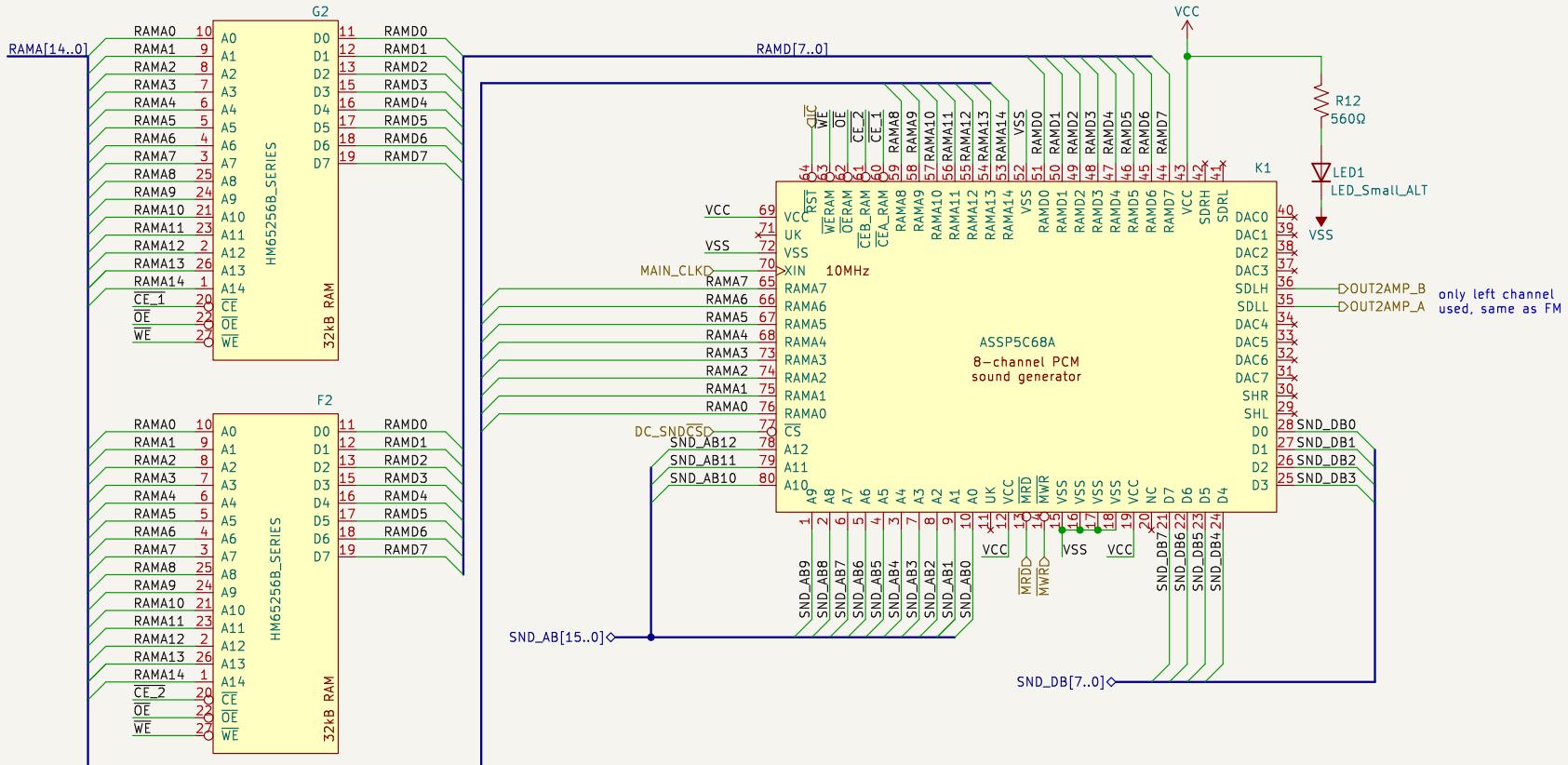


# MOTHER AND DAUGHTER PCB CONNECTORS







**PCM sound generator**[patreon.com/jotego](http://patreon.com/jotego)**JOTEGO**

Sheet: /Sound/PCM/

File: comm\_snd.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

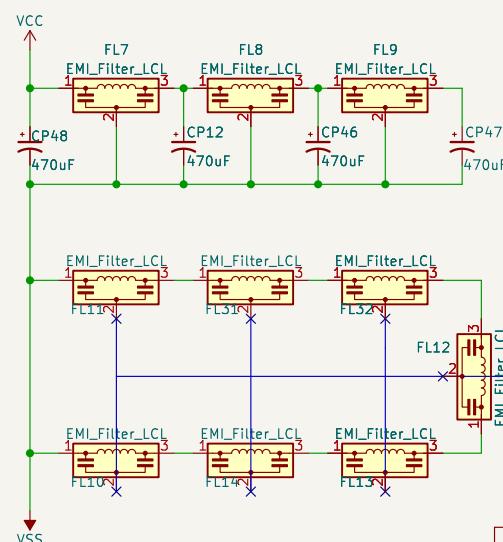
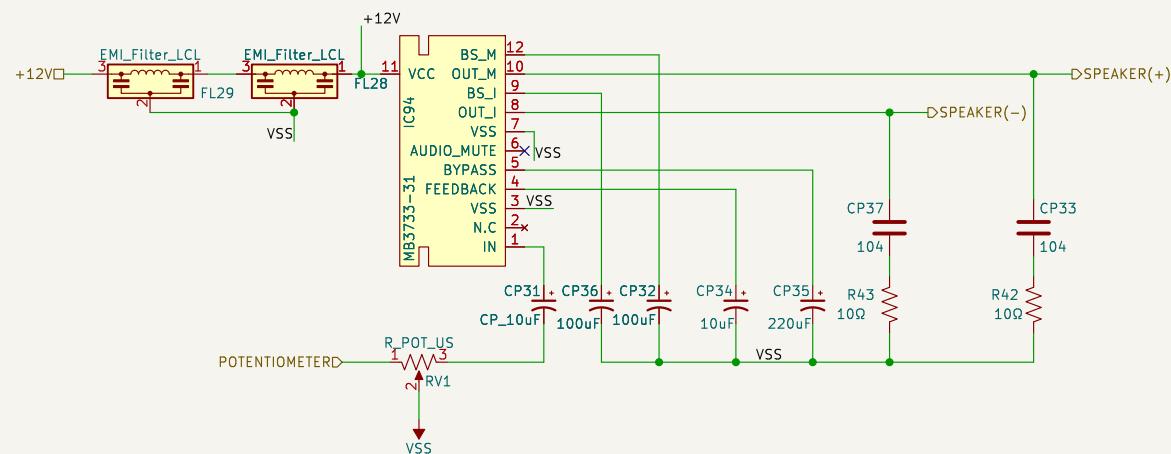
Size: A4 Date: 2022-03-18

KiCad E.D.A. 8.0.4



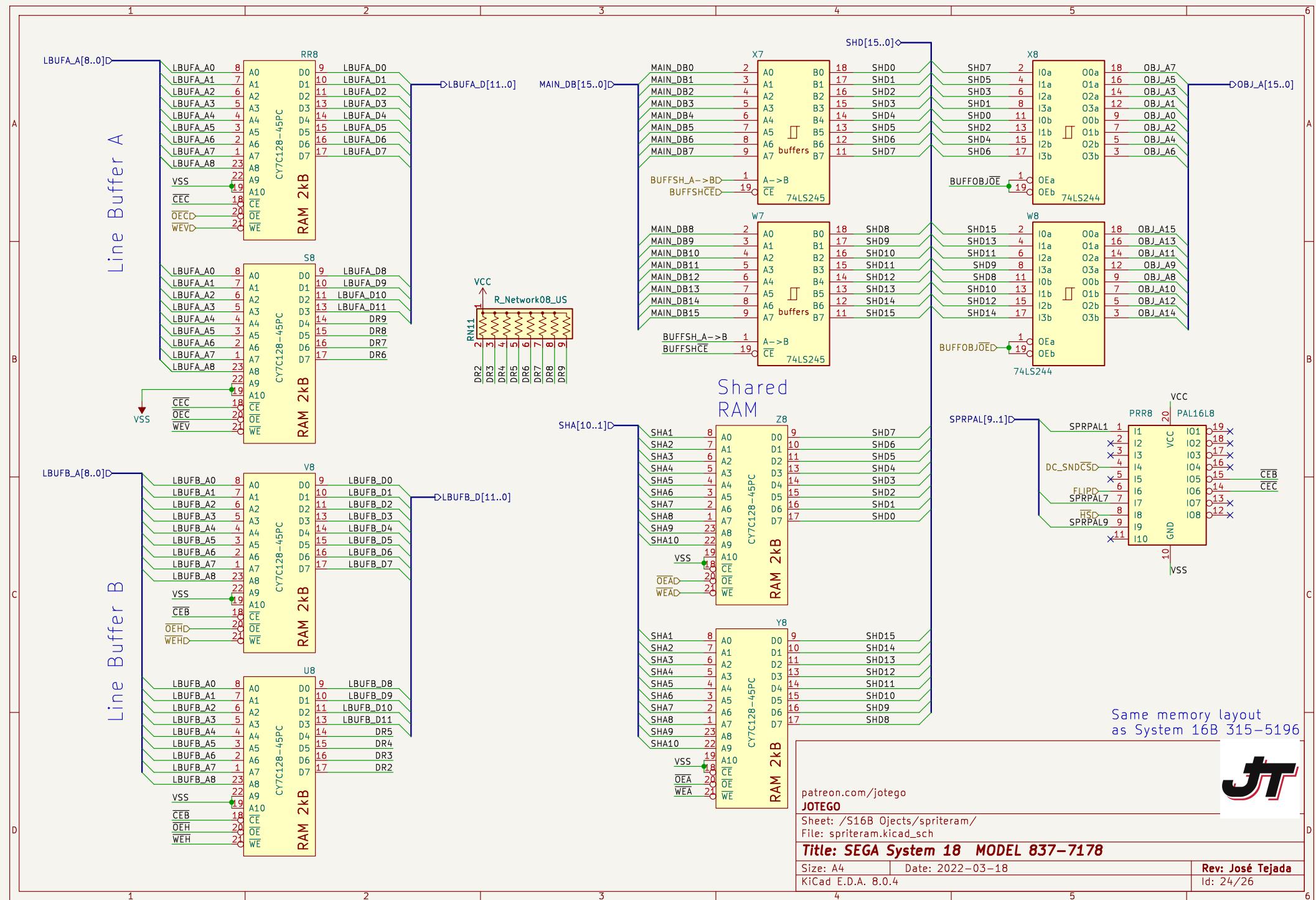
Rev: José Tejada

Id: 22/26

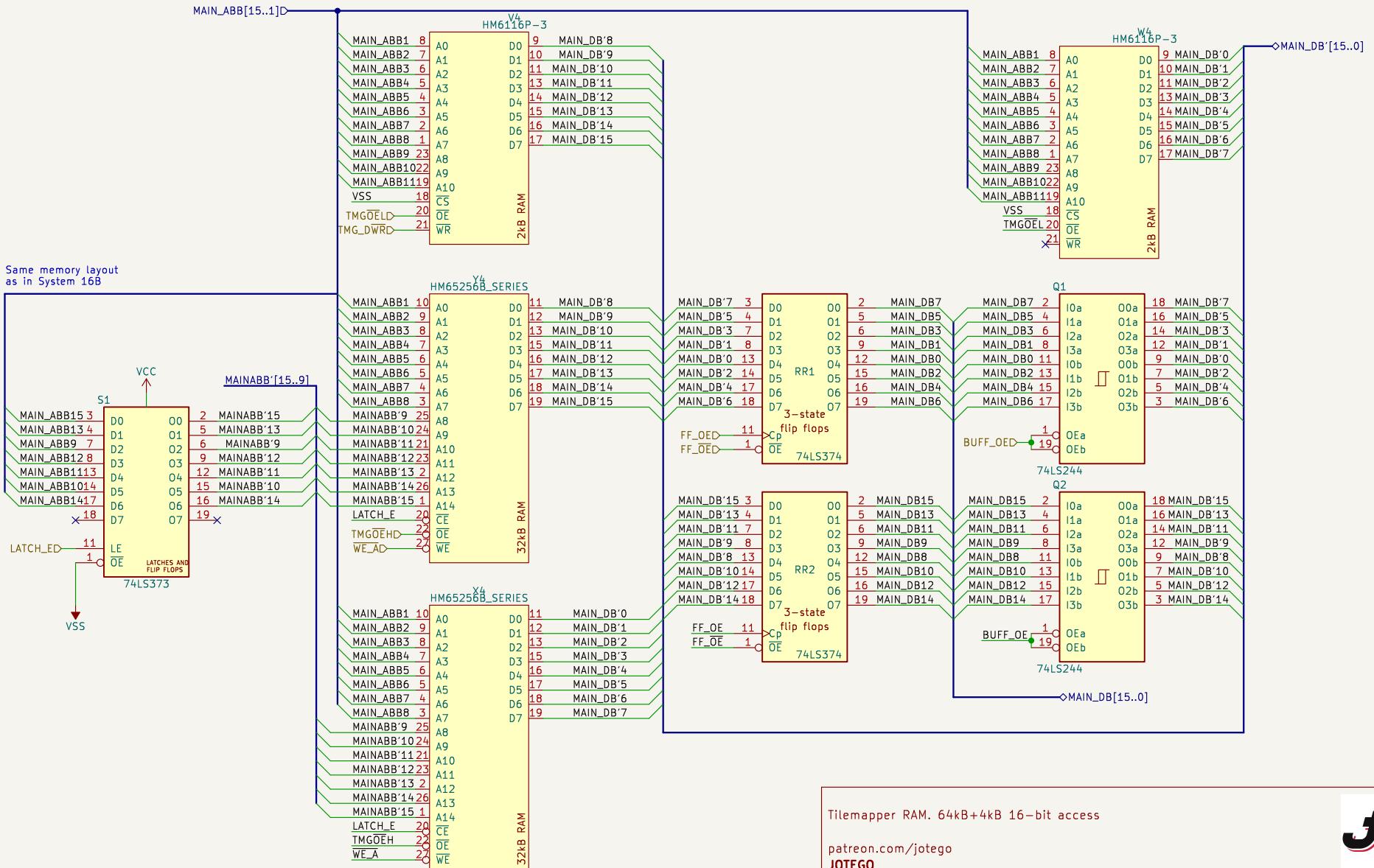


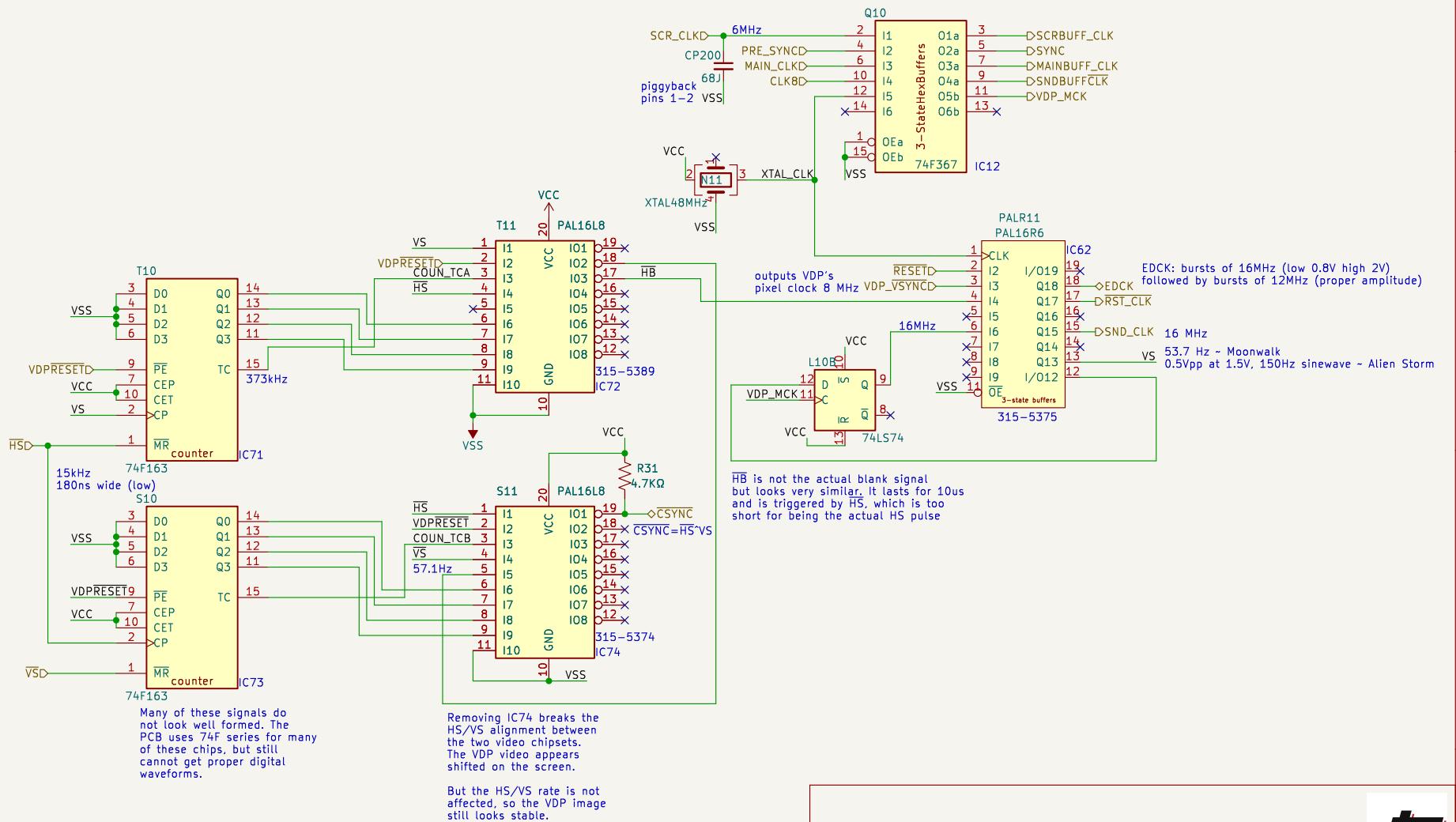
Leaded EMIFI  
Three-terminal Capacitor type  
DSS6NE52A222p  
2200pF 80/-20%  
100Vdc 6A -25 to +85°C

A kind of a track is observed between the two PCB layers, as if it were another layer, but there is no certainty about that. The track seems to be used to delimit or isolate the EMI (ElectroMagnetic Interference) filters. They may have wanted to isolate the noise between digital and analog devices.



1 2 3 4 5 6





VDP pixel clock and csync generation  
[patreon.com/jotego](https://patreon.com/jotego)

JOTEGO

Sheet: /Video Sync/  
 File: buffers.kicad\_sch

**Title: SEGA System 18 MODEL 837-7178**

Size: A4 Date: 2022-03-18  
 KiCad E.D.A. 8.0.4



Rev: José Tejada  
 Id: 26/26